



EE224 - Digital Design Project

IITB-CPU Project Report

Team Id : 22

Group Members:

Sathwik Shetty - 22B2209

Parinay Dewangan - 22B1286

Shruti Ghoniya - 22B1288

Khushi Wandile - 22B1285

Work Distribution

Khushi - Components(Register file, Sign extender), FSM, Control path

Parinay - Components(Temp Register, RAM), Dataflow, Control path

Shruti - Components(Left Shifter 7), Testbench, Testing, Control Path

Sathwik - Components(Memory,ALU), Control path, Testing, Control path

Testbench has been updated.

We have added Sw to select the register and its bits which would be shown in L.

There was a delay issue in the clock which was sorted out.