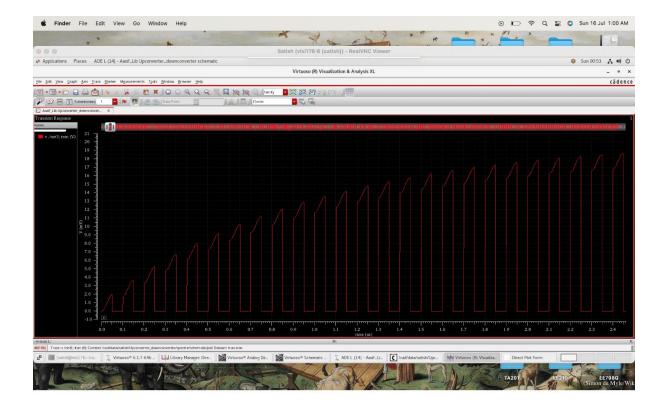
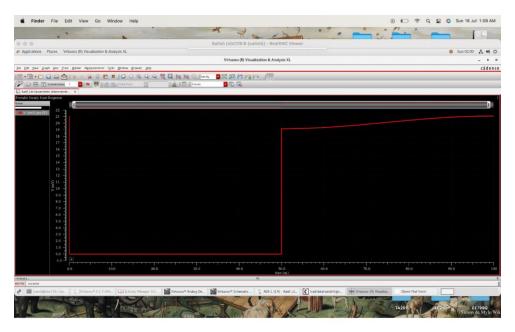
Basic RC filter with upconverter and downconverter switches

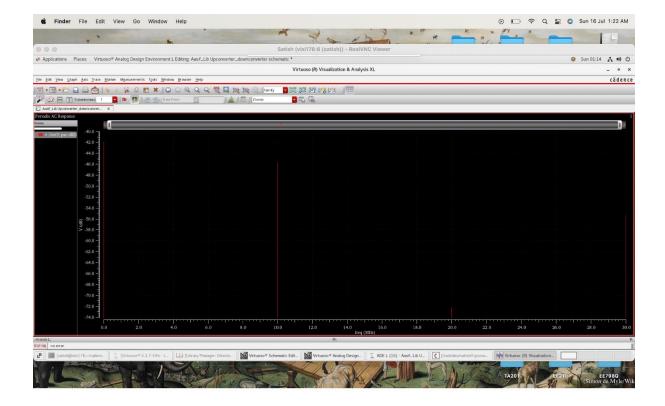


This is the basic transient simulation.

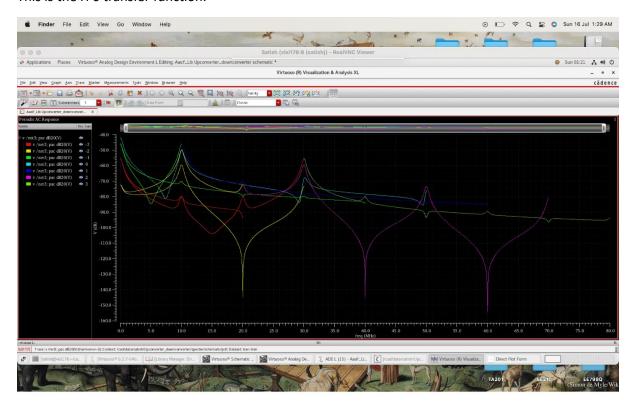


This is the PSS graph which takes less time for computing.

Basic RC filter with upconverter and downconverter switches

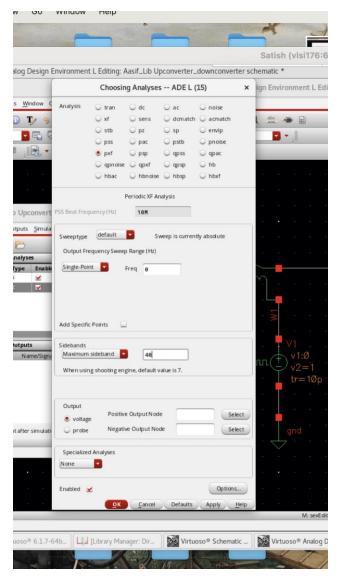


This is the H 0 transfer function.

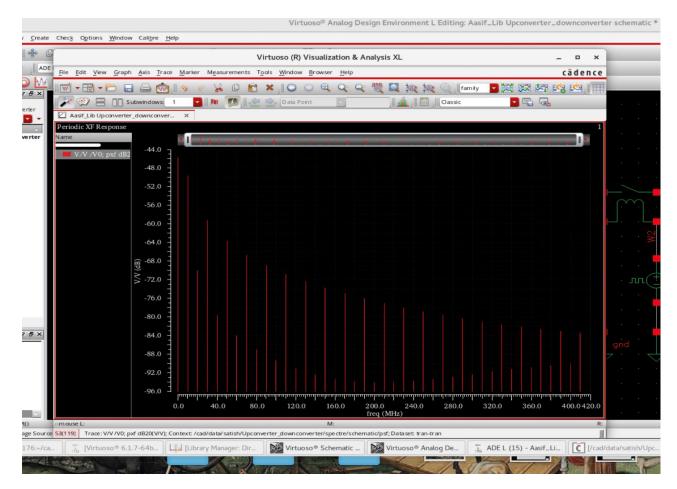


These are the first 3 harmonics

Basic RC filter with upconverter and downconverter switches



The setting for PXF. Took first 40 frequencies from which folding back may occur. The result is below



The result for PXF. Below is circuit schematic with R=10K, C=160p, Freq of switches = 10M Hz, BW = 10Kz

