



Department of Electronics & Telecommunication Engineering

TERM TEST-II

Time Table

AY-2025-26

Year: SE

Time	12/11/2025	13/11/2025	14/11/2025
8.00 am to 8.45 am	Open Electives	Mathematics for Telecommunication Engineering	Electronics Devices & Circuits
9.15 am to 10.00 am	Economics and Finance Management	Digital System Design	AIML(H)/VLSI(H)

Year: TE

Time	12/11/2025	13/11/2025	14/11/2025
8.00 am to 8.45 am	Digital Signal Processing	Analog & Digital Communication	DLE
9.15 am to 10.00 am	Wave Theory & Radio Frequency Design	Data Base Management System	AIML(H)/VLSI (H)

Year: BE

14/11/2025	17/11/2025	19/11/2025
10.30 am to 11.30 am	8.00 am to 9.00 am	8.00 am to 9.00 am
ILE	Microwave Engineering	Internet of Things
11.45 am to 12.45 pm	9.30 am to 10.30 am	9.30 am to 10.30 am
AIML(H)/ IoT(H)/ IoT (M)	Mobile Communication System	DLE (AIML/DCE/ES)

Prof. Dipti Kale

Term Test Coordinator (EXTC Dept.)

Prof. Amit A Deshmukh

Head, EXTC Dept.