Smart Garage System

MuP design problem No. 19

On Paper Design

Group 93:

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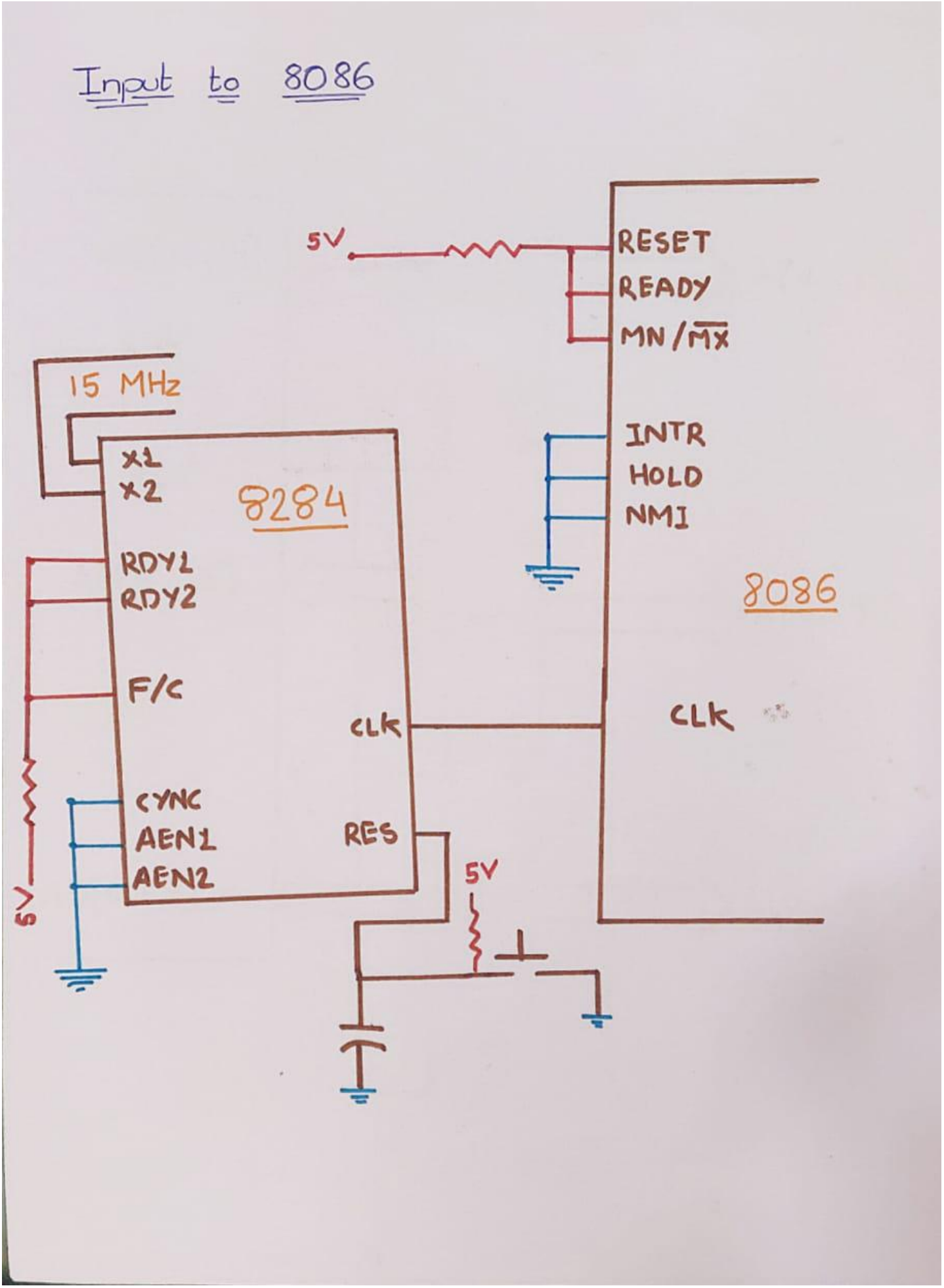
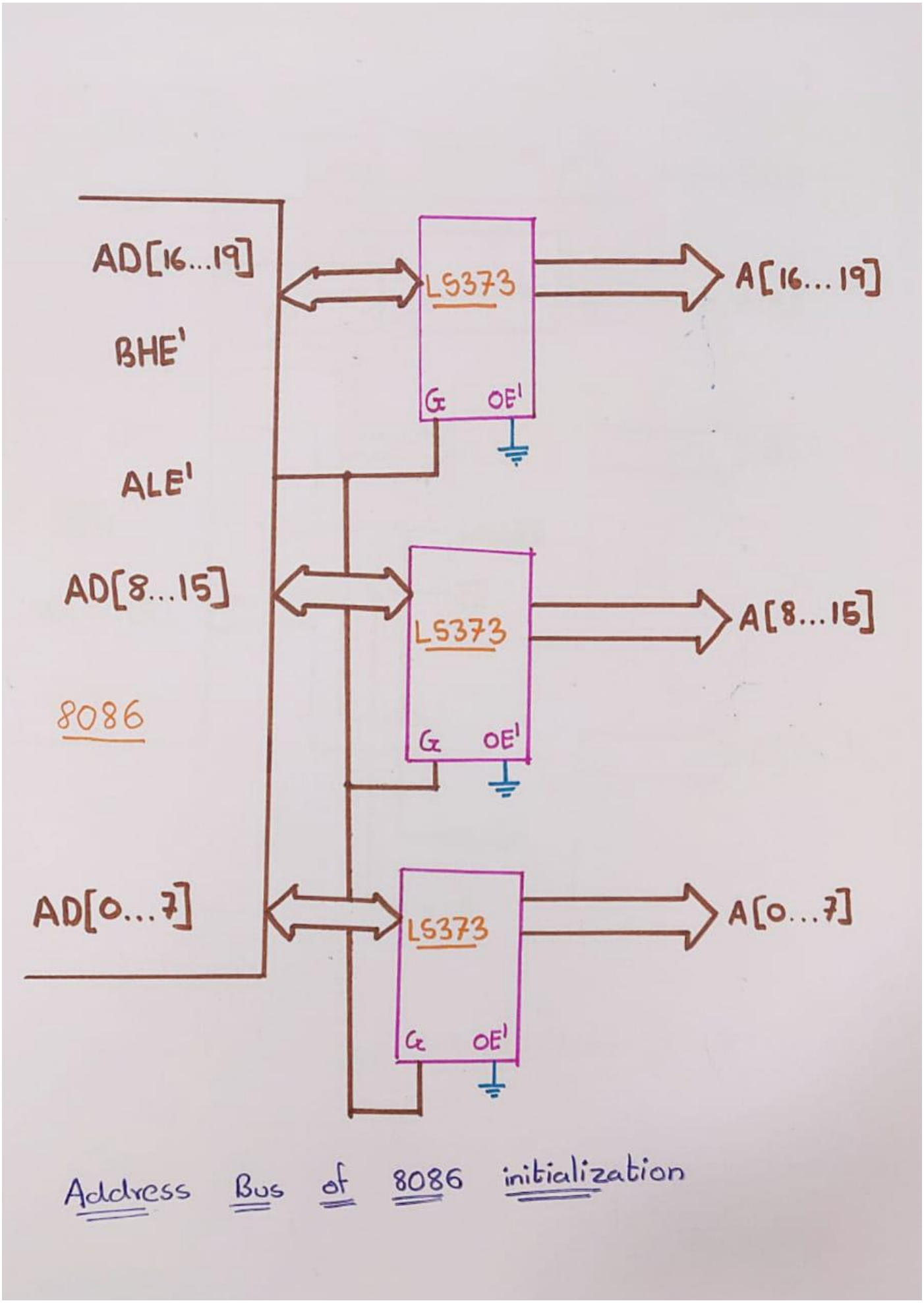
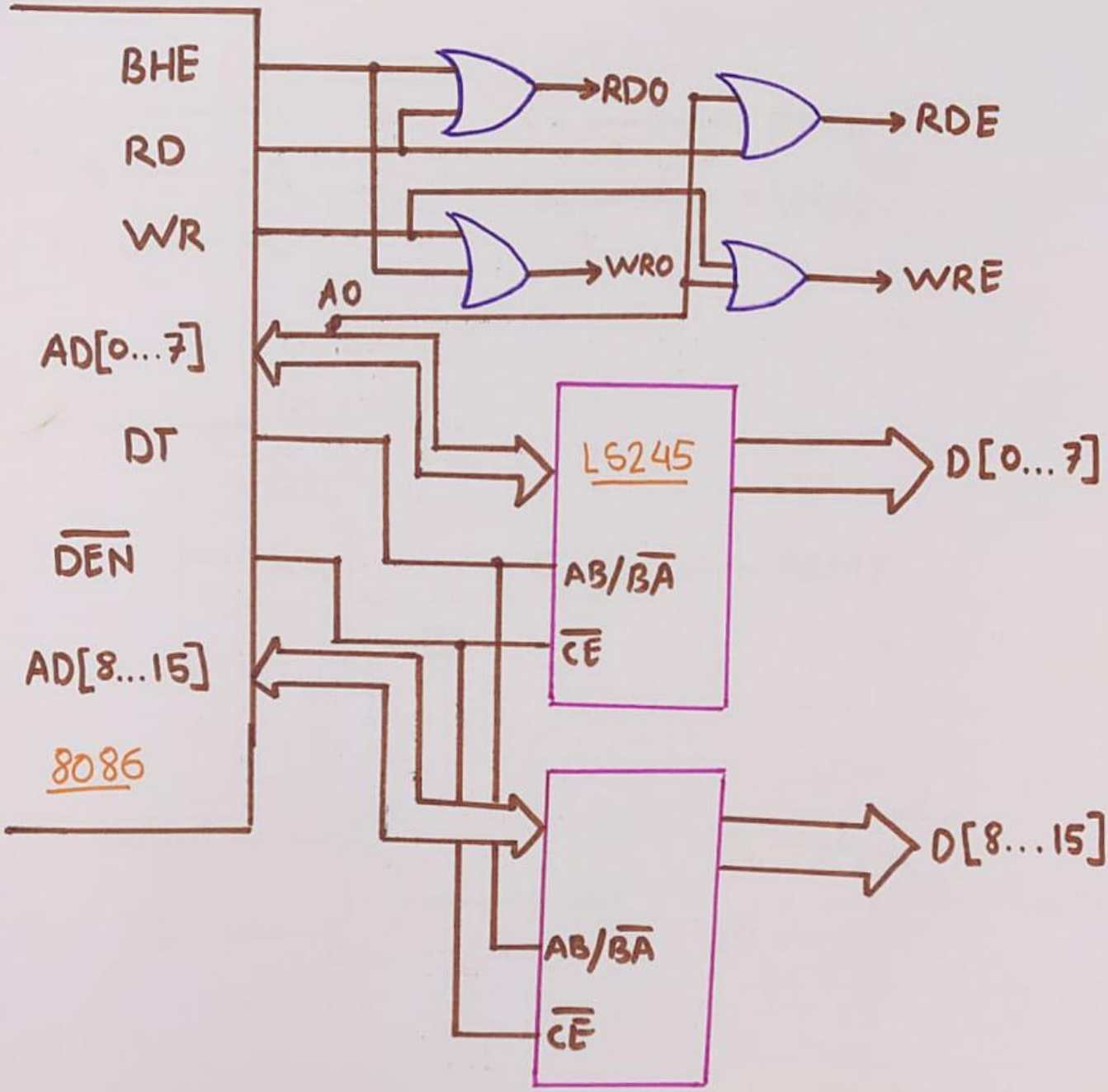
4. **Samesh Lakhotia (2018AAPS0553G)**
5. **Ritick Srivastava (2018AAPS0366G)**

Input to 8086

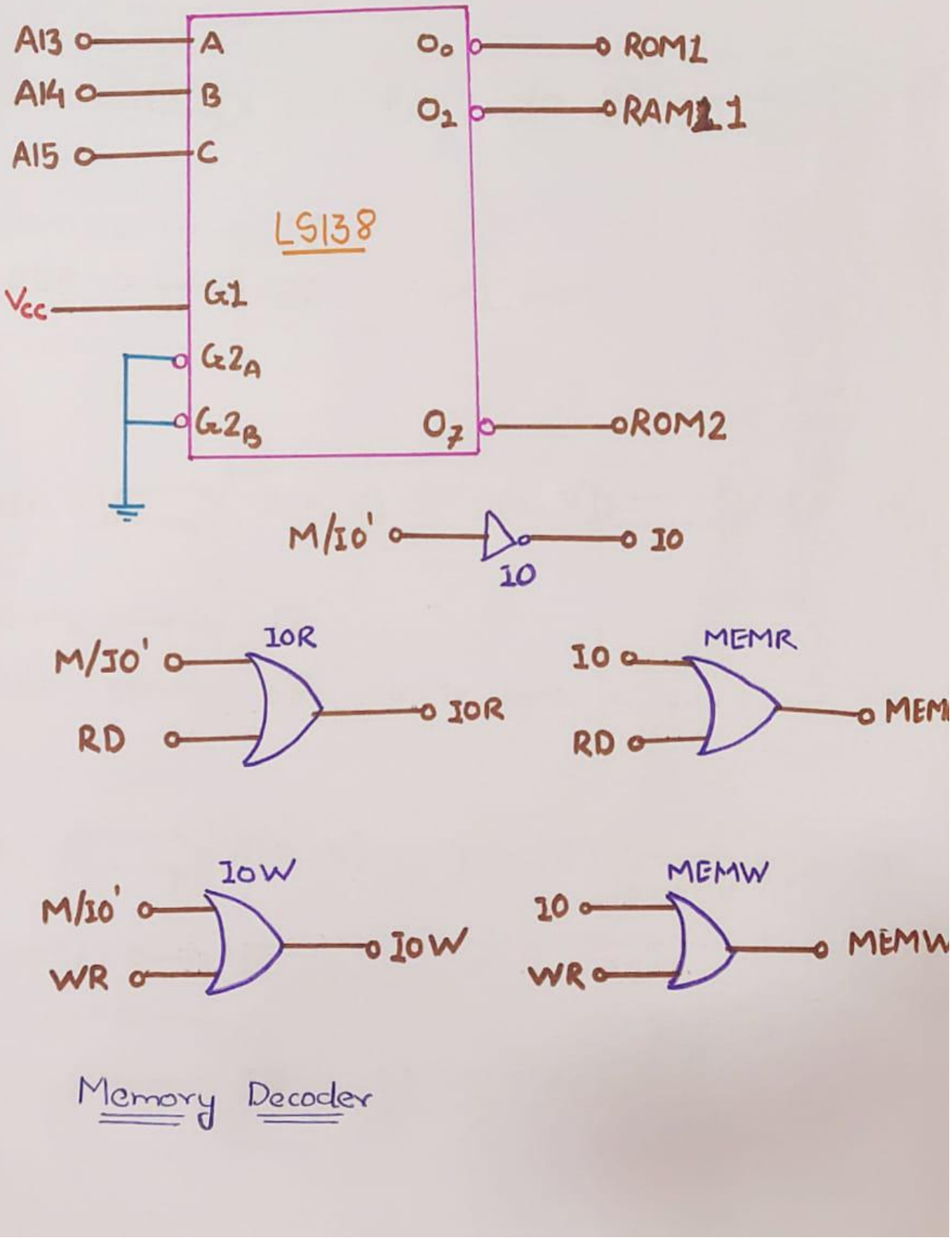




Address Bus of 8086 initialization

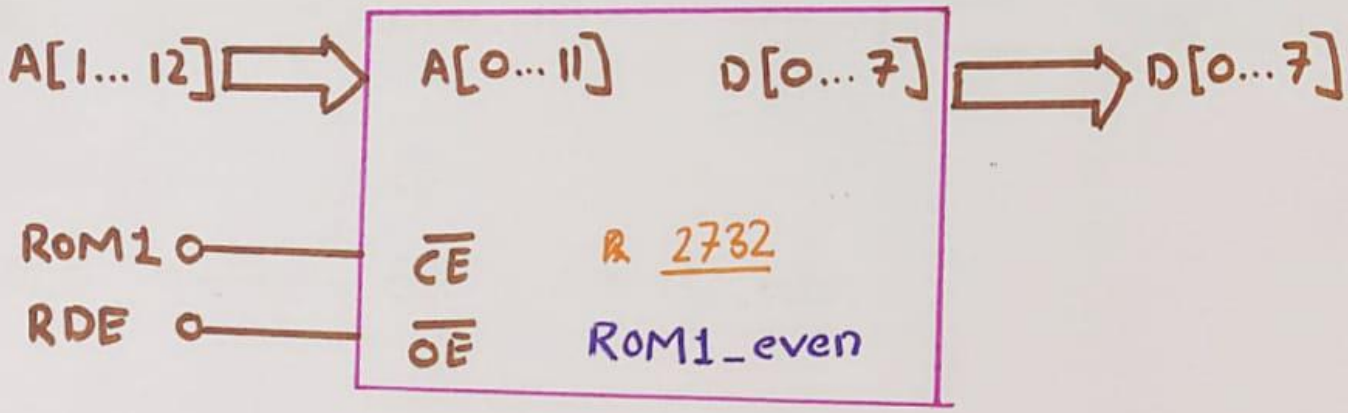


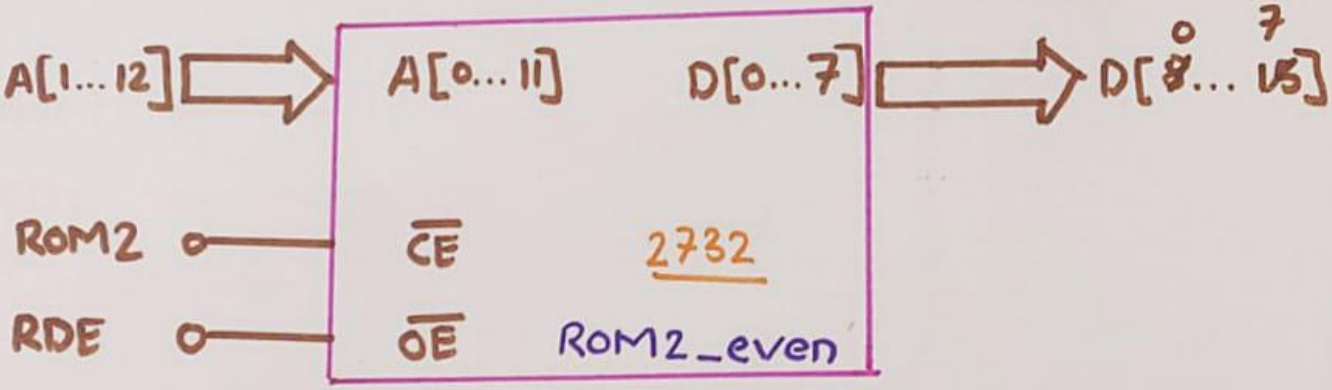
Data + Control bus initialization of 8086

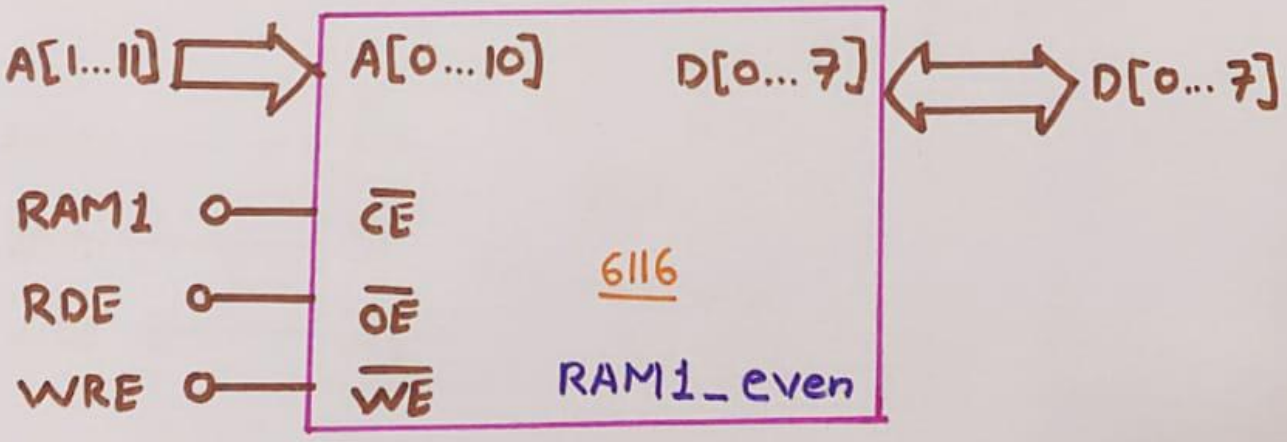


Memory Decoder

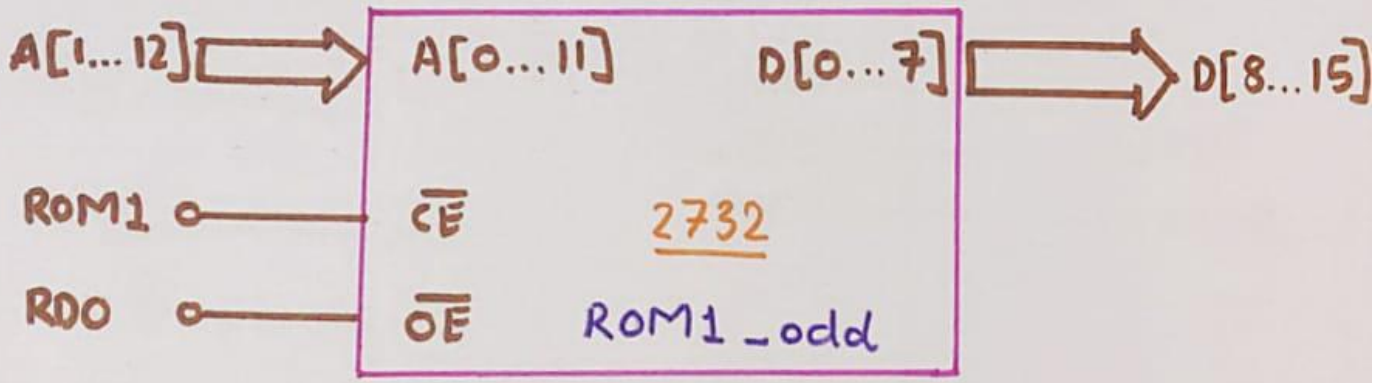
ROM(1+2) + RAM1 Even banks

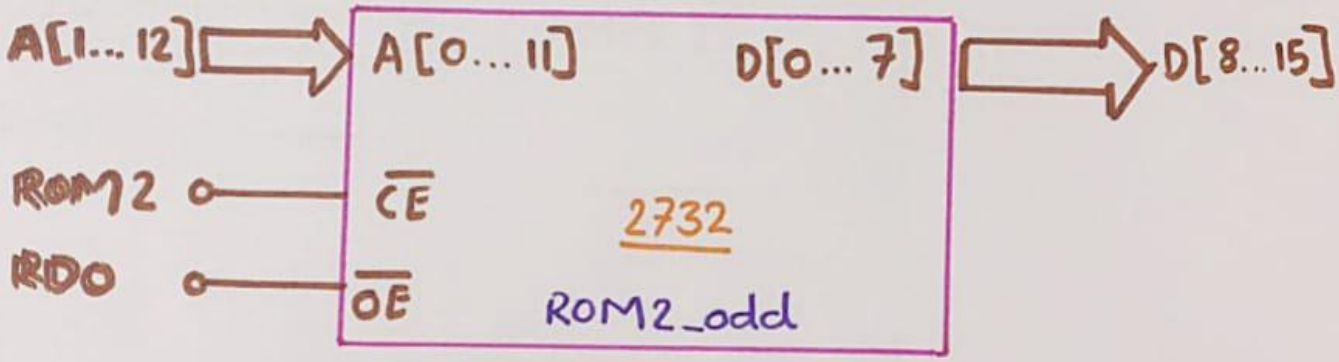


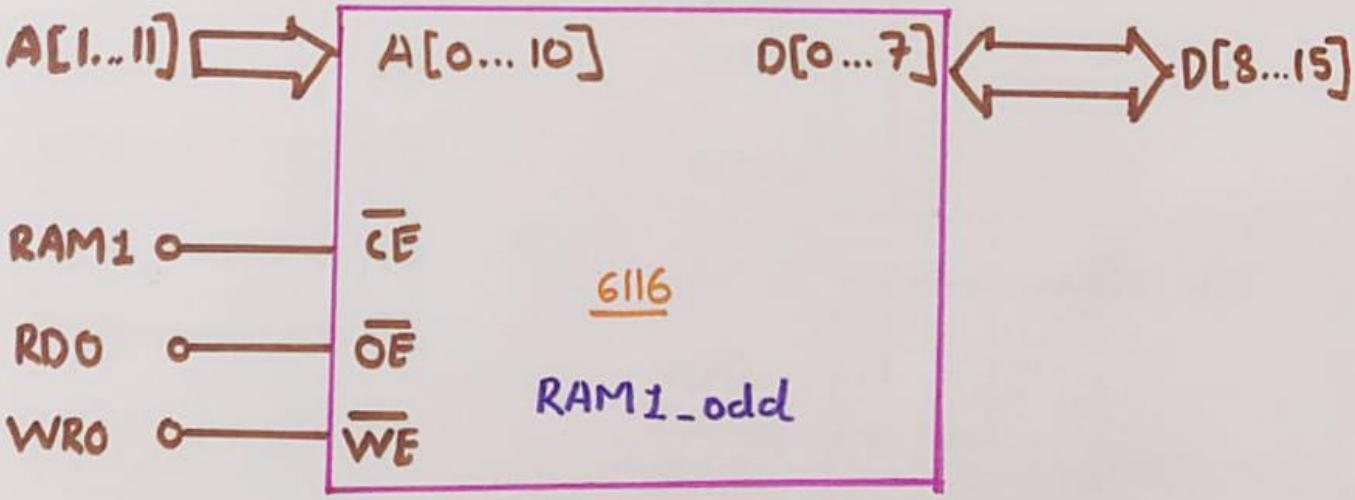




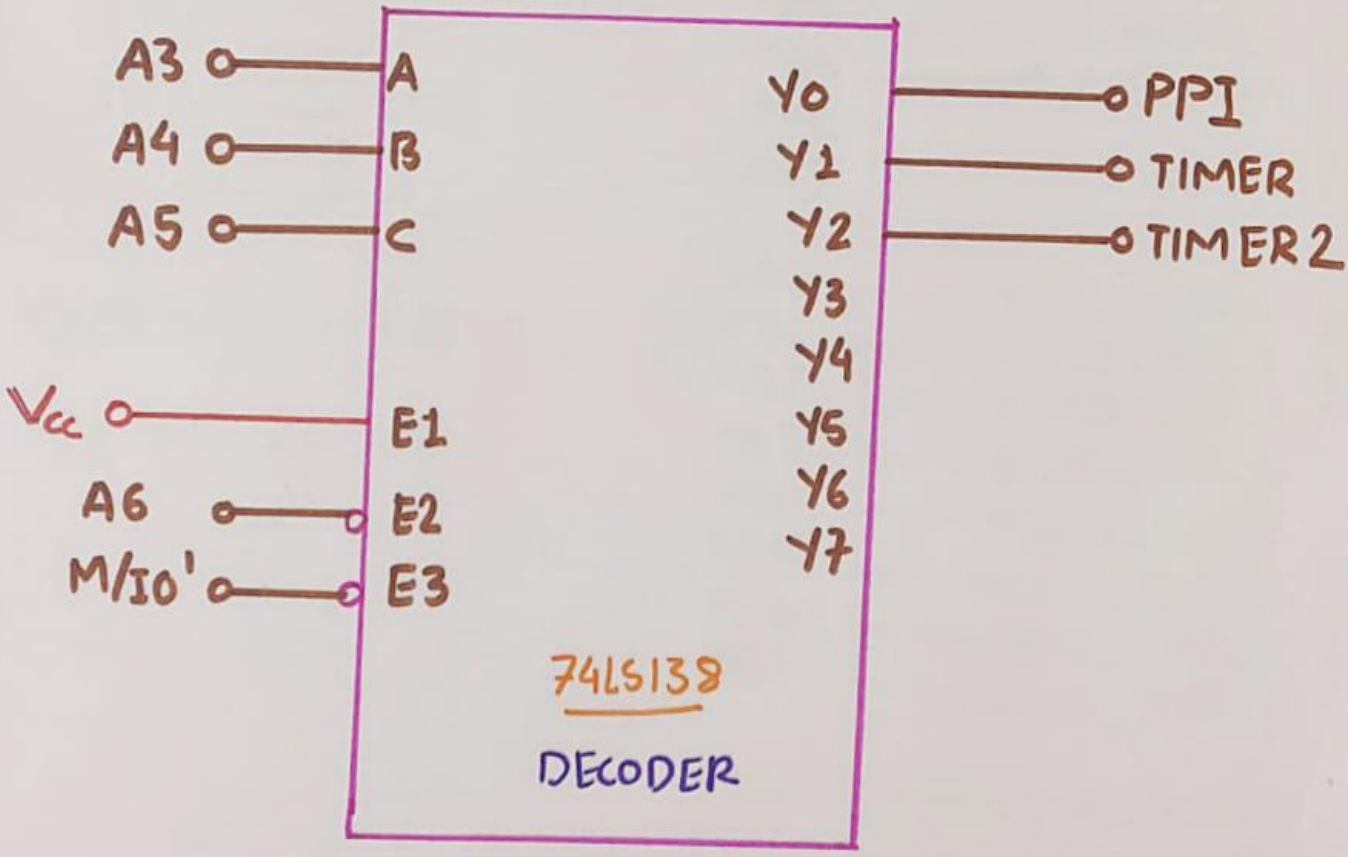
ROM(1+2) + RAM1 Odd Banks



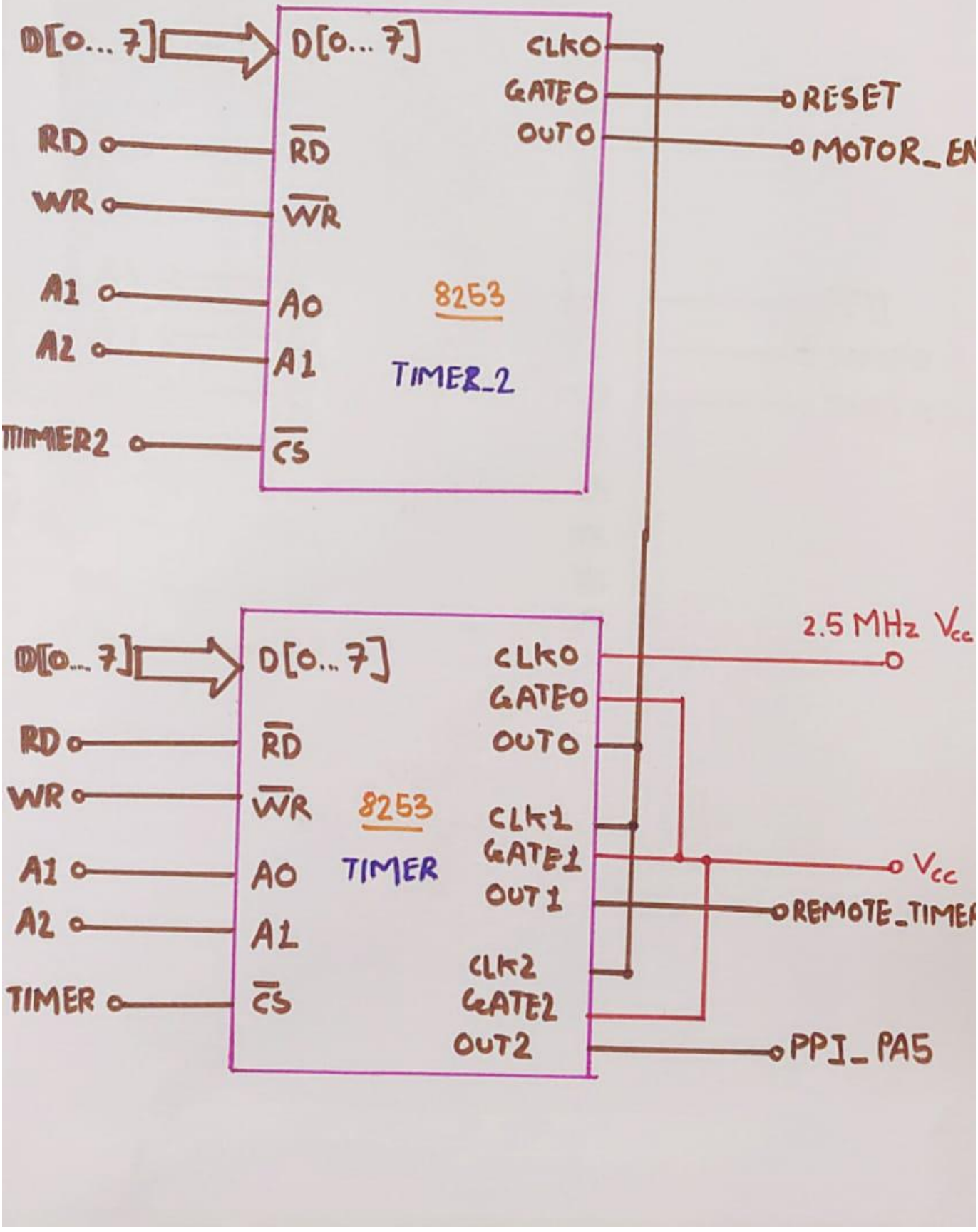




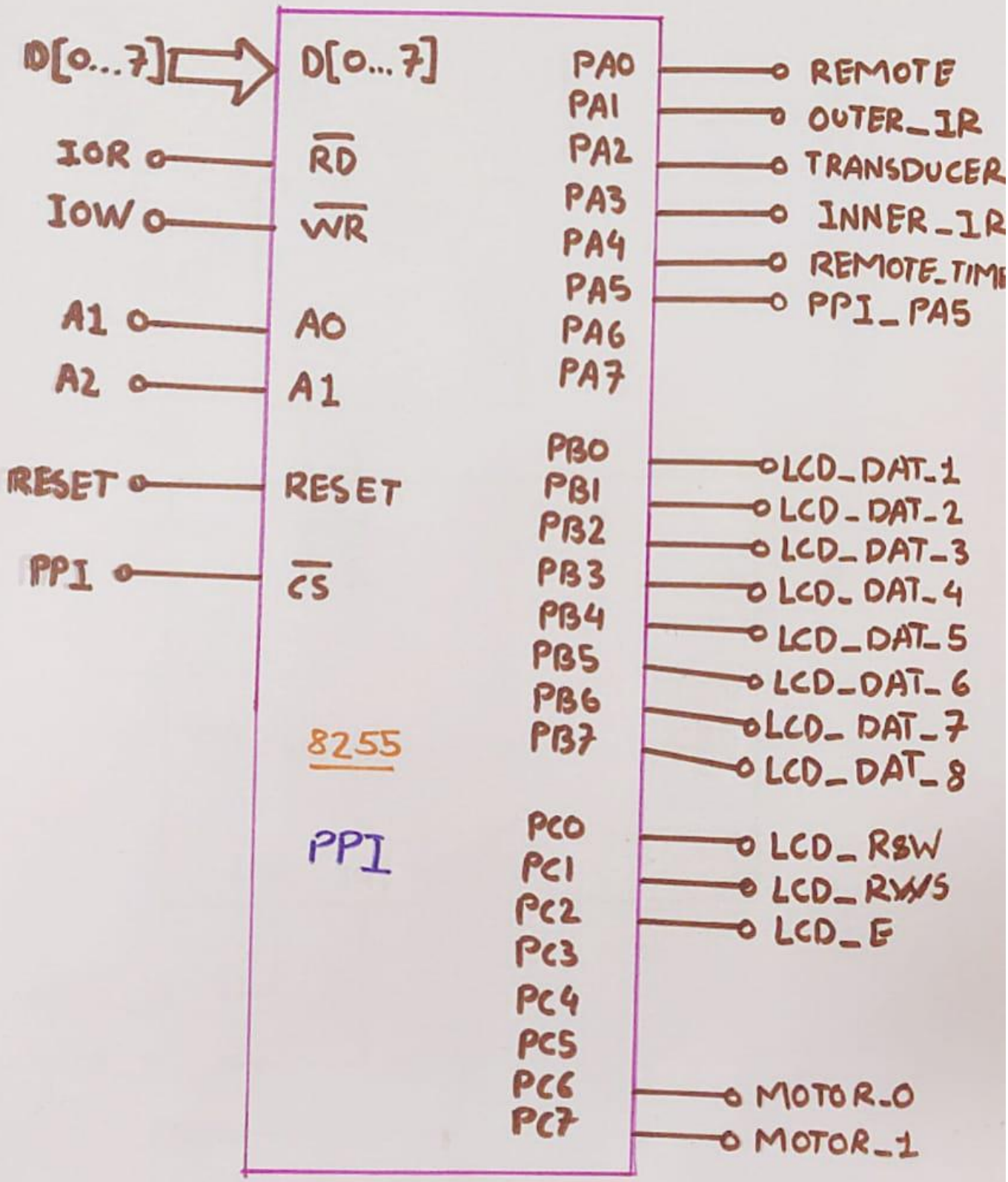
Address lines to PPI decoder



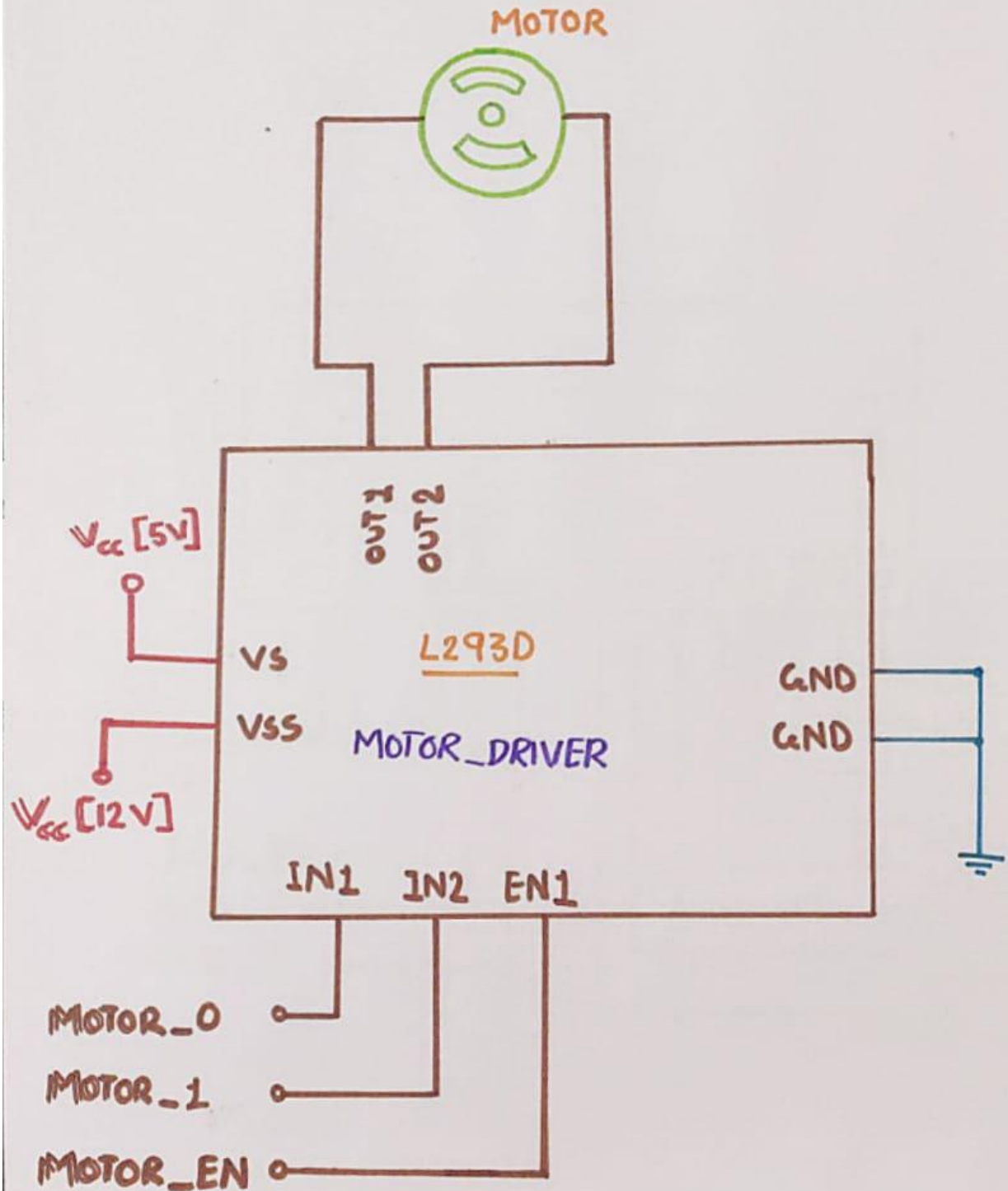
Timer (1+2) connections



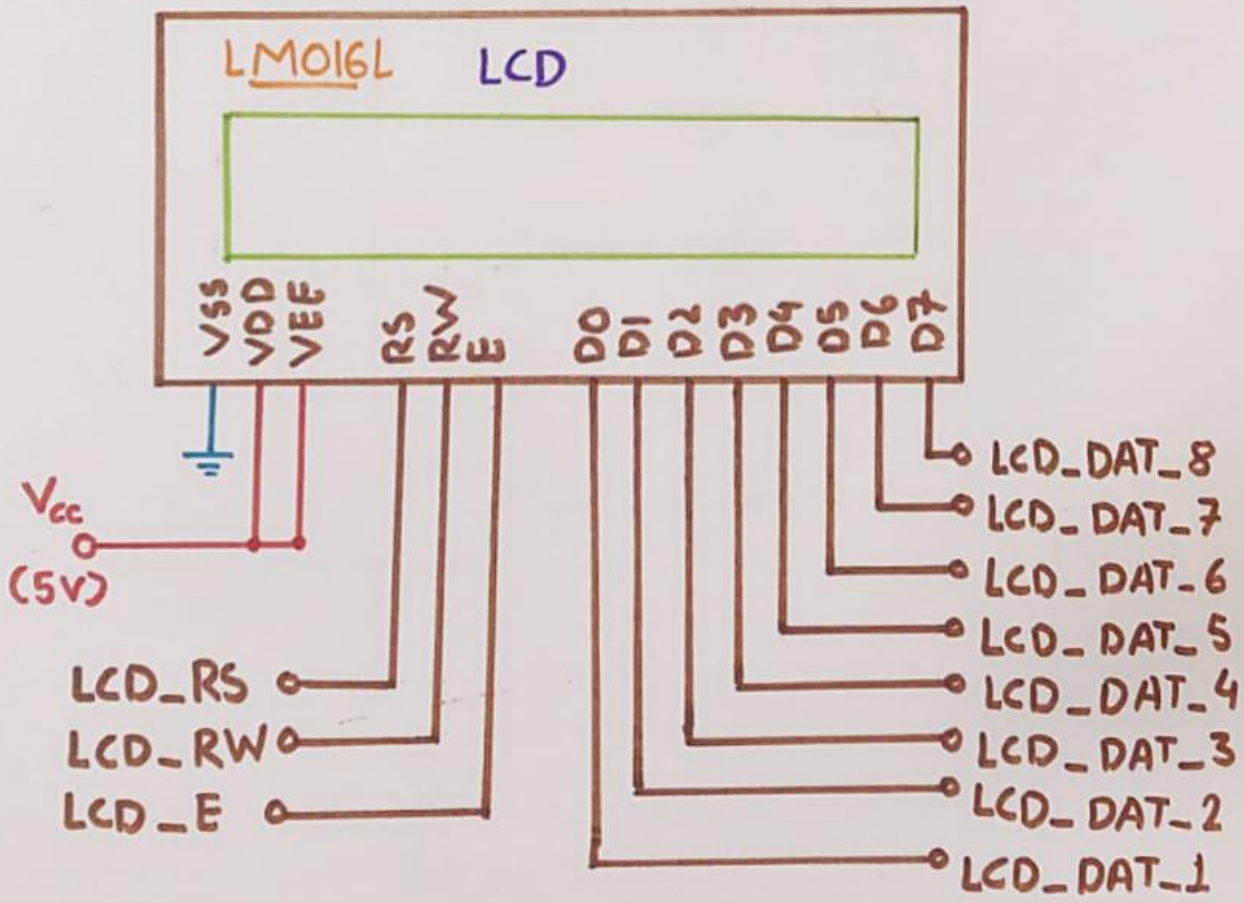
PPI connections

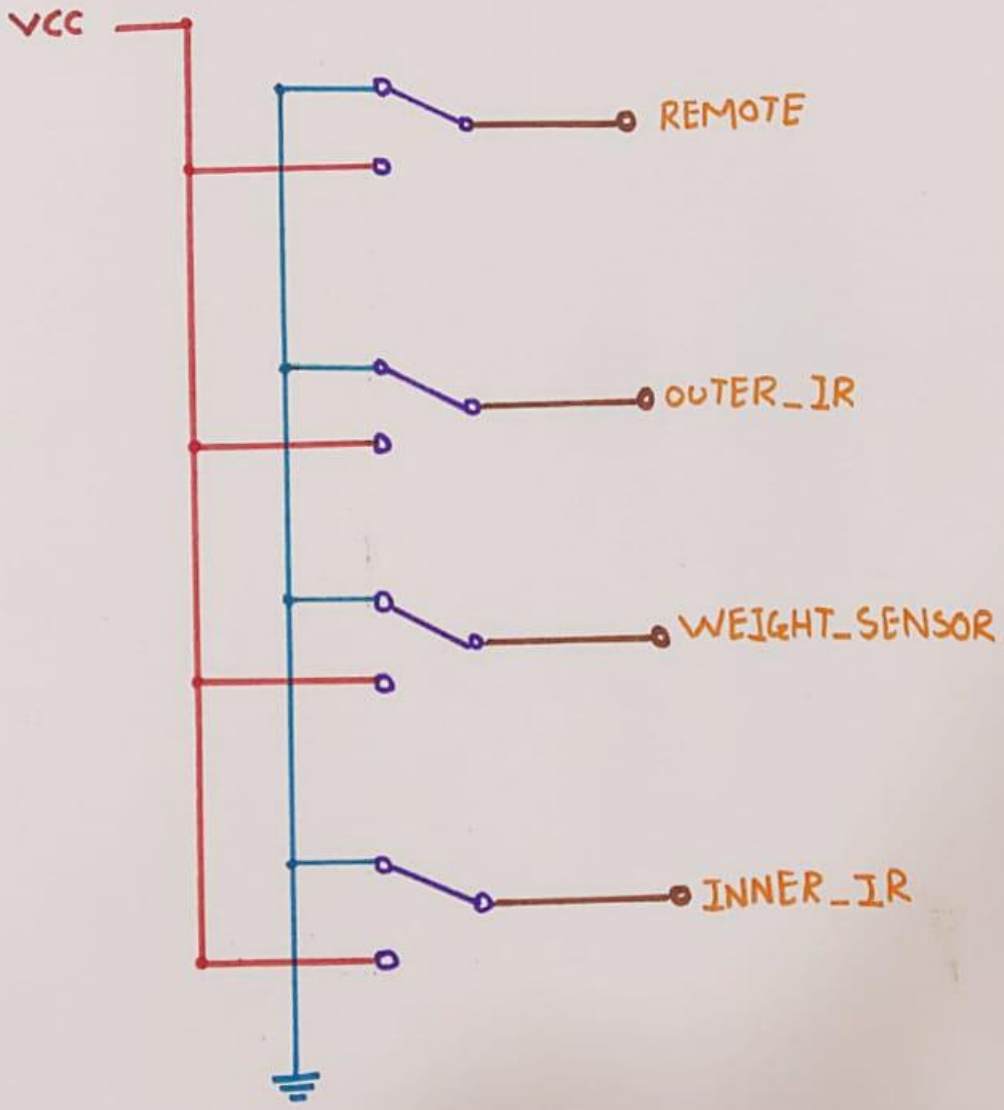


Motor and Motor driver connections



LCD connections





Remote + Sensor switches