



IEEE
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**11th IEEE International Students' Conference on
Electrical, Electronics and Computer Science**



**SCEECS 2026
MANIT, Bhopal**

**Slot No. CSE P1
Date: 31-01-2026**

**Time: 2:45 PM - 5:45 PM
Discipline: CSE**

S.NO.	PAPER ID	TITLE
1	432	Selective Clustering Based Deep Learning System for Early Skin Cancer Detection Using ISIC-2020 Dataset
2	517	A Detailed Experiment Based Review on the Segmentation Models Used in Medical Images
3	780	A Review on Wearable ADM for Metabolic Health Assessment
4	800	Brain-Computer Interface Based Mental Workload Classification: Utilizing EEG Signal and N-Back Test Experiment
5	426	Optimization Accuracy of Sentiment Analysis for Twitter Data using LSTM Model
6	806	Cattle Breed Classification Using Superpixel-Driven Graph Neural Network
7	477	Demosaicing of Microscopic Vine Wood Images Using Plug-and-Play Proximal Graphical Models
8	200	"VirtuScribe : Brain-to-Text Intelligent Note-Taking Assistant for Meetings"
9	189	HiDeNET: Hybrid Deep Neural Architecture for Multilevel Sentiment Classification of Cryptocurrency Comments



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**SCEECS 2026
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**Slot No. ECE P1
Date: 31-01-2026**

**Time:11:45 AM - 1:45 PM
Discipline: ECE**

S.NO.	PAPER ID	TITLE
1	299	Design and Analysis of 6T SRAM using MTCMOS and Adaptive Body Biasing (ABB) for Low-Power, High-Performance Operation at 45nm in Cadence Virtuoso
2	778	Design of a High Performance Energy -Efficient 4:2 Compressor Using Modified XOR-XNOR Logic
3	849	Architectural Assessment of a CIM-OR hard-block for FPGAs
4	554	Analysis of Asynchronous Up/Down Counters Using Conventional and Reversible Logic Gates
5	583	Review of Machine Learning Techniques for Enhancing WDM-RoF System Capacity and Performance
6	770	Design and Analysis of Circular Patch Based Circularly Polarized Wideband Antenna for K-band applications
7	565	Design of a Metasurface-Based Circularly Polarized Patch Antenna for Operation in the C and X Bands



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**SCEECS 2026
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**Slot No. ECE P2
Date: 31-01-2026**

**Time: 2:45 PM - 5:45 PM
Discipline: ECE**

1	693	Investigation and Design of a Full Adder (FA) circuit from 3T XOR and Mux logic at 45 nm node for Low Power VLSI Circuits
2	574	Design of Rectangular Slotted Microstrip Patch Antenna for 5G Applications at 26GHz
3	717	A U-Net-Based Deep Learning Framework for Breast Ultrasound Lesion Segmentation
4	796	Enhancement of 6T CNFET SRAM Performance: A Comparison of Traditional and Stacking Methods in 45nm Technology
5	165	Terahertz CSRR Loaded Patch Antenna for Breast Cancer Cell Detection
6	815	Design and Simulation of a Power-Aware Advanced ALU with Interrupt-Driven FSM for Real-Time Health Monitoring.
7	826	Automatic Sleep Apnea Detection Using 1D CNN-BiLSTM on Single-Lead ECG Signals



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**SCEECS 2026
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**Slot No. EE P1
Date:31-01-2026**

**Time:11:45 AM - 1:45 PM
Discipline: EE**

EE:

S.NO.	PAPER ID	TITLE
1	551	A Novel Lower-Device Count 9-Level Asymmetrical MLI for PV Application
2	801	Minimize THD of seven level three phase buck converter for Electric Vehicles
3	496	A Soft-Switched CUK Converter Design and Analysis
4	553	Smart Energy Management Framework for Optimal Integration of Renewable Energy in Modern Power Systems
5	562	Fault Diagnosis and Isolation in Electric Vehicles: A Comprehensive Review
6	220	Elasticity-Driven Hybrid Demand Response with Rooftop PV: Enhancing End-User Profitability and Peak Reduction in Residential Smart Grids
7	570	INTEGRATED OPTIMIZATION FRAMEWORK FOR PV, EV, AND DEMAND RESPONSE IN SMART HOMES