

374 CPU Design Project

374 CPU Design Project			Sub Tasks	Task Prog.	23	24	25	26	27	28	1	2	3	4	5	6	7	8	9	10	11	12	13	14
0	Testbench a full adder	0%			<div></div>	Testbench a full adder																		
1	Build a 4 bit ripple carry adder	0%			<div></div>	Build a 4 bit ripple carry adder																		
2	Testbench 4 bit ripple carry adder	0%			<div></div>	Testbench 4 bit ripple carry adder																		
3	Build AND, OR, NOT logic circuits	0%			<div></div>	Build AND, OR, NOT logic circuits																		
4	Testbench AND, OR, NOT logic circuits	0%			<div></div>	Testbench AND, OR, NOT logic circuits																		
5	Get multiplier circuit from Drew	0%			<div></div>	Get multiplier circuit from Drew																		
6	Build 32 bit ripple carry adder	0%			<div></div>	Build 32 bit ripple carry adder																		
7	Testbench 32 bit ripple carry adder	0%			<div></div>	Testbench 32 bit ripple carry adder																		
8	Build divider circuit	0%			<div></div>	Build divider circuit																		
9	Testbench divider circuit	0%			<div></div>	Testbench divider circuit																		
10	Testbench multiplier circuit	0%			<div></div>	Testbench multiplier circuit																		
11	Combine circuits all into ALU	0%			<div></div>	Combine circuits all into ALU																		
12	Testbench ALU	0%			<div></div>	Testbench ALU																		
13	Testbench registers	0%			<div></div>	Testbench registers																		
14	Testbench memory	0%			<div></div>	Testbench memory																		
15	Testbench data path between components	0%			<div></div>	Testbench data path between components																		