Tobias Sautter BSYS HW5 30.04.2023

Question 1

Generate random addresses with the following arguments: -s 0 -n 10, -s 1 -n 10, and -s 2 -n 10. Change the policy from FIFO, to LRU, to OPT.

Compute whether each access in said address traces are hits or misses.

```
-s 0 -n 10 -p FIF0
                                -s 0 -n 10 -p LRU
                                                                 -s 0 -n 10 -p 0PT
ARG numaddrs 10
                                ARG numaddrs 10
                                                                 ARG numaddrs 10
ARG policy FIF0
                                ARG policy LRU
                                                                 ARG policy OPT
ARG cachesize 3
                                ARG cachesize 3
                                                                 ARG cachesize 3
ARG seed 0
                                ARG seed 0
                                                                 ARG seed 0
                                Access: 8
                                           Miss
Access: 8 Miss
                  [8]
                                                  [8]
                                                                 Access: 8
                                                                            Miss
                                                                                    [8]
Access: 7
           Miss
                  [8,7]
                                Access: 7
                                           Miss
                                                  [8,7]
                                                                 Access: 7
                                                                            Miss
                                                                                    [8,7]
Access: 4
           Miss
                  [8,7,4]
                                Access: 4
                                           Miss
                                                  [8,7,4]
                                                                 Access: 4
                                                                            Miss
                                                                                    [8,7,4]
                                Access: 2
Access: 2
                                                                 Access: 2
                                                                                    [7,4,2]
                  [7,4,2]
           Miss
                                           Miss
                                                  [7,4,2]
                                                                            Miss
                                Access: 5
Access: 5 Miss
                  [4,2,5]
                                                                 Access: 5
                                                                            Miss
                                                                                    [7,4,5]
                                           Miss
                                                  [4,2,5]
                  [4,2,5]
Access: 4 Hit
                                                  [2,5,4]
                                Access: 4
                                           Hit
                                                                 Access: 4
                                                                             Hit
                                                                                    [7,4,5]
                                Access: 7
Access: 7
                                                                 Access: 7
                                           Miss
                                                  [5,4,7]
                                                                                    [7,4,5]
           Miss
                  [2,5,7]
                                                                             Hit
                                Access: 3
                  [5,7,3]
                                           Miss
                                                                                   [4,5,3]
Access: 3
           Miss
                                                  [4,7,3]
                                                                 Access: 3
                                                                            Miss
Access: 4
           Miss
                  [7,3,4]
                                Access: 4
                                           Hit
                                                  [7,3,4]
                                                                 Access: 4
                                                                             Hit
                                                                                    [4,5,3]
Access: 5
                  [3,4,5]
                                Access: 5
                                                                 Access: 5
                                                                                    [4,5,3]
                                                  [3,4,5]
           Miss
                                           Miss
                                                                            Hit
10% hit rate
                                20% hit rate
                                                                 40% hit rate
```

For a cache of size 5, generate worst-case address reference streams for each of the following policies: FIFO, LRU, and MRU (worst-case reference streams cause the most misses possible).

Question 2

For the worst case reference streams, how much bigger of a cache is needed to improve performance dramatically and approach OPT? python3 paging-policy.py -C 5 -p FIF0 -a 1,2,3,4,5,6,1,2,3,4 -c python3 paging-policy.py -C 5 -p LRU -a 1,2,3,4,5,6,1,2,3,4 -c python3 paging-policy.py -C 5 -p MRU -a 1,2,3,4,5,6,5,6,5,6 -c

```
If the cache size is equal to the maxpage we don't need to throw anything out and get the same result as OPT with every policy
```

misses 45

Question 3

Generate a random trace (use python or perl). How would you expect the different policies to perform on such a trace?

The different policies perform as expected and like they did before.

OPT:

import random

hitrate 55.00

in range(100)]

```
with open("no-locality.txt", "w") as file:
   for number in numbers:
        file.write(str(number) + "\n")
```

numbers = [random.randint(1, 10) for

FINALSTATS hits 55

How can you generate such a trace?

How about CLOCK with different numbers of clock bits?

FINALSTATS hits 38

-f locality-80-20.txt -p CLOCK -c -N -b 0

valgrind --tool=lackey --trace-mem=yes ls

How does LRU perform on it?

import random

```
FINALSTATS hits 38
   LRU:
                                   misses 62
                                                hitrate 38.00
             FINALSTATS hits 34
   FIF0:
                                   misses 66
                                                hitrate 34.00
   CLOCK:
             FINALSTATS hits 34
                                   misses 66
                                                hitrate 34.00
             FINALSTATS hits 31
                                                hitrate 31.00
                                   misses 69
   MRU:
             FINALSTATS hits 33
                                                hitrate 33.00
   RAND:
                                   misses 67
Question 4
   Now generate a trace with some locality.
```

How much better than RAND is LRU? How does CLOCK do?

```
frequent_numbers = [random.randint(1, 5) for _ in range(80)]
infrequent_numbers = [random.randint(6, 10) for _ in range(20)]
numbers = frequent numbers + infrequent numbers
random.shuffle(numbers)
with open("locality-80-20.txt", "w") as file:
    for number in numbers:
        file.write(str(number) + "\n")
OPT:
         FINALSTATS hits 62
                             misses 38
                                         hitrate 62.00
        FINALSTATS hits 48
                             misses 52
RAND:
                                         hitrate 48.00
        FINALSTATS hits 47
                             misses 53
                                         hitrate 47.00
LRU:
        FINALSTATS hits 45
CLOCK:
                             misses 55
                                         hitrate 45.00
                                         hitrate 44.00
        FINALSTATS hits 44
                             misses 56
FIFO:
```

misses 62

```
-f locality-80-20.txt -p CLOCK -c -N -b 1
-f locality-80-20.txt -p CLOCK -c -N -b 2
-f locality-80-20.txt -p CLOCK -c -N -b 3
                                                                                   hitrate 41.00
Question 5
    Use a program like valgrind to instrument a real application and generate a virtual page reference stream.
```

will output a nearly-complete reference trace of every instruction and data reference made by the program Is. To make this useful for the simulator above, you'll have to first transform each virtual memory reference into a virtual

For example, running

args = parser.parse_args() policy = args.filename

MRU:

import subprocess import platform import argparse

page-number reference (done by masking off the offset and shifting the resulting bits downward).

```
# Parse the command line argument
parser = argparse.ArgumentParser()
parser.add_argument("filename", help="the name of the file to trace")
```

hitrate 38.00

hitrate 34.00

hitrate 37.00 hitrate 45.00

```
PAGE_SIZE = 4096
system = platform.system()
# check which system we are running and set the page size correct
if system == 'Windows':
    import ctypes
    sysinfo = ctypes.windll.kernel32.GetSystemInfo()
    PAGE_SIZE = sysinfo.dwPageSize
elif system == 'Linux' or system == 'Darwin':
    import resource
    PAGE_SIZE = resource.getpagesize()
else:
    PAGE SIZE = None
    print('Unknown operating system, PAGE_SIZE not set')
print('PAGE_SIZE:', PAGE_SIZE)
# Run the valgrind command and save the output to a file
subprocess.run("valgrind --tool=lackey --trace-mem=yes ls &> ls_trace.txt", shell=True)
# Convert virtual memory references to virtual page numbers in decimal and write to file
with open("ls_trace.txt", "r") as f:
    with open("ls_trace_vpn.txt", "w") as fout:
        for line in f:
            if not line.startswith("=") and "," in line:
                address = int(line[3:line.index(",")], 16)
                page_number = address // PAGE_SIZE
                fout.write(str(page_number) + "\n")
# Call paging policy.py with the given policy
command = "python3 paging-policy.py -p " + policy + " -f ls_trace_vpn.txt -c -N"
subprocess.run(command, shell=True)
subprocess.run("rm ls_trace_vpn.txt", shell=True)
subprocess.run("rm ls_trace.txt",
                                  shell=True)
                     This code can take a long time, depending on your system.
                              Mainly because the simulator is really inefficient
```

How big of a cache is needed for your application trace in order to satisfy a large fraction of requests?

Plot a graph of its working set as the size of the cache increases.

