Felix_Chen_Lab4

:≡ Tags

Part 1

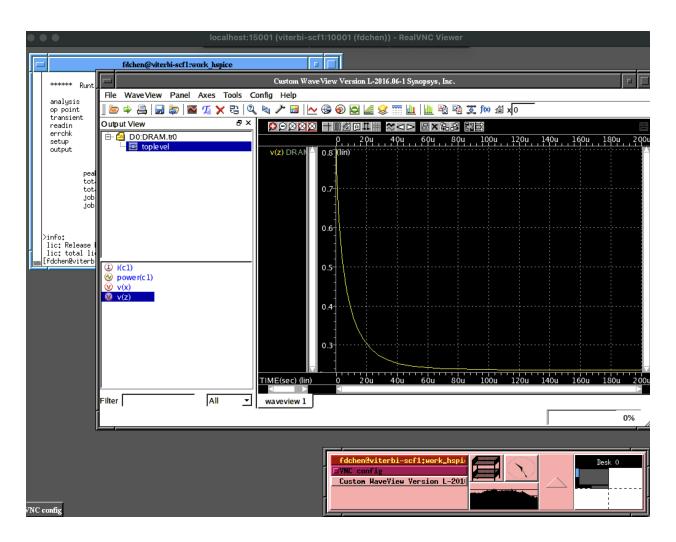
DRAM

The hspice file

```
** Simple DRAM using PTM 45 nm Node
.include CMOSP.inc
.include CMOSN.inc
.PARAM VDD = 0.8
** Circuit Netlist
** Supply and Input Sources
VSUP X 0 'VDD/2'
VG Y 0 0
** Transistors
M1 Z Y X VDD CMOSN L=45n W=120n
** Capacitor
C1 Z 0 C=10f
** Initial condition
IC V(Z) = 'VDD'
** Analysis Setup
.TRAN 0.001u 800u
** Control Information
.OPTION POST BRIEF NOMOD PROBE MEASOUT
** Print and Measurement
.PRINT V(X) V(Z)
.MEASURE TRAN RTL TRIG AT=0 TARG V(Z) VAL=0.6 FALL=1
.MEASURE TRAN AVG_CUR AVG I(C1) FROM 0 to 'RTL'
.MEASURE TRAN AVG_PWR AVG P(C1) FROM 0 to 'RTL'
. END
```

C=0.1fF

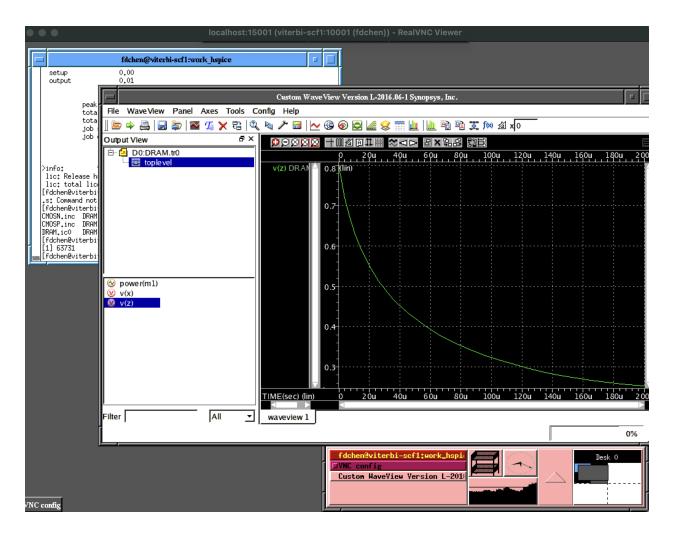
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RTL = $2.151e^{-6}$ average power = $-6.546e^{-12}$ average current = $-9.342e^{-12}$

C=1fF

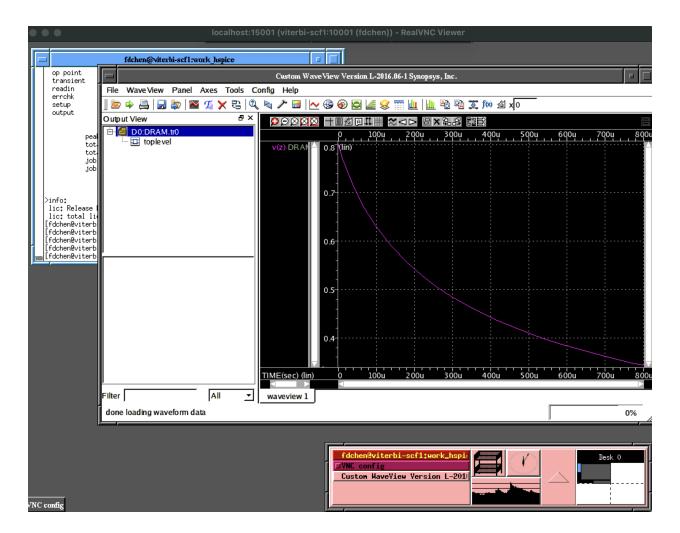
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RTL = $1.370e^{-5}$ average power = $-1.024e^{-11}$ average current = $-1.457e^{-11}$

C=10fF

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$$\mathsf{RTL} = 1.29e^{-4}$$

 ${\rm average\ power} = -1.093e^{-11}$

 ${\rm average\ current} = -1.557e^{-11}$

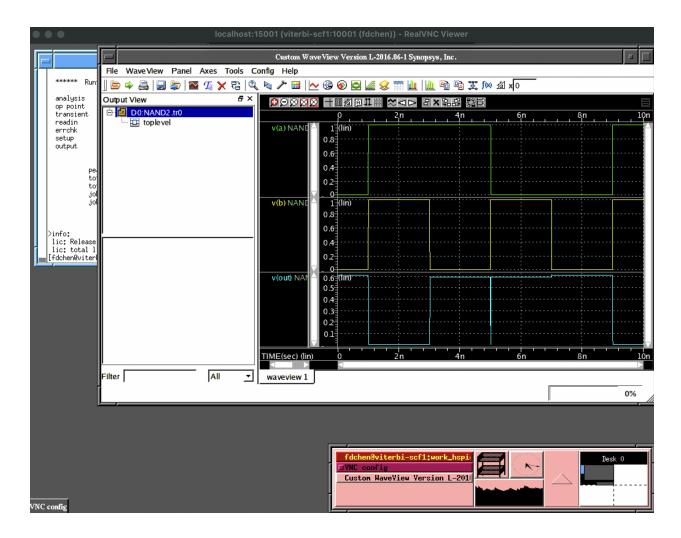
Observation

The higher the capacitance of the capacitor is, the longer it takes to discharge. This makes sense because capacitors with the most capacitance holds the most charge for the same voltage.

NAND2

Waveform

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File

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```
 CMOSN.inc
                 ■ DRAM.sp
                                 ■ NAND2.sp × ■ inverter.sp
                                                                   ≡ RC.sp
Lab4 > 

■ NAND2.sp
      ** Simple DRAM using PTM 45 nm Node
      .include CMOSP.inc
      .include CMOSN.inc
      ** Circuit Netlist
      ** Supply and Input Sources
      V_SUP VDD 0 1
      VPIN_A A 0 PULSE 0 1.0 1ns 10ps 10ps 4ns 8ns
      VPIN_B B 0 PULSE 0 1.0 1ns 10ps 10ps 2ns 4ns
      ** Transistors
      M_PU_A OUT A VDD CMOSP L=45n W=180n
      M_PU_B OUT B VDD CMOSP L=45n W=180n
      M_PD_A OUT A X CMOSN L=45n W=240n
      M_PD_B X B 0 CMOSN L=45n W=240n
      ** Initial condition
      IC V(X) = 0
      ** Analysis Setup
      .TRAN 0.1ns 10n
      ** Control Information
      .OPTION POST BRIEF NOMOD PROBE MEASOUT
      ** Print and Measurement
      .PRINT V(A) V(B) V(OUT)
      . END
```

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