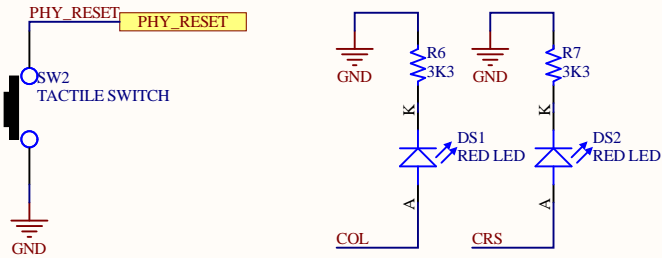
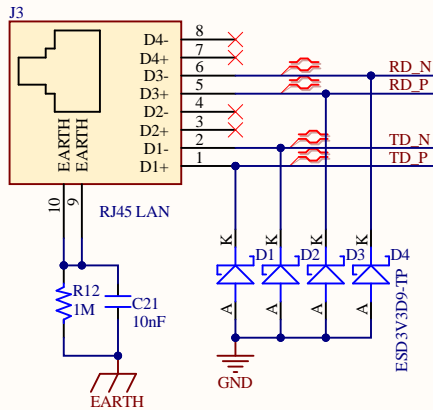


Ethernet PHY Page

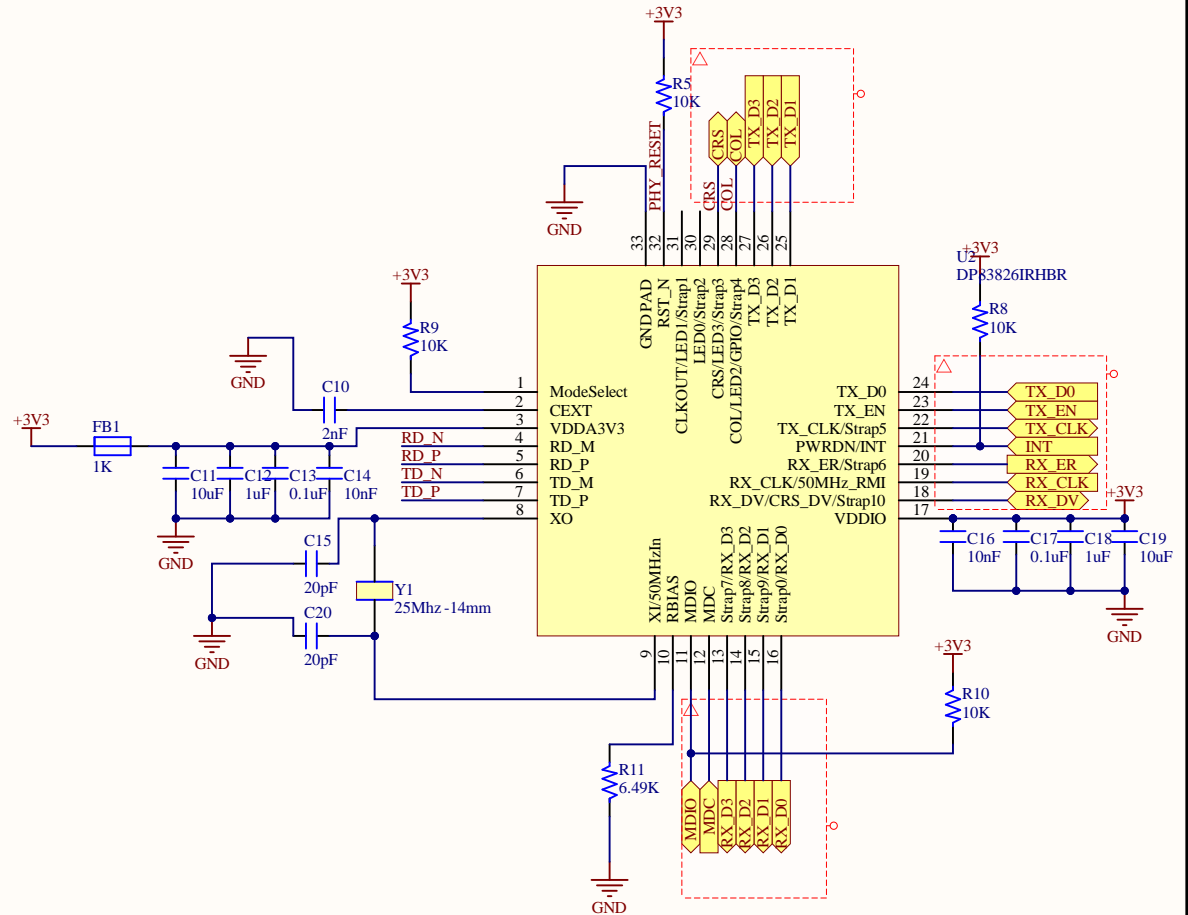
ETH Reset and Fault LEDs



RJ45 Connector



Ethernet PHY



Title: **Ethernet PHY**

Size: A4 Prj: Mixed Signal PCB

Date: 9/17/2023 3:29:53 AM Sheet 4 of 11

Git Hash: Not in version control

File: C:\Users\admin\Documents\Company_Works\Altium\Design_Circuits\Mixed_Signal_PCB\4] - Ethernet PHY.SchDoc

Author: Saviour Emmanuel

Approved: e^x Designs

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Edited: 9/1/2023

Variant: [No Variations]

SW Version: 22.5.1.42

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