

# DC DROP VIA CURRENT

Saturday, February 24, 2024 15:52:03

Design file: "C:\Users\admin\Documents\Company\_Works\Altium\Altium Course on Mixed Signals\Mixed Signal Course\Section-10+Documents\Mixed Signal Board Project - final\Mixed Signal Board Project.pjh" Designer: admin

HyperLynx BoardSim VX.2.10

