



Note

1. NRST is the reset pin. The default is high. It is reset when the input is low.
2. PB00 and PB01 default to the TX and RX interfaces of UART1 after power-on. Functions can be changed in the program.
3. When using UART programming, it is necessary to force PB14 high
4. If DCDC is not used to reduce power consumption, replace L1 with 0 ohm resistor, C4 capacitor can be omitted
5. When using TX power above 10dBm, a 1uF capacitor must be added around VDD_PAHP.

| Title | | |
|-------------------|--------------------------------------|-----------------|
| LE5010_SIMPLE_SCH | | |
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