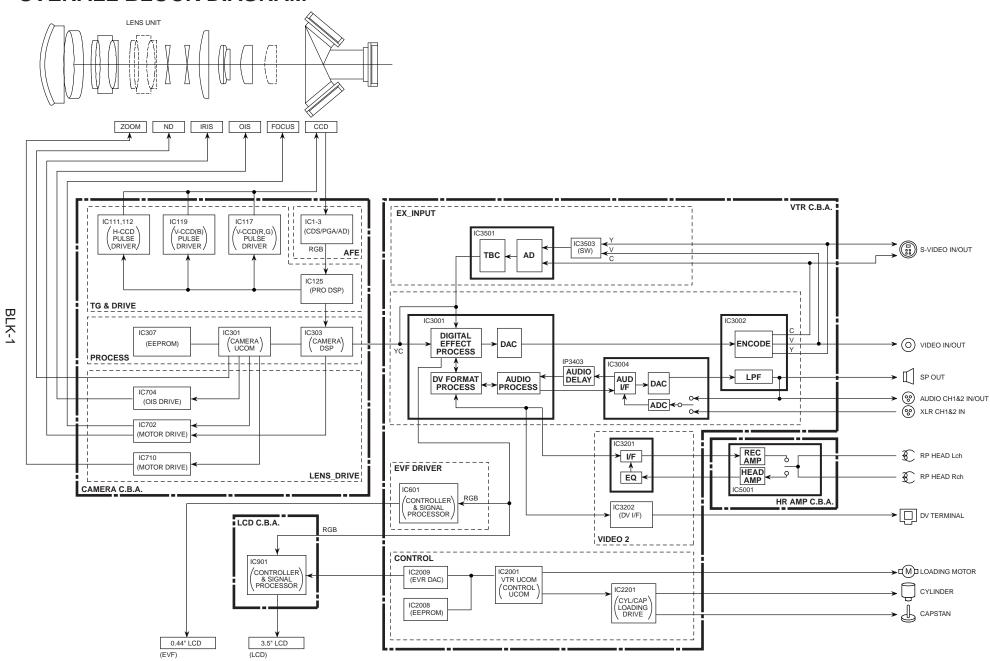
BLOCK DIAGRAMS

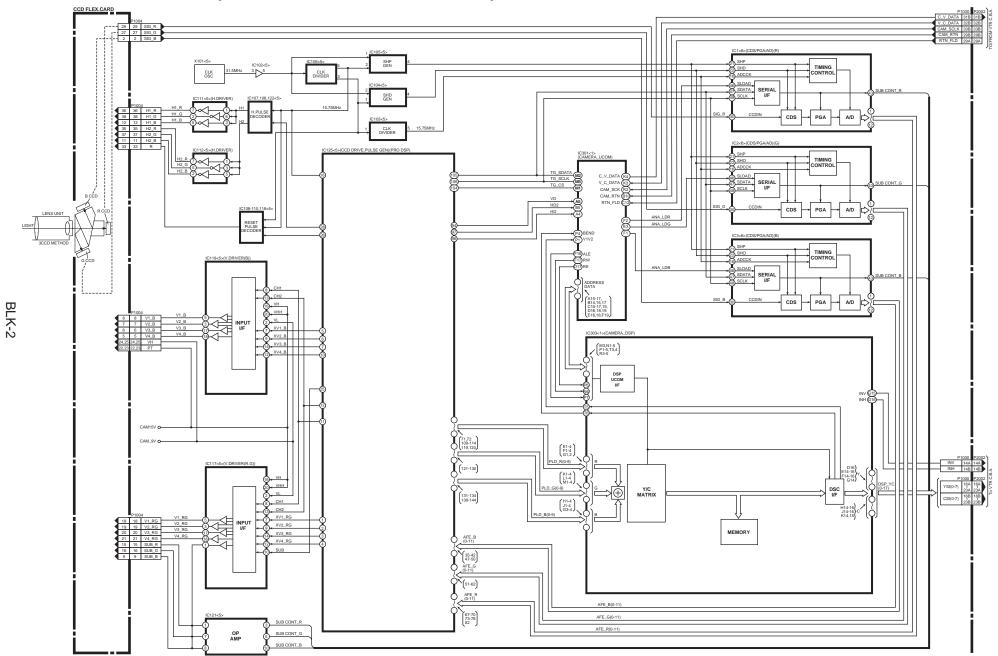
CONTENTS

OVERALL BLOCK DIAGRAM	BLK-1
SENSOR/PROCESS (AFE / TG & DRIVE / PROCESS) BLOCK DIAGRAM	BLK-2
LENS DRIVE (LENS DRIVE / PROCESS) BLOCK DIAGRAM	BLK-3
VIDEO (VIDEO 1/ VIDEO 2/ EX_INPUT) BLOCK DIAGRAM	BLK-4
MONITOR (EVF DRIVER / LCD) BLOCK DIAGRAM	BLK-5
CONTROL (CONTROL/DRIVE) BLOCK DIAGRAM	BLK-6

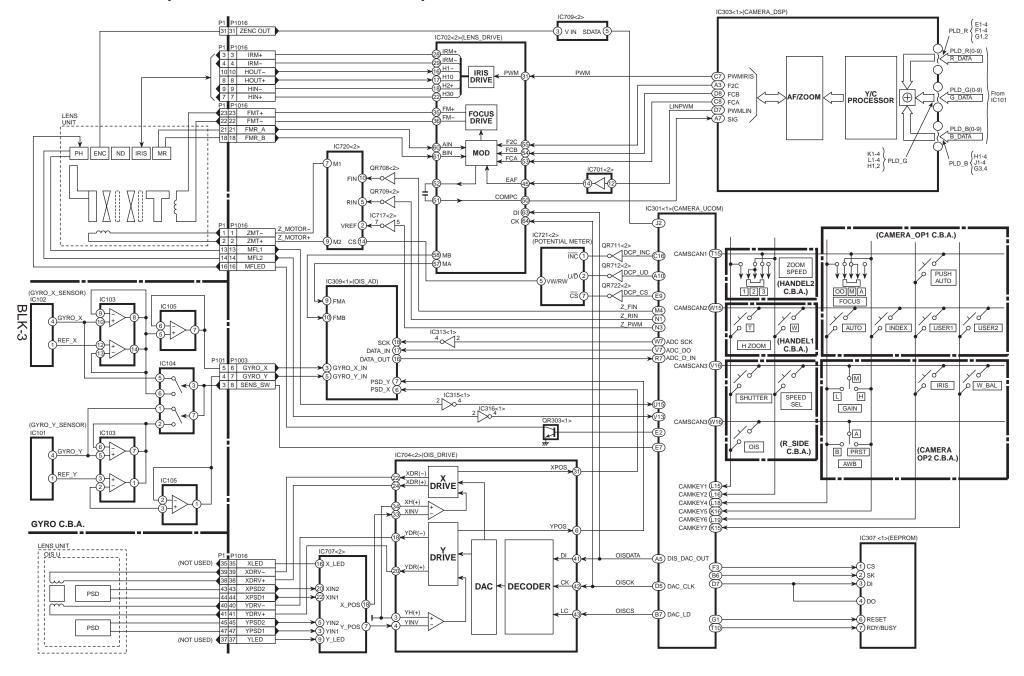
OVERALL BLOCK DIAGRAM



SENSOR/PROCESS(AFE/TG&DRIVE/PROCESS) BLOCK DIAGRAM

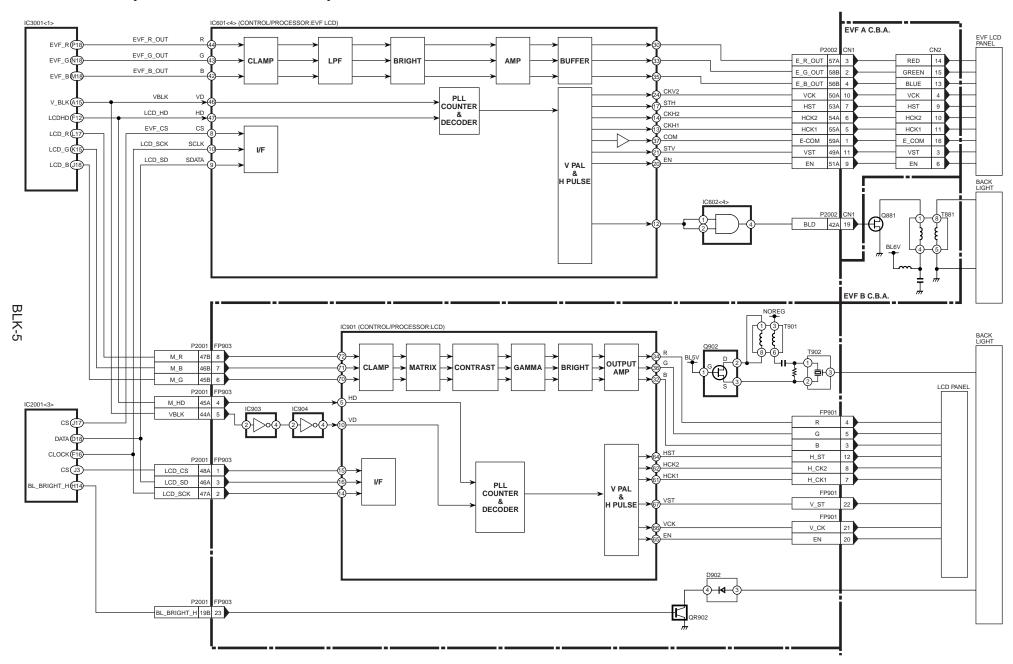


LENS DRIVE (LENS DRIVE / PROCESS) BLOCK DIAGRAM



VIDEO (VIDEO 1/ VIDEO 2/ EX_INPUT) BLOCK DIAGRAM MAIN SIGNAL PATH IN PLAYBACK MODE <3>from IC2001-V14 SIF_SCK <3>from IC2001-T12 SIF_SDA IC3501<5> (INPUT) <3>from IC2001-T13 SIF_CS IC3001<1> A7,8 B7,C7 D7,E9 F8,9 Y P12,13 R14,T14 U15,16 V15,16 U COM SDA V15,16 DIGITAL EFFECT PROCESS YSI(0-7) CLK CHANGE IC3002<1> (AV DRIVE) CSI(0-7) A9,B8,10 C8,9 D8,9 TO S_VHS TERMINAL → CLAMP C OUT →BUFF C_OUT 13 13 TBC J4401 (SIDE_JACK C.B.A.) Y_OUT 11 11 MIX VOUT AGC CLAMP AD E1 CIN CLK27B (U6) → N6 CLK27M → A10 CLK135M > BUFF > CLAMP Y/C SEP P4608 (R_SIDE C.B.A.) CLK135 (V8) DV FORMAT SP OUT1 58B SP OUT 2 DAC SP OUT2 59B **PROCESS** LINE LINE_R P2209 P49012 TO HP OUT (REAR_JACK C.B A) IC3008<1> →(3)HP AMP(HP L 8B 13 → ⑤ MŮTE IC3004<1> (ALIDIO AD/DA) TO LINE IN/OUT J4451 (SIDE_JACK C.B.A.) V I/O 15 15 IP3403(PLD)<7> LINE_L A CH1 IO 17 17 × AMP - ○ AMP-0-LINE_R A CH2 IO AUDIO AMIC L FP2204 P4003 DELAY **∢**AMP—○ CH1_MIC_OUT 5 3 — from MIC_CH1 C.B.A. CH2_MIC_OUT 7 3 — from MIC_CH2 C.B.A. MIC_R AUDIO VF CONTROL RESISTOR SBA_DIO from IC2001-U13<3> 28 SBA_DIO from IC2001-U13<3> 29 SBA_DIO from IC2001-U13<3> 20 SBA_DIO from IC2001-U13<3> BCLK SDTO AUDIO ADC SBA_DI from IC2001-P12<3> PROCESS-IC3201<2> (RI0) IC5001<1> (HEAD/REC-AMP) P500 CH2F RP Rch FP5001 CH2S DBR(0-3) (A) (B) (A) (B) I/F ENCODER 👄 HSE 121 AGC ---CH1S **←** ⇒ RP Lch CH1F AGC DET DATA/ ADDRESS VITABI CEQ RF AMP AMP-0 RF_AGCOUT 9 9 AGC FP5001 F10-12,G11,12,H11,12 J10-12,K12,L11,12,M10-12 FP500 A10 CLK27A RECI LOGIC HID1 HID2 4 HR AMP C.B.A ADM(0-15) IC3202<2> (PLANET) TO/FROM D7,10,E8-10 F6,F8-10 G8,9,H8-10 J9,K10 DATA/ADDRESS FP2008 TPB-TPB-TO DV TERMINAL TPB+ DV I/F TPB+ TPA-TPA-J4401 (SIDE JACK C.B.A.) TPA+

MONITOR (EVF DRIVER / LCD) BLOCK DIAGRAM



CONTROL (CONTROL/DRIVE) BLOCK DIAGRAM

