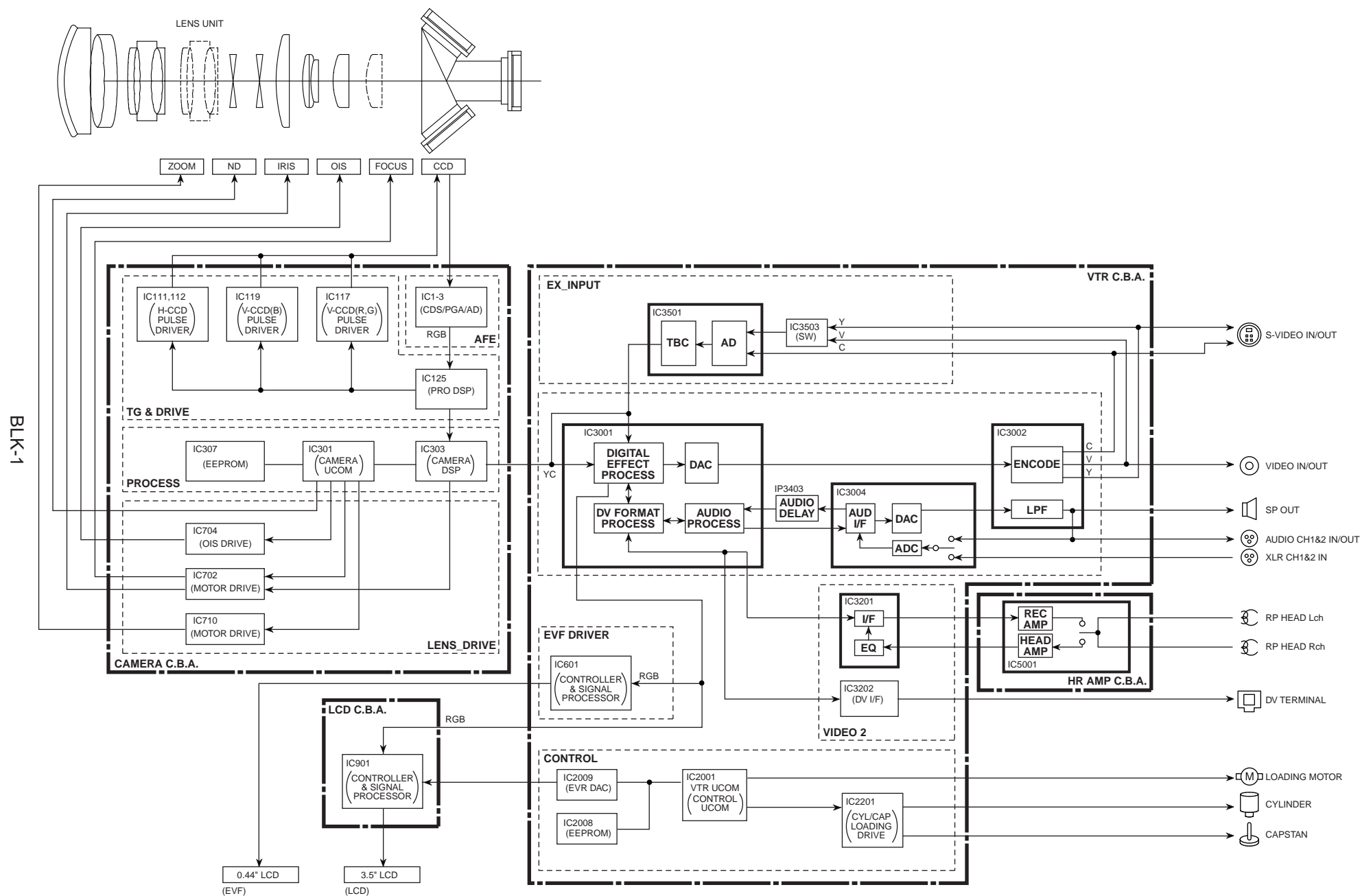
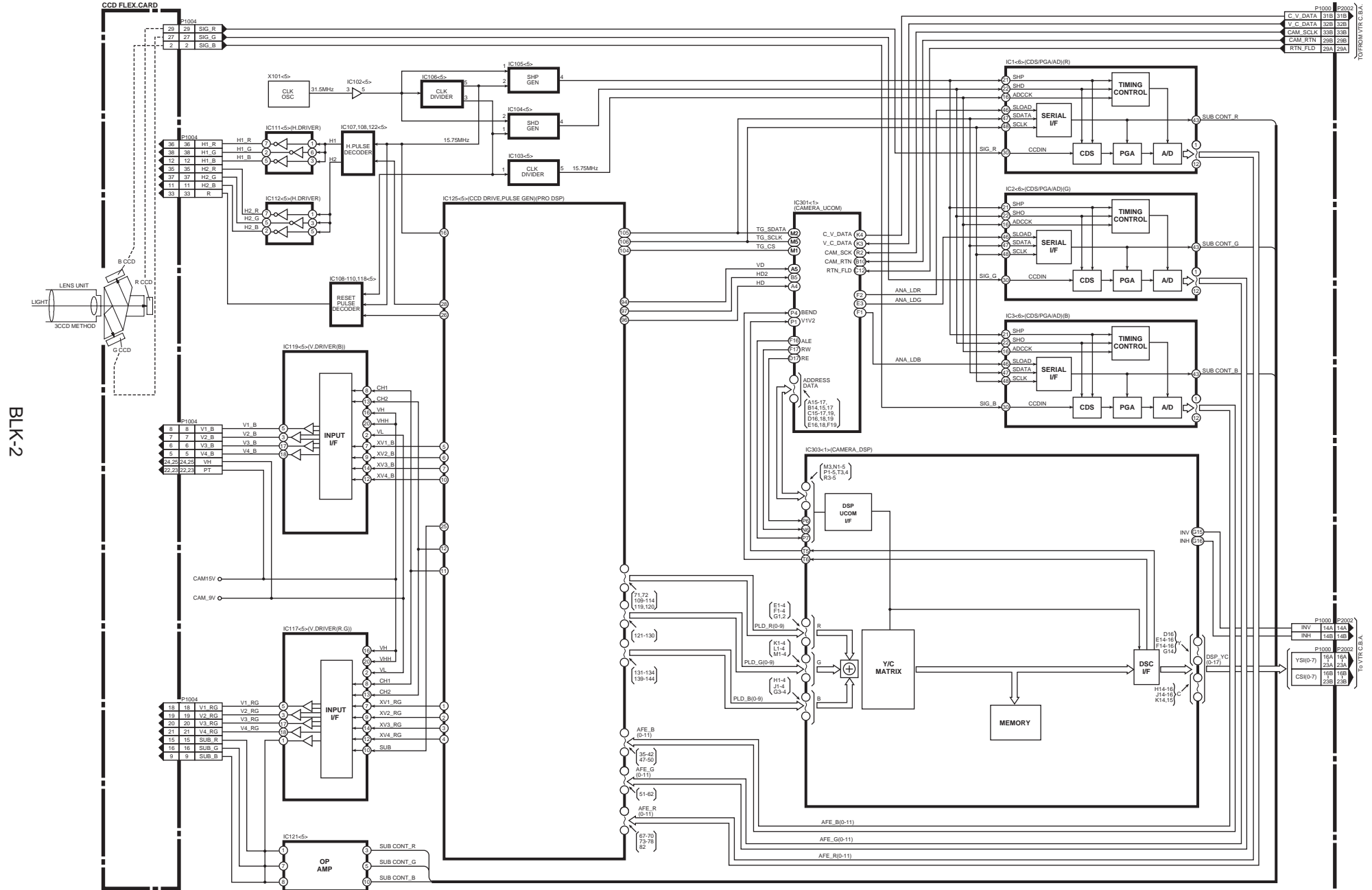


OVERALL BLOCK DIAGRAM



SENSOR/PROCESS(AFE/TG&DRIVE/PROCESS) BLOCK DIAGRAM



The diagram illustrates a camera system architecture, divided into several functional blocks and interconnected by a complex network of electrical connections.

LENS UNIT: Includes components like PH, ENC, ND, IRIS, MR, and a Z MOTOR. It is connected to the P1016 connector.

GYRO C.B.A. (Gyro Control Board Assembly): Contains gyro sensors (GYRO_X, GYRO_Y) and associated ICs (IC101, IC102, IC103, IC104, IC105). It interfaces with the P1016 connector.

GYRO X SENSOR (IC102): Receives REF_X and outputs GYRO_X.

GYRO Y SENSOR (IC101): Receives REF_Y and outputs GYRO_Y.

CAMERA ICs and Control Logic:

- P1016:** A multi-pin connector providing power and control signals to various ICs.
- P1003:** A multi-pin connector providing power and control signals to the gyro sensors.
- IC702<2> (LENS_DRIVE):** Controls the lens drive (IRM+, IRM-, H1-, H10, H2+, H30, FM+, FM-, F2C, FCB, FCA).
- IC701<2>:** Controls the focus drive (F2C, FCB, FCA, EAF, COMP, DI, CK).
- IC721<2> (POTENTIAL METER):** Provides feedback for the focus drive (VW/RW, CS).
- IC309<1> (OIS_AD):** Controls the optical image stabilization (OIS) drive (FMA, FMB, SCK, DATA_IN, DATA_OUT).
- IC313<1>:** Controls the OIS drive (XDR(-), XDR(+), XH(+), XINV, YDR(-), YDR(+), YH(+), YINV).
- IC315<1>:** Controls the OIS drive (XDR(-), XDR(+), XH(+), XINV, YDR(-), YDR(+), YH(+), YINV).
- IC316<1>:** Controls the OIS drive (XDR(-), XDR(+), XH(+), XINV, YDR(-), YDR(+), YH(+), YINV).
- IC704<2> (OIS_DRIVE):** Controls the OIS drive (XDR(-), XDR(+), XH(+), XINV, YDR(-), YDR(+), YH(+), YINV).
- IC707<2>:** Controls the OIS drive (XDR(-), XDR(+), XH(+), XINV, YDR(-), YDR(+), YH(+), YINV).
- IC709<2>:** Controls the OIS drive (XDR(-), XDR(+), XH(+), XINV, YDR(-), YDR(+), YH(+), YINV).
- IC303<1> (CAMERA_DSP):** The main camera processor, receiving signals from the lens drive, focus drive, and OIS drive. It outputs signals to the AF/ZOOM, Y/C PROCESSOR, and PLD.
- IC307<1> (EEPROM):** Provides non-volatile storage for camera settings.

CAMERA OP1 C.B.A. (Camera Operation 1 Control Board Assembly): Contains various control buttons and switches, including ZOOM SPEED, H ZOOM, W ZOOM, AUTO, INDEX, USER1, USER2, SHUTTER, SPEED SEL, OIS, GAIN, IRIS, W_BAL, and AWB.

CAMERA OP2 C.B.A. (Camera Operation 2 Control Board Assembly): Contains various control buttons and switches, including ZOOM SPEED, H ZOOM, W ZOOM, AUTO, INDEX, USER1, USER2, SHUTTER, SPEED SEL, OIS, GAIN, IRIS, W_BAL, and AWB.

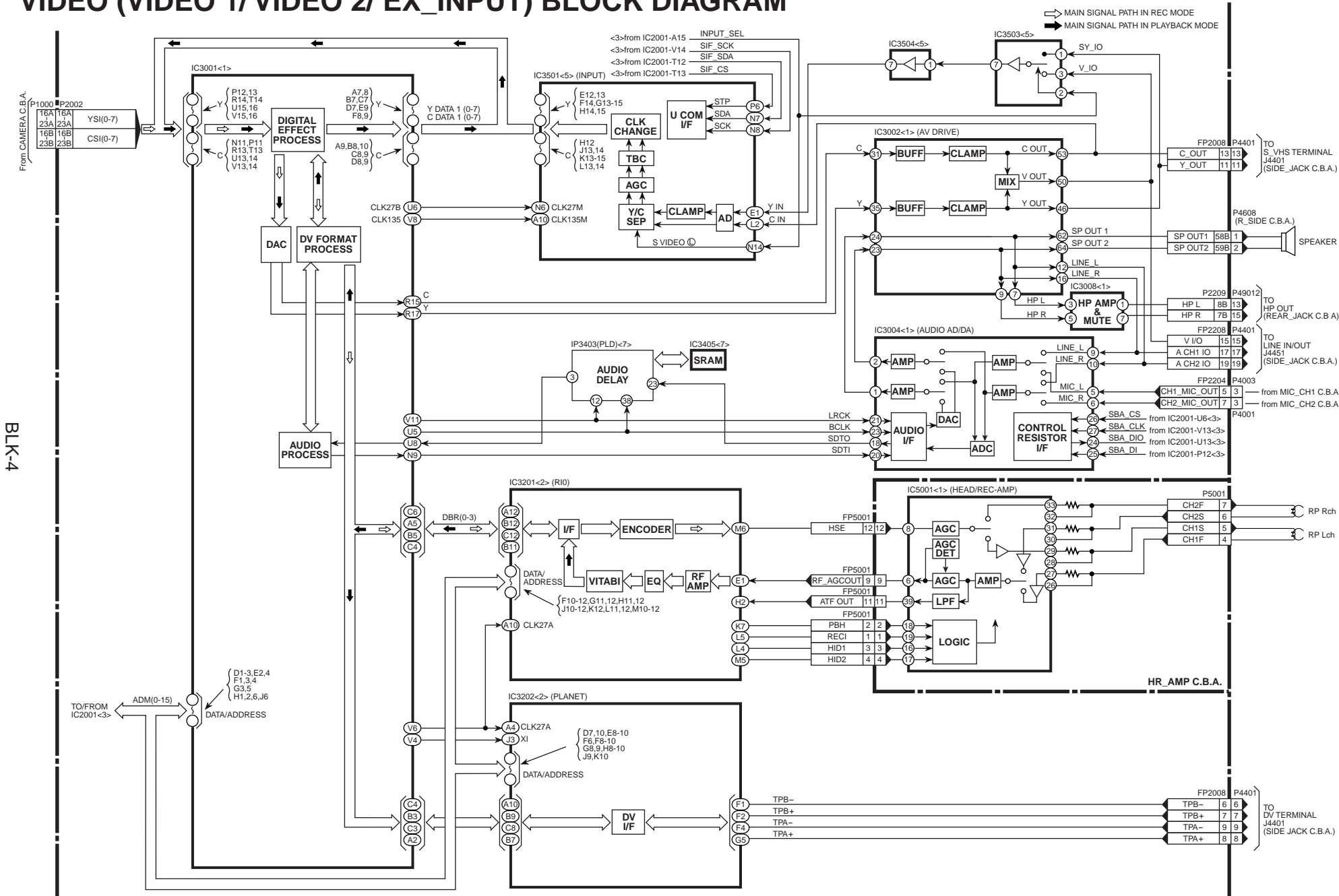
CAMERA DSP (Camera Digital Signal Processor): The main processing unit, receiving signals from the lens drive, focus drive, and OIS drive. It outputs signals to the AF/ZOOM, Y/C PROCESSOR, and PLD.

AF/ZOOM and Y/C PROCESSOR: These blocks process the signals from the camera DSP and output the final video signal.

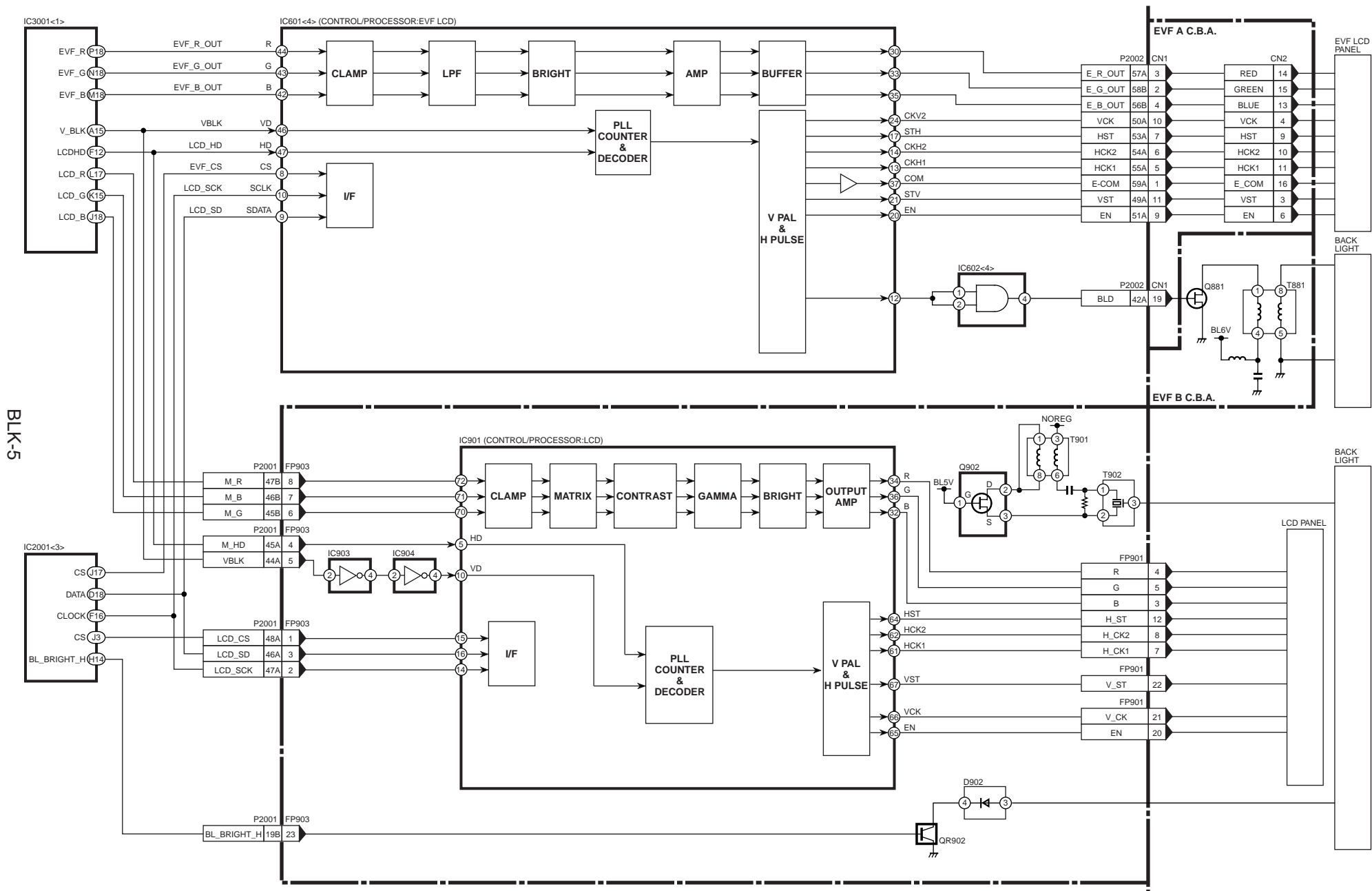
PLD (Programmable Logic Device): Receives signals from the camera DSP and outputs the final video signal.

Other Components: The diagram includes numerous other components, including various ICs, connectors, and passive components, all interconnected to form a complete camera system.

VIDEO (VIDEO 1/VIDEO 2/ EX_INPUT) BLOCK DIAGRAM



MONITOR (EVF DRIVER / LCD) BLOCK DIAGRAM



24

