

Q1. Answer the following: each carry 2 marks.

(10 Marks)

(a) Draw the o/p waveform, assume ideal conditions.

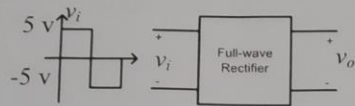


Fig. 1.1

Ans:



(b) Assume $r_z = 0$, determine I_z and also the power dissipated in Zener.

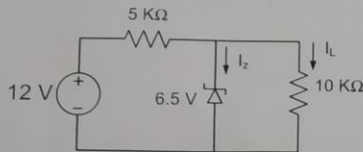


Fig. 1.2

Ans:

$$I_z = 0.45 \text{ mA}$$

$$P_z = 0.45 \times 6.5 = 2.9 \text{ mW}$$

(c) If v_i is having 10 V peak amplitude, determine the current gain (i_o/i_i)

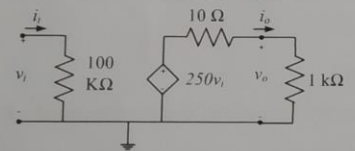


Fig. 1.3

Ans:

$$\frac{P}{I_1} = \frac{10}{100 \text{ K}} = 0.1 \text{ mA}$$

$$\frac{P}{I_0} = \frac{2500}{1010} = 2.475 \text{ A}$$

$$\frac{P}{I_0} = \frac{2500}{1010} = 2.475 \text{ K (A/A)}$$

(d) Design a noninverting amplifier with $|A| = 4$ (v/v). At the maximum output voltage of 12 V the current in the voltage divider is 5 mA.

Ans:

$$R_1 = 0.6 \text{ K}, R_2 = 1.8 \text{ K}$$

$$V_0 = 12 \text{ V}$$

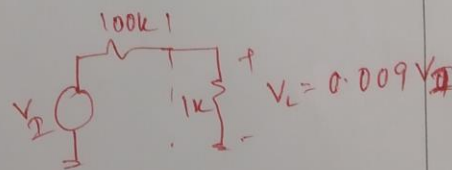
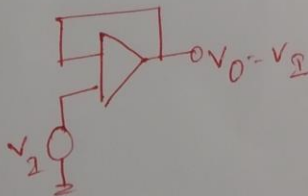
$$V_0 = 4 V_2 \Rightarrow V_2 = 3 \text{ V}$$

$$R_2 = 3 R_1$$

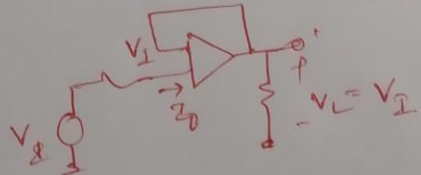
$$R_1 = \frac{3}{5} = 0.6 \text{ K}, R_2 = \frac{9}{5} = 1.8 \text{ K}$$

(e) Draw the voltage follower circuit, explain how it acts as an impedance transformer.

Ans:



$$V_L = 0.009 \text{ V}$$



$$V_L = V_i$$

Q2. Analyse the circuit to determine, V_C , V_B , V_E , I_C , I_B , and I_E . Assume that $V_{BE} = 0.6$ V, and $\beta = 49$. Also comment on the mode of operation. (6 Marks)

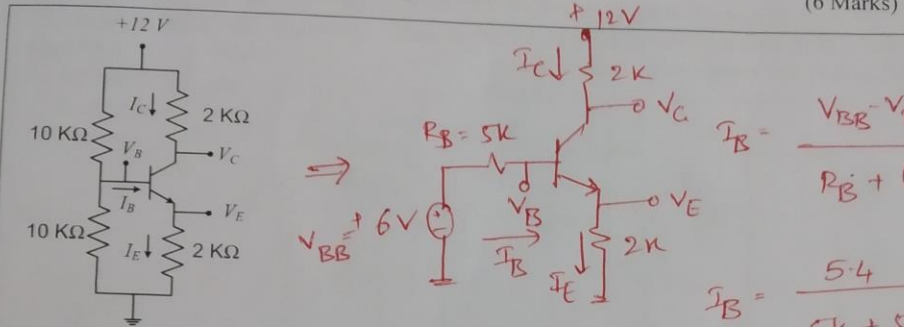


Fig. 2.

$$V_B = V_{BE} + I_E R_E$$

$$= 0.6 + 2.57 \times 2$$

$$V_B = 5.74 \text{ V}$$

$$V_E = I_E R_E = 5.14 \text{ V}$$

$$V_C = V_{CC} - I_C R_C = 12 - 2.52 \times 2$$

$$V_C = 6.96 \text{ V}$$

$$V_E < V_B < V_C$$

Active mode

$$I_B = \frac{V_{BB} - V_{BE}}{R_B + (\beta + 1) R_E}$$

$$I_B = \frac{5.4}{5 \text{ K} + 50 \times 2 \text{ K}} = 51.4 \text{ } \mu\text{A}$$

$$I_C = \beta I_B$$

$$I_C = 2.52 \text{ mA}$$

$$I_E = 2.57 \text{ mA}$$

$$I_B = 0.051 \text{ mA}$$

$$I_E \approx I_C \approx I_B$$

Rough work:

Q3. Two amplifiers are connected in cascade between a 10 mV source with resistance 100 k Ω and a 1 k Ω load, shown in Fig. 3. First amplifier is having open circuit voltage gain, input resistance, and output resistance as 100 (V/V), 100 k Ω , and 1 k Ω , and the second amplifier is having short circuit transconductance, input resistance and output resistance as 60 mA/V, 5 k Ω , and 15 k Ω respectively. Determine i_o/v_s . (4 Marks)

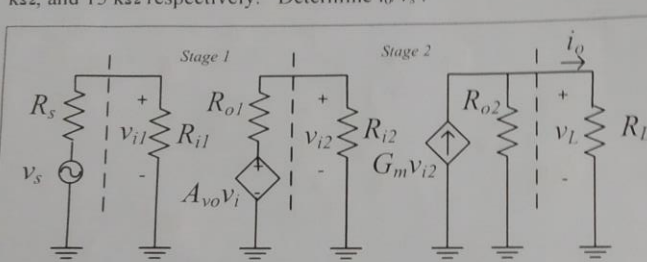


Fig. 3. Two-stage amplifier circuit.

$$V_{i1} = \frac{V_s \times R_{i1}}{R_s + R_{i1}} \Rightarrow \frac{V_{i1}}{V_s} = \frac{R_{i1}}{R_{i1} + R_s} = 0.5 \text{ (V/V)}$$

$$\frac{V_{i2}}{V_{i1}} = \frac{A_{vo} R_{i2}}{R_{o1} + R_{i2}} = \frac{100 \times 5k}{5k + 1k} = 83.3 \text{ (V/V)}$$

$$\frac{I_o}{V_{i2}} = \frac{G_m R_{o2}}{R_{o2} + R_L} = \frac{60 \times 10^{-3} \times 15k}{15k + 1k} = 56.25 \text{ m (A/V)}$$

$$\frac{I_o}{V_s} = \frac{I_o}{V_{i2}} \times \frac{V_{i2}}{V_{i1}} \times \frac{V_{i1}}{V_s} = 2.34 \text{ (A/V)}$$

Rough work:

$$V_{i1} = 5 \text{ mV}$$

$$V_{i2} = 416.5 \text{ mV}$$

$$I_o = 23.43 \text{ mA}$$

Q4. For the Op-amp circuit shown in fig. 4, determine v_o for, (a) $R_1 = R_2 = R_3 = R_4 = R$, and for (b) $R_1 = R_2/2 = R_3/4 = R_4/6 = R$ (4 Marks)

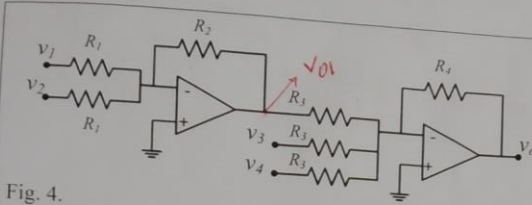


Fig. 4.

(a) $R_1 = R_2 = R_3 = R_4 = R$.

$$v_{01} = -(v_1 + v_2);$$

$$v_o = -(v_3 + v_4) + v_1 + v_2 = v_1 + v_2 - v_3 - v_4$$

(b) $R_1 = R$; $R_2 = 2R$; $R_3 = 4R$; $R_4 = 6R$

$$v_o = \frac{3}{2} \times (v_1 + v_2) - \frac{3}{2} (v_3 + v_4)$$

$$v_o = 3v_1 + 3v_2 - 1.5v_3 - 1.5v_4$$

$$\frac{R_4}{R_3} = \frac{6}{4} = \frac{3}{2}$$

$$\frac{R_2}{R_1} = 2$$

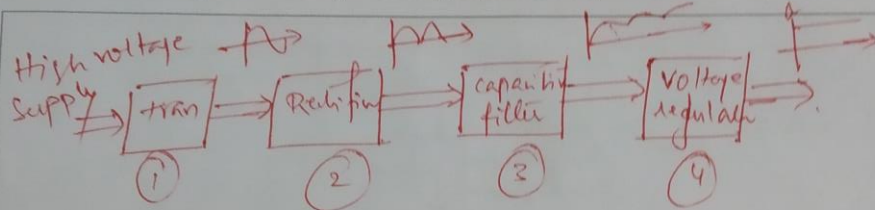
Rough Work:

$$v_{01} = -\frac{R_2}{R_1} v_1 - \frac{R_2}{R_1} v_2 = -\frac{R_2}{R_1} (v_1 + v_2)$$

$$v_o = -\frac{R_4}{R_3} (v_{01} + v_3 + v_4) = \left(\frac{R_4}{R_3}\right) \left(\frac{R_2}{R_1}\right) (v_1 + v_2) - \frac{R_4}{R_3} (v_3 + v_4)$$

$$v_o = \left(\frac{R_4}{R_3}\right) \left(\frac{R_2}{R_1}\right) (v_1 + v_2) - \frac{R_4}{R_3} (v_3 + v_4)$$

Q5. Draw the block diagram of a DC power supply, explain its operation, and also draw the waveforms at the output of each stage. Make appropriate assumptions if necessary. (6 Marks)



(1) provides electrical isolation

(b) The high voltage AC is down converted to specific value

(2) converts bipolar AC signal to unipolar

(3) fills the time dependency of the unipolar signal.

(4) Reduces the ripple due to filter operation and stabilizes the magnitude of the dc output voltage.