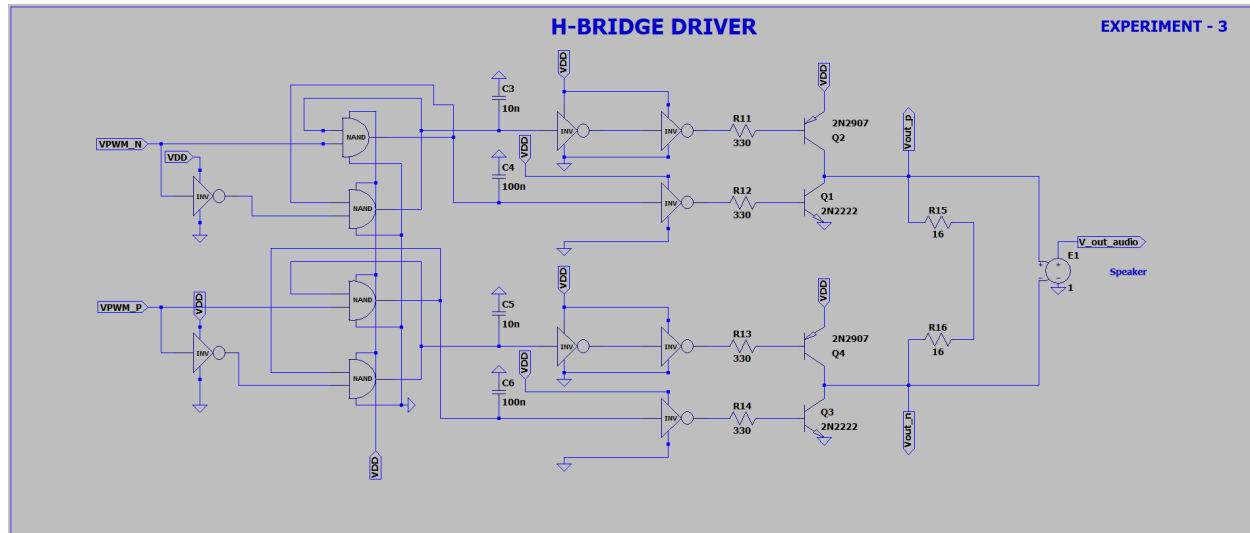
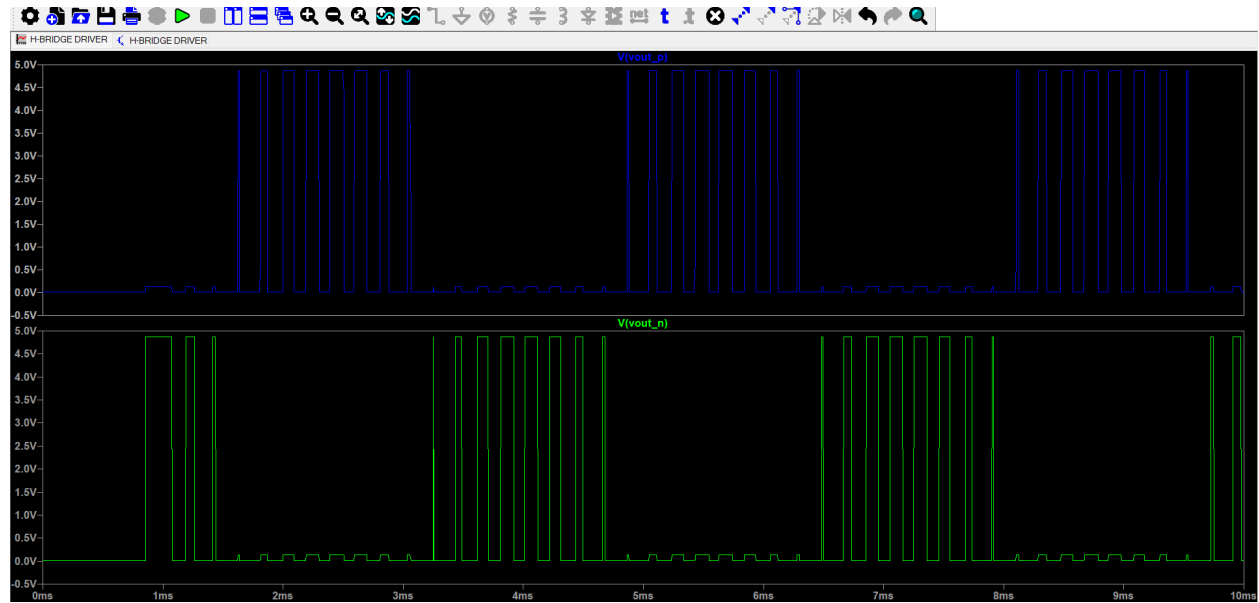


EXPERIMENT-3: H-BRIDGE DRIVER AND INTEGRATION

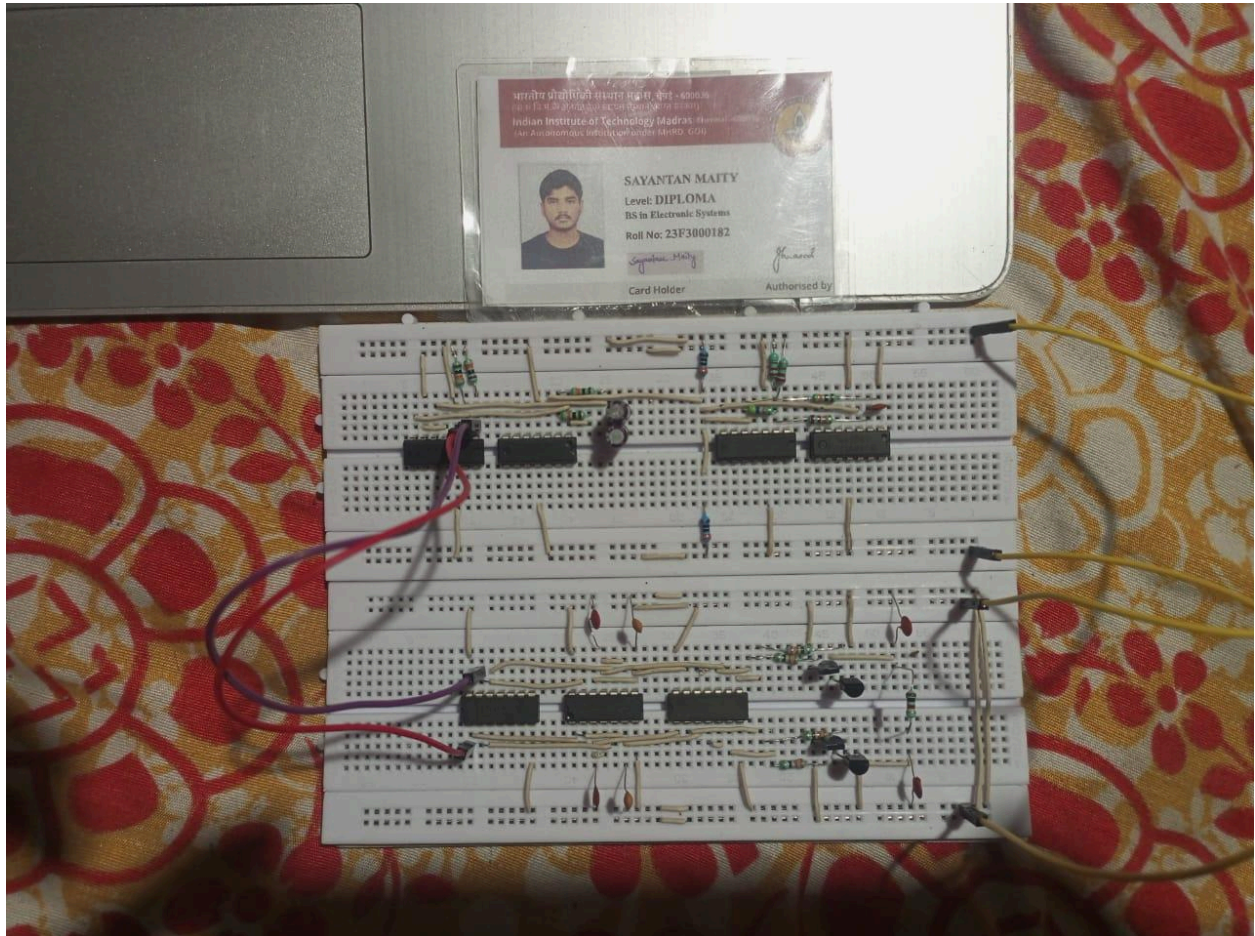
LTSpice Model:



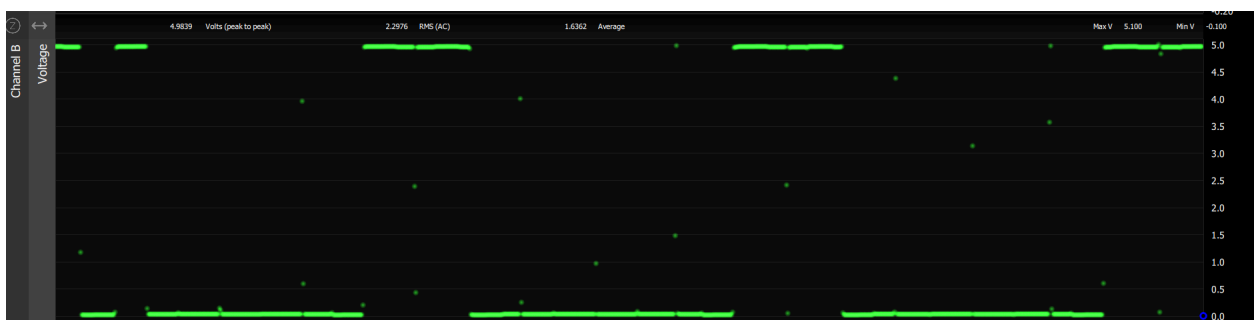
LTSpice Output:



Real Circuit:



Vout_n:



Vout_p:

