Product: KS8999 Errata and Revision history

Revision: A

Product Status: Engineering evaluation
First Silicon for the engineering evaluation only

Revision: A2

Product Status: Engineering sample.
Yield improvement on analog circuitry.

Revision: A3

Product Status: production

1. LED blinking after unplug the cable

LED blinking may occur after unplug the cable. This problem has been fixed in A3 version.

2. LED blinking after plug-in the cable or at idle mode

LED blinking may occur after plug-in the cable or at idle mode. This problem will be fixed in next revision.
Workaround: none

3. Improve cable length for yield improvement

This enhancement will be implemented in next revision
Workaround: do not use cable length longer than 90 meter.

4. Fast Link Pulse (FLP) amplitude not meeting the IEEE spec

The amplitude of FLP is slightly lower and does not meet the IEEE spec. This issue will be fixed in next revision.
Workaround: none

5. Sensitive to Noise

KS8999 is sensitive to VDD noise. This issue will be fixed in next revision.
Workaround: VDD is recommended to increase to 2.2 Volt.

6. LED [1:9] [0] Mode 1 (1/28/2003)

The LED blinking rate is not periodic for the LED [1:9][0] indicator pins in LED mode 1. This problem will cause the inconsistent LED blinking for both 10Mb and 100Mb.
Workaround: recommend to use other LED mode.

Revision: A4

Product Status: production

Fixed most of the known issues in A3 version, except the following:

1. LED [1:9] [0] Mode 1
The LED blinking rate is not periodic for the LED [1:9][0] indicator pins in LED mode 1. This problem will cause the inconsistent LED blinking for both 10Mb and 100Mb.
Workaround: recommend to use other LED mode

2. Power Supply voltage range
The KS8999 will not meet the IEEE 802.3 waveform spec if the Vdd is at 2.0 volt (typical).
Workaround: recommend Vdd operating at 2.1 volts (typical) see datasheet for detailed information.

3. System get hung when tag insertion and collision happen at the same time
If any particular port operates at half duplex mode and with tag insertion on, sending and receiving packets at the same time will hang the system
This issue will be fixed in next revision.
Workaround: none

Revision: A5

Product Status: production

Fixed the item 3 (System get hung when tag insertion and collision happen at the same time) in revision A4.

For any questions about this errata and sample request, please contact your local Sale Representative or FAE.