EH26 Series

- Crystal Clock Oscillators
- LVCMOS Output
- +3.3V Supply Voltage
- Tri-State Output Function
- 4 Pad Ceramic SMD Package
- RoHS Compliant (Pb-Free)





NOTES

ELECTRICAL SPECIFICATIONS

Frequency Range					1.000MHz to 155.520MHz		
Operating Temperature Range					0°C to +70°C or -40°C to +85°C		
Storage Temperature Range					-55°C to +125°C		
Supply Voltage (V _{DD})					3.3V _{DC} ±0.3V _{DC}		
Input Current					35mA Maximum (Unloaded)		
Frequency Tolerance / Stability		Inclusive of all conditions: Calibration Tolerance at 25°C,			±100ppm, ±50ppm, ±25ppm, or		
		Frequency Stability over the Operating Temperature Range,			±20ppm Maximum		
		Supply Voltage Change, Output Load Change, First Year Aging					
		at 25°C, Shock, and Vibration					
Output Voltage	Logic High (V _{OH})				2.7V _{DC} Minimum	$I_{OH} = -8$	8mA
Output Voltage	Logic Low (V _{OL})				0.5V _{DC} Maximum	$I_{0L} = +$	8mA
Rise Time / Fall Time		≤70.000MHz 20% to 80% of Waveform			6 nSeconds Maximum		
		>70.000MHz 20% to 80% of Waveform			4 nSeconds Maximum		
Load Drive Capability		≤70.000MHz			30pF Maximum		
		>70.000MHz			15pF Maximum		
Duty Cycle (at V _{DD} =3.3V _{DC})		at 50% of Waveform			50 ±10(%) (Standard) or		
					50 ±5(%) (Optiona	al)	
Tri-State Input Voltage		V _{IH} :No Connection or ≥2.2V _{DC}			Enables Output		
		V_{IL} : $\leq 0.8 V_{DC}$			Disables Output: High Impedance		
Aging (at 25°C)					±5ppm/year Maximum		
Start Up Time					10mSeconds Maximum		
Period Jitter: Absolute					±250pSec Maximum, ±100pSec Typic		
Period Jitter: 0	ne Sigma				±50pSec Maximum	ı, ±40pS	Sec Typical
MANUFACTURER ECLIPTEK CORP.	CATEGORY OSCILLATOR	SERIES EH26	PACKAGE CERAMIC	VOLTAGE 3.3V	CLASS OS48		REV = DA 06/10

PART NUMBERING GUIDE

