Backplane Power Flow Diagram Rev A(Dt: 29/11/12) Main Connector **ECAT EEC** Mod Train 24V B 2V5_APP_EEC-(A1/B1) DIO_24V_A DIO_24V_B AIO_24V_A AIO_24V_B 3V3_(A/B)_IES 6V5_(A/B) 6V5_(A/B) **Document** 48A 48A **6A 6A 6A** NA 0.25 NA 0.5 NA 0.3 NA 0.3 NA NA NA **Fuse/ Maximum Rating** NA NA 2V5_APP_EEC-(A2/B2) **Practical Rating** XX XX XX XX XX XX 0.5 NA PCC **IOP AM APP IES IOP DM** 24V_A 6.5V(A/B) 6.5V(A/B) 6.5V(A/B) 6.5V(A/B) 30 18 4 3 3 1.5 5 2.25 24V_F(A/B) 24V_F(A/B) 24V_B 3V3_A_IES 2V5_(A1/B1) 2 **30** NA 0.5 24VD_A_(IOPA/B) 24VD_A_(IOPA/B) 24V_F(A/B) 2V5 (A2/B2) NA NA 8 24VD B (IOPA/B) NA 0.5 24VD_B_(IOPA/B) 6.5V(A/B) NA NA 24VA_A_(IOPA/B) 24VA_A_(IOPA/B) NA 25 2 24VA_B_(IOPA/B) 24VA_B_(IOPA/B) NA FTC AM FTC DM DIO_24V_(A/B) DIO_24V_(A/B) Note(*) = 1A ifNA .2+(*) NA FTC I/O terminal 24V_D is used 24V_F_(A/B) 24V_F_(A/B) NA NA 2 AIO_24V_(A/B) AIO_24V_(A/B) 2 NA 24VA_(A/B)_IOP(A/B) 24VA_(A/B)_IOP(A/B) NA 24VD_(A/B)_IOP(A/B) 24VD_(A/B)_IOP(A/B) 1.2 .3 NA