

10/100BASE-T LAN MAGNETICS

S558-5999-Q9-F Transformer Module for Auto MDIX



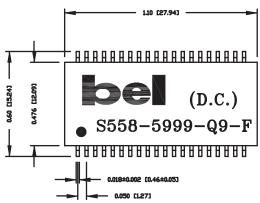
ELECTRICAL CHARACTERISTICS @ 25°C

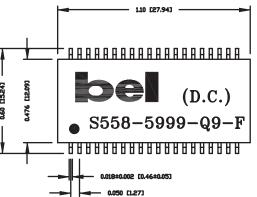
SCHEMATIC		TURNS RATIO	
TD4+ 1 1CT : 1CT	40 TX4+	T× SIDE: R× SIDE:	1CT : 1CT 1CT : 1CT
TD4- 2	39 TX4- 38 CTCG4 37 RX4+	POLARITY:	R× 4°&37°, 7°&34°, 14°&27°, 17°&24° T× 1°&40°, 10°&31°, 11°&30°, 20°&21° IN PHASE
RD4- 5	36 RX4- 35 RX3-	OCL	350µH MIN. @ 100kHz, 100mV WITH 8mA D.C. BIAS.
CTVD3 8	→ 34 RX3+ → 33 CTCG3 → 32 TX3-	INSERTION LOSS RETURN LOSS	@ 100kHz - 80MHz 1.0 dB MAX
TD3+ 10	31 TX3+ 30 TX2+	MEASURED UNDER A 100 ΩENVIR□NMENT	@ 0.1MHz - 30MHz
TD2- 12 - 3 E CTVD2 13 - 1CT : 1CT RD2+ 14 - 3 d	29 TX2- 28 CTCG2 27 RX2+	COMMON TO DIFFERENTIAL MODE REJECTION	@ 0.1MHz TO 100MHz 40 dB MIN.
RD2- 15		COMMON TO COMMON MODE REJECTION	@ 0.1MHz TO 100MHz 35 dB MIN.@ 100MHz TO 500MHz 20 dB MIN.
RD1- 16 - 1CT : 1CT	25 RX1-	CROSSTALK	@ 0.1MHz T□ 100MHz 32 dB MIN.
RD1+ 17 • ICT : 1CT TD1- 19 • ICT : 1CT	24 RX1+ 23 CTCG1 22 TX1-	HIPOT	1500 Vrms Meets all IEEE 802.3 standards including 350µH with 8mA DC bias

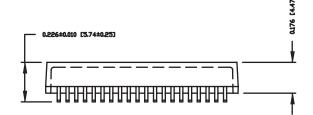
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NOTES:

- 1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
- 2. PACKAGE CODE: "RRS001".



SUGGESTED PCB PAD LAYOUT

