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DWG. NO. SH REV.

D

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C

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B

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A

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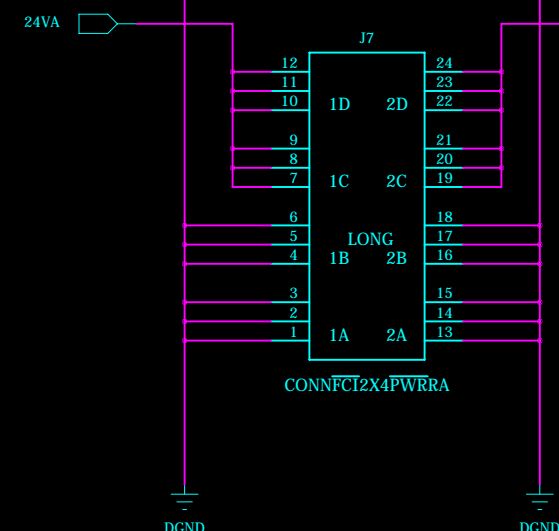
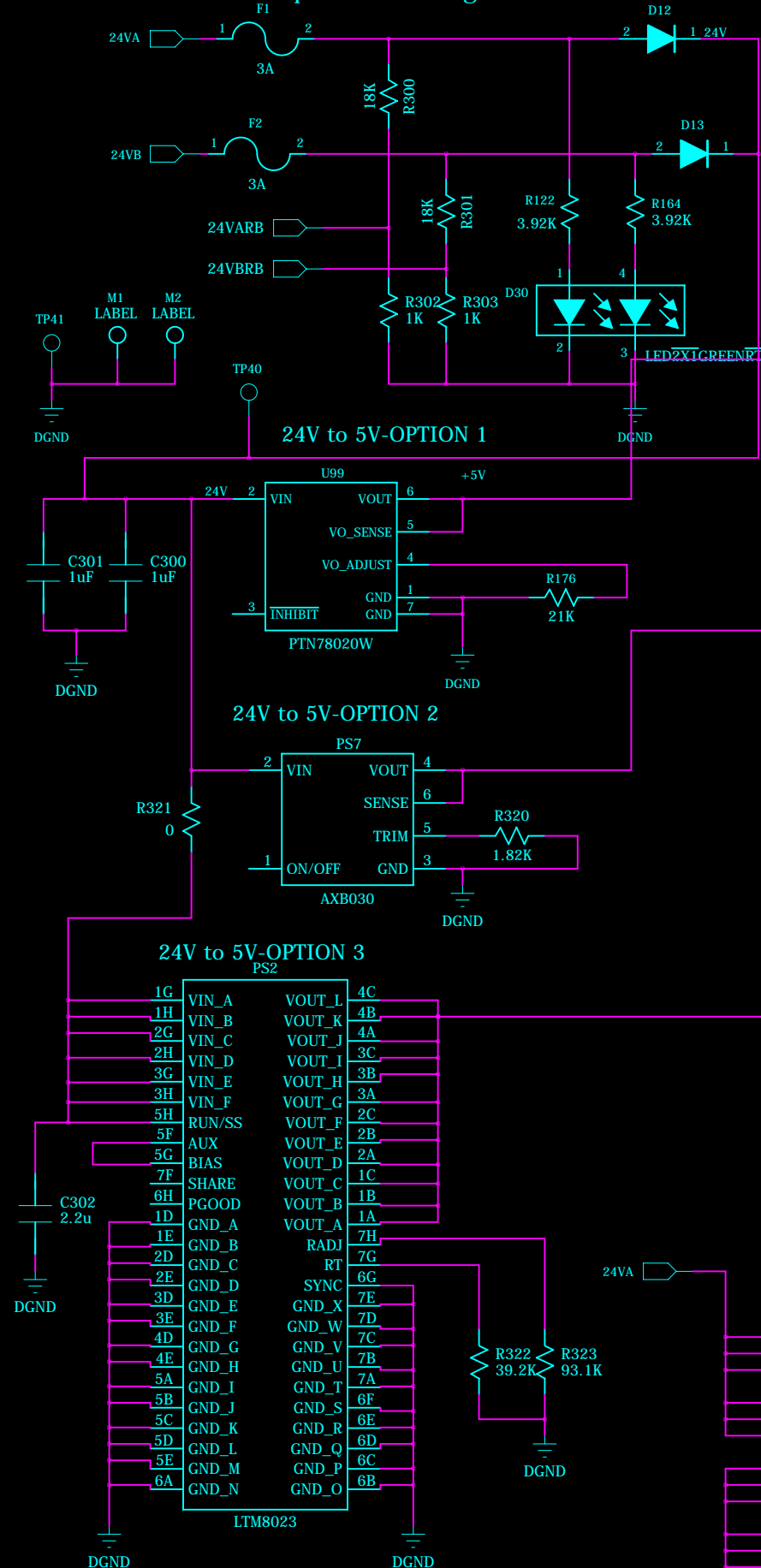
6

Change D14 & 15 to 24V LEDs

Add 24V Test Points

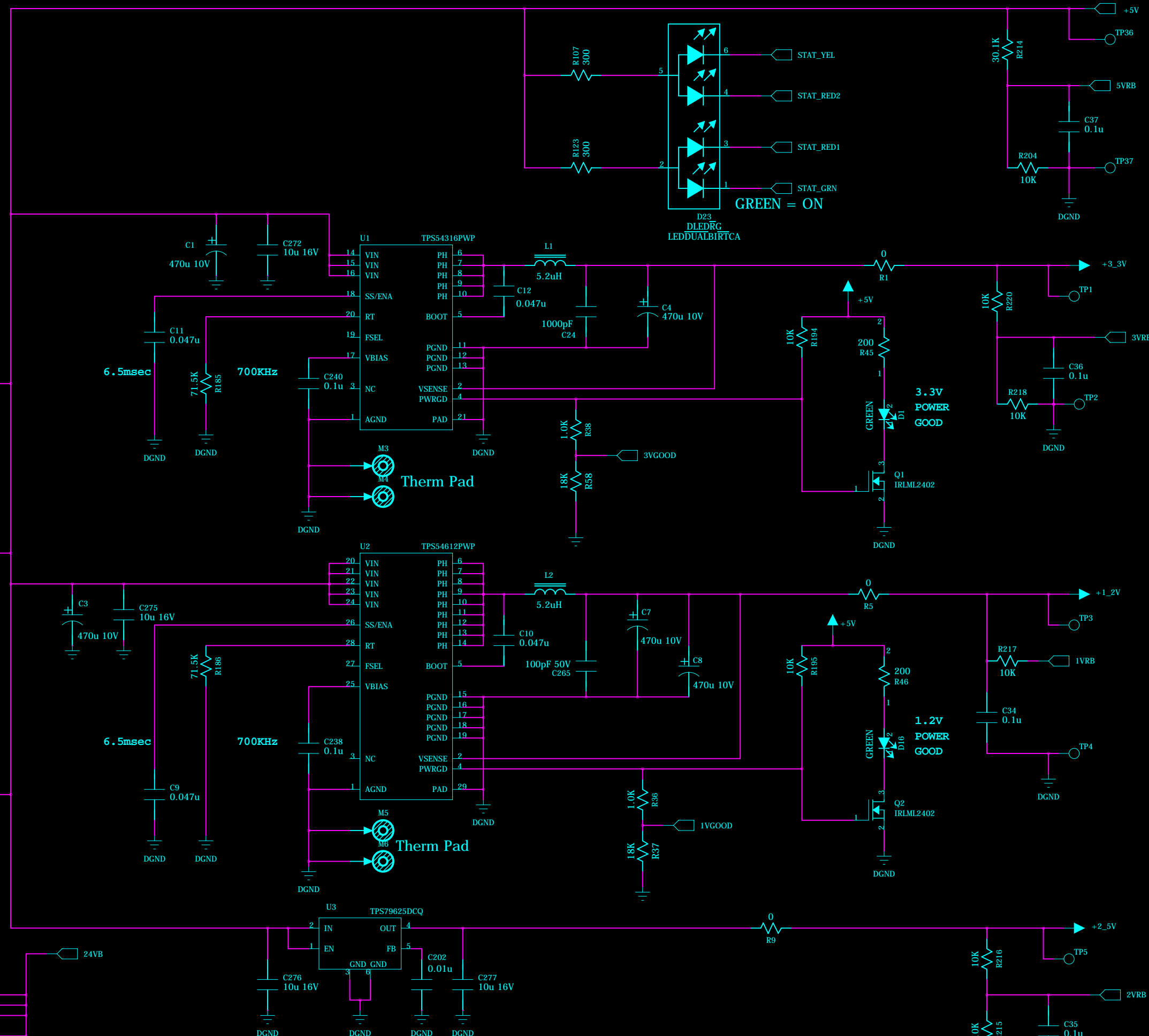
Add 5V LED

Add Caps across Voltage readbacks



Addr: 1001000 1=Read
Addr: 1001000 0=Write 3.3V max input

36V = 3.27V
24V = 2.18V
18V = 1.64V



ANALOG
POWER FEED

FPGA VCCO,
DSP I/O,
PCI BRIDGE
I/O

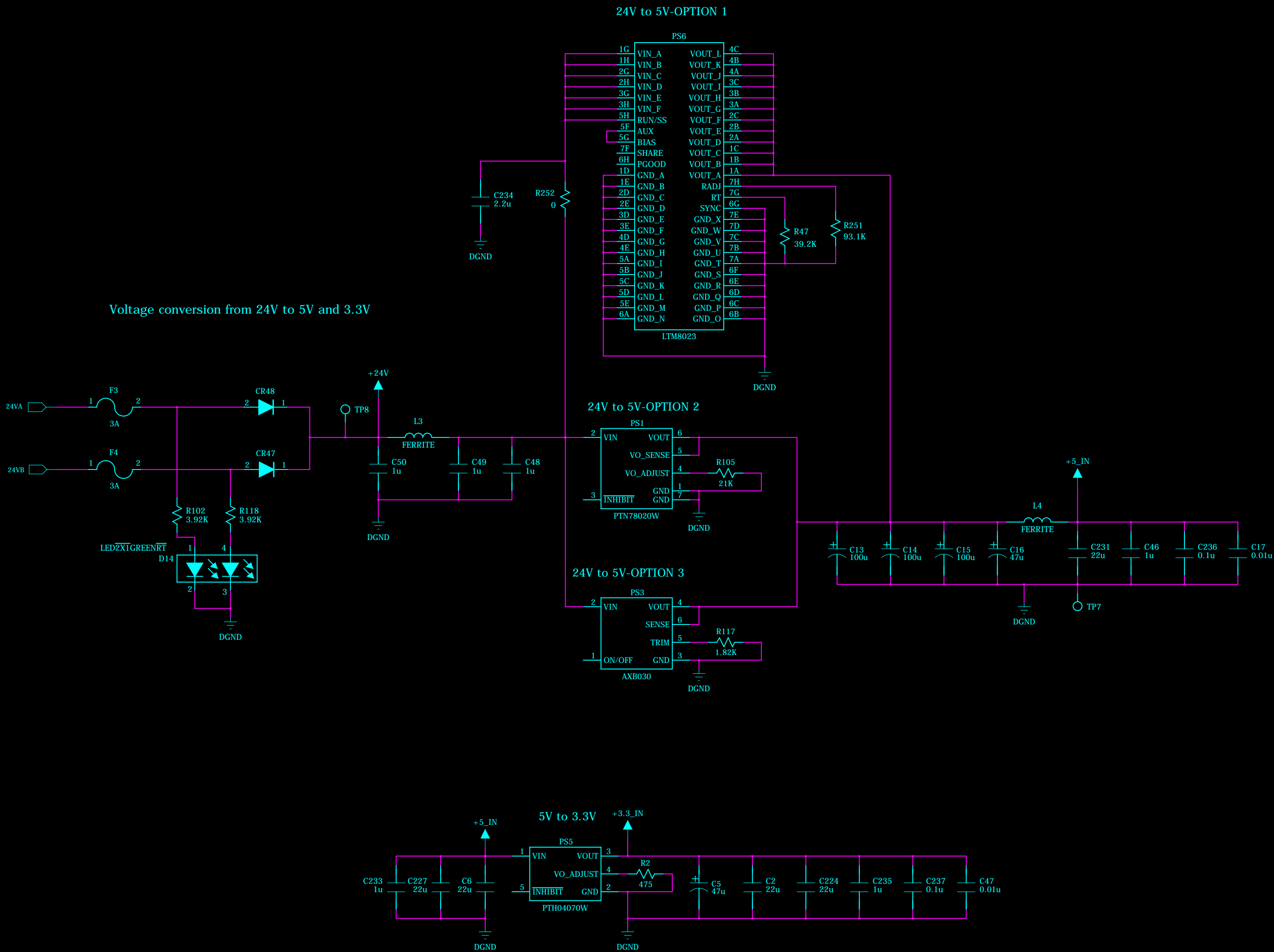
FPGA VCCINT,
DSP CORE

FPGA VCCAUX

APPROVALS		DATE	COMPRESSOR CONTROLS CORPORATION		
DRAWN	Dave Baggett	5/31/2007	4725 121st Street - Des Moines, Iowa 50322		
CHECKED	Tom Stoll	6/18/2007	VIOC		
ENGINEER	Luke Brown	5/14/2008	DIGITAL POWER		
SIZE	C	DWG. NO.	18-590570-00X	SHEET	2 of 15
FILE	VIOC RA.SCH	SCALE	none	PROD REV.	A

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DWG. NO. 18-590400-001 SH 10 REV. A



APPROVALS		DATE	COMPRESSOR CONTROLS CORPORATION			
DRAWN	Steve Chebuhar	2/19/08	4725 121st Street - Des Moines, Iowa 50323			
CHECKED	Steve Chebuhar	7/7/08	SCHEMATIC DRAWING			
ENGINEER	Steve Chebuhar	2/19/08	VIOB			
			24V TO 5V & 3.3V			
			SIZE	DWG. NO.	SHEET	PROD
			C	18-590400-001	10 OF 13	REV.
				FILE	SCALE	A
				VIOBRA.SCH	NONE	