

Layer	Stack up	Description	Base Thickness	Finish Thickness	Isolation Distance	Copper Coverage	
		Taiyo PSR 2000					
1	↑	Copper Foil 18 microns	0.689	1.968		100.000	
		EM827B Prepreg 2113	4.850	3.850	3.384		
2		EM827 4 mil core 1/1	1.260 4.000 1.260	1.260 4.000 1.260	4.000	63.000 18.000	
		EM827B Prepreg 1080	4.050	3.050	2.792		
		EM827B Prepreg 7628LR	8.350	7.350	6.729		
		EM827B Prepreg 7628LR	8.350	7.350	6.729		
4 6	62.7388	EM827 4 mil core 1/1	1.260 4.000 1.260	1.260 4.000 1.260	4.000	63.000 59.000	
5 }	89	EM827B Prepreg 7628LR	8.350	7.350	6.714	59.000	
		EM827B Prepreg 7628LR	8.350	7.350	6.714		
		EM827B Prepreg 1080	4.050	3.050	2.786		
6		511007 4 11 414	1.260	1.260	4.000	19.000	
7		EM827 4 mil core 1/1	4.000 1.260	4.000 1.260	4.000	64.000	
•		EM827B Prepreg 2113	4.850	3.850	3.396	3	
8	↓ 3 5 3 5 3 5 3 5 3 5 5 5 5 5 5 5 5 5 5	Copper Foil 18 microns	0.689	1.968		100.000	
-		Taiyo PSR 2000					

Copper Thickness = 11.496 | Dielectric Thickness = 51.244 | Overall Processed Thickness = 62.740

Impedance ID	Structure Name	Impedance Signal Layer	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Lower Trace Width	Upper Trace Width	Trace Separation	Lower Ground Strip Width	Upper Ground Strip Width	Ground Strip Separation	Broadside 2nd Layer	Trace Offset	Calculated Impedance	Target Impedance	Tol (+/- %)
1	Coated Microstrip 1B	1	2	0	5.031	4.031	0.000	0.000	0.000	0.000	0	0.000	50.000	50.000	10.000
2	Coated Microstrip 1B	1	2	0	7.966	6.966	0.000	0.000	0.000	0.000	0	0.000	40.000	40.000	10.000
3	Edge Coupled Coated Microstrip 1B	1	2	0	3.928	2.928	8.172	0.000	0.000	0.000	0	0.000	99.990	100.000	10.000
4	Edge Coupled Coated Microstrip 1B	1	2	0	5.988	4.988	6.012	0.000	0.000	0.000	0	0.000	79.990	80.000	10.000
5	Offset Stripline 1B2A	3	2	4	4.954	3.954	0.000	0.000	0.000	0.000	0	0.000	50.000	50.000	10.000
6	Offset Stripline 1B2A	3	2	4	7.781	6.781	0.000	0.000	0.000	0.000	0	0.000	40.000	40.000	10.000
7	Edge Coupled Offset Stripline 1B2A	3	2	4	3.984	2.984	8.016	0.000	0.000	0.000	0	0.000	100.000	100.000	10.000
8	Edge Coupled Offset Stripline 1B2A	3	2	4	6.038	5.038	5.962	0.000	0.000	0.000	0	0.000	80.000	80.000	10.000

StackName: mindtek-17-600400-001-rev1-appcrd	Version:	Revision:	Modification:	Date of Revision:	Editor		
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Department:							
Site:	1						



Impedance ID	Structure Name	Impedance Signal Layer	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Lower Trace Width	Upper Trace Width	Trace Separation	Lower Ground Strip Width	Upper Ground Strip Width	Ground Strip Separation	Broadside 2nd Layer	Trace Offset	Calculated Impedance	Target Impedance	Tol (+/- %)	
9	Offset Stripline 1B2A	6	5	7	4.954	3.954	0.000	0.000	0.000	0.000	0	0.000	49.990	50.000	10.000	
10	Offset Stripline 1B2A	6	5	7	7.781	6.781	0.000	0.000	0.000	0.000	0	0.000	40.000	40.000	10.000	
11	Edge Coupled Offset Stripline 1B2A	6	5	7	3.984	2.984	8.016	0.000	0.000	0.000	0	0.000	100.000	100.000	10.000	
12	Edge Coupled Offset Stripline 1B2A	6	5	7	6.038	5.038	5.962	0.000	0.000	0.000	0	0.000	80.000	80.000	10.000	
13	Coated Microstrip 1B	8	7	0	5.053	4.053	0.000	0.000	0.000	0.000	0	0.000	50.000	50.000	10.000	
14	Coated Microstrip 1B	8	7	0	7.998	6.998	0.000	0.000	0.000	0.000	0	0.000	40.010	40.000	10.000	
15	Edge Coupled Coated Microstrip 1B	8	7	0	3.940	2.940	8.160	0.000	0.000	0.000	0	0.000	100.000	100.000	10.000	
16	Edge Coupled Coated Microstrip 1B	8	7	0	6.002	5.002	5.998	0.000	0.000	0.000	0	0.000	80.000	80.000	10.000	

Notes

StackName: mindtek-17-600400-001-rev1-appcrd	Version:	Revision:	Modification:	Date of Revision:	Editor		
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