DATE	REVISION NUMBER	INITIALS	DESCRIPTION
12/10/09	ETB-000155-010	lzhou	INITIAL REVISION
06/03/10	ETB-000155-210	FEI GAO	All phy chips and FPGA on board

ETB-000155-210

Atheros Arctic PHY Demo Board

AR8031/AR8033

ATHEROS CONFIDENTIAL
PRELIMINARY

Silkscreen on Board:

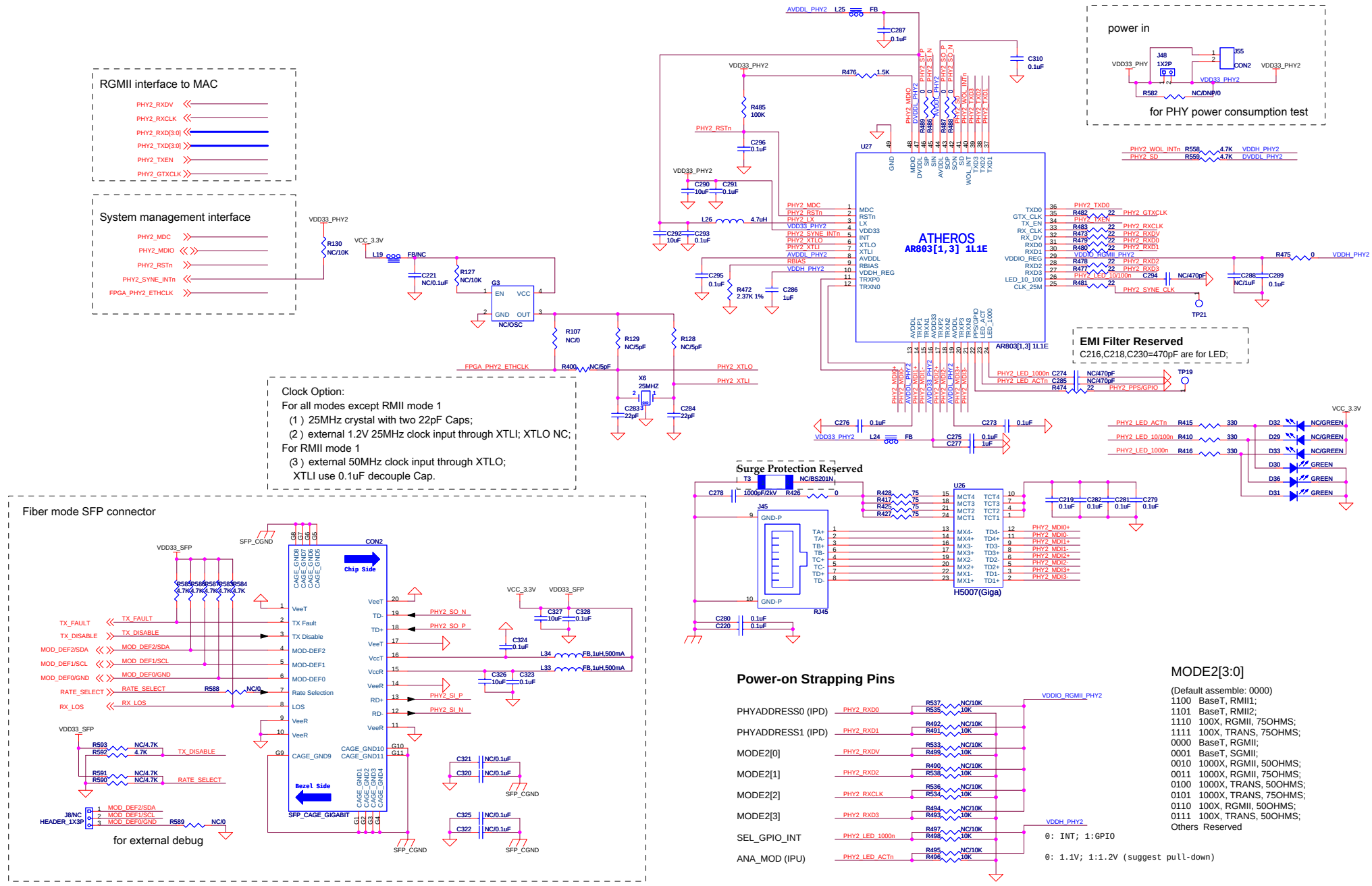
AR5BEU-08031A rev.010 ☐

AR5BEU-08033A rev.010 ☐

Atheros Communications, Inc.
5480 Great America Parkway
Santa Clara, CA 95054

Title: Title and Rev

Date: Friday, September 10, 2010 | Size: C | Rev: 1.0 | Sheet: 1 of 10 | DWG NO: ETB-000155-210



Atheros Communications, Inc. 5480 Great America Parkway Santa Clara, CA 95054	Title: PHY		
Date: Friday, September 10, 2010	Size: C	Rev: 1.0	Sheet 2 of 2
DWG NO: ETB-000155-210			