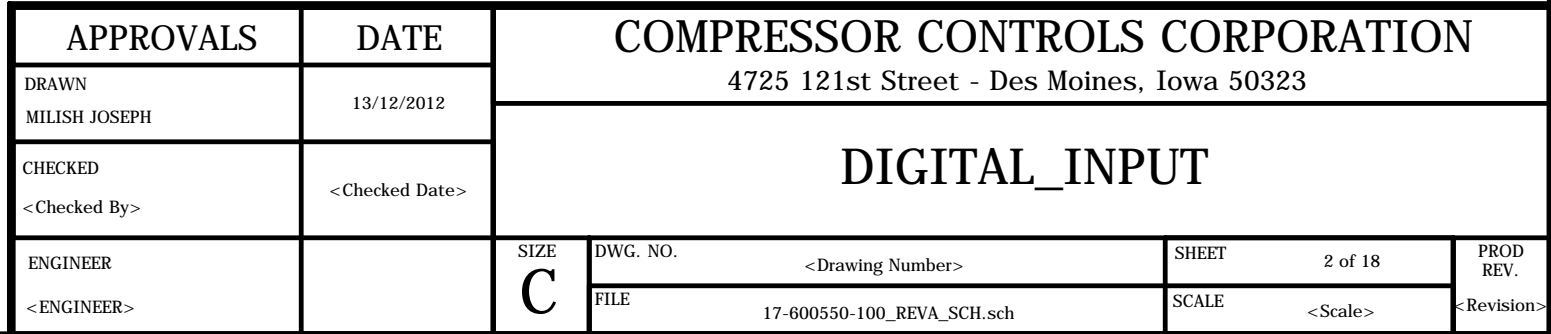


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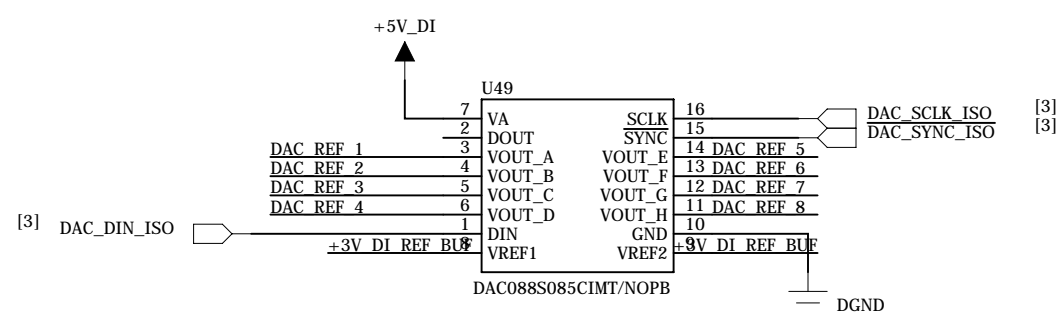


Figure 10 shows the pin connections for the MM74HC595MX chip. The diagram is divided into two sections, each showing a different set of connections for the same chip.

Top Section:

- VCC (Pin 16):** Connected to +5V_DI.
- GND (Pin 8):** Connected to DGND.
- Data Pins (Pins 1-7):** QA (1), QB (2), QC (3), QD (4), QE (5), QF (6), QG (7) are connected to CH1_DIAG_EN, CH2_DIAG_EN, CH2_DIAG_ACT, CH3_DIAG_EN, CH3_DIAG_ACT, CH4_DIAG_EN, and CH4_DIAG_ACT respectively.
- SR Inputs (Pins 10-14):** SR_SCLR_ISO (10), SR_SCK_ISO (11), SR_RCK_ISO (12), SR_G_ISO (13), and SR_SER_ISO (14) are connected to a common bus.

Bottom Section:

- VCC (Pin 16):** Connected to +5V_DI.
- GND (Pin 8):** Connected to DGND.
- Data Pins (Pins 1-8):** QA (1), QB (2), QC (3), QD (4), QE (5), QF (6), QG (7), and QH (8) are connected to CH5_DIAG_EN, CH5_DIAG_ACT, CH6_DIAG_EN, CH6_DIAG_ACT, CH7_DIAG_EN, CH7_DIAG_ACT, CH8_DIAG_EN, and CH8_DIAG_ACT respectively.
- SR Inputs (Pins 10-14):** SR_SCLR_ISO (10), SR_SCK_ISO (11), SR_RCK_ISO (12), SR_G_ISO (13), and SR_SER_ISO (14) are connected to a common bus.

The schematic diagram illustrates the internal connections of two ADUM3400CRWZ-RL components, labeled U13 and U14. Both components are connected to a +3.3V supply and a +5V_D1 supply. The components are connected to a 4.7k resistor network (R208, R249) and a GND connection.

U13 Connections:

- Power Supply:** VDD1 (pin 1) and VDD2 (pin 16) are connected to +3.3V. GND1 (pin 8) and GND2 (pin 9) are connected to GND.
- Data Pins:** IN1 (pin 3), IN2 (pin 4), IN3 (pin 5), and IN4 (pin 6) are connected to +3.3V. OUT1 (pin 14), OUT2 (pin 13), OUT3 (pin 12), and OUT4 (pin 11) are connected to +5V_D1.
- Control Pins:** SR_SCLR (pin 3), SR_SCK (pin 4), SR_RCK (pin 5), and SR_SER (pin 6) are connected to +3.3V. SR_SCLR_ISO (pin 14), SR_SCK_ISO (pin 13), SR_RCK_ISO (pin 12), and SR_SER_ISO (pin 11) are connected to +5V_D1.

U14 Connections:

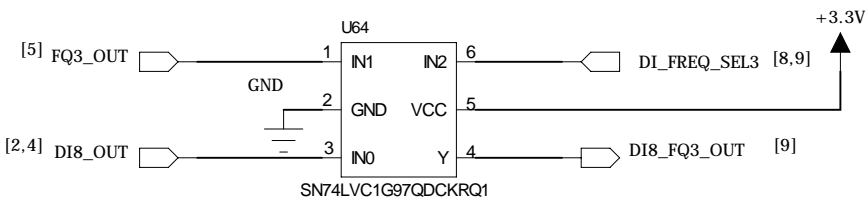
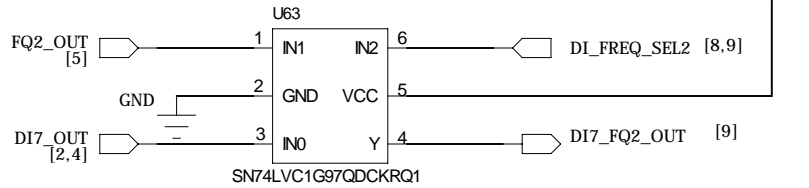
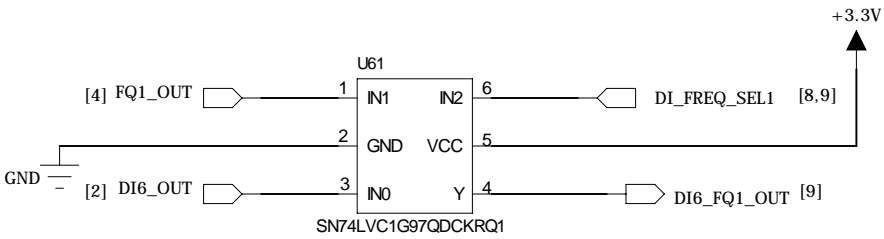
- Power Supply:** VDD1 (pin 1) and VDD2 (pin 16) are connected to +3.3V. GND1 (pin 8) and GND2 (pin 9) are connected to GND.
- Data Pins:** IN1 (pin 3), IN2 (pin 4), IN3 (pin 5), and IN4 (pin 6) are connected to +3.3V. OUT1 (pin 14), OUT2 (pin 13), OUT3 (pin 12), and OUT4 (pin 11) are connected to +5V_D1.
- Control Pins:** SR_G (pin 3), DAC_DIN (pin 4), DAC_SCLK (pin 5), and DAC_SYNC (pin 6) are connected to +3.3V. SR_G_ISO (pin 14), DAC_DIN_ISO (pin 13), DAC_SCLK_ISO (pin 12), and DAC_SYNC_ISO (pin 11) are connected to +5V_D1.

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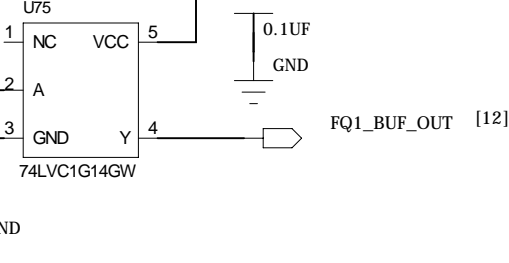
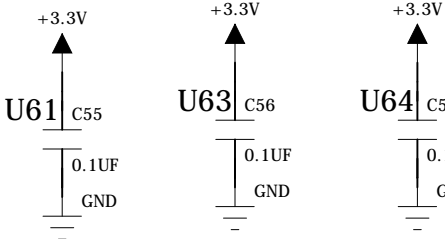
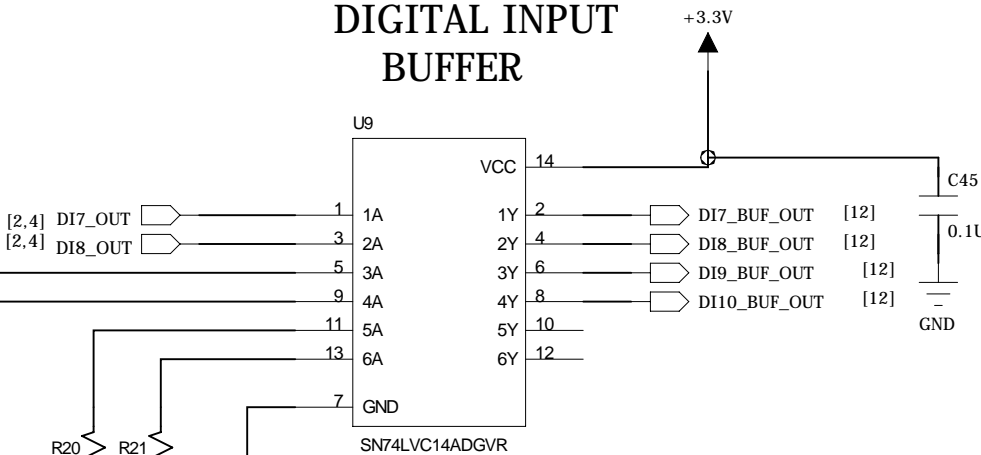
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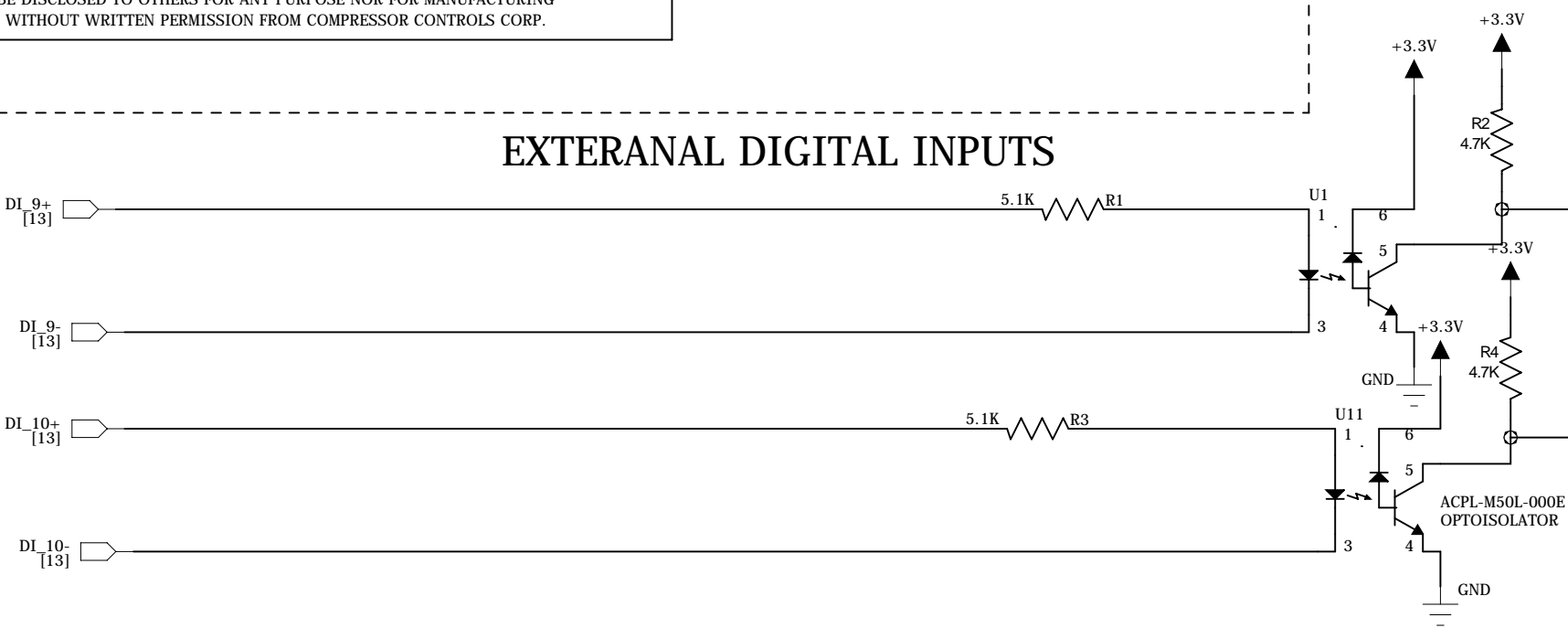
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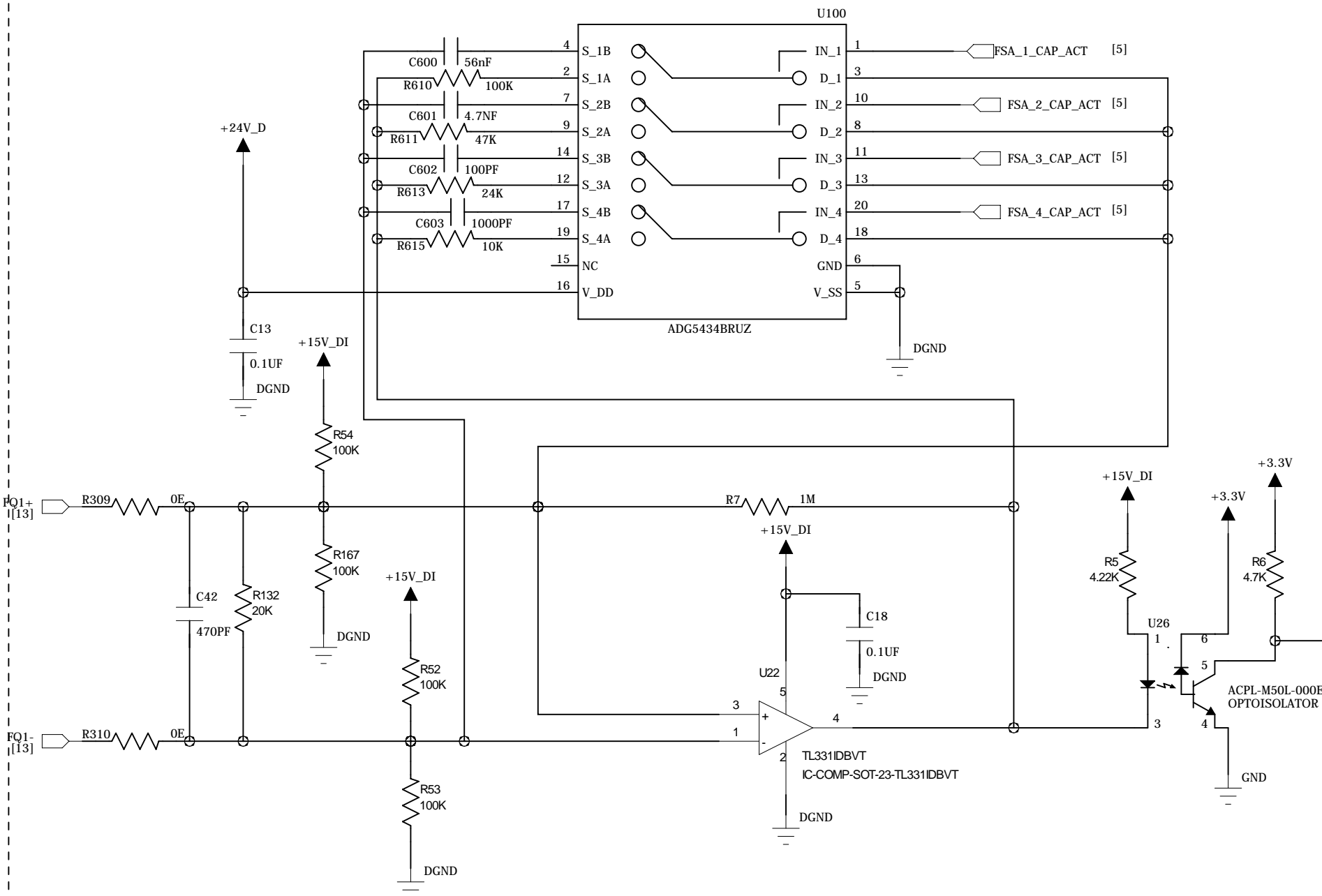
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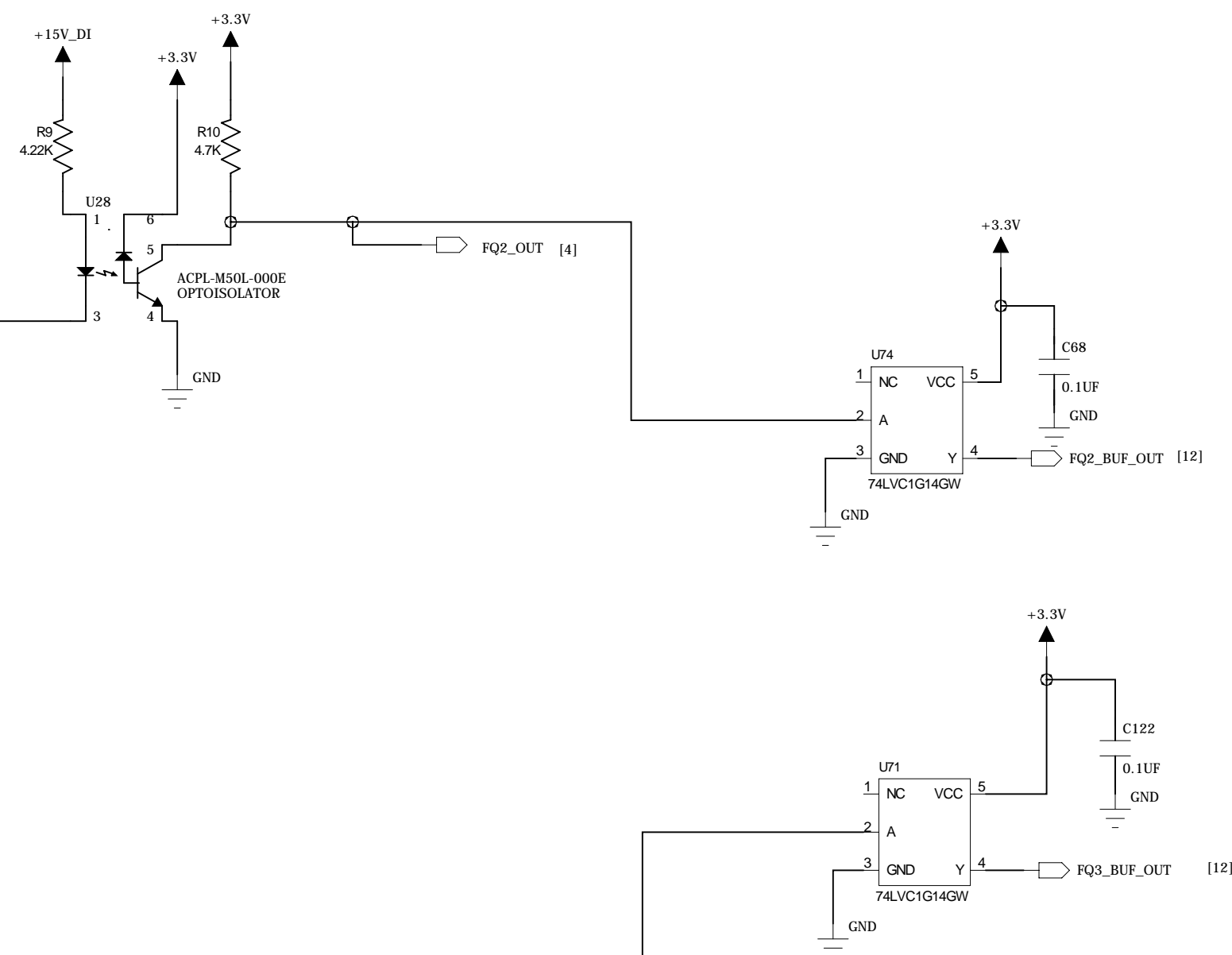
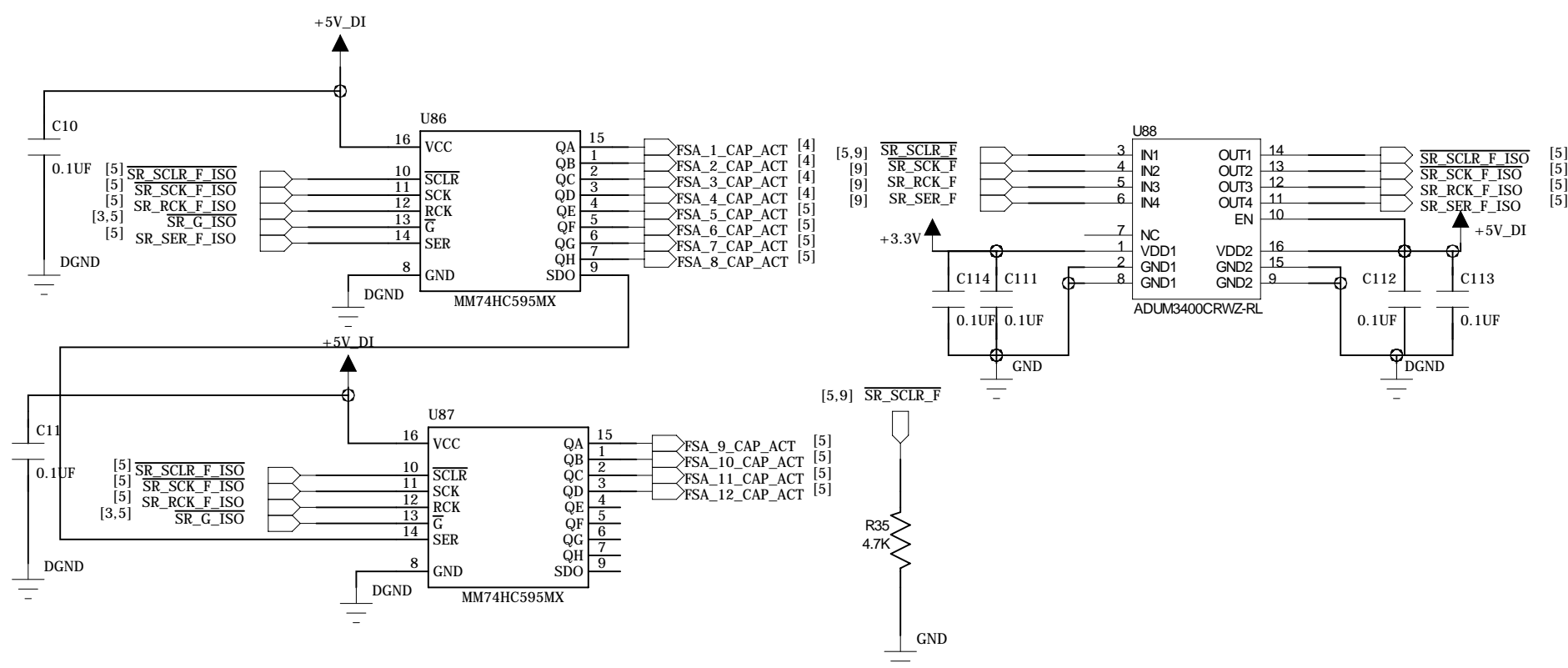
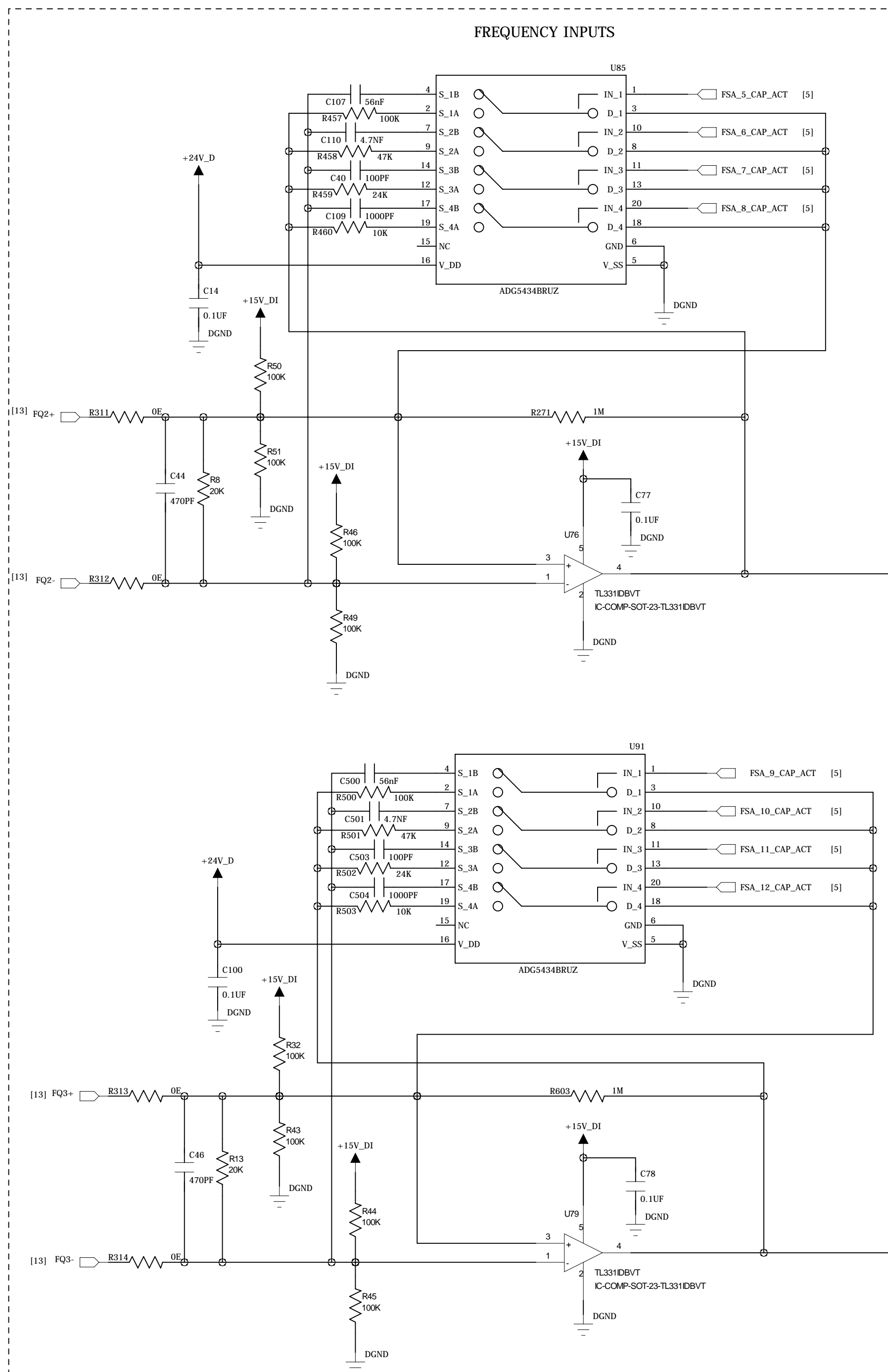
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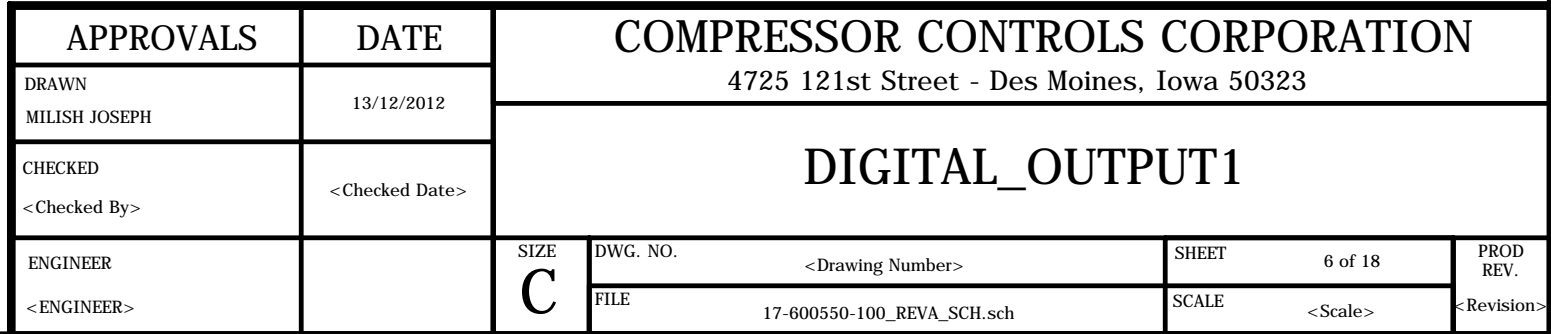
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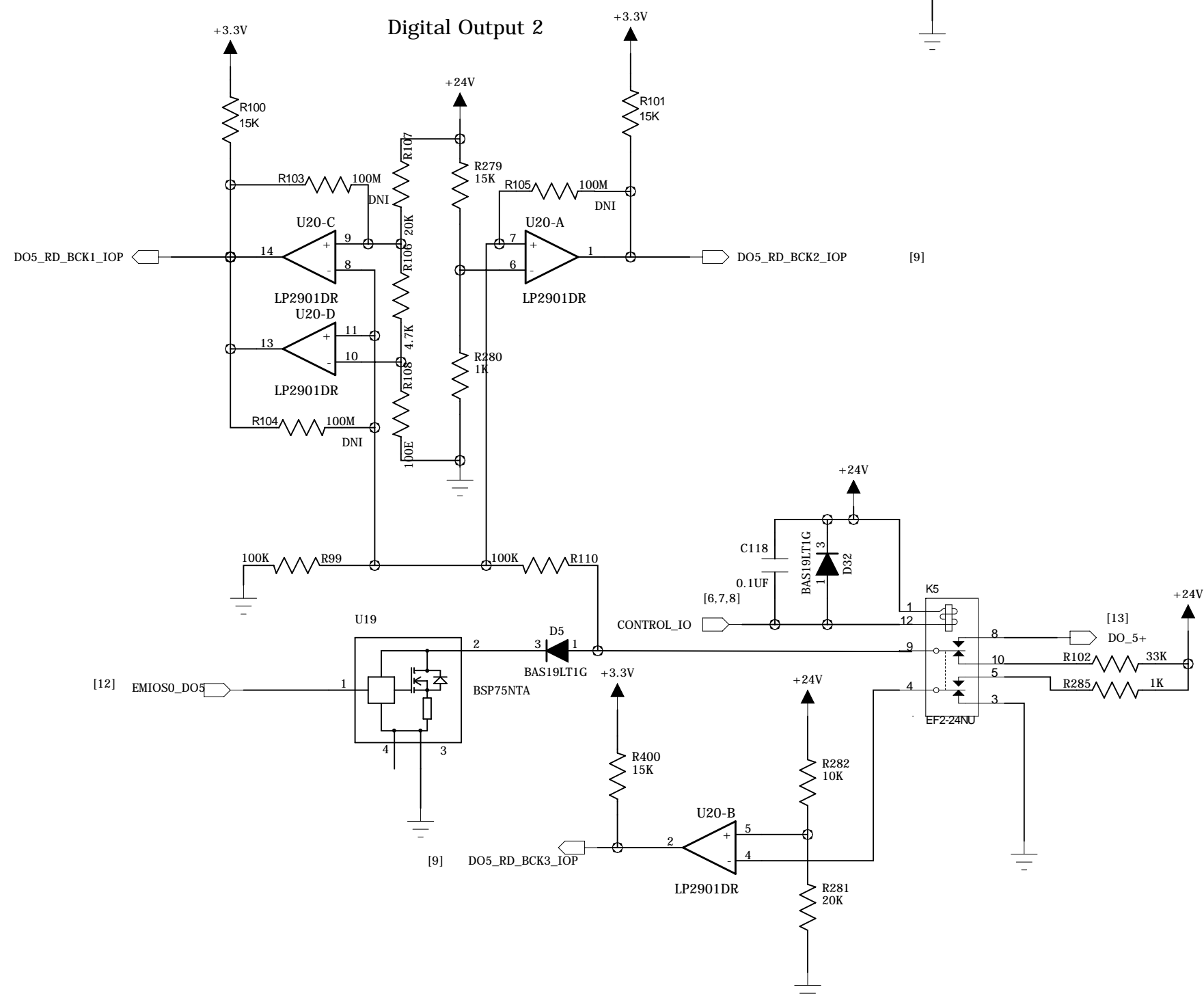
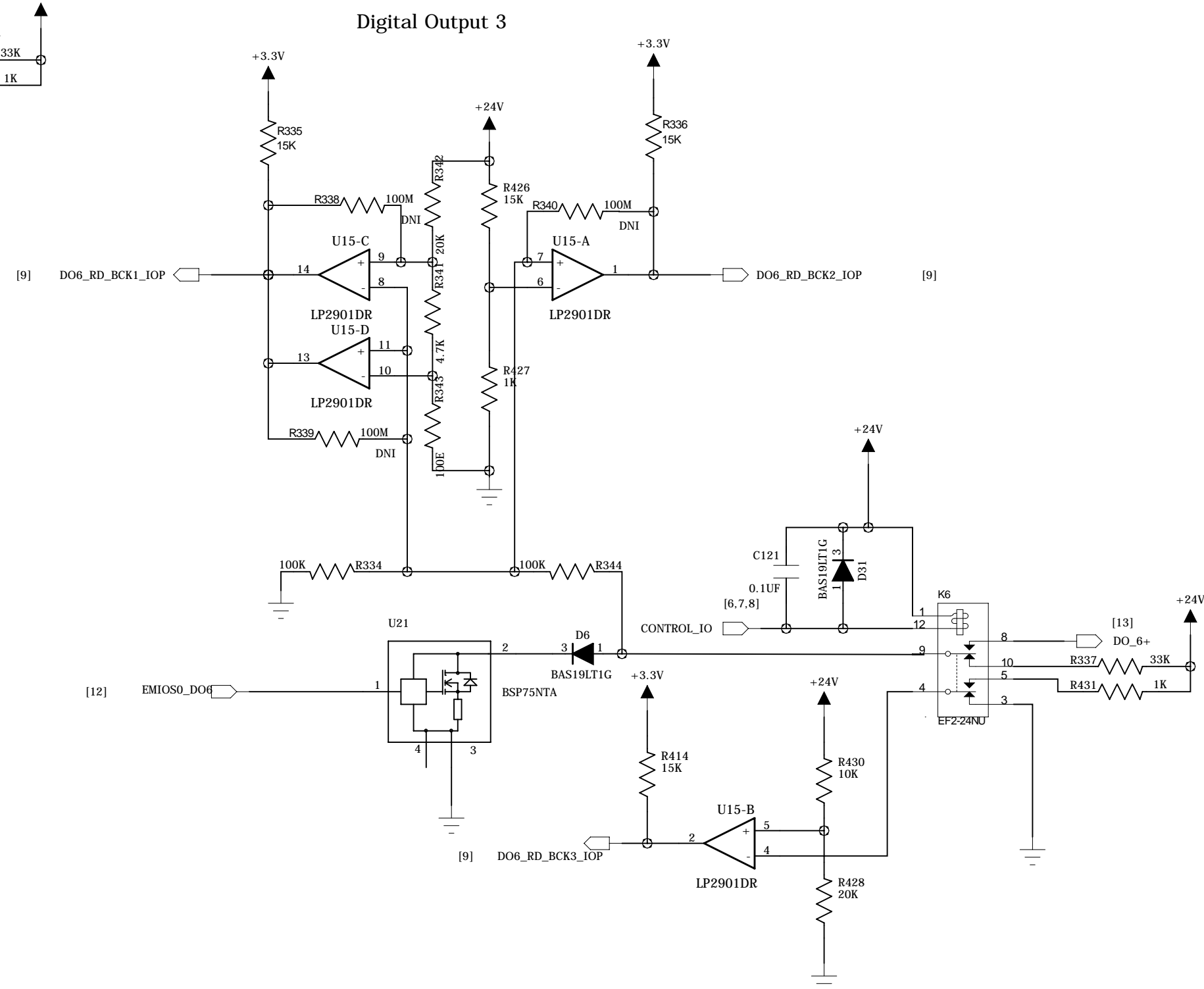
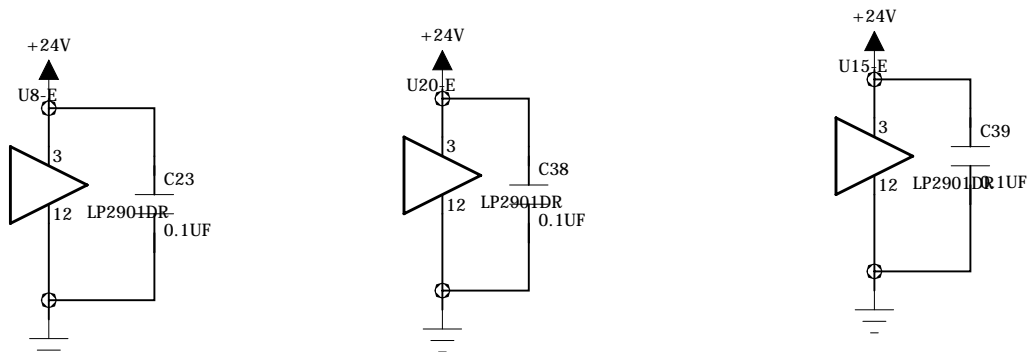
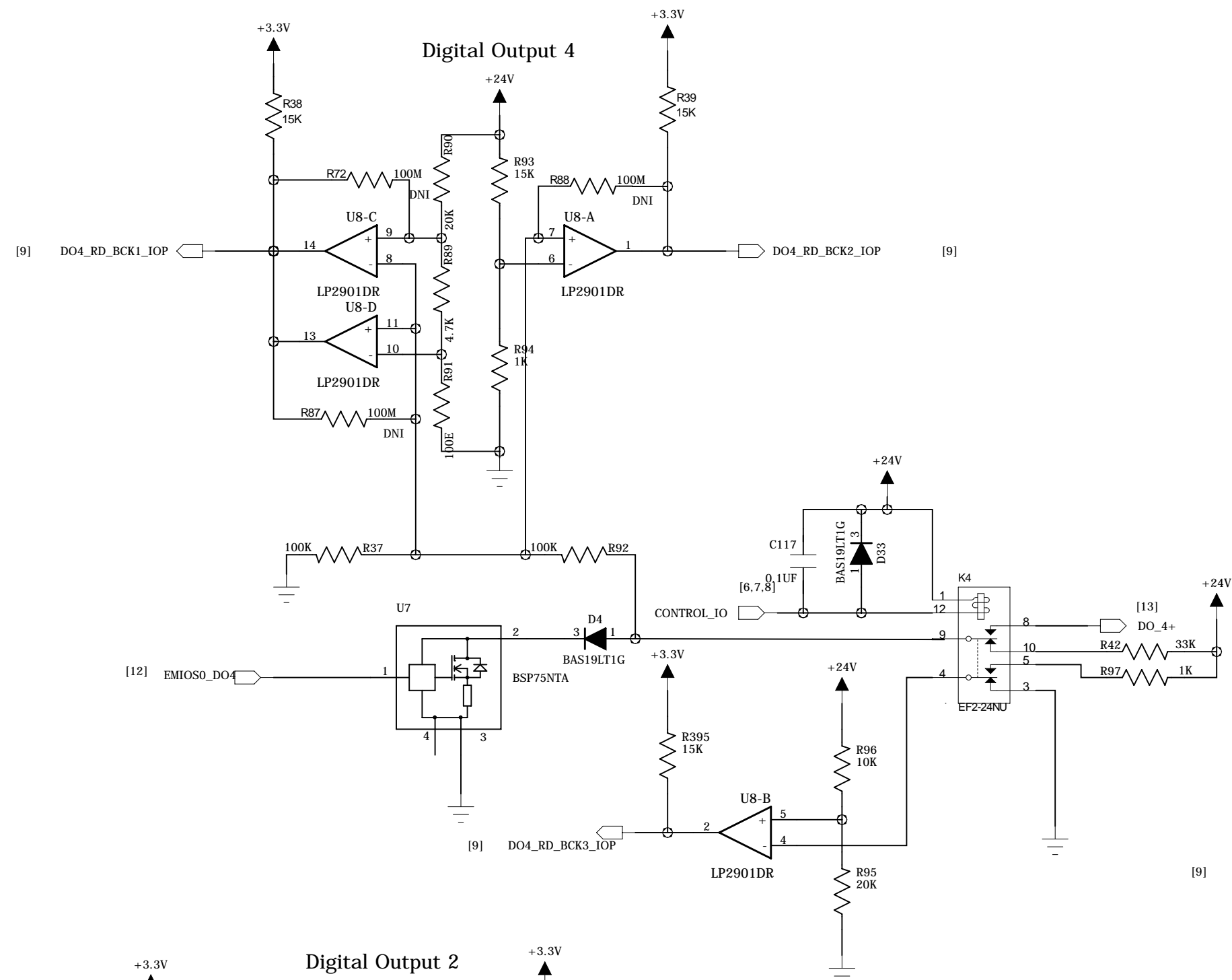


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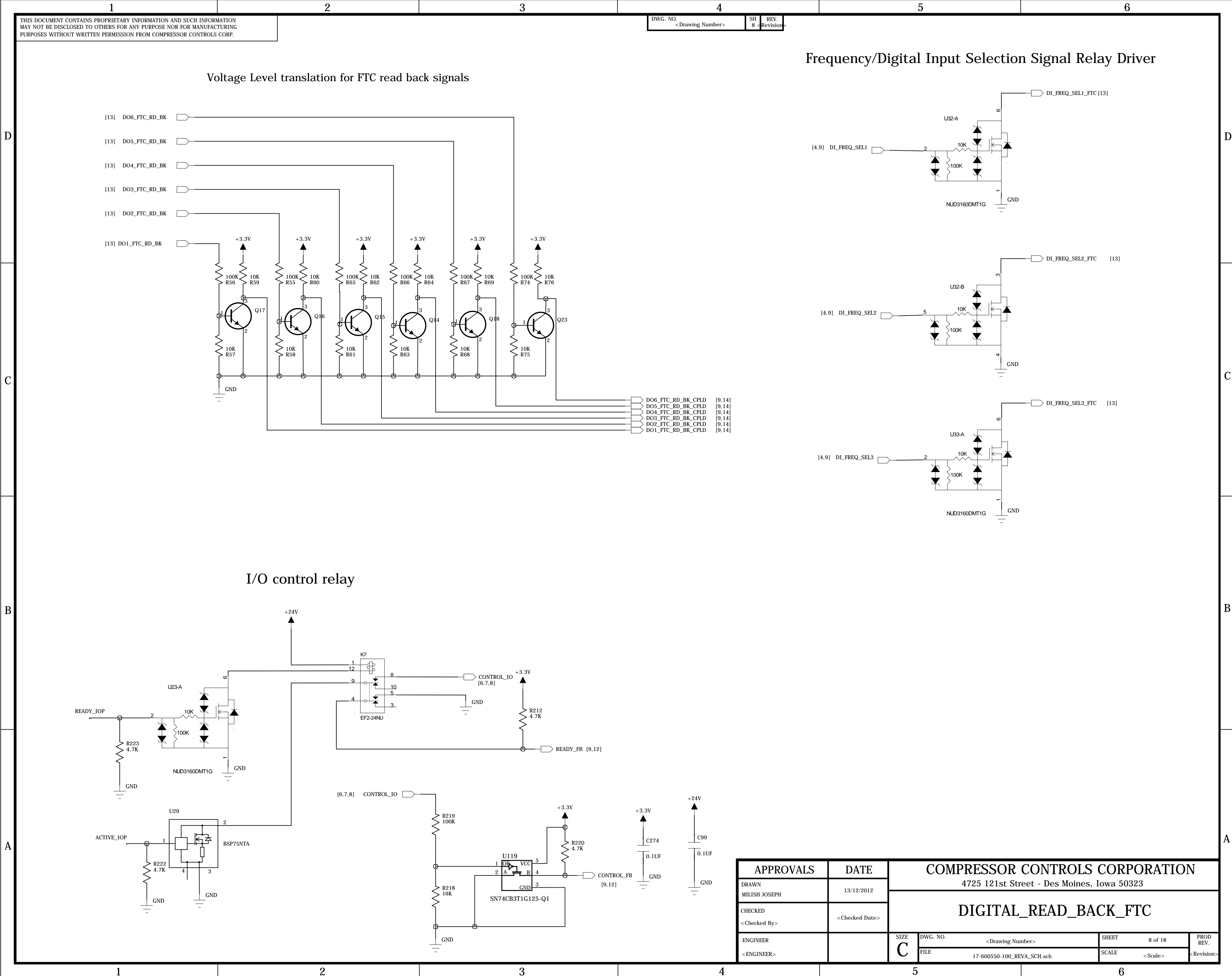


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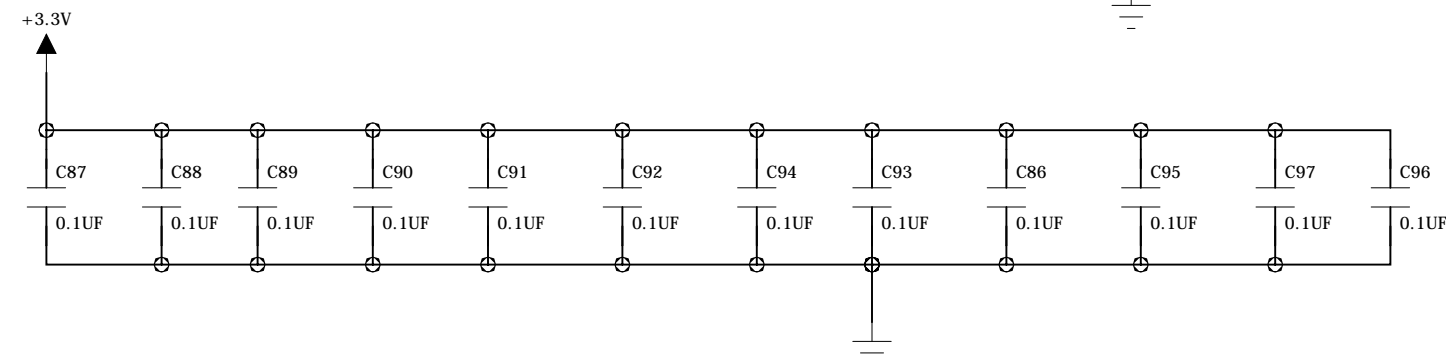
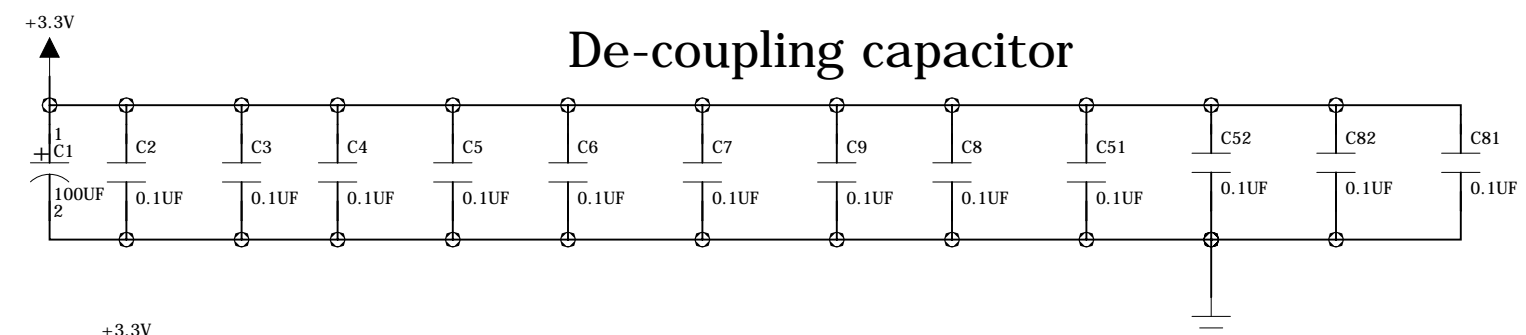
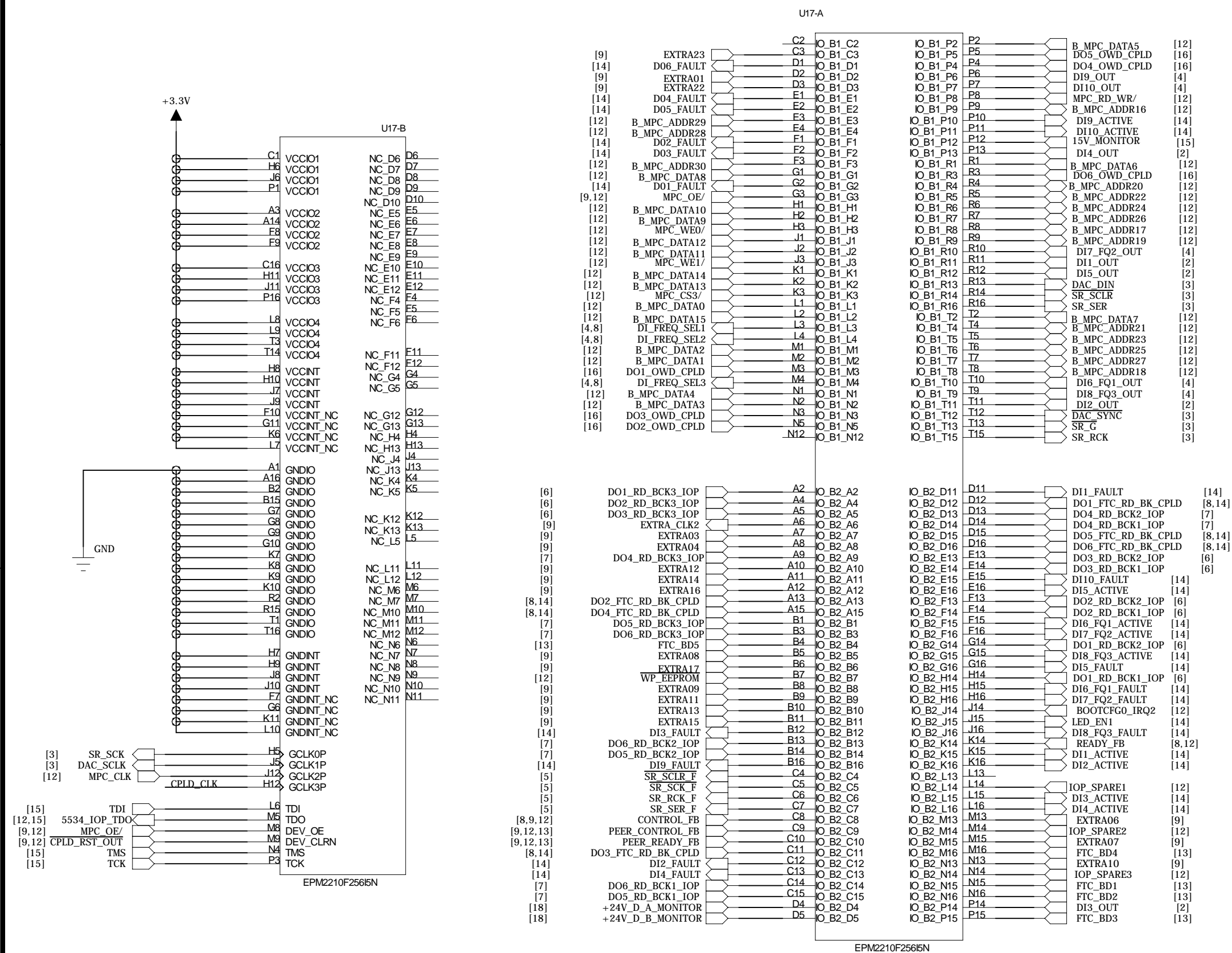
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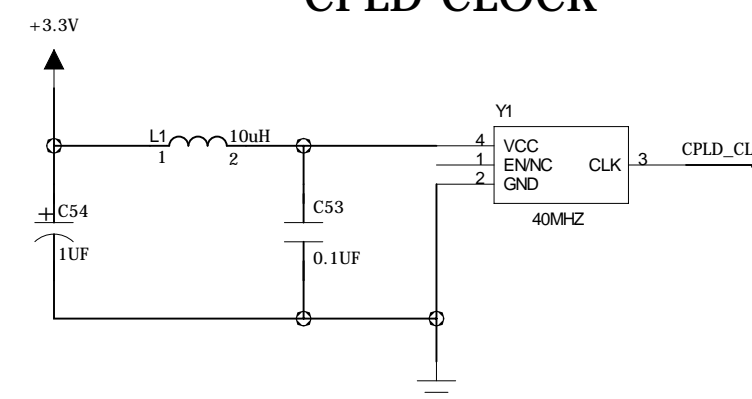
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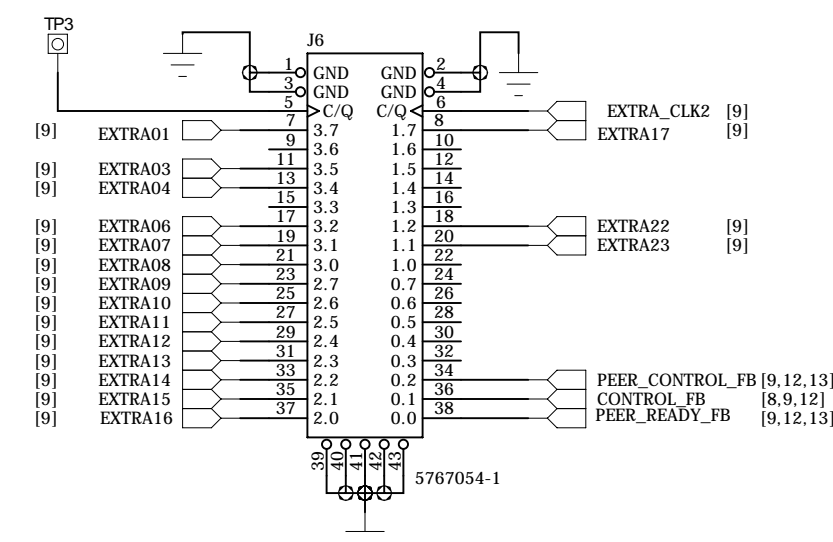
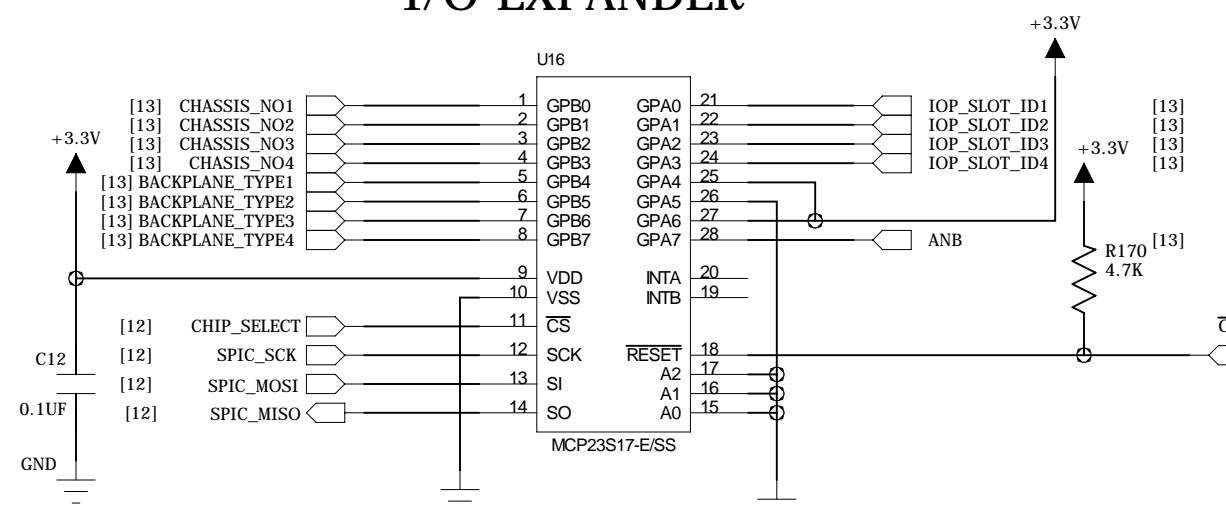
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CPLD CLOCK



I/O EXPANDER



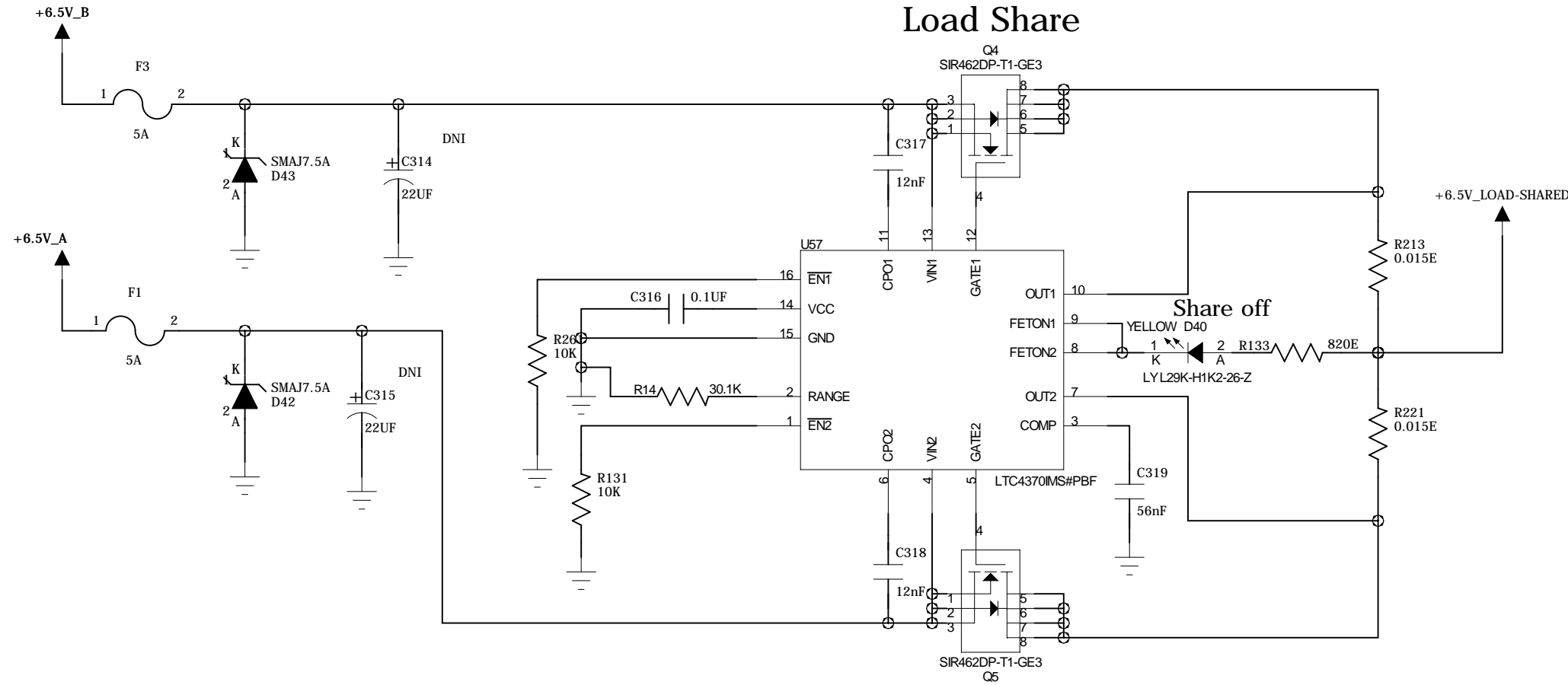
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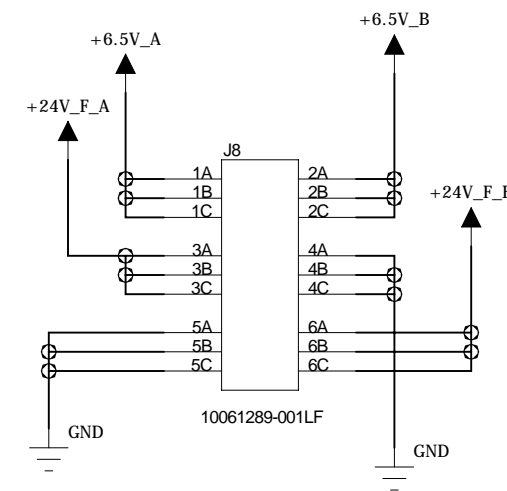
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6.5V LOAD SHARE

Load Share

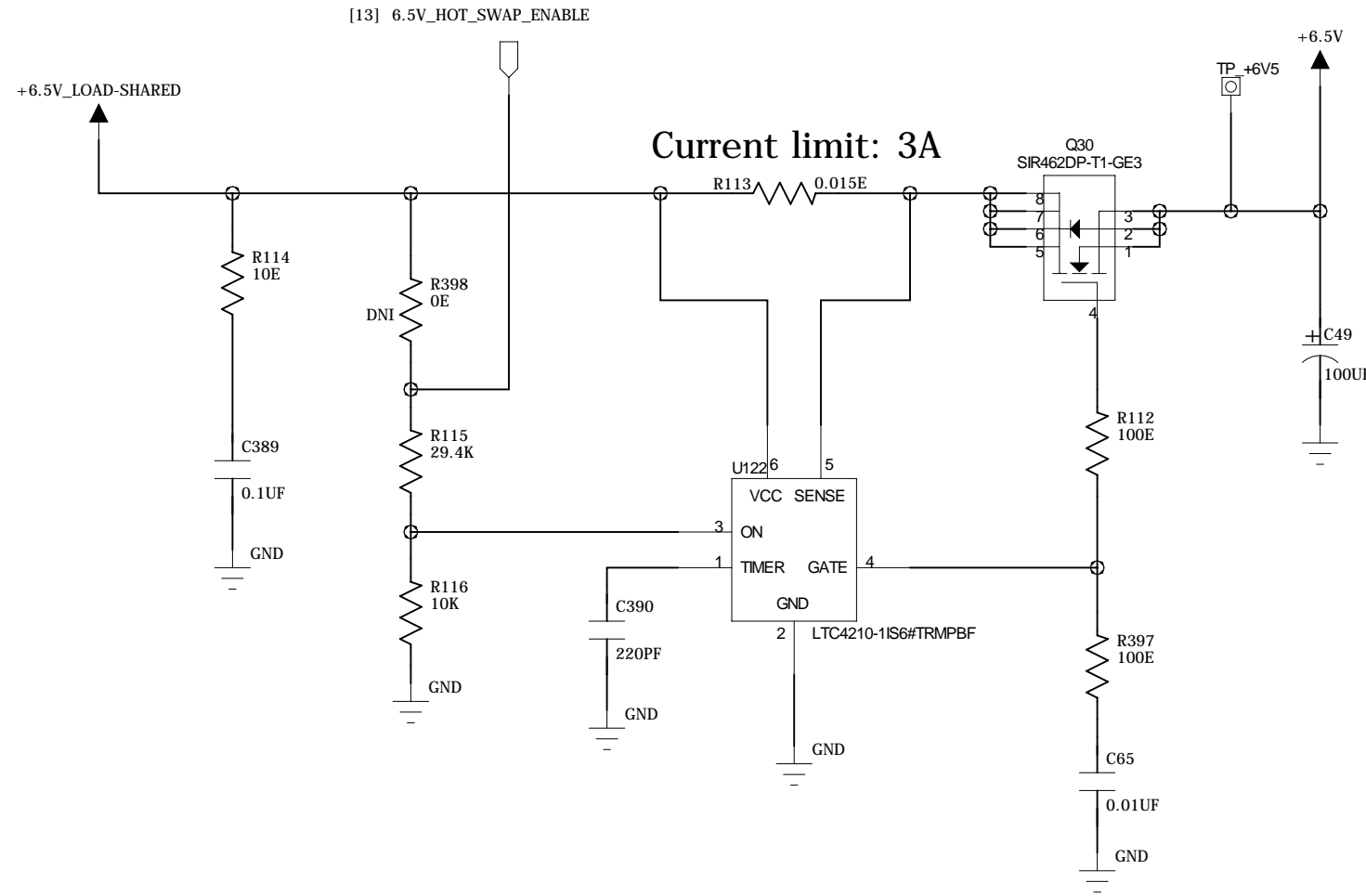


POWER CONNECTOR

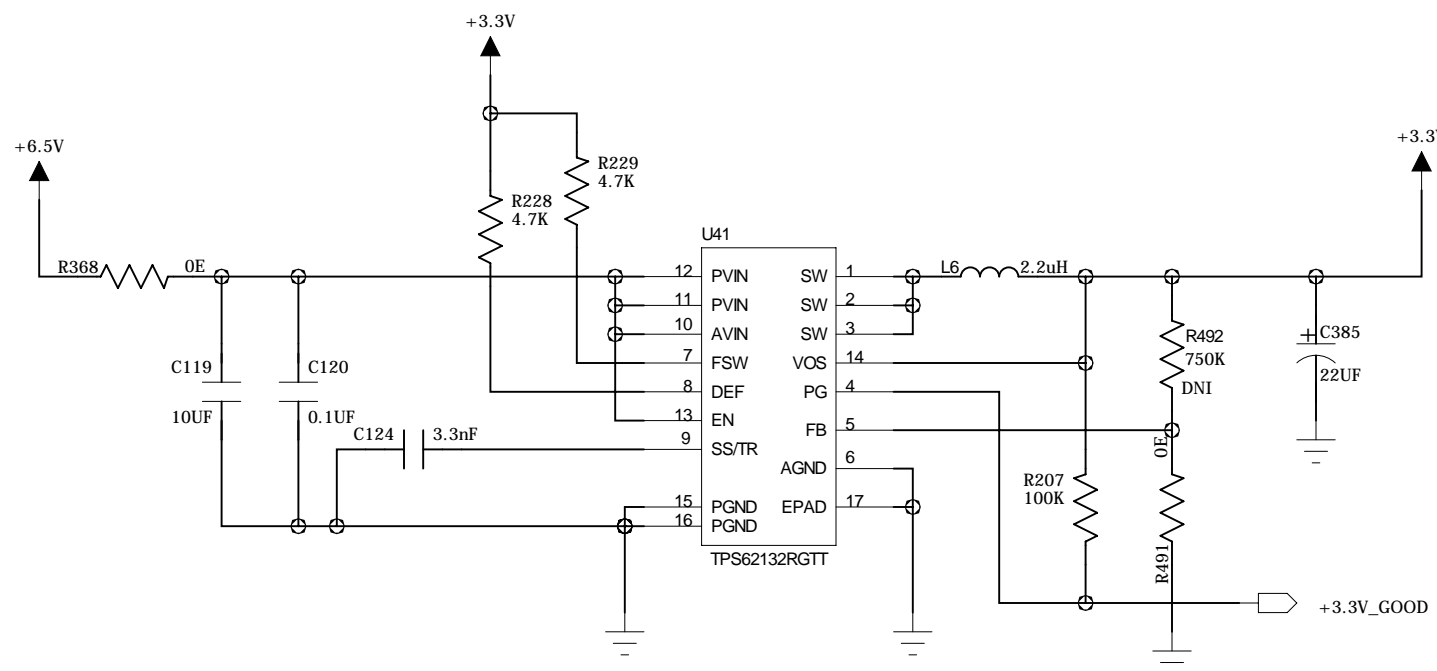


6.5V HOTSWAP

Current limit: 3A

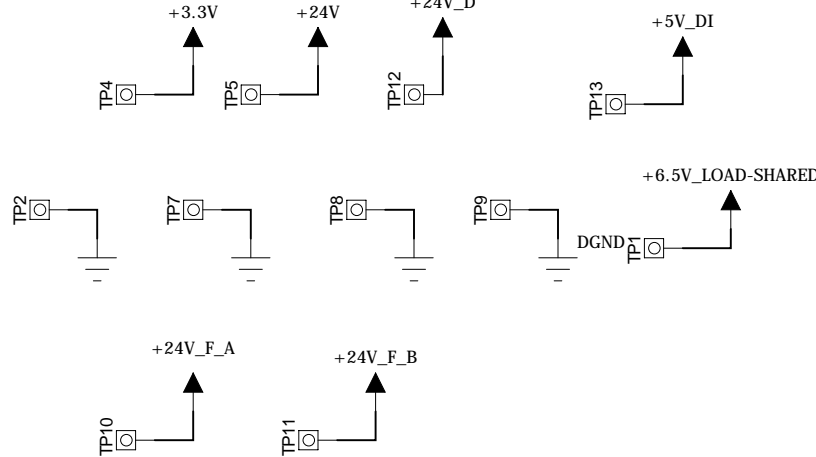


SWITCHING REGULATOR 6.5V to 3.3V

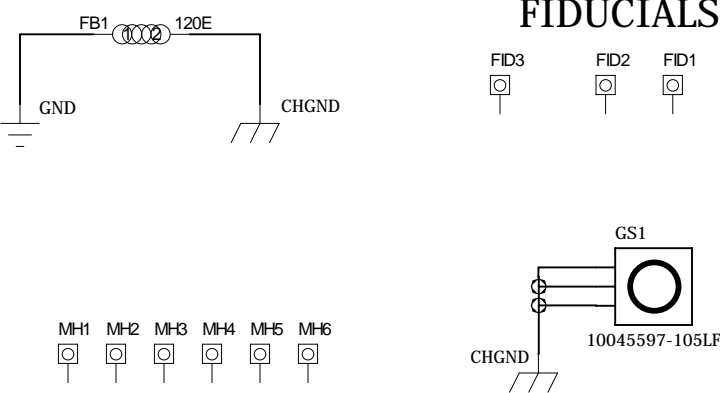


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FIXED REGULATOR	TPS62132	DNI	0E

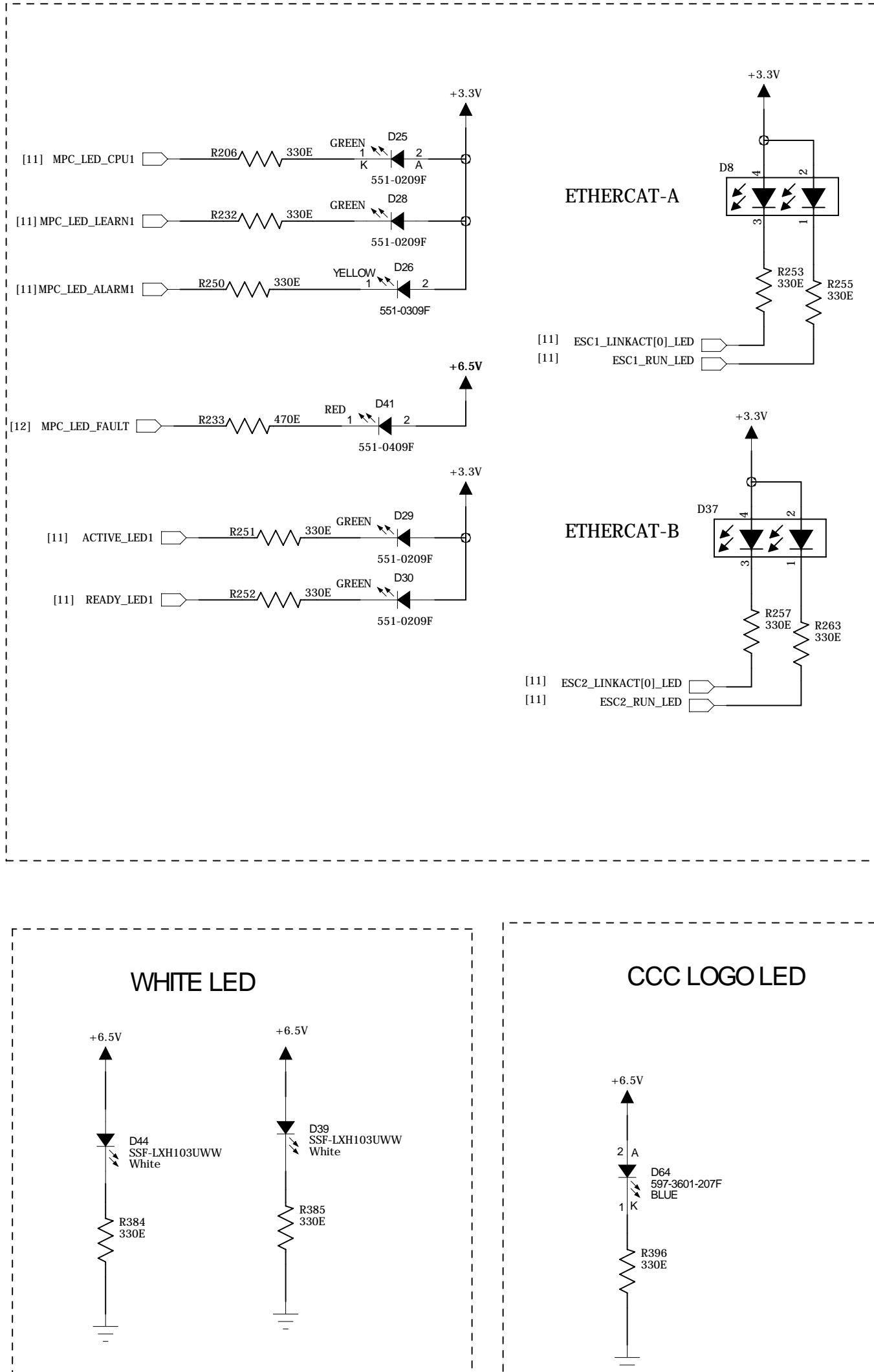
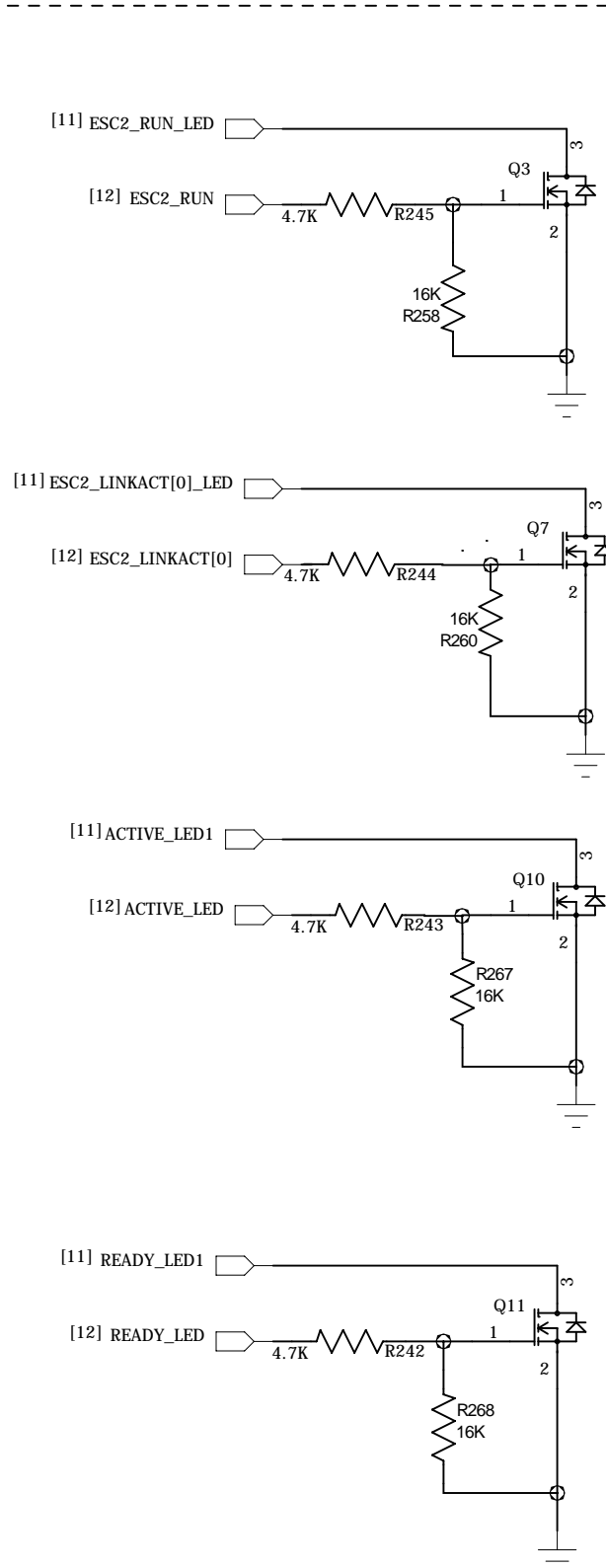
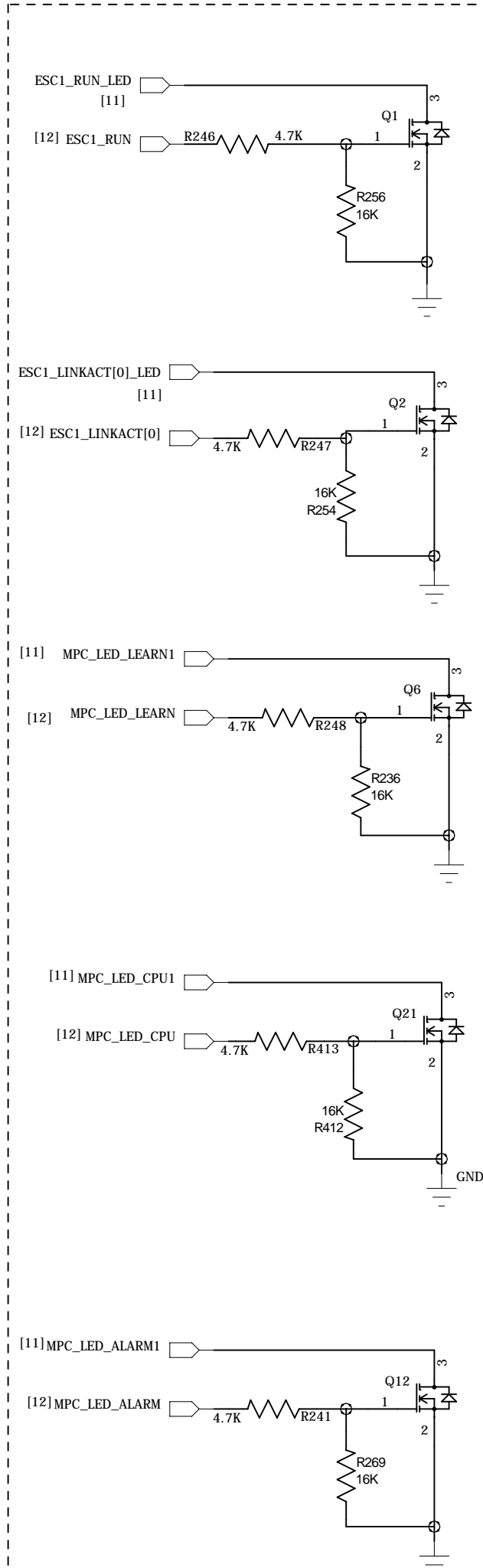
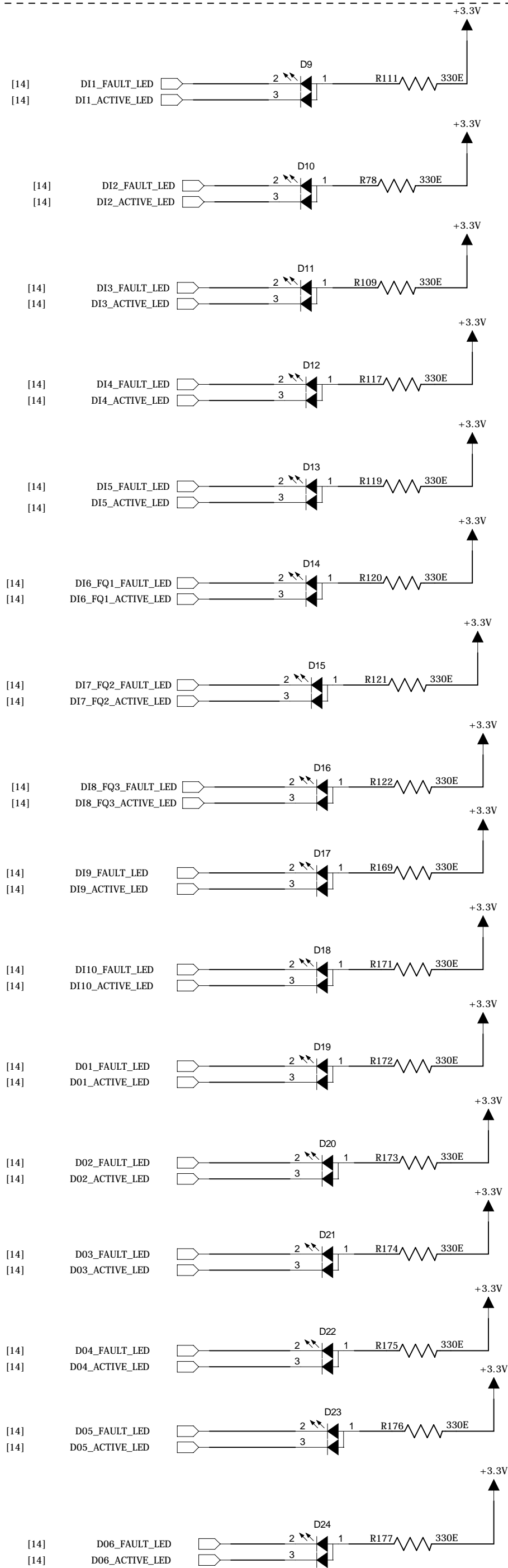
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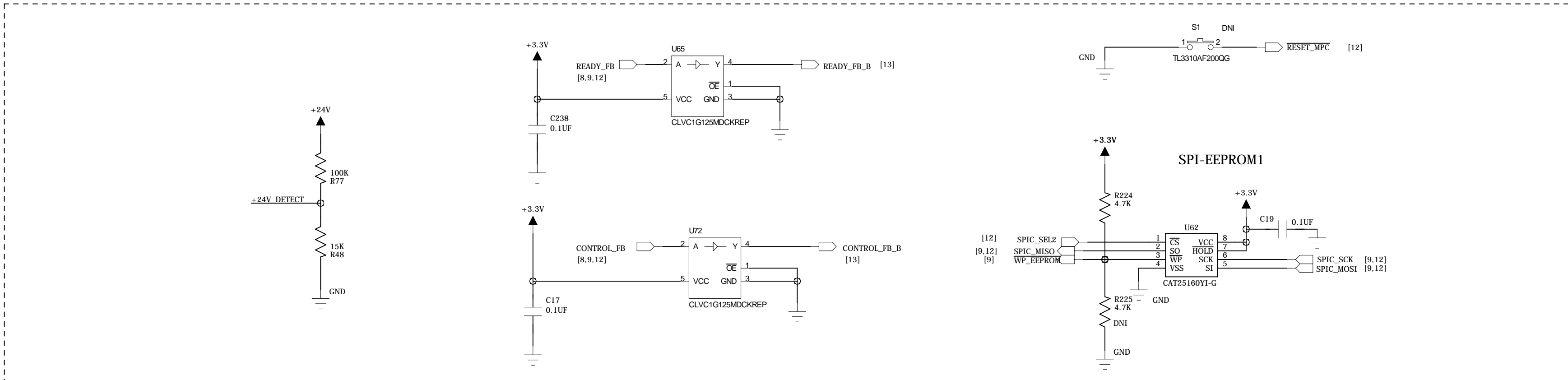
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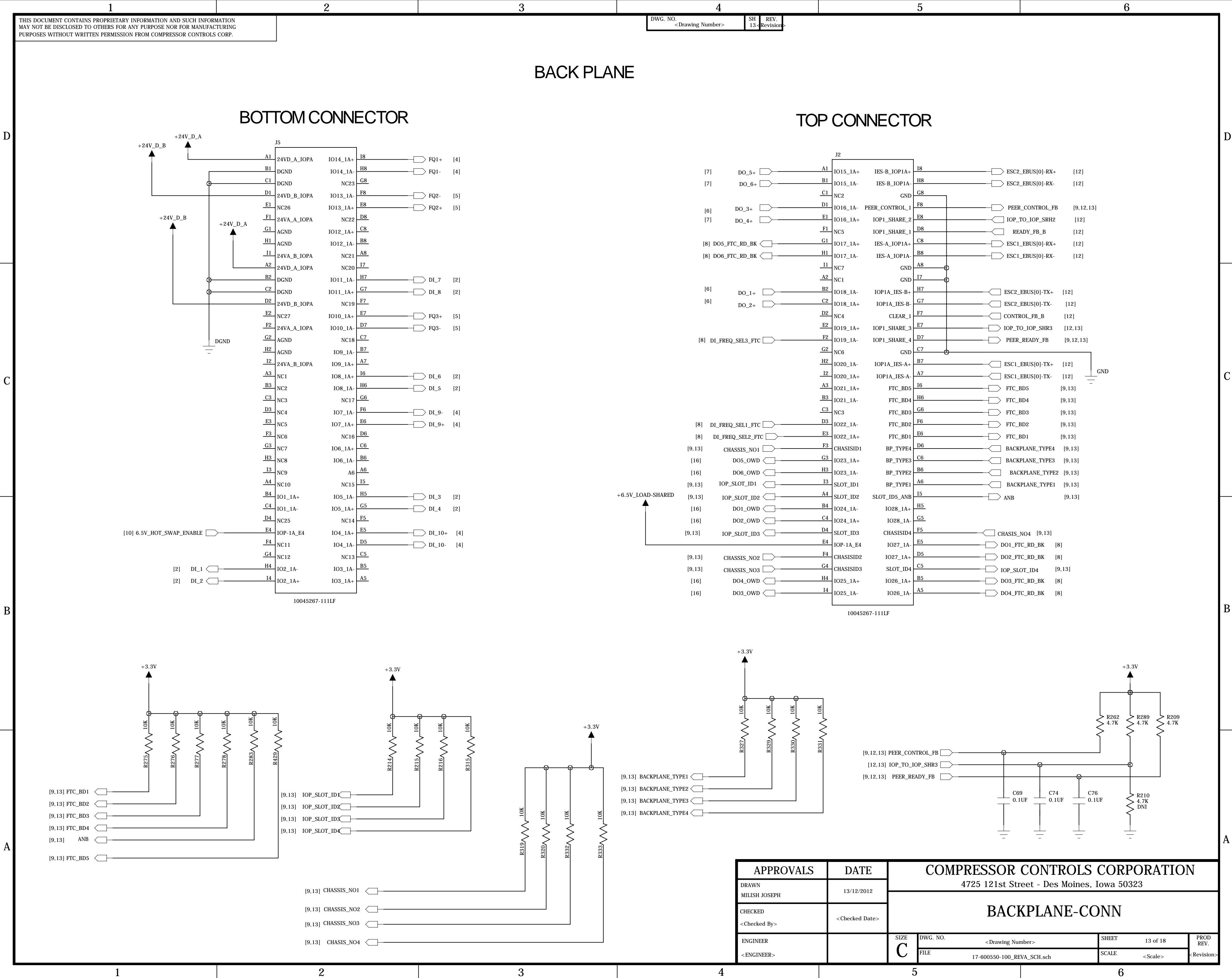
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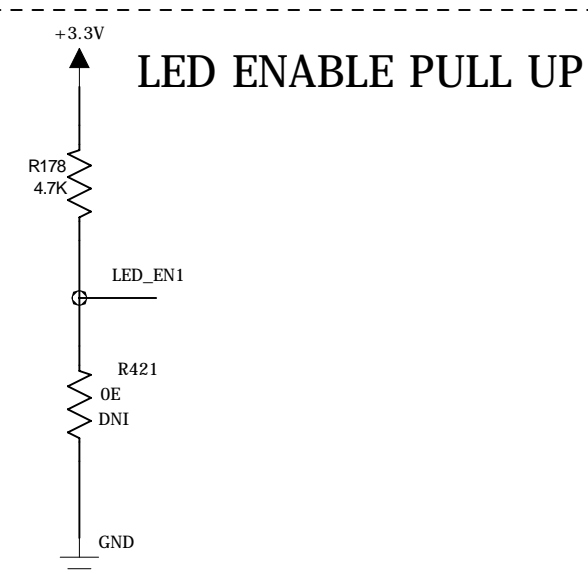
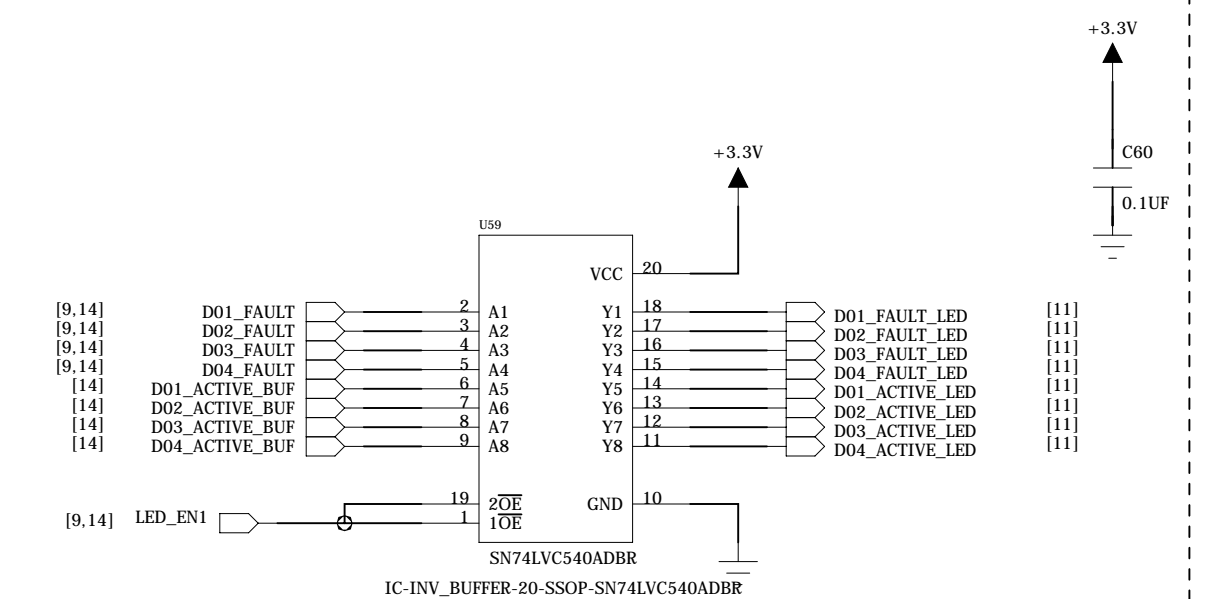
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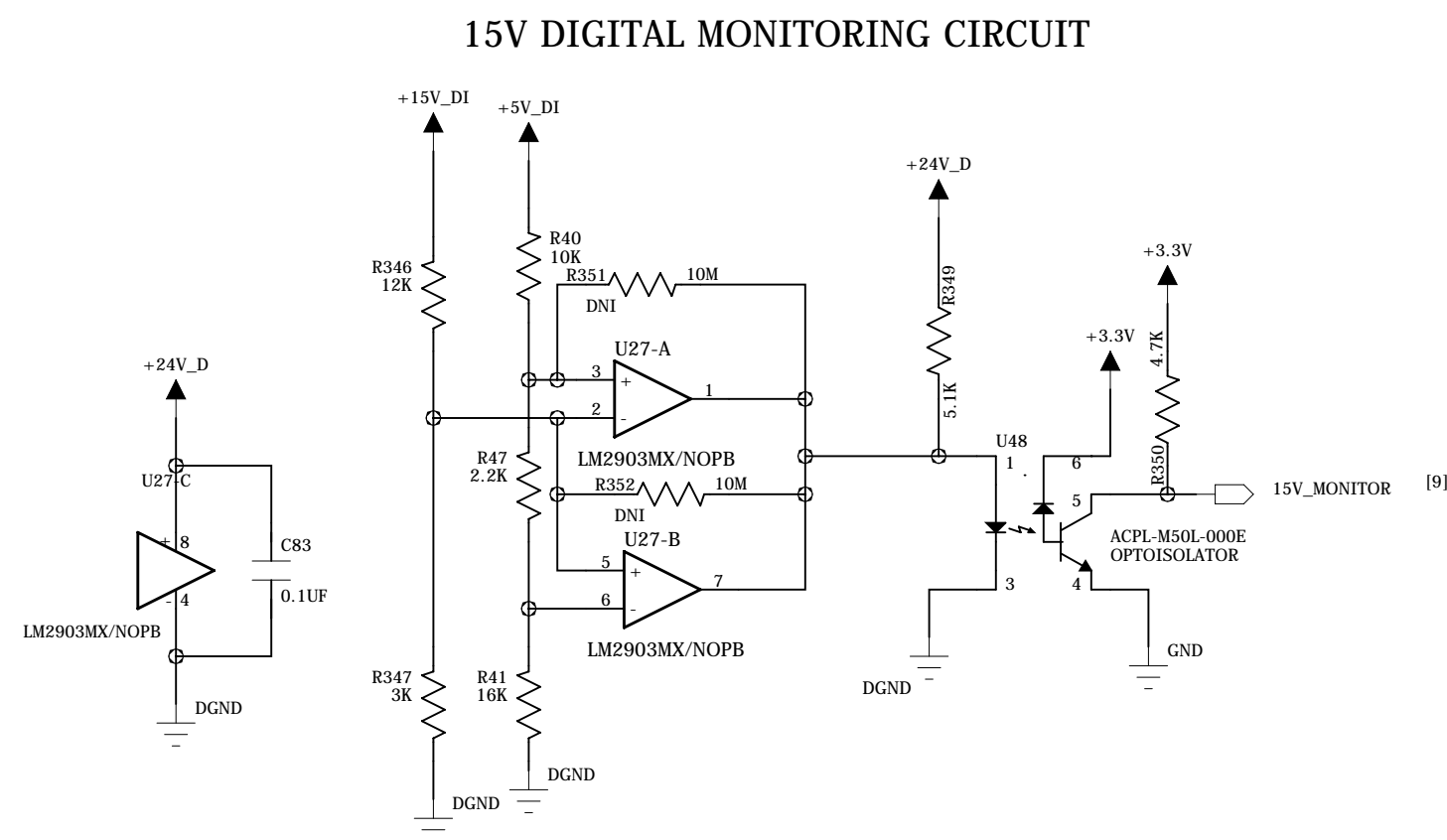
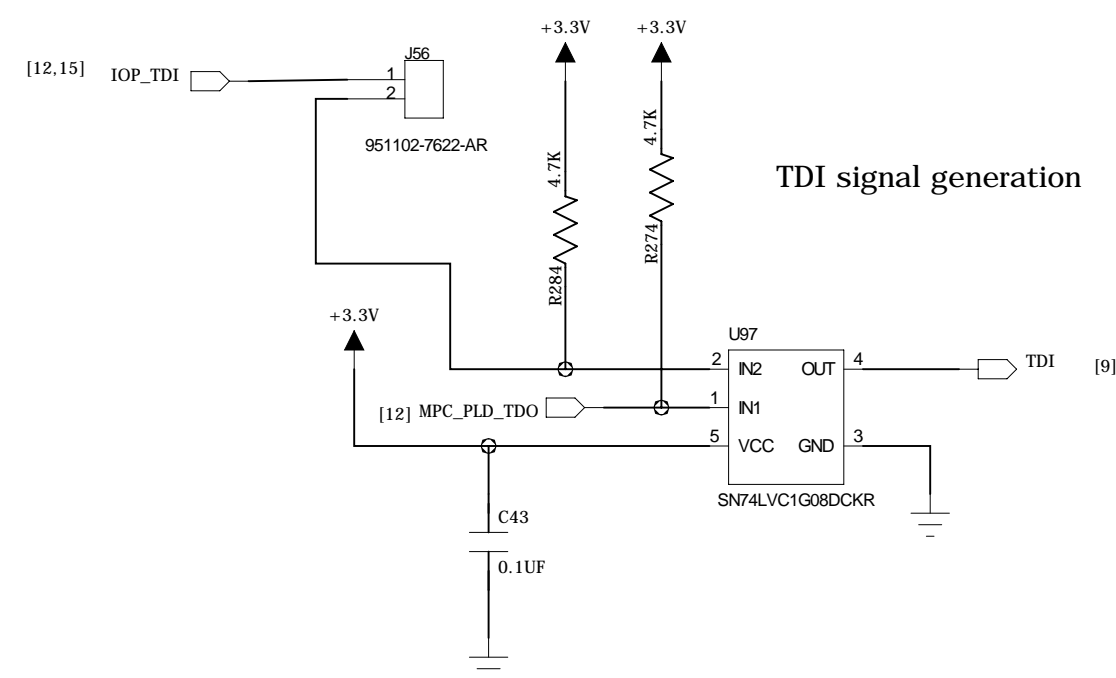
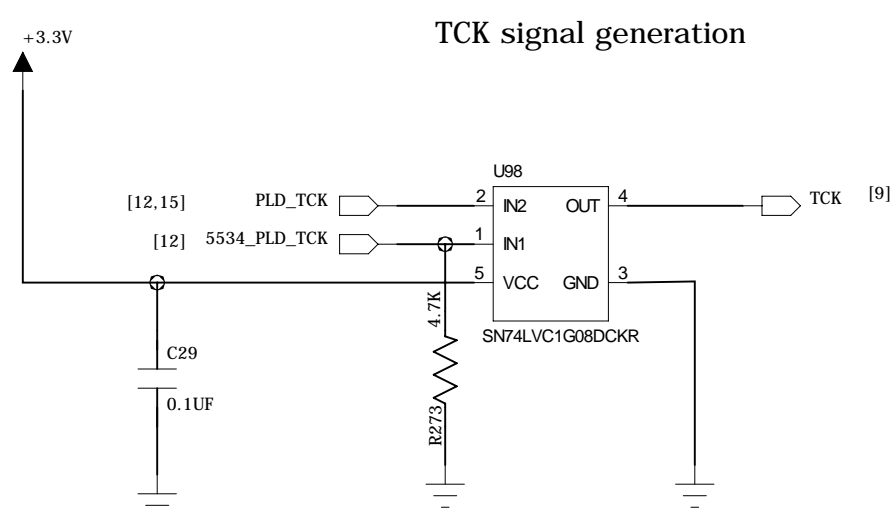
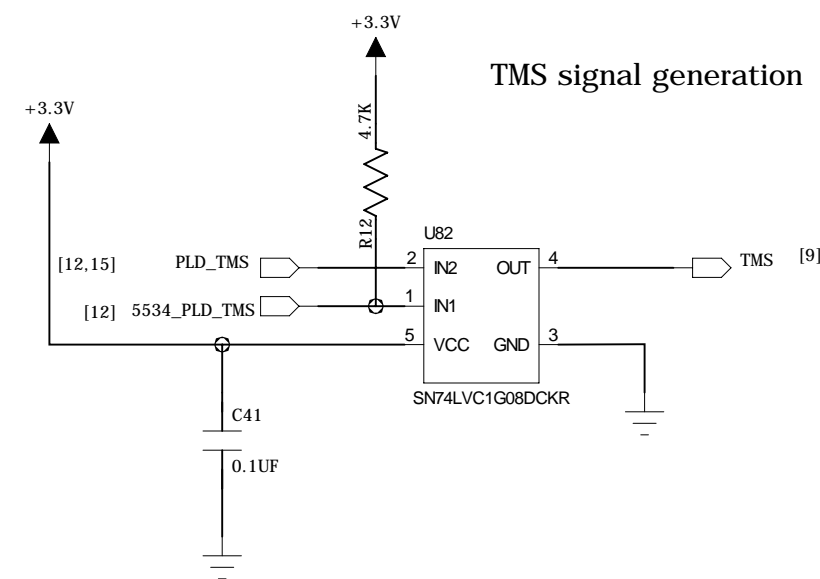
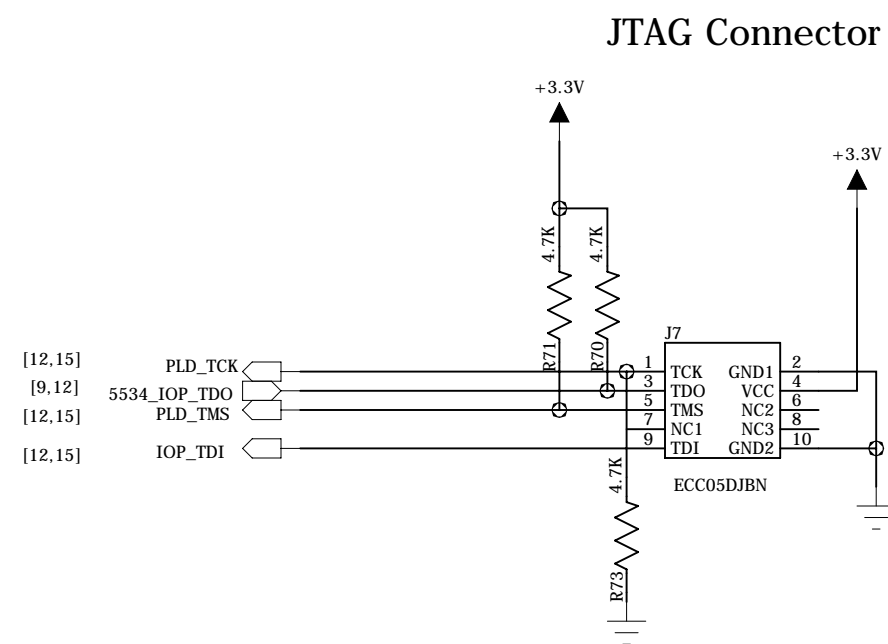


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SH	REV.
15	Revisions

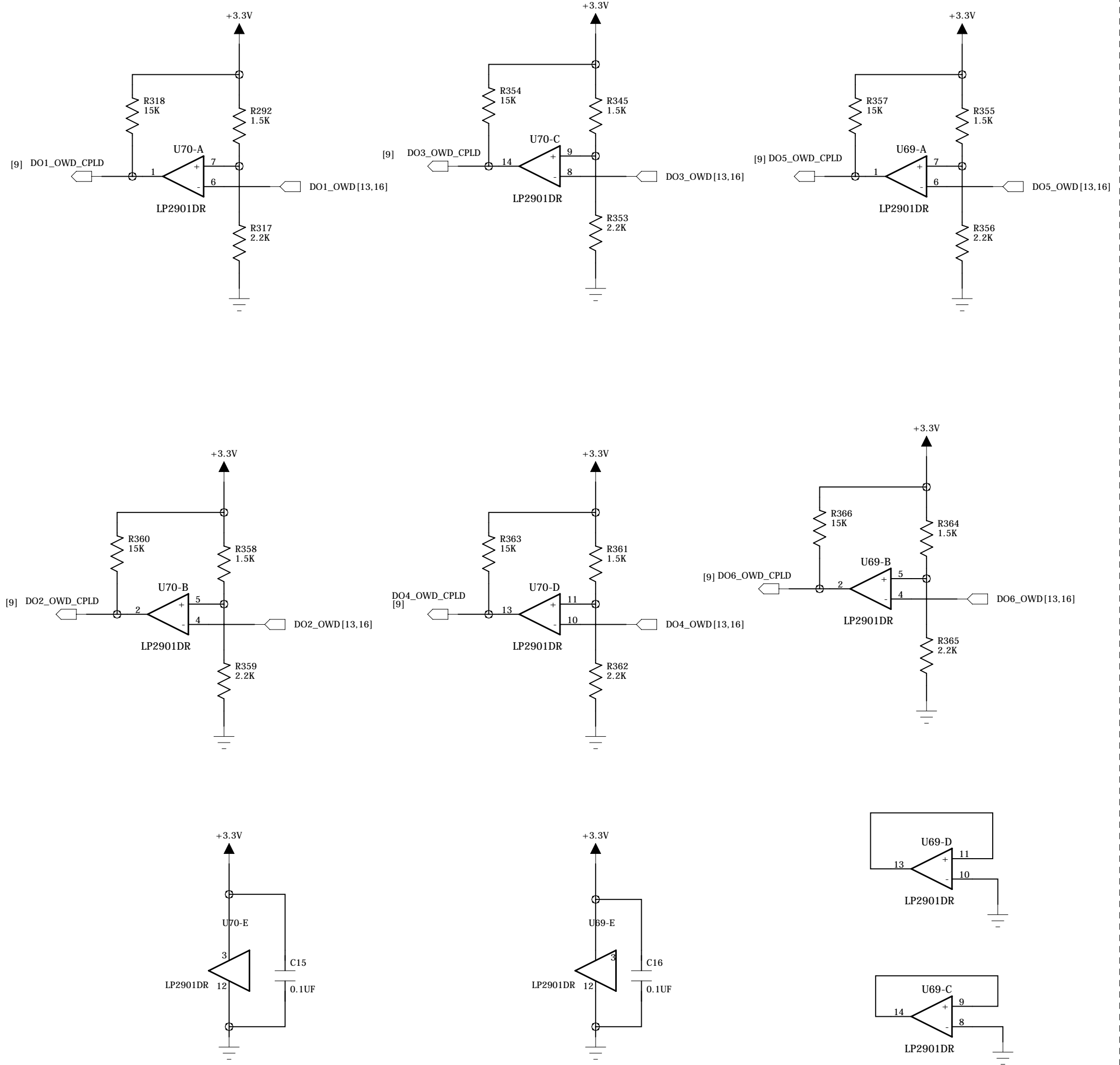


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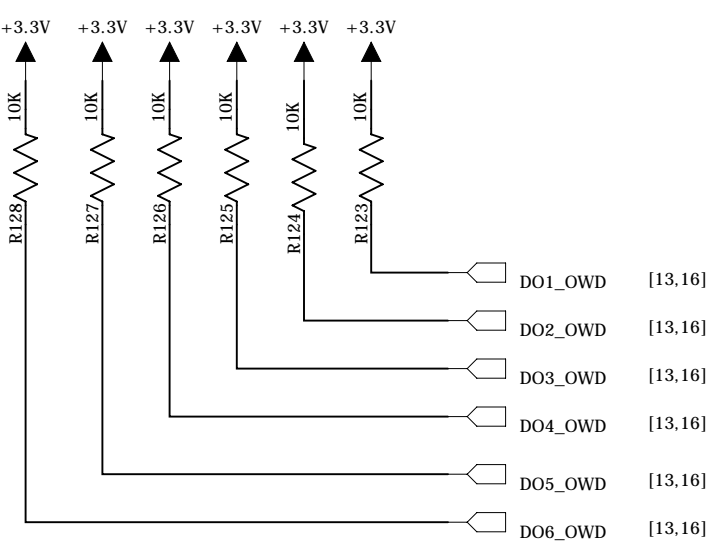
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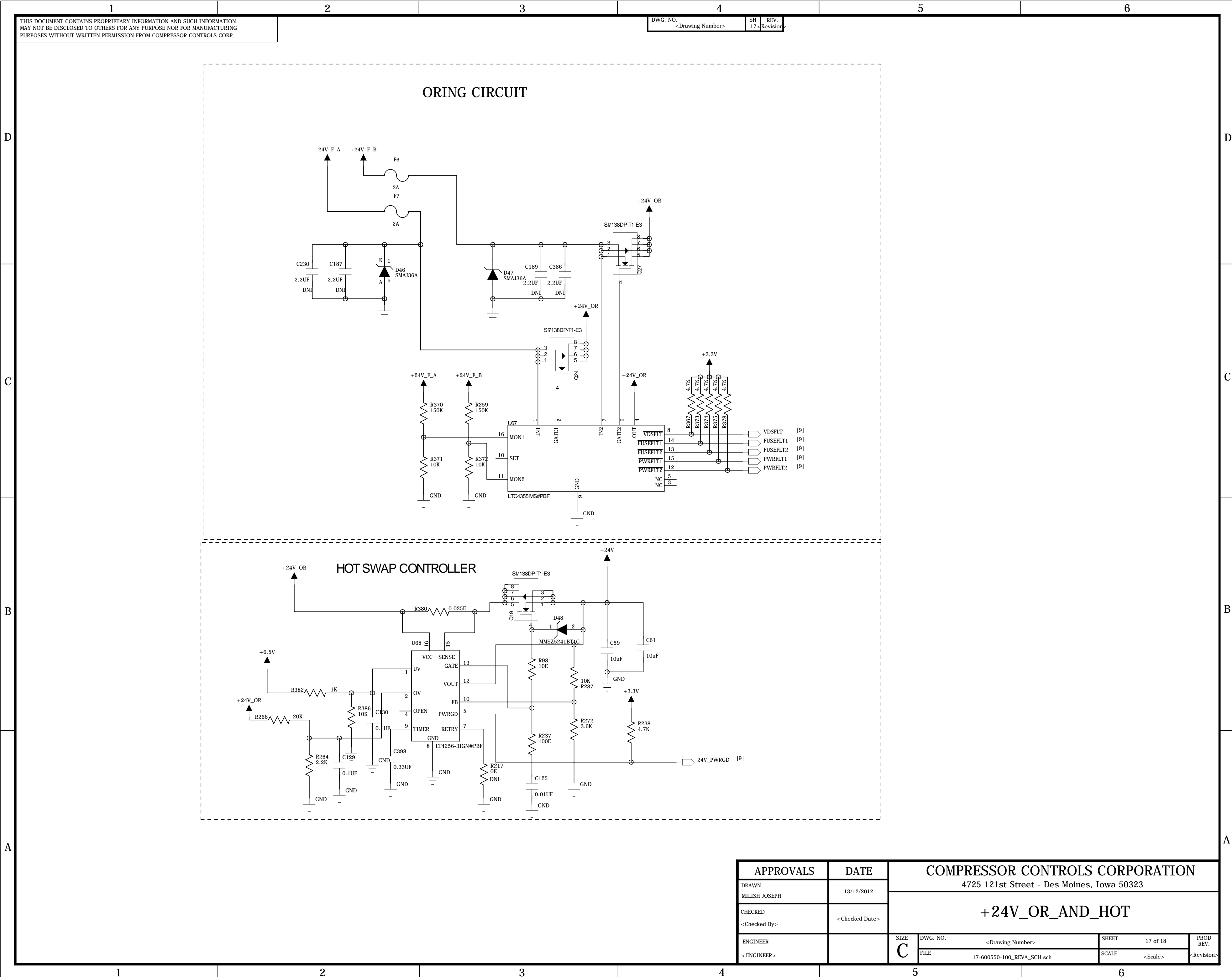
COMPARATORS _TTL LOGIC_OPEN WIRE



Open Wire Detect line Pull-ups



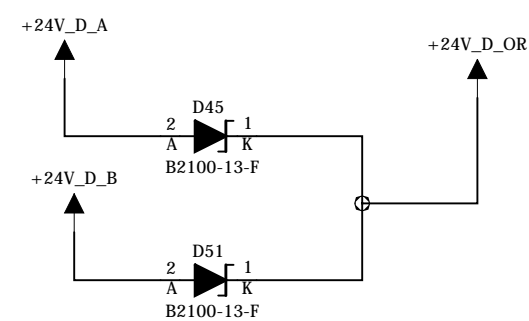
APPROVALS		DATE	COMPRESSOR CONTROLS CORPORATION			
DRAWN MILLISH JOSEPH		13/12/2012	4725 121st Street - Des Moines, Iowa 50323			
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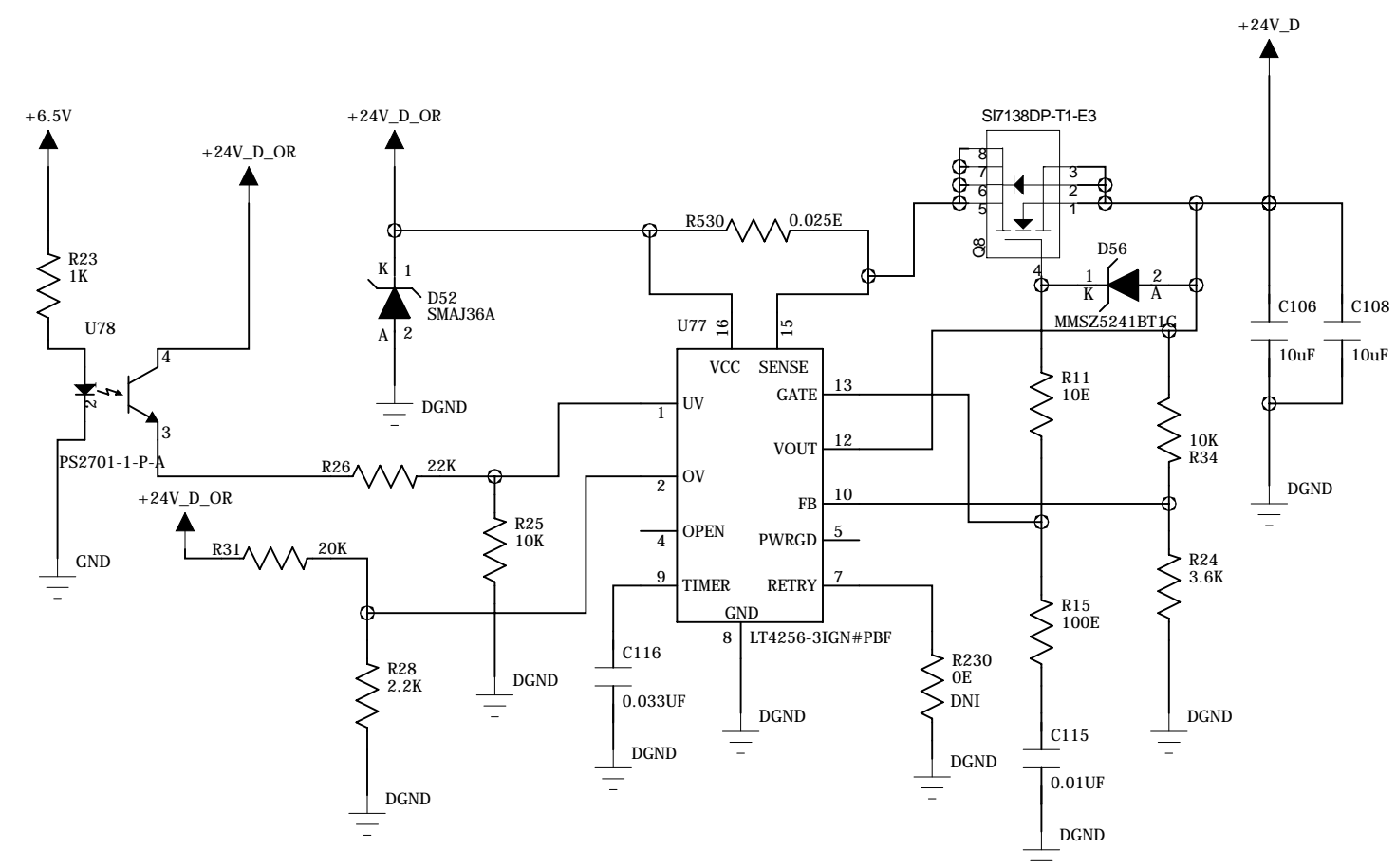
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PURPOSES WITHOUT WRITTEN PERMISSION FROM COMPRESSOR CONTROLS CORP.

DWG. NO.	SH	REV.
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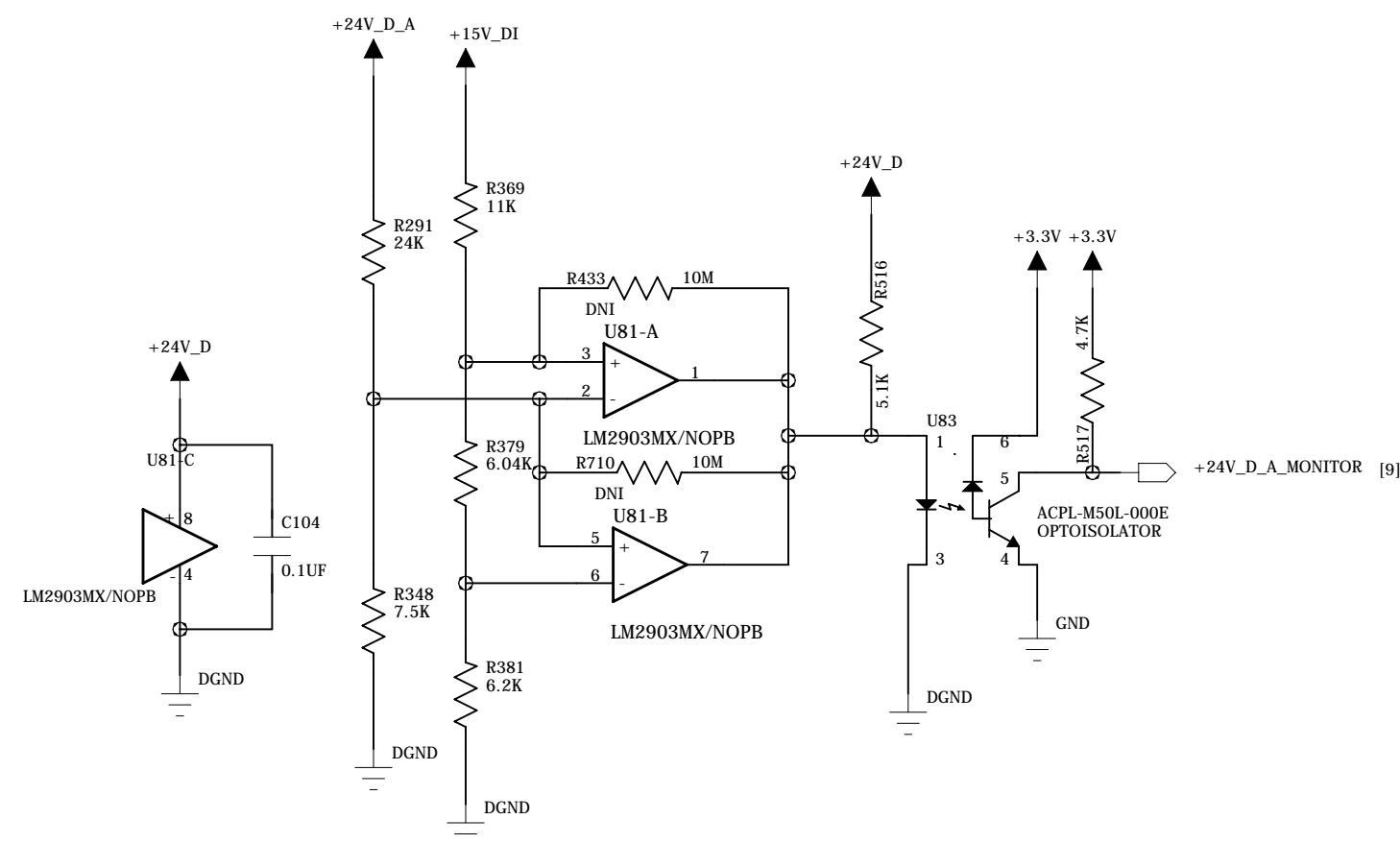
24V DIGITAL ORING



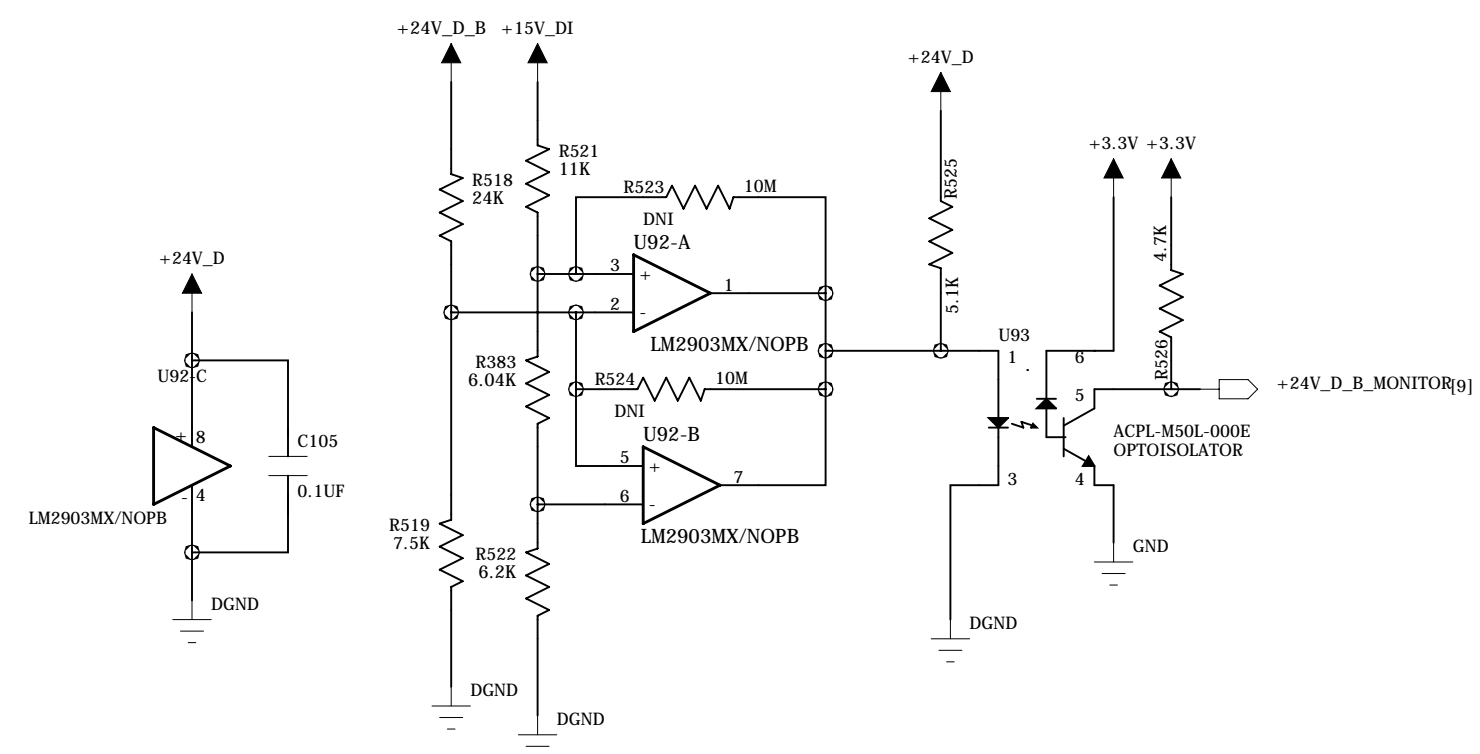
24V DIGITAL HOTSWAP



24V _ A DIAGNOSTIC CIRCUIT



24V _ B DIAGNOSTIC CIRCUIT



APPROVALS		DATE		COMPRESSOR CONTROLS CORPORATION			
DRAWN MILISH JOSEPH		13/12/2012		4725 121st Street - Des Moines, Iowa 50323			
CHECKED <Checked By>		<Checked Date>		+24V_DI_HOT			
ENGINEER <ENGINEER>		SIZE C	DWG. NO. <Drawing Number>		SHEET	18 of 18	
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