

**Buildup for Mindteck**  
**mind-07dec2k12-11799**

0.0350	copper plating layer 1		
0.0700	copper foil layer1	Plane	
0.2311	Prepreg 1-2		
0.0350	copper foil layer 2	Signal	6.5 mil traces 50 ohms single ended 5 mil traces8.95 mil seperation 100 ohms differential
0.1524	Core 2-3		
0.0350	copper foil layer 3	Plane	
0.1524	Prepreg 3-4		
0.0350	copper foil layer 4	Signal	8.5220 mil traces 50 ohms single ended 5.2535 mil traces 6.7465 mil seperation 100 ohms differential
0.3556	Core 4-5		
0.0350	copper foil layer 5	Plane	
0.5850	Prepreg 5-6		
0.0350	copper foil layer 6	Plane	
0.3556	core 6-7		
0.0350	copper foil layer 7	Signal	8.5220 mil traces 50 ohms single ended 5.2535 mil traces 6.7465 mil seperation 100 ohms differential
0.2492	prepreg 7-8		
0.0350	copper foil layer 8	Plane	
0.1524	core 8-9		
0.0350	copper foil layer 9	Signal	6.5 mil traces 50 ohms single ended 5 mil traces8.95 mil seperation 100 ohms differential
0.2311	prepreg 9-10		
0.0700	copper foil layer 10	plane	
0.0350	copper plating layer 8		
2.9548	total pcb thickness in mm		

Disclaimer: This stack-up has assumptions regarding the copper area per layer, as well as prepreg distance & dielectric constants. An impedance tolerance of +/- 10% can only be achieved if we adjust the trace width & prepreg height to meet the impedance requirement after receipt of the gerber files.