6 3 5 THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND SUCH INFORMATION MAY NOT BE DISCLOSED TO OTHERS FOR ANY PURPOSE NOR FOR MANUFACTURING PURPOSES WITHOUT WRITTEN PERMISSION FROM COMPRESSOR CONTROLS CORP. **REVISION HISTORY SHEETS** 1. MAIN **REV** DATE **AUTHOR CHANGES** BLOCK DIAGRAM 1 BLOCK DIAGRAM 2 17 JULY 2011 Akhilesh P1021\_DDR3 MEMORY Akhilesh R404 and net CLK\_CPLD\_66M added 20 JULY 2011 1.1 P1021\_FLASH INTERFACE U53 changed to 25MHz part U3 Address connections updated P1021\_SYSTEM CONTROL R405 and R406 added ETHERNET\_EEC GND connection from J6-C1 removed ETHERNET\_IES\_ECAT GIGABIT ETHERNET I2C\_SPI\_SD-CARD ET1100 USB\_SERIAL 13. P1021\_POWER POWER ON STRAPPING - 1 POWER ON STRAPPING - 2 CPLD\_HOT SWAP\_LED POWER SUPPLY BACKPLANE INTERFACE APPROVALS DATE COMPRESSOR CONTROLS CORPORATION 4725 121st Street - Des Moines, Iowa 50323 20-JULY-2011 AKHILESH JAISWAL CHECKED PROD REV. SHEET 18-600400-001 SCALE 1.1 APP CARD SCHEMATIC 4



































