```
//Rev. A - 12/07/00 - First "Official" Release
//Rev. B - 05/31/01 - Add Voltage Parameter
//Rev. C - 05/31/01 - Supposed to be change to correct Lattic pin D10 bug.  Fix actually
makes no
            difference so file is the same as Rev. B

DESIGN addrbuf;

PART ispGDX80VA-5T100I;

PARAM VOLTAGE VCC;

SET ProcAddr [ProcAddr8..ProcAddr31];
SET Buf1Addr [Buf1Addr8..Buf1Addr31];
SET Buf2Addr [Buf2Addr8..Buf2Addr31];

INPUT  ProcAddr {A0..A11, B0..B11};
INPUT  nHreset  {A17};

OUTPUT Buf1Addr {C0..C19, A12..A15};
OUTPUT Buf2Addr {D0..D19, B12..B15};



BEGIN

    Buf1Addr.oe = nHreset;
    Buf1Addr.m0 = ProcAddr;

    Buf2Addr.oe = nHreset;
    Buf2Addr.m0 = ProcAddr;

END
```