

ASSIGNMENT-1

CIRCUIT

MAJORITY VOTING LOGIC CIRCUIT

INTRODUCTION:

Write down the functionality, block diagram, truth table, and application of this circuit.

CIRCUIT DIAGRAM:

Draw the gate-level architecture of the circuit.

VERILOG PROGRAM:

Write down the Verilog program of the above circuit.

REFERENCE:

NOTES:

- *Each figure should contain a caption (IEEE format) with proper numbering below the figure (if any).*
- *Image resolution should be greater than 300 dpi (if any).*
- *Each table should contain a caption (IEEE format) with proper numbering above the table (if any).*
- *Use MS Word / LaTeX as word processor.*
- *Assignment should be submitted in PDF format (File name: Dept.Name_Student ID Number)*

SUBMISSION DEADLINE:

1/2/23 (D/M/Y)