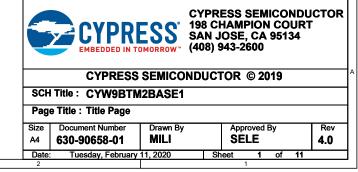
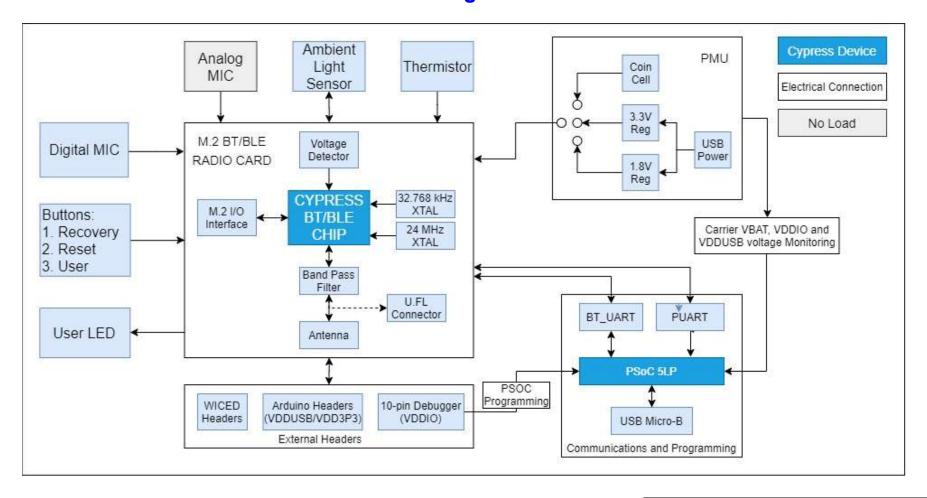
CYW9BTM2BASE1 CYPRESS M.2 BT/BLE BASEBOARD

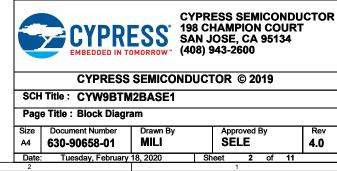
| CONTENTS | | | |
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| 09 | BT USB Connector, WICED Headers and Accessories | | |
| 10 | GPIOs Mapping and Configuration | | |
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| Drawing Numbers | | |
|-----------------|--------------|--|
| PCBA | 121-90658-01 | |
| PCB | 600-90658-01 | |
| FAB DRW | 610-90658-01 | |
| ASSY DRW | 620-90658-01 | |
| SCH DRW | 630-90658-01 | |

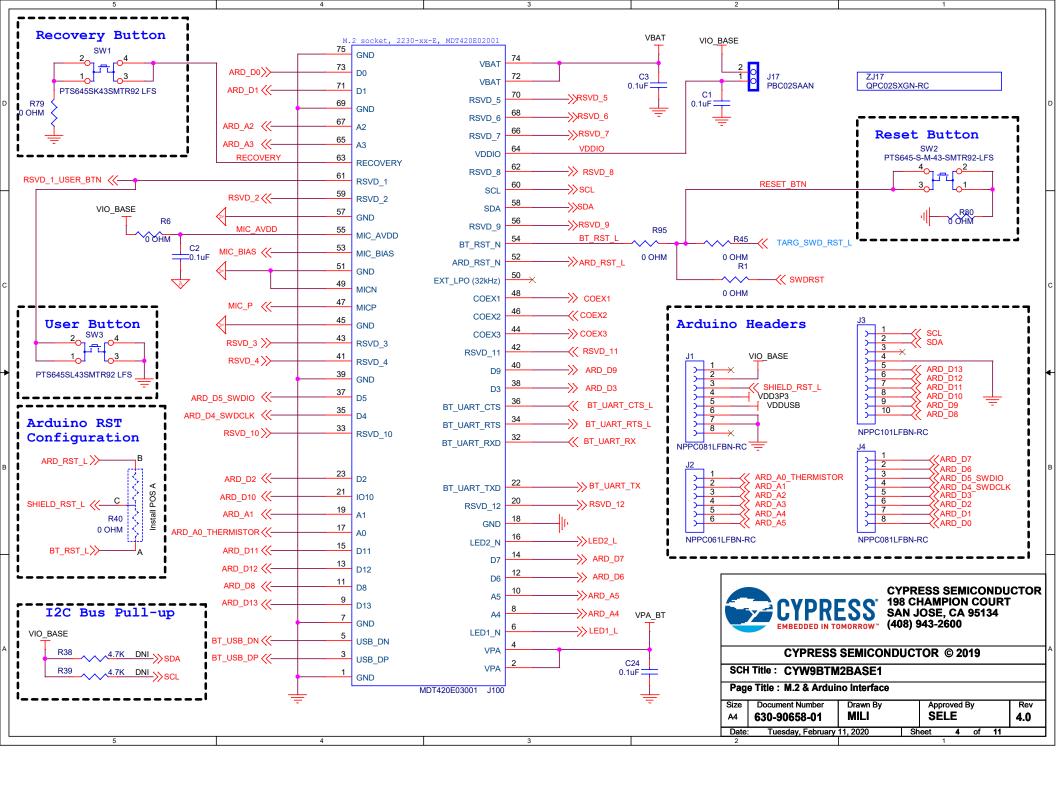


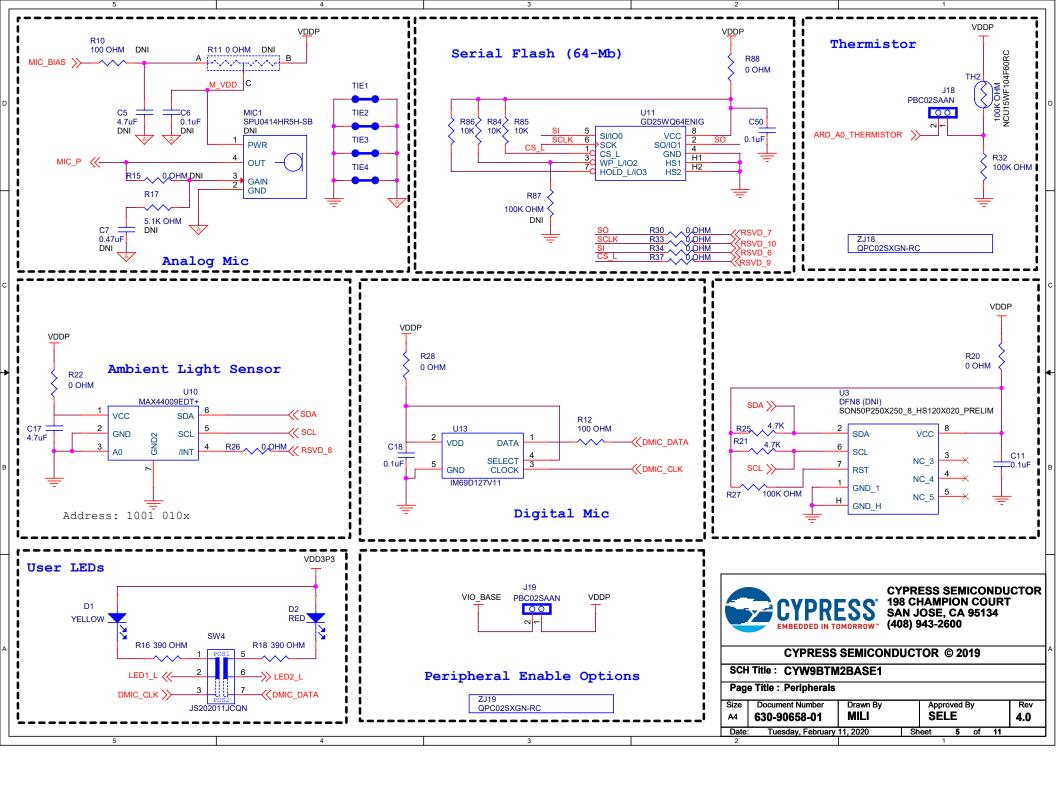
Block Diagram

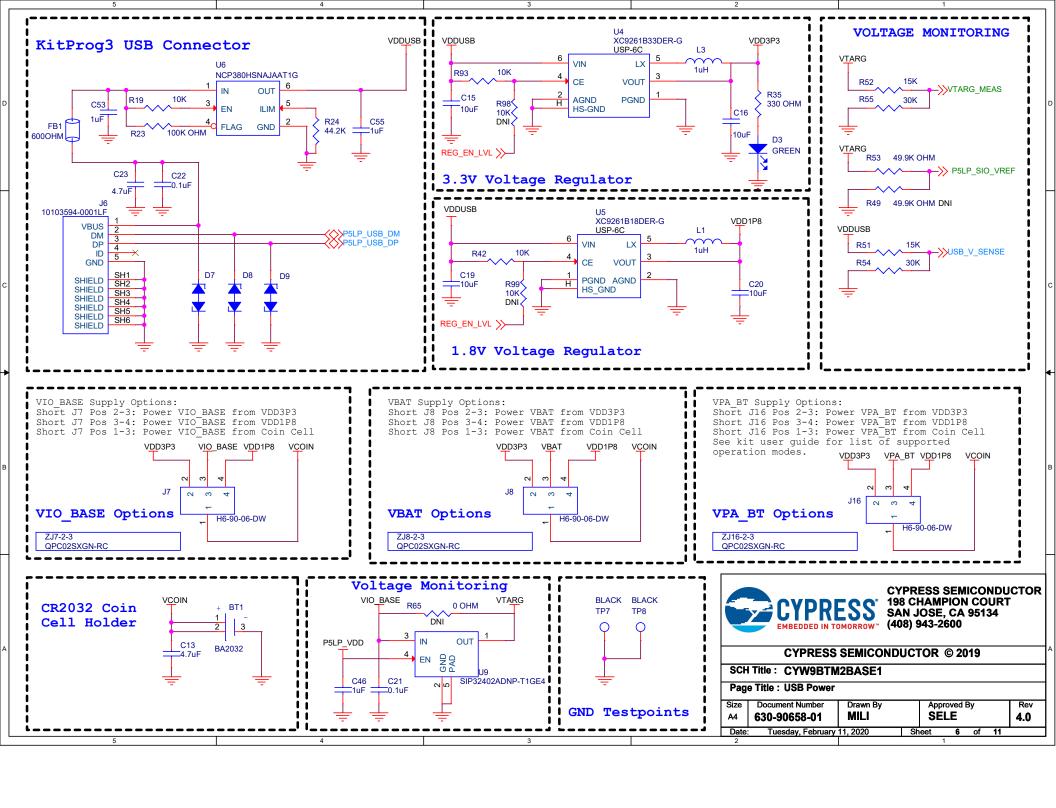


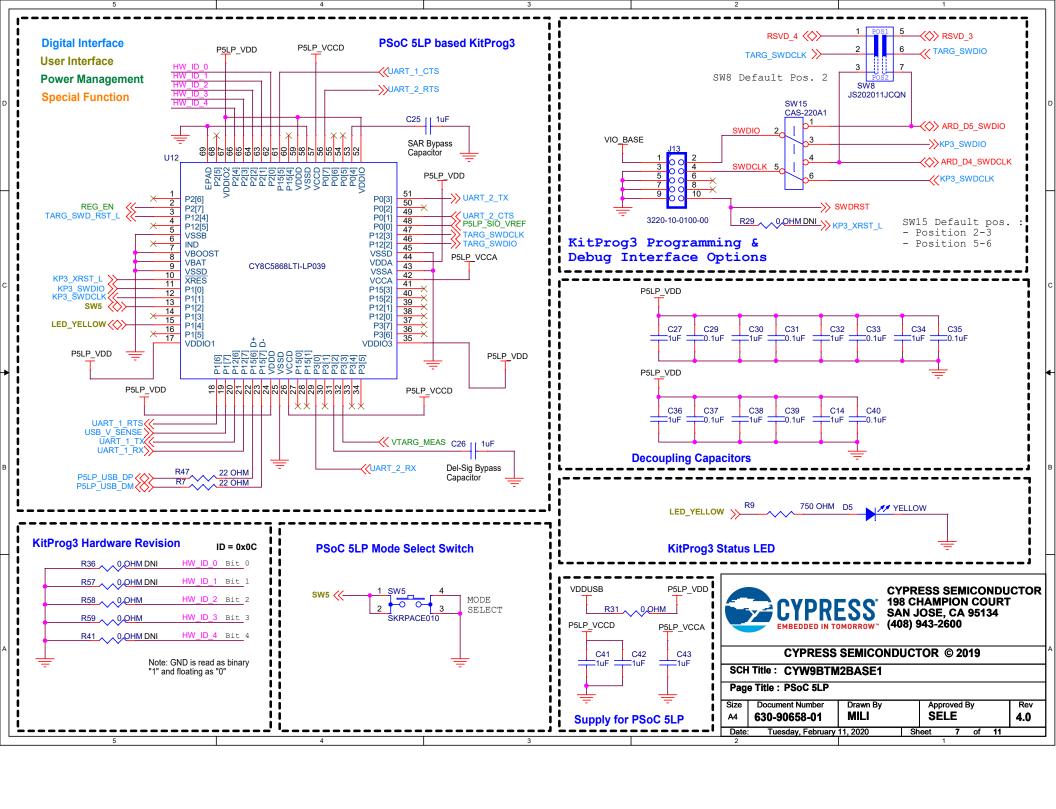


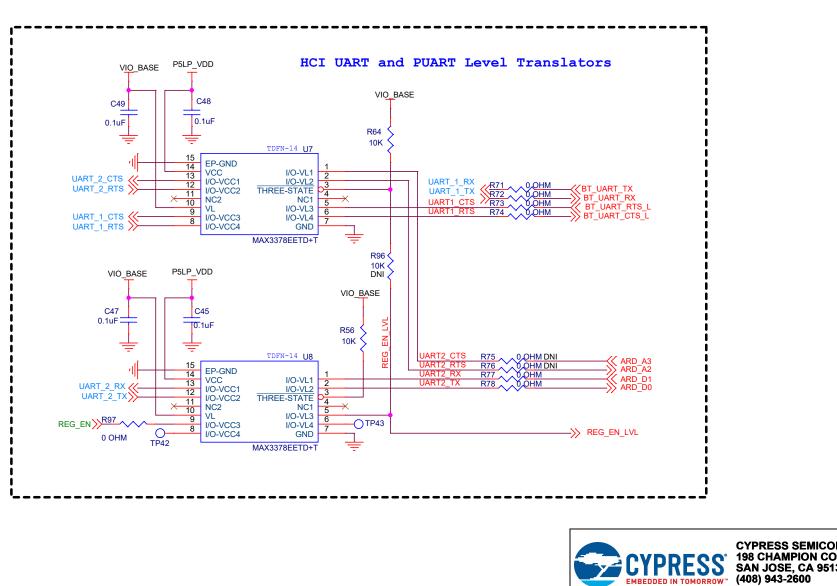
Power Management Unit Block Diagram VDDUSB ARDUINO PSoC 5LP VDD3P3 **HEADERS** VIO BASE Peripherals USB · Analog Mic 3.3V Reg Micro-B VDDP. · Digital MIC Serial Flash · Ambient Light sensor Thermistor 1.8V Reg VDDIO/ VBAT/ VPA BT M.2 Card Coin Cell CYPRESS SEMICONDUCTOR 198 CHAMPION COURT SAN JOSE, CA 95134 (408) 943-2600 **CYPRESS SEMICONDUCTOR © 2019** SCH Title: CYW9BTM2BASE1 Page Title: PMU Block Diagram Document Number Drawn By Approved By SELE MILI 630-90658-01 4.0 Tuesday, February 18, 2020

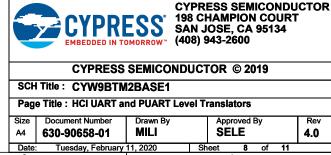


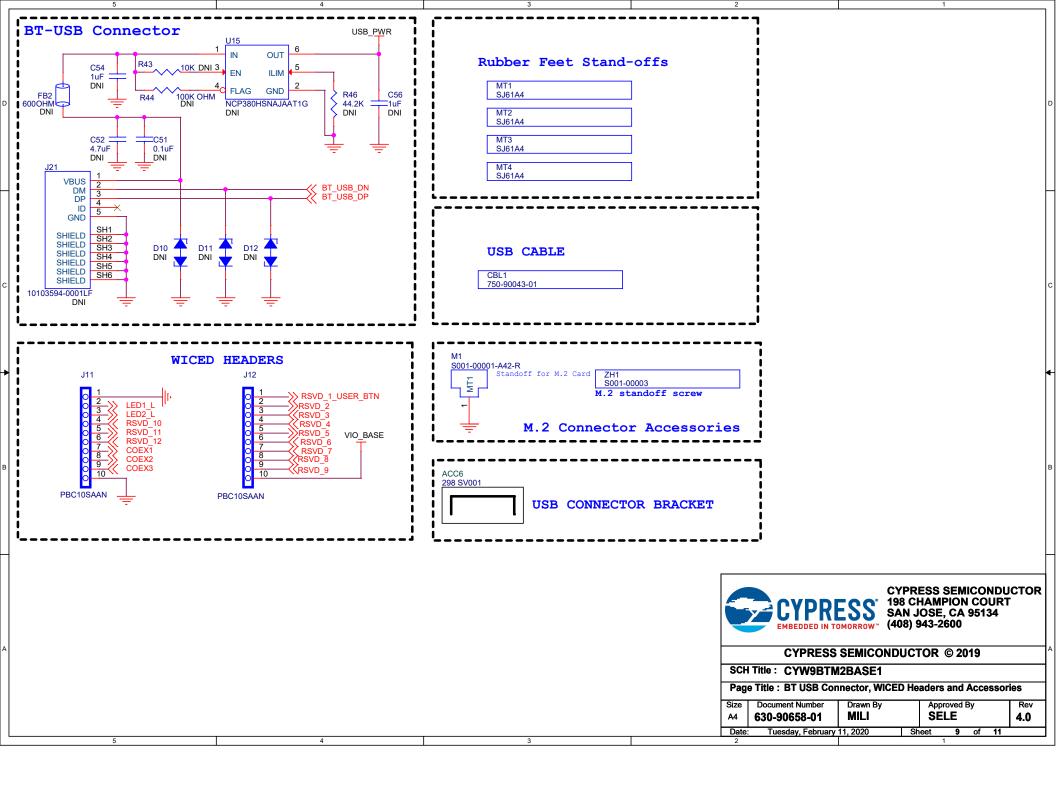












| Arduino I/F: | 208xx GPIOs: | Shared Functions: |
|--------------|--------------|--------------------------|
| | 200XX GPIOS. | |
| A0 | Pxx | Thermistor ADC |
| A1 | Pxx | |
| A2 | Pxx | PUART_CTS_L |
| A3 | Pxx | PUART_RTS_L |
| A4 | Pxx | |
| A5 | Pxx | |
| D0 | Pxx | PUART_RX |
| D1 | Pxx | PUART_TX |
| D2 | Pxx | |
| D3 | Pxx | |
| D4 | Pxx | SWDCLK |
| D5 | Pxx | SWDIO |
| D6 | Pxx | |
| D7 | Pxx | |
| D8 | Pxx | |
| D9 | Pxx | |
| D10 | Pxx | |
| D11 | Pxx | |
| D12 | Pxx | |
| D13 | Pxx | |
| SDA | Pxx | |
| SCL | Pxx | |
| | | |
| Peripherals: | 208xx GPIOs: | |
| LED1 | Pxx | DMIC CLK |
| LED2 | Pxx | DMIC DATA |
| RSVD 1 | Pxx | USER BUTTON |
| RSVD 2 | Pxx | |
| RSVD 3 | Pxx | SWDIO ALT |
| RSVD_4 | Pxx | SWDCK_ALT |
| RSVD_5 | Pxx | |
| RSVD_6 | Pxx | SF_MOSI |
| RSVD_7 | Pxx | SF_MISO |
| RSVD_8 | Pxx | AMBIENT LIGHT SENSOR INT |
| RSVD_9 | Pxx | SF_CS_L |
| RSVD_10 | Pxx | SF_SCLK |

| Peripherals: | 208xx GPIOs | : Shared Functions: |
|--------------|-------------|---------------------|
| RSVD_11 | Pxx | |
| RSVD_12 | Pxx | |



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Page Title: GPIOs Mapping and Configuration

Approved By SELE Drawn By Document Number

MILI 630-90658-01 Tuesday, February 11, 2020

| REVISION HISTORY | | | | | |
|------------------|--|--------------------|------------|--|--|
| REV | DESCRIPTION OF CHANGE | Orig. of Change | DATE | | |
| 1.0 | Initial Release | MILI | 2019/03/06 | | |
| 1.1 | DNI R96, Install 10K Ohm at R64 | MILI | 2019/04/18 | | |
| 2.0 | Schematic updated to Cypress template. Removed PSoC5 unused testpoints and SW14. Removed PSoC5 custom app button and LED. Motion sensor replaced by ambient light sensor. | MILI | 2019/05/30 | | |
| 3.0 | R49 changed to DNI. Removed R66, R67, R68 and R69. SW4 and SW8 changed to slide switch. Arduino Digital Pins changed to "Dx" naming convention Corrected PUART_RX to D0 and PUART_TX to D1 | MILI | 2019/07/16 | | |
| 4.0 | Added digital MIC, U13 Serial flash changed to 64Mbit | MILI | 2020/01/20 | | |
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SCH Title: CYW9BTM2BASE1

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