

REVISION HISTORY		
Rev	DESCRIPTION	DATE
1.0	Initial Release	02/20/2020
1.1	Updated MIC1 and MIC2 to Merry 88A301006001	11/30/2020

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PAGE	DESCRIPTION
01	Cover Page
02	Block Diagram
03	CS47L35 CODEC
04	MICs & Audio Connectors
05	Shield Headers & Buttons
06	Power
07	USB

Drawing Numbers	
PCBA	121-90698-01
PCB	600-90698-01
FAB DRW	610-90698-01
ASSY DRW	620-90698-01
SCH DRW	630-90698-01



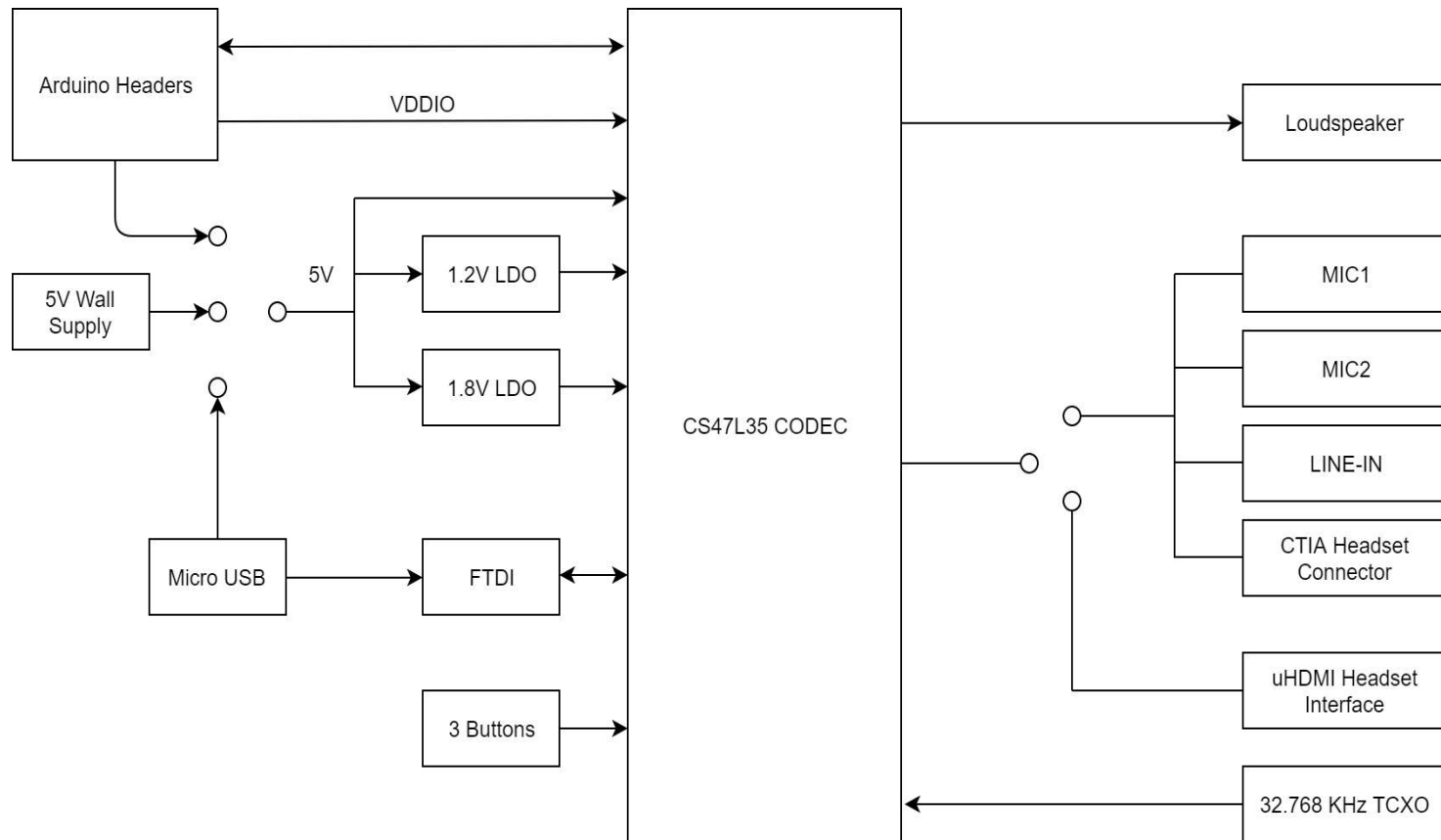
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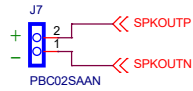
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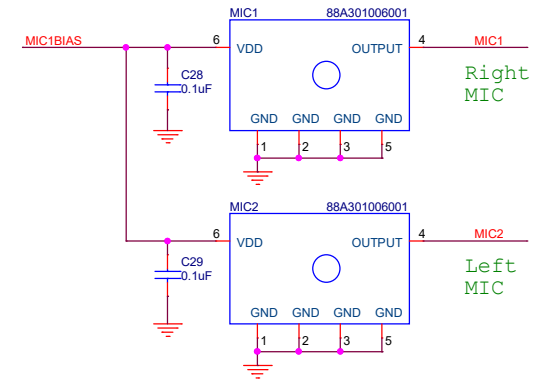
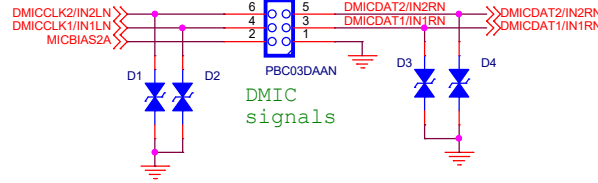
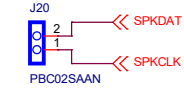
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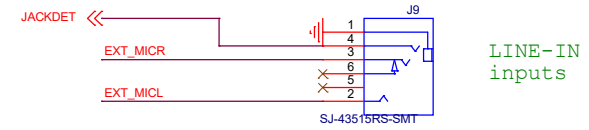
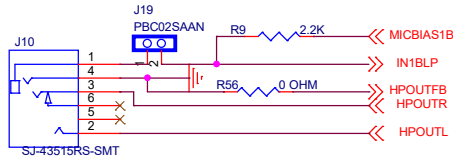
Speaker output



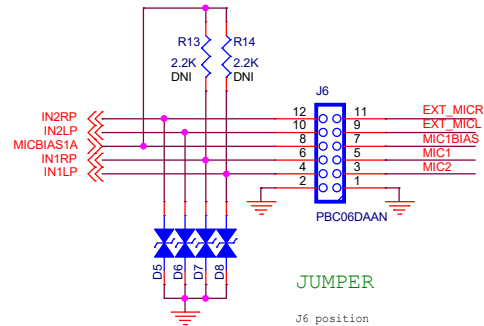
Digital Speaker output



CTIA Headset Connector



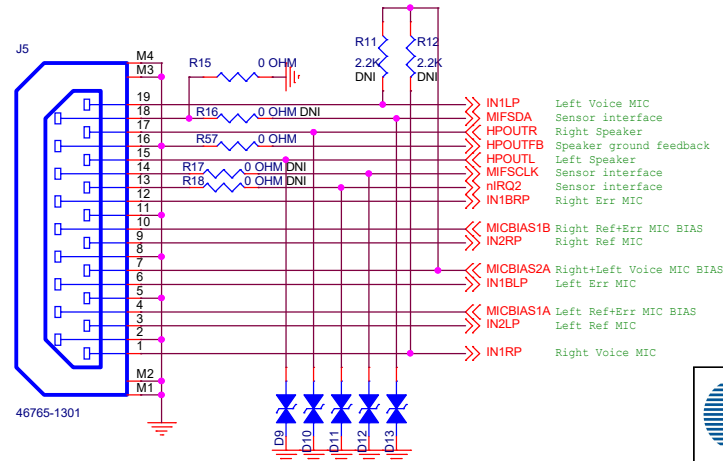
uHDMI HEADSET INTERFACE




JUMPER

J6 position		
Short 7-8	microphones	Do not
Short 5-6	selected	connect the
Short 3-4	(MIC1 + MIC2)	uHDMI headset
Short 11-12	Line-in	Do not
Short 9-10	selected	connect the
	(J9 jack)	uHDMI headset
Open ALL	Line-in and	uHDMI Headset
	microphones	microphones
	not selected	selected

ZJ6-3-4	SNT-100-BK-G
ZJ6-5-6	SNT-100-BK-G
ZJ6-7-8	SNT-100-BK-G
ZJ19-1-2	SNT-100-BK-G





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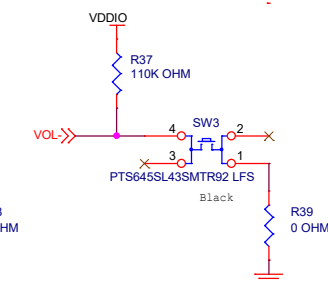
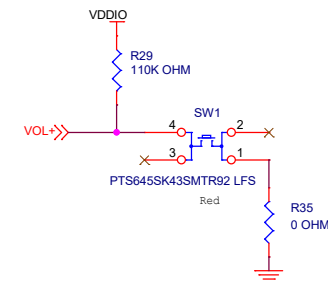
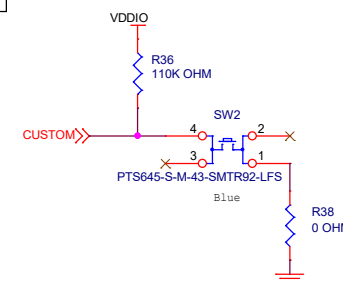
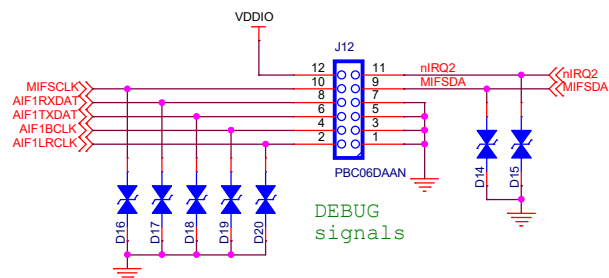
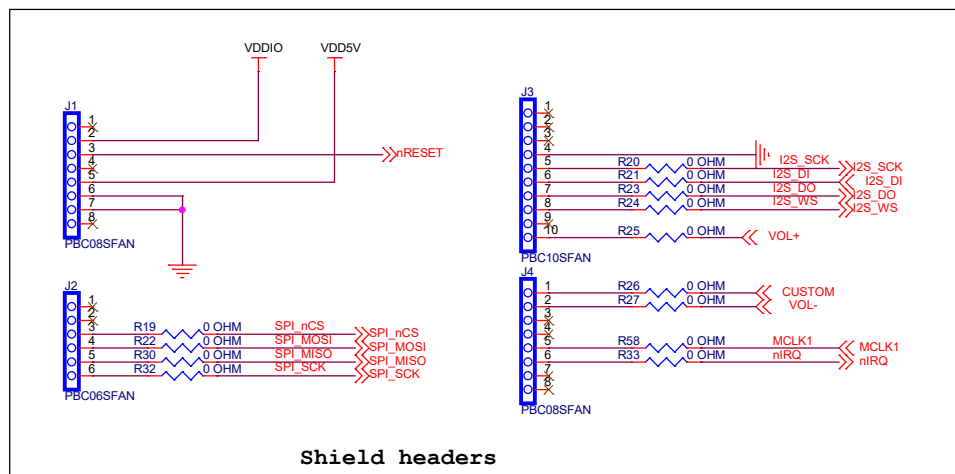
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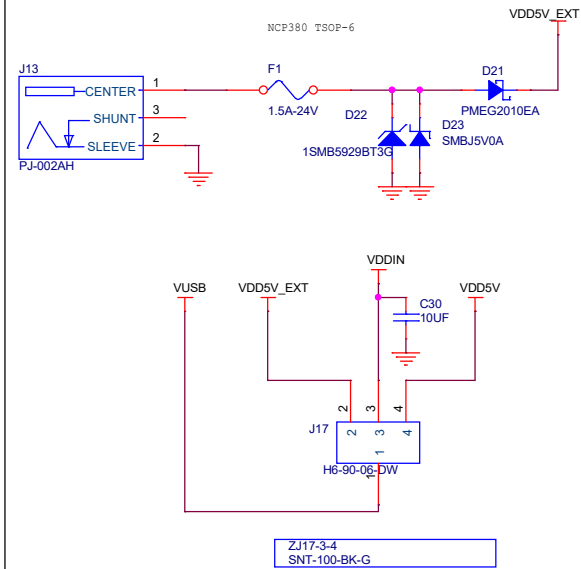
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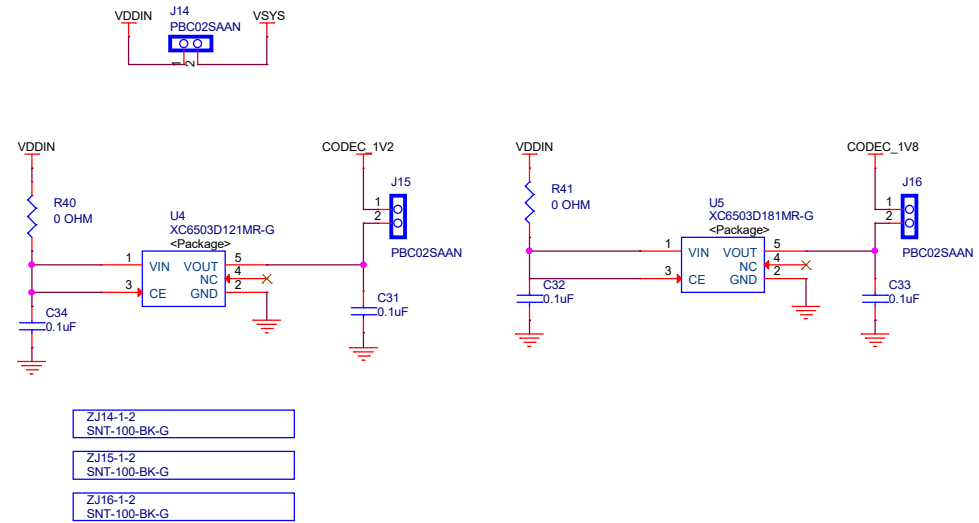
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POWER SOURCE



VOLTAGE REGULATORS



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MicroAB USB connector for
DSP tuning

Micro USB
connector

USB to I2C/SPI
(COMn)

USB to UART
(COMn+1)



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