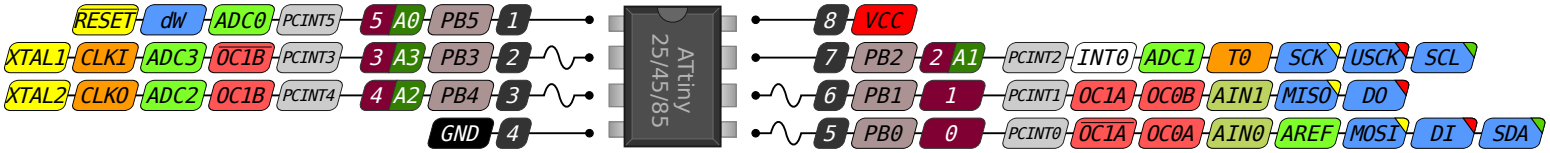
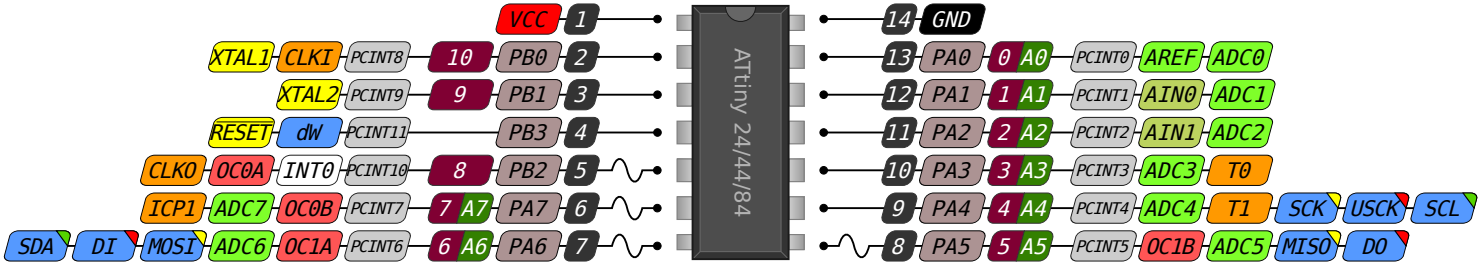
ATTINY - PINOUT GUIDE

- 10 Physical Pin
- PA0 Port Pin
- 10 A1 IDE Pin
- VCC Power
- GND Ground
- PCINT10 Pin Change Interrupt
- INT0 External Interrupt
- PWM-capable Pin
- OC0A Timer Output (PWM)
- AIN1 Analog Comparator
- ADC1 ADC Channel
- CLKO Clock or Timing Pin
- MISO Serial I/O
  - USCK USI
  - MISO SPI
  - SDA I²C
- RESET Control



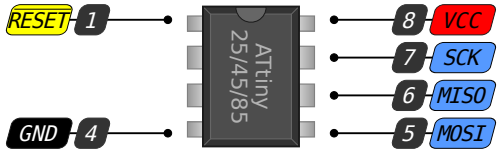


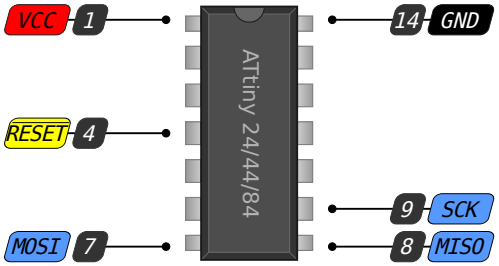
Absolute max 60mA for entire chip

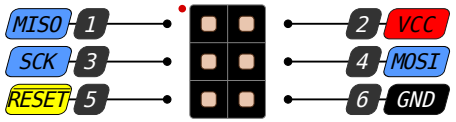
Absolute max 40mA per pin, recommended 10mA @ 5V, 5mA @ 3V

Vcc may range from 1,8V to 5,5V. The maximum clock frequency decreases with lower voltages. For the internal 8 MHz oscillator, the minimum voltage is 2,7V.

In-System Programming







When using In-System Programming, the pins for SPI communication should not be driven by connected hardware. It is advisable add a resistor between the pin and the attached device, if possible. The ISP then uses a direct connection to the pin to program the µC.

16. Dec 2020
ver 1 rev 0