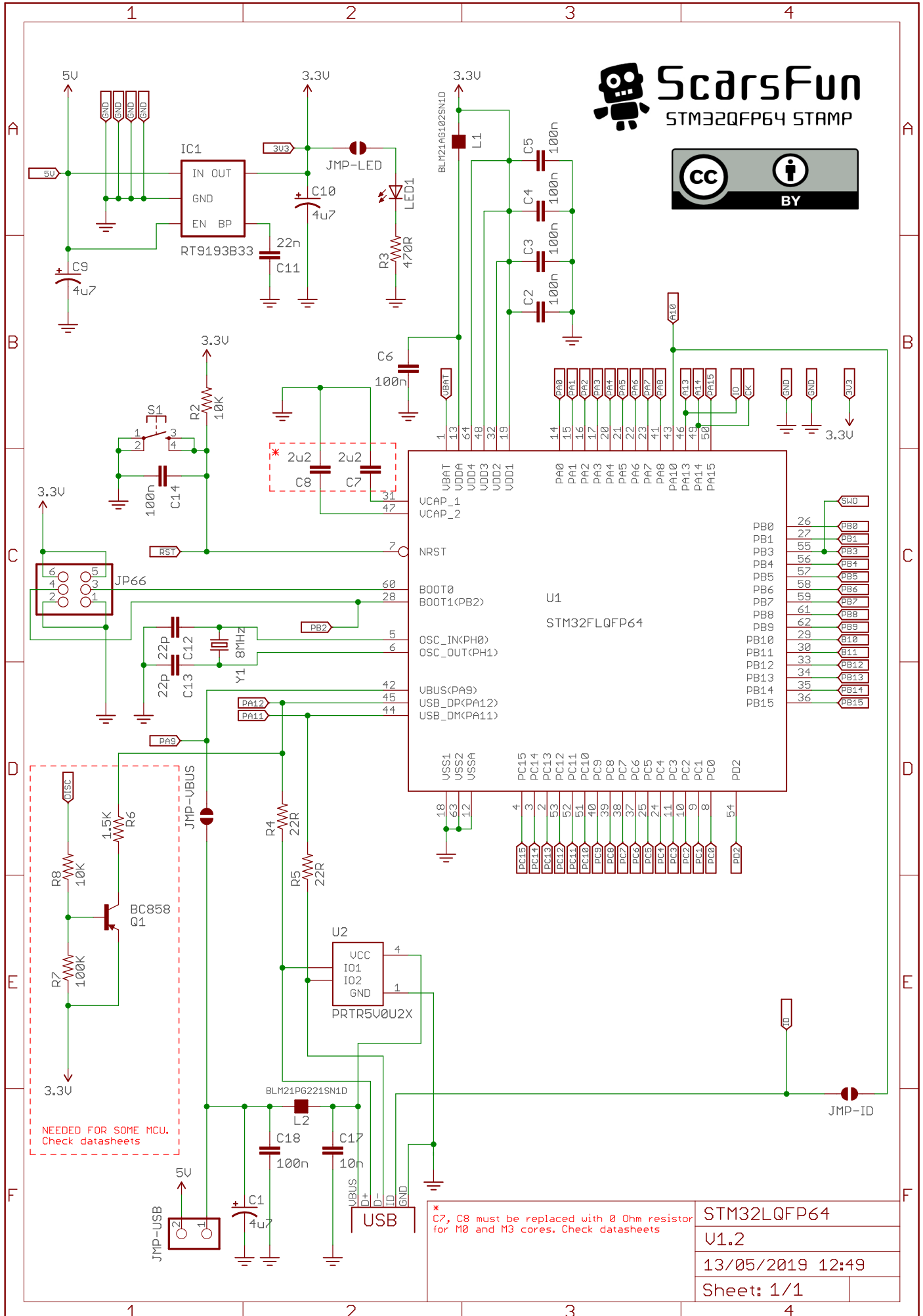




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STM32QFP64 STAMP



* C7, C8 must be replaced with 0.0hm resistor for M0 and M3 cores. Check datasheets

STM32LQFP64

V1.2

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