Experiment noig

Wane: Binary parallel adder, substractor.

Aims: Implement a circuit using a 4-bit binory addy (1C=1483) inquer Added -sustractor.

Componers: Bread board | Eit, & 7483

Theory:

Parallel adder. IC type 78483 15 a 4-bit binary Parallel adder. the 2 4-bit Input binary numbers are A1 through A4 and B1 through B4. The 4bit Sum 15 obtained from S1 through S4. Co 13 the Input carry and C4 15 the output carry.

Adder substractor: two binary numbers can be Substracted by taking the 2's complined to the substracted and adding. It to the minuted. The 2's complement can be obtained by taking the 1's component and adding I to perform A-B, are component the for beit of B, add them to the 4-bit of A and add 1 through the input Carry.

Procedure:

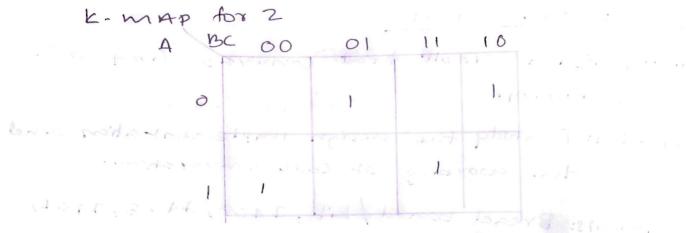
2.

3.

connections one made as the te circuit diagrams switch on the poner supply

apploy different combinations of inputs and observe te output compre te output s aith te truth table

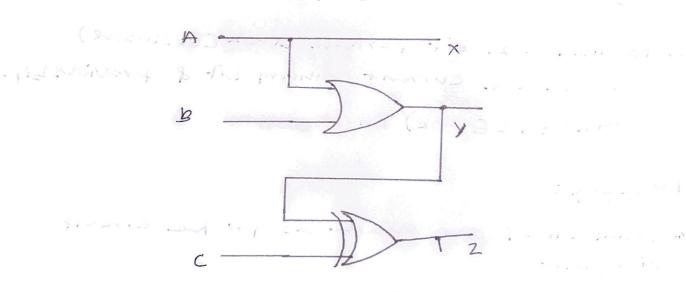
Different logic cricils are constructed and their that tables are varified



X= ABC+ABC+ABC.

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2= A (B (C)



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	83				
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