

CircuiTikZ version git:10aa7de (2019/04/12)

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1 Introduction

1.1 About

CircuiTikZ was initiated by Massimo Redaelli in 2007, who was working as a research assistant at the Polytechnic University of Milan, Italy, and needed a tool for creating exercises and exams. After he left University in 2010 the development of CircuiTikZ slowed down, since LaTeX is mainly established in the academic world. In 2015 Stefan Lindner and Stefan Erhardt, both working as research assistants at the University of Erlangen-Nürnberg, Germany, joined the team and now maintain the project together with the initial author. In 2018 Romano Giannetti, full professor of Electronics at Comillas Pontifical University of Madrid, joined the team.

The use of CircuiTikZ is, of course, not limited to academic teaching. The package gets widely used by engineers for typesetting electronic circuits for articles and publications all over the world.

1.2 Loading the package

IAT _E X	ConT _E Xt ¹
\usepackage{circuitikz}	\usemodule[circuitikz]

TikZ will be automatically loaded.

CircuiTikZ commands are just TikZ commands, so a minimum usage example would be:

1.3 Installing a new version of the package.

The stable version of the package should come with your LATEX distribution. Downloading the files from CTAN and installing them locally is, unfortunately, a distribution-dependent task and sometime not so trivial. If you search for local texmf tree and the name of your distribution on https://tex.stackexchange.com/ you will find a lot of hints.

Anyway, the easiest way of using whichever version of CircuiTikZ is to point to the github page https://circuitikz.github.io/circuitikz/ of the project, and download the version you want. You will download a simple (biggish) file, called circuitikz.sty.

Now you can just put this file in your local texmf tree, if you have one, or simply adding it into the same directory where your main file resides, and then use

```
\usepackage[...options...]{circuitikzgit}
```

instead of circuitikz. This is also advantageous for "future resilience"; the authors try hard not to break backward compatibility with new versions, but sometime things happen.

1.4 Requirements

- tikz, version ≥ 3 ;
- xstring, not older than 2009/03/13;
- siunitx, if using siunitx option.

 $^{^{1}}$ ConTEXt support was added mostly thanks to Mojca Miklavec and Aditya Mahajan.

1.5 Incompatible packages

TikZ's own circuit library, which is based on CircuiTikZ, (re?)defines several styles used by this library. In order to have them work together you can use the compatibility package option, which basically prefixes the names of all CircuiTikZ to [] styles with an asterisk.

So, if loaded with said option, one must write (0,0) to [*R] (2,0) and, for transistors on a path, (0,0) to [*Tnmos] (2,0), and so on (but (0,0) node [nmos] {}). See example at page 84.

1.6 License

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1.7 Feedback

The easiest way to contact the authors is via the official Github repository: https://github.com/circuitikz/circuitikz/issues

1.8 Incompabilities between version

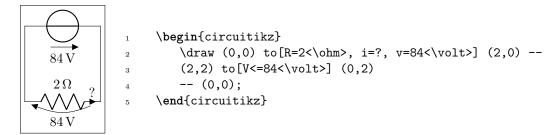
Here, we will provide a list of incompabilitys between different version of circuitikz. We will try to hold this list short, but sometimes it is easier to break with old syntax than including a lot of switches and compatibility layers. You can check the used version at your local installation using the macro \pgfcircversion{}.

- After v0.8.4: the parameters tripoles/american or port/aaa, ...bbb, ...ccc and ...ddd are no longer used and are silently ignored; the same stands for nor, xor, and xnor ports.
- After v0.8.4: voltage and current directions/sign (plus and minus signs in case of american voltages and arrows in case of european voltages have been rationalized with a couple of new options (see details in section 4.2. The default case is still the same as v0.8.4.
- Since v0.8.2: voltage and current label directions(v<= / i<=) do NOT change the orientation of the drawn source shape anymore. Use the "invert" option to rotate the shape of the source. Furthermore, from this version on, the current label(i=) at current sources can be used independent of the regular label(l=).
- Since v0.7?: The label behaviour at mirrored bipoles has changes, this fixes the voltage drawing, but perhaps you have to adjust your label positions.
- Since v0.5.1: The parts pfet,pigfete,pigfetebulk and pigfetd are now mirrored by default. Please adjust your yscale-option to correct this.
- Since v0.5: New voltage counting direction, here exists an option to use the old behaviour

For older projects, you can use an older version locally using the git-version and picking the correct commit from the repository (branch gh-pages).

1.9 Package options

Circuit people are very opinionated about their symbols. In order to meet the individual gusto you can set a bunch of package options. The standard options are what the authors like, for example you get this:



Feel free to load the package with your own cultural options:

```
IATEX ConTEXt

\usepackage[american]{circuitikz} \usemodule[circuitikz][american]
```

Here is the list of all the options:

- europeanvoltages: uses arrows to define voltages, and uses european-style voltage sources;
- straightvoltages: uses arrows to define voltages, and and uses straight voltage arrows;
- americanvoltages: uses and + to define voltages, and uses american-style voltage sources;
- europeancurrents: uses european-style current sources;
- americancurrents: uses american-style current sources;
- europeanresistors: uses rectangular empty shape for resistors, as per european standards;
- americanresistors: uses zig-zag shape for resistors, as per american standards;
- europeaninductors: uses rectangular filled shape for inductors, as per european standards;
- americaninductors: uses "4-bumps" shape for inductors, as per american standards;
- cuteinductors: uses my personal favorite, "pig-tailed" shape for inductors;
- americanports: uses triangular logic ports, as per american standards;
- europeanports: uses rectangular logic ports, as per european standards;
- americangfsurgearrester: uses round gas filled surge arresters, as per american standards;

- europeangfsurgearrester: uses rectangular gas filled surge arresters, as per european standards:
- european: equivalent to europeancurrents, europeanvoltages, europeanresistors, europeaninductors, europeanports, europeangfsurgearrester;
- ullet american: equivalent to americancurrents, americanvoltages, americanresistors, americaninductors, americanports, americangfsurgearrester;
- siunitx: integrates with SIunitx package. If labels, currents or voltages are of the form #1<#2> then what is shown is actually \SI{#1}{#2};
- nosiunitx: labels are not interpreted as above;
- fulldiode: the various diodes are drawn and filled by default, i.e. when using styles such as diode, D, sD, ...Other diode styles can always be forced with e.g. Do, D-, ...
- strokediode: the various diodes are drawn and stroke by default, i.e. when using styles such as diode, D, sD, ...Other diode styles can always be forced with e.g. Do, D*, ...
- emptydiode: the various diodes are drawn but not filled by default, i.e. when using styles such as D, sD, ...Other diode styles can always be forced with e.g. Do, D-, ...
- arrowmos: pmos and nmos have arrows analogous to those of pnp and npn transistors;
- noarrowmos: pmos and nmos do not have arrows analogous to those of pnp and npn transistors;
- fetbodydiode: draw the body diode of a FET;
- nofetbodydiode: do not draw the body diode of a FET;
- fetsolderdot: draw solderdot at bulk-source junction of some transistors;
- nofetsolderdot: do not draw solderdot at bulk-source junction of some transistors;
- emptypmoscircle: the circle at the gate of a pmos transistor gets not filled;
- lazymos: draws lazy nmos and pmos transistors. Chip designers with huge circuits prefer this notation;
- straightlabels: labels on bipoles are always printed straight up, i.e. with horizontal baseline:
- rotatelabels: labels on bipoles are always printed aligned along the bipole;
- smartlabels: labels on bipoles are rotated along the bipoles, unless the rotation is very close to multiples of 90°;
- compatibility: makes it possibile to load CircuiTikZ and TikZ circuit library together.
- Voltage directions: until v0.8.3, there was an error in the coherence between american and european voltages styles (see section 4.2 for the batteries. This has been fixed, but to guarantee backward compatibility and nasty surprises, the fix is available with new options:
 - oldvoltagedirection: Use old way of voltage direction having a difference between european and american direction, with wrong default labelling for batteries;
 - nooldvoltagedirection: The standard from 0.5 onward, utilize the (German?) standard of voltage arrows in the direction of electric fields (without fixing batteries);

- RPvoltages (meaning Rising Potential voltages): the arrow is in direction of rising potential, like in oldvoltagedirections, but batteries and current sources are fixed to follow the passive/active standard;
- EFvoltages (meaning Electric Field voltages): the arrow is in direction of the electric field, like in nooldvoltagedirections, but batteries are fixed;

If none of these option are given, the package will default to nooldvoltagedirections, but will give a warning. The behavior is also selectable circuit by circuit with the voltage dir style.

• betterproportions²: nicer proportions of transistors in comparision to resistors;

The old options in the singular (like american voltage) are still available for compatibility, but are discouraged.

Loading the package with no options is equivalent to the following options: [nofetsolderdot, europeancurrents, europeanvoltages, americanports, americanresistors, cuteinductors, europeangfsurgearrester, nosiunitx, noarrowmos, smartlabels, nocompatibility].

In ConTEXt the options are similarly specified: current= european|american, voltage= european|american, resistor= american|european, inductor= cute|american|european, logic= american|european, siunitx= true|false, arrowmos= false|true.

2 Tutorials

To draw a circuit, you have to load the circuitikz package; this can be done with

1 \usepackage[siunitx, RPvoltages]{circuitikz}

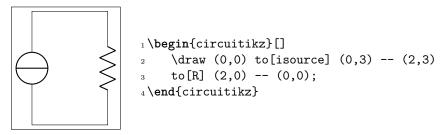
somewhere in your document preamble. It will load automatically the needed packages if not already done before.

2.1 Getting started with CircuiTikZ: a current shunt

Let's say we want to prepare a circuit to teach how a current shunt works; the idea is just draw a current generator, a couple of resistors in parallel, and the indication of currents and voltages for the discussion.

A circuit in CircuiTikZis drawn into a circuitikz environment (which is really an alias for a tikzpicture one). In this first example we will use absolute coordinates. The electrical components can be divided in two big categories: the one that are bipoles and are placed along a path (also known as to-style component, for their usage) and components that are nodes and can have any number of poles, or connections.

Let's start with the first type of components and build a basic mesh:

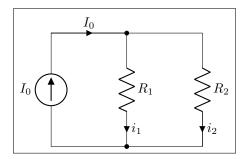


²May change in the future!

The symbol for the current source can surprise somebody; this is actually the european-style symbol, and the symbols chosen reflects the default options of loaded in the package (see section 1.9). Let's change the style for now (the author of the tutorial, Romano, is European but he has used since ever American style circuit, so...); and while at it, let add the other branch and some label.

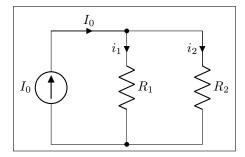


You can use a single path or multiple path when drawing your circuit, it's just a question of style (but be aware that closing path could be non-trivial, see section 4.11), and you can use standard TikZ lines (--, |- or similar) for the wires. Nonetheless, sometime using the CircuiTikZ specific short component for the wires can be useful, because then we can add labels and nodes at it, like for example in the following circuit.



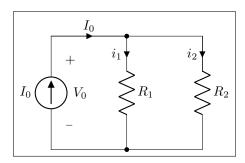
```
1 \begin{circuitikz} [american]
2    \draw (0,0) to[isource, 1=$I_0$] (0,3)
3    to[short, -*, i=$I_0$] (2,3)
4    to[R=$R_1$, i=$i_1$] (2,0) -- (0,0);
5    \draw (2,3) -- (4,3)
6    to[R=$R_2$, i=$i_2$]
7    (4,0) to[short, -*] (2,0);
8 \end{circuitikz}
```

One of the problems with this circuit is that we would like to have the current in a different position, such as for example on the upper side of the resistors, so that the Kirchoff's Current Law at the node is better shown to students. No problem; as you can see in section 4.2 you can use the position specifier <>^_} after the key i:

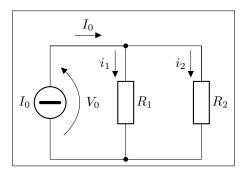


```
1 \begin{circuitikz} [american]
2     \draw (0,0) to [isource, l=$I_0$] (0,3)
3     to [short, -*, i=$I_0$] (2,3)
4     to [R=$R_1$, i>=$i_1$] (2,0) -- (0,0);
5     \draw (2,3) -- (4,3)
6     to [R=$R_2$, i>=$i_2$]
7     (4,0) to [short, -*] (2,0);
8 \end{circuitikz}
```

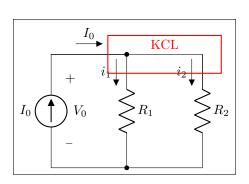
Finally, we would like to add voltages indication for carrying out the current formulas; as the default position of the voltage signs seems a bit cramped to me, I am adding the voltage shift parameter to make a bit more space for it...



Et voilá!. Remember that this is still LATEX, which means that you have done a description of your circuit, which is, in a lot of way, independent of the visualization of it. If you ever have to adapt the circuit to, say, a journal that force European style and flows instead of currents, you just change a couple of things and you have what seems a completely different diagram:



And finally, this is still TikZ, so that you can freely mix other graphics element to the circuit.



2.2 A more complex tutorial: circuits Romano's style.

The idea is to draw a two-stage amplifier for a lesson, or exercise, on the different qualities of BJT an MOSFET transistors. Notice that this is a more "personal" tutorial, showing a way to draw circuits that is, in the author's opinion, highly reusable and easy to do. The idea is using relative coordinates and named nodes as much as possible, so that changes in the circuit are easily done by changing keys numbers of position, and crucially, each block is reusable in other diagrams.

First of all, let's define a handy function to show the position of nodes:

```
1\def\coord(#1){coordinate(#1)}
2\def\coord(#1){node[circle, red, draw, inner sep=1pt,pin={[red, overlay, inner sep=0.5pt, font=\tiny, pin distance=0.1cm, pin edge={red, overlay,}]45:#1}](#1){}}
```

The idea is that you can use \coord() instead of coordinate() in paths, and that will draw sort of *markers* showing them. For example:

After the circuit is drawn, simply commenting out the second definition of \coord will hide all the markers.

So let's start with the first stage transistor; given that my preferred way of drawing a MOSFET is with arrows, I'll start issuing the command \ctikzset{tripoles/mos style/arrows}:

Another thing I like to modify with respect to the standard is the position of the arrows in transistors, which are normally midway the symbol. Issuing the following settings will moe the arrows to the end or start of the corresponding pin.

```
1\ctikzset{tripoles/mos style/arrows,
2 tripoles/npn/arrow pos=0.8,
3 tripoles/pnp/arrow pos=0.8,
4 tripoles/nmos/arrow pos=0.8,
5 tripoles/pmos/arrow pos=0.6, }
```

The tricky thing about \killdepth{} macro is a finicky details; I do not like the standard position of labels on transistors (which is near the collector/drain) so I plot the label at the right of the center anchor. Without the \killdepth macro, the labels of different transistor will be adjusted so that the center of the box is at the center anchor, and as an effect, labels with descenders (like Q) will have a different baseline than labels without. You can see this here (it's really subtle):

```
1\begin{circuitikz} [american,]
2\draw (0,0) node [nmos] (Q1) {} ++(2,0) node [nmos] (M1) {};
3\draw (Q1.center) node [right] {q1};
4\draw (M1.center) node [right] {m1};
5\draw [red] (Q1.center) ++(0,-0.7ex) -- ++(3,0);
6\draw (0,-2) node [nmos] (Q1) {} ++(2,0) node [nmos] (M1) {};
7\draw (Q1.center) node [right] {\killdepth{q1}};
8\draw (M1.center) node [right] {\killdepth{m1}};
9\draw [red] (Q1.center) ++(0,-0.7ex) -- ++(3,0);
10\end{circuitikz}
```

We will start connecting the first transistor with the power supply with a couple of resistors. Notice that I am naming the nodes GND, VCC and VEE, so that I can use the coordinates to have all the supply rails at the same vertical position (more on this later).



After that, let's add the input part. I will use a named node here, to refer to it to add the input source. Notice how the ground node is positioned: the coordinate (in \mid - GND) is the point with the horizontal coordinate of (in) and the horizontal one of (GND), lining it up with the ground of the capacitor C_1 .



```
1 \begin{circuitikz}[american, scale=0.7]
     \draw (0,0) node[nmos,](Q1){};
     \draw (Q1.center) node[right]
         {\killdepth{Q1}};
     \draw (Q1.S) to [R, 12^=$R_S$ and \SI
         \{5\}\{k \in \} ++(0,-3)
         node[vee](VEE){$V_{EE}=SI{-10}{V}}
             }$};
     \draw (Q1.D) to[R, 12_=$R_D$ and \SI
         \{10\}\{k \cap \}\} ++(0,3)
        node[vcc](VCC) {\$V_{CC}=\SI{10}{V}}
             }$};
     \draw (Q1.S) to[short] ++(2,0) to[C
         =$C_1$] ++(0,-1.5) node[ground](
         GND){};
     \draw (Q1.G) to[short] ++(-1,0)
         \coord (in) to [R, 12^=R_G and \
11
             SI\{1\}\{M\setminus ohm\}]
         (in |- GND) node[ground]{};
12
     draw (in) to [C, 1_=$C_2$,*-o]
13
         ++(-1.5,0) node[left](vi1){$v_i=
         v_{i1}$};
14 \end{circuitikz}
```

Notice that the only absolute coordinate here is the first one, (0,0); so the elements are connected with relative movements and can be moved by just changing one number (for example, changing the to [C=\$C_1\$] ++(0,-1.5) will move all the grounds down).

This is the final circuit, with the nodes still marked:

```
1\tikzset{blockdef/.style={%
              {Straight Barb[harpoon, reversed, right, length=0.2cm]}-{Straight Barb[
                        harpoon, reversed, left, length=0.2cm]},
              blue, %densely dotted,
 4 }}
 5 \def\killdepth#1{{\raisebox{Opt}[\height][Opt]{#1}}}
 6 \def\coord(#1) {coordinate(#1)}
 7\def\coord(#1){node[circle, red, draw, inner sep=1pt,pin={[red, overlay, inner
                  sep=0.5pt, font=\tiny, pin distance=0.1cm, pin edge={red, overlay
                ,}]45:#1}](#1){}}
 s\begin{circuitikz}[american, ]
              draw (0,0) node[nmos,](Q1){};
              \draw (Q1.center) node[right]{\killdepth{Q1}};
10
              11
                         _{EE}=SI_{-10}_{V}; %define VEE level
              \displaystyle (Q1.S) \ to[short] ++(2,0) \ to[C=$C_1$] ++(0,-1.5) \ node[ground](GND){};
12
              \label{lem:condition} $$\operatorname{Q1.G}$ to[short] ++(-1,0) \coord (in) to[R, 12^=$R_G$ and \SI\{1\}_{M} \coord (in) to
13
                        ohm }] (in |- GND) node [ground] {};
              \draw (in) to [C, 1_=$C_2$,*-o] ++(-1.5,0) node [left] (vi1) {$v_i=v_{i1}}$;
14
              \displaystyle (Q1.D) \ to[R, 12_=$R_D$ \ and \SI{10}{k\ohm}] ++(0,3) \ node[vcc](VCC){$V}
15
                          _{CC}=\SI{10}{V}$};
              \draw (Q1.D) to[short, -o] ++(1,0) node[right](vo1){$v_{o1}};
16
17
              \path (vo1) -- ++(3,0) \coord(bjt);
18
19
```

```
\draw (bjt) node[npn, ](Q2){};
20
      \draw (Q2.center) node[right]{\killdepth{Q2}};
21
      \draw (Q2.B) to [short, -o] ++(-0.5,0) node [left] (vi2) {$v_{12}$};
22
      \del{condition} \delcase (Q2.E) to [R,12^=$R_E$ and $SI{9.3}{k\ohm}] (Q2.E |- VEE) node [vee]{};
23
      \draw (Q2.E) to[short, -o] ++(1,0) node[right](vo2){$v_{o2}$};
24
      \draw (Q2.C) to[short] (Q2.C |- VCC) node[vcc]{};
25
26
      \path (vo2) ++(1.5,0) \coord(load);
27
      \label{local_state} $$ \operatorname{load} \ to[C=\C_3\] \ ++(1,0) \ \operatorname{local}(tmp) \ to[R=\R_L\] \ (tmp \ |-\ GND) \ node[
28
          ground]{};
      \draw [densely dashed] (vo2) -- (load);
29
30
      \draw [densely dashed] (vo1) -- (vi2);
31
32
      \draw [blockdef](vi1|-VEE) ++(0,-2) \coord(tmp)
33
            -- node[midway, fill=white]{bloque 1} (vo1|- tmp);
34
            \draw [blockdef] (vi2|-VEE) ++(0,-2) \coord(tmp)
35
            -- node[midway, fill=white]{bloque 2} (vo2|- tmp);
36
37
38 \end{circuitikz}
                         V_{CC} = 10 \,\mathrm{V}
```



3 The components

Components in CircuiTikZ come in two forms: a path-style form, to be used in to path specifications, and node-style, which will be instantiated by a node specification.

3.1 Path-style components

The path-style components are used as in the following way:

- begin{circuitikz}
 draw (0,0) to[#1=#2, #options] (2,0);
- 3 \end{circuitikz}

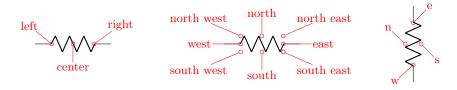
where #1 is the name of the component, #2 is an (optional) label, and options are optional labels, annotations, style specifier that will be explained in the rest of the manual.

Transistors and some other node-style components can also be placed using the syntax for bipoles. See section 3.18.2.

Most path-style components can be used as a node-style components; to access them, you add a shape to the main name of component (for example, diodeshape). Such a "node name" is specified in the description of each component.

3.1.1 Anchors

Normally, path-style component do not need anchors, although they have them just in case you need them. You have the basic "geographical" anchors (bipoles are defined horizontally and then rotate as needed):



In the case of bipoles, also shortened geographical anchors exists. In the description, it will be shown when a bipole has additional anchors. To use the anchors, just give a name to the bipole element.

```
1 \begin{circuitikz}
2 \draw (0,0) to[potentiometer, name=P, mirror] ++(0,2);
3 \draw (P.wiper) to[L] ++(2,0);
4 \end{circuitikz}
```

Alternatively, that you can use the shape form, and then use the left and right anchors to do your connections.

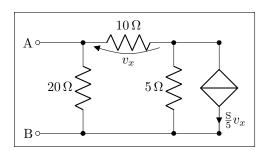
```
1\begin{circuitikz}
2 \draw (0,0) node[potentiometershape, rotate=-90](P){};
3 \draw (P.wiper) to[L] ++(2,0);
4\end{circuitikz}
```

3.1.2 Customization

Pretty much all CircuiTikZ relies heavily on pgfkeys for value handling and configuration. Indeed, at the beginning of circuitikz.sty and in the file pfgcirc.define.tex a series of key definitions can be found that modify all the graphical characteristics of the package.

All can be varied using the \ctikzset command, anywhere in the code.

3.1.2.1 Components size Perhaps the most important parameter is bipoles/length (default 1.4 cm), which can be interpreted as the length of a resistor (including reasonable connections): all other lengths are relative to this value. For instance:



```
1\ctikzset{bipoles/length=1.4cm}
2\begin{circuitikz}[scale=1.2]\draw
3  (0,0) node[anchor=east] {B}
4          to[short, o-*] (1,0)
5          to[R=20<\ohm>, *-*] (1,2)
6          to[R=10<\ohm>, v=$v_x$] (3,2) -- (4,2)
7          to[cI=$\frac{\si{\siemens}}{5} v_x$, *-*] (4,0) -- (3,0)
8          to[R=5<\ohm>, *-*] (3,2)
9  (3,0) -- (1,0)
10  (1,2) to[short, -o] (0,2) node[anchor=east]{A}
11;\end{circuitikz}
```



3.1.2.2 Thickness of the lines (globally)

You can change the thickness of the components lines with the parameter bipoles/thickness (default 2). The number is relative to the thickness of the normal lines leading to the component.

3.1.2.3 Shape of the components (on a per-component-class basis)

The shape of the components are adjustable with a lot of parameters; in this manual we will comment the main ones, but you can look into the source files specified above to find more.

3.1.3 Descriptions

The typical entry in the component list will be like this:

```
resistor: resistor, american style, type: path-style, nodename: resistorshape. Aliases: R, american resistor.

pR: potentiometer, american style, type: path-style, nodename: potentiometershape. Aliases: pR, american potentiometer.
```

where you have all the needed information about the bipole, with also no-standard anchors. If the component can be filled it will be specified in the description. In addition, as an example, the component shown will be filled with the option fill=cyan!30!white:

```
ammeter: Ammeter, type: path-style, fillable, nodename: ammetershape.
```

3.2 Node-style components

Node-style components (monopoles, multipoles) can be drawn at a specified point with this syntax, where #1 is the name of the component:

```
1\begin{circuitikz}
2 \draw (0,0) node[#1,#2] (#3) {#4};
3\end{circuitikz}
```

Explanation of the parameters:
#1: component name³ (mandatory)
#2: list of comma separated options (optional)
#3: name of an anchor (optional)
#4: text written to the text anchor of the component (optional)

Most path-style components can be used as a node-style components; to access them, you add a shape to the main name of component (for example, diodeshape). Such a "node name" is specified in the description of each component.

Notice: Nodes must have curly brackets at the end, even when empty. An optional anchor (#3) can be defined within round brackets to be addressed again later on. And please don't forget the semicolon to terminate the \draw command.

Also notice: If using the \tikzexternalize feature, as of Tikz 2.1 all pictures must end with \end{tikzpicture}. Thus you cannot use the circuitikz environment.

Which is ok: just use the environment tikzpicture: everything will work there just fine.

3.2.1 Mirroring and flipping

Mirroring and flipping of node components is obtained by using the TikZ keys xscale and yscale. Notice that this parameters affect also text labels, so they need to be un-scaled by hand.

3.2.2 Anchors

Node components anchors are variable across the various kind of components, so they will described better after each category is presented in the manual.

3.2.3 Descriptions

The typical entry in the component list will be like this:



³For using bipoles as nodes, the name of the node is #1shape.

All the shapes defined by CircuiTikZ. These are all pgf nodes, so they are usable in both pgf and TikZ. If the component can be filled it will be specified in the description. In addition, as an example, the component shown will be filled with the option fill=cyan!30!white:



3.3 Grounds and supply voltages

For the grounds, the center anchor is put on the connecting point of the symbol, so that you can use them directly in a path specification.

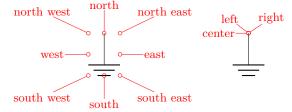
center =	Ground, type: node (node[ground]{})
	Reference ground, type: node (node[rground]{})
\downarrow	Signal ground, type: node, fillable (node[sground]{})
_	Thicker ground, type: node (node[tground]{})
	Noiseless ground, type: node (node[nground]{})
	Protective ground, type: node (node[pground]{})
	Chassis ground ⁴ , type: node (node[cground]{})
7/77	European style ground, type: node (node[eground]{})
777	European style ground, version 2 ⁵ , type: node (node[eground2]{})
\uparrow	VCC/VDD, type: node (node[vcc]{})
\downarrow	VEE/VSS, type: node (node[vee]{})

 $^{^4{\}rm These}$ last three were contributed by Luigi «Liverpool»)

 $^{^5\}mathrm{These}$ last two were contributed by ${\tt Qfotesan})$

3.3.1 Grounds anchors

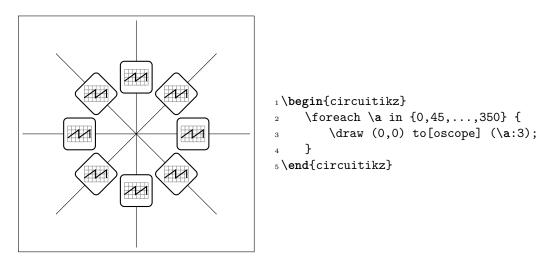
Anchors for grounds are a bit strange, given that they have the center spot at the same location than north and all the ground will develop "going down":



3.4 Instruments

	ammeter: Ammeter, type: path-style, fillable, nodename: ammetershape.
	<pre>voltmeter: Voltmeter, type: path-style, fillable , nodename: voltmetershape.</pre>
<u></u>	<pre>ohmmeter: Ohmmeter, type: path-style, fillable , nodename: ohmmetershape.</pre>
in 1 in 2	$ \begin{array}{lll} \text{oscope: } Oscilloscope^6, \text{type: path-style, fillable,} \\ \text{nodename: oscopeshape.} \end{array} $

The oscope element will not rotate the "graph" shown with the component:



Moreover, the node-style usage of the oscope is also interesting, using the additional in 1 and in 2 anchors; notice that in this case you can use the text content of the node to put labels above it. Moreover, you can change the size of the osciloscope by changing bipoles/oscope/width and bipoles/oscope/height keys (which both default at 0.6).

 $^{^6}$ Suggested by Qnobrl on GitHub

3.5 Resistive bipoles

	<pre>short: Short circuit, type: path-style , nodename: shortshape.</pre>
	open: Open circuit, type: path-style , nodename: openshape.
	<pre>generic: Generic (symmetric) bipole, type: path-style, fillable , nodename: genericshape.</pre>
	tgeneric: Tunable generic bipole, type: path-style, fillable, nodename: tgenericshape.
	ageneric: Generic asymmetric bipole, type: path-style, fillable, nodename: agenericshape.
	fullgeneric: Generic asymmetric bipole (full), type: path-style, nodename: fullgenericshape.
	tfullgeneric: Tunable generic bipole (full), type: path-style, nodename: tfullgenericshape.
— 	memristor: Memristor, type: path-style, fillable, nodename: memristorshape. Aliases: Mr.

If americanresistors option is active (or the style [american resistors] is used; this is the default for the package), the resistors are displayed as follows:

	R: Resistor, type: path-style, nodename: resistorshape. Aliases: american resistor.
	vR: Variable resistor, type: path-style, nodename: vresistorshape. Aliases: variable american resistor.
wiper ——wiper	pR: Potentiometer, type: path-style, nodename: potentiometershape. Aliases: american potentiometer.

	sR: Resisitive sensor, type: path-style, nodename: resistivesensshape. Aliases: american resisitive
label	sensor.

If instead europeanresistors option is active (or the style [european resistors] is used), the resistors, variable resistors and potentiometers are displayed as follows:

	R: Resistor, type: path-style, fillable, nodename: genericshape. Aliases: european resistor.
	vR: Variable resistor, type: path-style, fillable, nodename: tgenericshape. Aliases: variable european resistor.
o—wiper	pR: Potentiometer, type: path-style, fillable, nodename: genericpotentiometershape. Aliases: european potentiometer.
label	sR: Resistive sensor, type: path-style, fillable, nodename: thermistorshape. Aliases: european resistive sensor.

Other miscellaneous resistor-like devices:

U	<pre>varistor: Varistor, type: path-style, fillable , nodename: varistorshape.</pre>
	phR: Photoresistor, type: path-style, fillable, nodename: photoresistorshape. Aliases: photoresistor.
	thermocouple: Thermocouple, type: path-style , nodename: thermocoupleshape.
	thR: Thermistor, type: path-style, fillable, nodename: thermistorshape. Aliases: thermistor.
<i>₹</i>	thRp: PTC thermistor, type: path-style, fillable, nodename: thermistorptcshape. Aliases: thermistorptc.
∂ Å Ť	thRn: NTC thermistor, type: path-style, fillable, nodename: thermistorntcshape. Aliases: thermistorntc.
	<pre>fuse: Fuse, type: path-style, fillable , nodename: fuseshape.</pre>
	afuse: Asymmetric fuse, type: path-style, fillable, nodename: afuseshape. Aliases: asymmetric fuse.

3.5.1 Generic sensors anchors

Generic sensors have an extra label to help positioning the type of dependence, if needed:

The anchor is positioned just on the corner of the segmented line crossing the component.

3.6 Diodes and such

empty diode: Empty diode, type: path-style, fillable, nodename: emptydiodeshape. Aliases: Do.
empty Schottky diode: Empty Schottky diode, type: path-style, fillable, nodename: emptysdiodeshape. Aliases: sDo.
empty Zener diode: Empty Zener diode, type: path-style, fillable, nodename: emptyzdiodeshape. Aliases: zDo.
empty ZZener diode: Empty ZZener diode, type: path-style, fillable, nodename: emptyzzdiodeshape. Aliases: zzDo.
<pre>empty tunnel diode: Empty tunnel diode, type: path-style, fillable, nodename: emptytdiodeshape. Aliases: tDo.</pre>
empty photodiode: Empty photodiode, type: path-style, fillable, nodename: emptypdiodeshape. Aliases: pDo.
empty led: Empty led, type: path-style, fillable, nodename: emptylediodeshape. Aliases: leDo.
empty varcap: Empty varcap, type: path-style, fillable, nodename: emptyvarcapshape. Aliases: VCo.
<pre>empty bidirectionaldiode: Empty bidirectionaldiode, type: path-style, fillable, nodename: emptybidirectionaldiodeshape. Aliases: biDo.</pre>
full diode: Full diode, type: path-style, nodename: fulldiodeshape. Aliases: D*.

	full Schottky diode: Full Schottky diode, type: path-style, nodename: fullsdiodeshape. Aliases: sD*.
	full Zener diode: Full Zener diode, type: path-style, nodename: fullzdiodeshape. Aliases: zD*.
	full ZZener diode: Full ZZener diode, type: path-style, nodename: fullzzdiodeshape. Aliases: zzD*.
	full tunnel diode: Full tunnel diode, type: path-style, nodename: fulltdiodeshape. Aliases: tD*.
	full photodiode: Full photodiode, type: path-style, nodename: fullpdiodeshape. Aliases: pD*.
	full led: Full led, type: path-style, nodename: fulllediodeshape. Aliases: leD*.
	full varcap: Full varcap, type: path-style, nodename: fullvarcapshape. Aliases: VC*.
	full bidirectionaldiode: Full bidirectionaldiode, type: path-style, nodename: fullbidirectionaldiodeshape. Aliases: biD*.

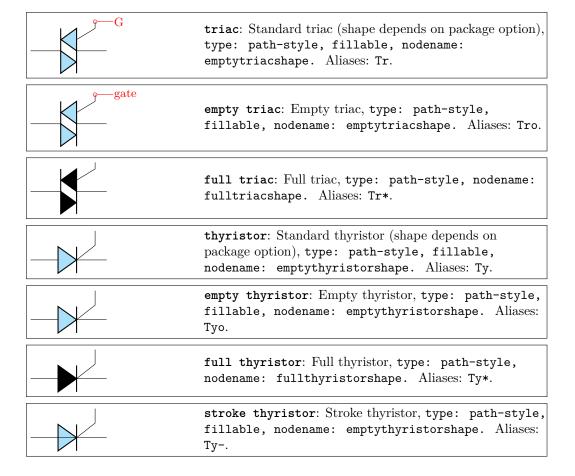
These shapes has no exact node-style counterpart, because the stroke line is build upon the empty variants:

stroke diode: Stroke diode, type: path-style, fillable, nodename: emptydiodeshape. Aliases: D
stroke Schottky diode: Stroke Schottky diode, type: path-style, fillable, nodename: emptysdiodeshape. Aliases: sD
stroke Zener diode: Stroke Zener diode, type: path-style, fillable, nodename: emptyzdiodeshape. Aliases: zD
stroke ZZener diode: Stroke ZZener diode, type: path-style, fillable, nodename: emptyzzdiodeshape. Aliases: zzD
stroke tunnel diode: Stroke tunnel diode, type: path-style, fillable, nodename: emptytdiodeshape. Aliases: tD
<pre>stroke photodiode: Stroke photodiode, type: path-style, fillable, nodename: emptypdiodeshape. Aliases: pD</pre>

stroke led: Stroke led, type: path-style, fillable, nodename: emptylediodeshape. Aliases: leD
stroke varcap: Stroke varcap, type: path-style, fillable, nodename: emptyvarcapshape. Aliases: VC

3.7 Tripole-like diodes

The following tripoles are entered with the usual command of the form



3.7.1 Triacs anchors

When inserting a thrystor, a triac or a potentiometer, one needs to refer to the third node—gate (gate or G) for the former two; wiper (wiper or W) for the latter one. This is done by giving a name to the bipole:

```
1\begin{circuitikz} \draw
2 (0,0) to[Tr, n=TRI] (2,0)
3 to[pR, n=POT] (4,0);
4 \draw[dashed] (TRI.G) -| (POT.wiper)
5;\end{circuitikz}
```

The package options fulldiode, strokediode, and emptydiode (and the styles [full diodes], [stroke diodes], and [empty diodes]) define which shape will be used by abbreviated commands such that D, sD, zD, zzD, tD, pD, leD, VC, Ty,Tr(no stroke symbol available!).

3.8 Basic dynamical bipoles

	capacitor: Capacitor, type: path-style , nodename: capacitorshape. Aliases: C.
<u> </u>	polar capacitor: Polar capacitor, type: path-style, nodename: polarcapacitorshape. Aliases: pC.
	ecapacitor: Electrolytic capacitor, type: path-style, fillable, nodename: ecapacitorshape. Aliases: eC,elko.
	variable capacitor: Variable capacitor, type: path-style, nodename: vcapacitorshape. Aliases: vC.
label	capacitive sensor: Capacitive sensor, type: path-style, nodename: capacitivesensshape. Aliases: sC.
	piezoelectric: Piezoelectric Element, type: path-style, fillable, nodename: piezoelectricshape. Aliases: PZ.

If (default behaviour) cuteinductors option is active (or the style [cute inductors] is used), the inductors are displayed as follows:

	L: Inductor, type: path-style, nodename: cuteinductorshape. Aliases: cute inductor.
	<pre>cute choke: Choke, type: path-style, nodename: cutechokeshape.</pre>
	vL: Variable inductor, type: path-style, nodename: vcuteinductorshape. Aliases: variable cute inductor.
label	sL: Inductive sensor, type: path-style, nodename: scuteinductorshape. Aliases: cute inductive sensor.

If americaninductors option is active (or the style [american inductors] is used), the inductors are displayed as follows:

 L: Inductor, type: path-style, nodename:
americaninductorshape. Aliases: american inductor.

	vL: Variable inductor, type: path-style, nodename: vamericaninductorshape. Aliases: variable american inductor.
label	sL: Inductive sensor, type: path-style, nodename: samericaninductorshape. Aliases: american inductive sensor.

Finally, if europeaninductors option is active (or the style [european inductors] is used), the inductors are displayed as follows:

	L: Inductor, type: path-style, nodename: fullgenericshape. Aliases: european inductor.
	vL: Variable inductor, type: path-style, nodename: tfullgenericshape. Aliases: variable european inductor.
——label	<pre>sL: Inductive sensor, type: path-style, nodename: sfullgenericshape. Aliases: european inductive sensor.</pre>

3.9 Stationary sources

	battery: Battery, type: path-style , nodename: batteryshape.
	<pre>battery1: Single battery cell, type: path-style , nodename: battery1shape.</pre>
 	<pre>battery2: Single battery cell, type: path-style , nodename: battery2shape.</pre>
	european voltage source: Voltage source (european style), type: path-style, fillable, nodename: vsourceshape.
	cute european voltage source: Voltage source (cute european style), type: path-style, fillable, nodename: vsourceCshape. Aliases: vsourceC, ceV.
	<pre>american voltage source: Voltage source (american style), type: path-style, fillable, nodename: vsourceAMshape.</pre>
	european current source: Current source (european style), type: path-style, fillable, nodename: isourceshape.
	cute european current source: Current source (cute european style), type: path-style, fillable, nodename: isourceCshape. Aliases: isourceC, ceI.



american current source: Current source (american style), type: path-style, fillable, nodename: isourceAMshape.

If (default behaviour) europeancurrents option is active (or the style [european currents] is used), the shorthands current source, isource, and I are equivalent to european current source. Otherwise, if americancurrents option is active (or the style [american currents] is used) they are equivalent to american current source.

Similarly, if (default behaviour) europeanvoltages option is active (or the style [european voltages] is used), the shorthands voltage source, vsource, and V are equivalent to european voltage source. Otherwise, if americanvoltages option is active (or the style [american voltages] is used) they are equivalent to american voltage source.

3.10 Sinusoidal sources

Here because I was asked for them. But how do you distinguish one from the other?!

	<pre>sinusoidal voltage source: Sinusoidal voltage source, type: path-style, fillable, nodename: vsourcesinshape. Aliases: vsourcesin, sV.</pre>
_ <u></u>	sinusoidal current source: Sinusoidal current source, type: path-style, fillable, nodename: isourcesinshape. Aliases: isourcesin, sI.

3.11 Controlled sources

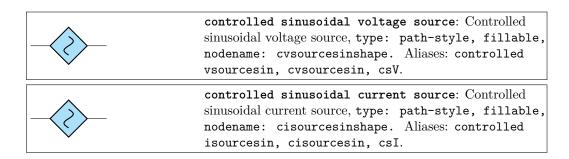
european controlled voltage source: Controlled voltage source (european style), type: path-style, fillable, nodename: cvsourceshape.
cute european controlled voltage source: Voltage source (cute european style), type: path-style, fillable, nodename: cvsourceCshape. Aliases: cvsourceC, cceV.
american controlled voltage source: Controlled voltage source (american style), type: path-style, fillable, nodename: cvsourceAMshape.
european controlled current source: Controlled current source (european style), type: path-style, fillable, nodename: cisourceshape.
cute european controlled current source: Current source (cute european style), type: path-style, fillable, nodename: cisourceCshape. Aliases: cisourceC, cceI.



american controlled current source: Controlled current source (american style), type: path-style, fillable, nodename: cisourceAMshape.

If (default behaviour) europeancurrents option is active (or the style [european currents] is used), the shorthands controlled current source, cisource, and cI are equivalent to european controlled current source. Otherwise, if americancurrents option is active (or the style [american currents] is used) they are equivalent to american controlled current source.

Similarly, if (default behaviour) europeanvoltages option is active (or the style [european voltages] is used), the shorthands controlled voltage source, cvsource, and cV are equivalent to european controlled voltage source. Otherwise, if americanvoltages option is active (or the style [american voltages] is used) they are equivalent to american controlled voltage source.

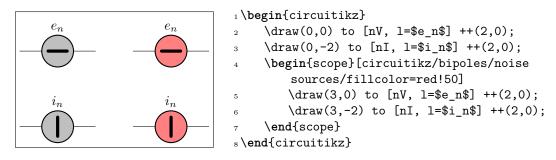


3.12 Noise sources

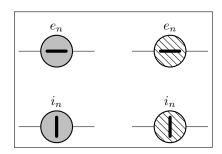
In this case, the "direction" of the source has no sense. Noise sources are filled in gray by default, but if you choose the dashed style, they become fillable.

	noise voltage source: Sinusoidal voltage source, type: path-style, nodename: vsourceNshape. Aliases: vsourceN, nV.
<u> </u>	noise current source: Sinusoidal current source, type: path-style, nodename: isourceNshape. Aliases: isourceN, nI.

You can change the fill color with the key circuitikz/bipoles/noise sources/fillcolor:

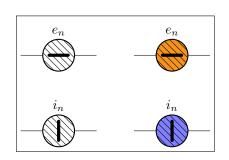


If you prefer a patterned noise generator (similar to the one you draw by hand) you can use the fake color dashed:



```
1 \begin{circuitikz}
2     \draw(0,0) to [nV, l=$e_n$] ++(2,0);
3     \draw(0,-2) to [nI, l=$i_n$] ++(2,0);
4     \begin{scope}[circuitikz/bipoles/noise sources/fillcolor=dashed]
5     \draw(3,0) to [nV, l=$e_n$] ++(2,0);
6     \draw(3,-2) to [nI, l=$i_n$] ++(2,0);
7     \end{scope}
8 \end{circuitikz}
```

Notice that if you choose the dashed style, the noise sources are fillable:



3.13 Special sources

	square voltage source: Square voltage source, type: path-style, fillable, nodename: vsourcesquareshape. Aliases: vsourcesquare, sqV.
_ <u>\</u>	<pre>vsourcetri: Triangle voltage source, type: path-style, fillable , nodename: vsourcetrishape. Aliases: tV.</pre>
<u> </u>	esource: Empty voltage source, type: path-style, fillable, nodename: esourceshape.
	<pre>pvsource: Photovoltaic-voltage source, type: path-style, fillable , nodename: pvsourceshape.</pre>
<u> </u>	<pre>ioosource: Double Zero style current source, type: path-style, fillable, nodename: oosourceshape.</pre>
<u> </u>	<pre>voosource: Double Zero style voltage source, type: path-style, fillable, nodename: oosourceshape.</pre>

3.14 DC sources

dcvsource: DC voltage source, type: path-style, fillable, nodename: dcvsourceshape.
dcisource: DC current source, type: path-style, fillable, nodename: dcisourceshape.

3.15 Mechanical Analogy

damper: Mechanical Damping, type: path-style, fillable, nodename: dampershape.
 <pre>spring: Mechanical Stiffness, type: path-style , nodename: springshape.</pre>
mass: Mechanical Mass, type: path-style, fillable, nodename: massshape.

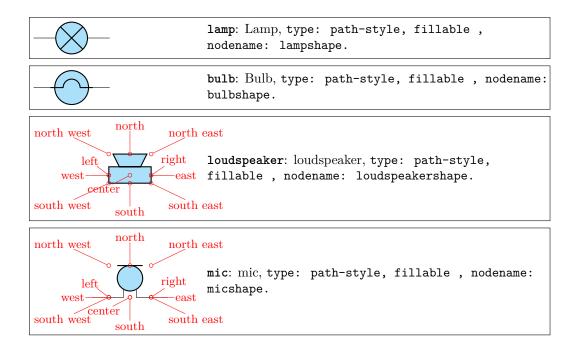
3.16 Other bipoles

Here you'll find bipoles that are not easily grouped in the categories above.

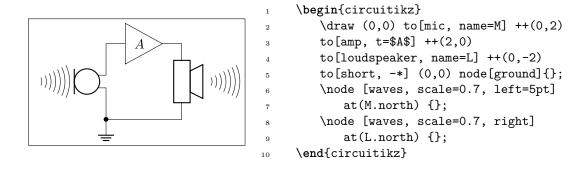
_ _	$\mbox{\tt squid} \colon \mbox{Squid}, \mbox{\tt type} \colon \mbox{\tt path-style}$, nodename: squidshape.
_ _	barrier: Barrier, type: path-style , nodename: barriershape.
-	european gas filled surge arrester: European gas filled surge arrester, type: path-style, fillable, nodename: european gas filled surge arrestershape.
	american gas filled surge arrester: American gas
	filled surge arrester, type: path-style, fillable, nodename: american gas filled surge arrestershape.

If (default behaviour) europeangfsurgearrester option is active (or the style [european gas filled surge arrester] is used), the shorthands gas filled surge arrester and gf surge arrester are equivalent to the european version of the component.

If otherwise americangfsurgearrester option is active (or the style [american gas filled surge arrester] is used), the shorthands the shorthands gas filled surge arrester and gf surge arrester are equivalent to the american version of the component.



You can use microphones and loudspeakers with waves (see section 3.24) too:



3.17 Block diagram components

Contributed by Stefan Erhardt.

1-0-3	MIXER, TYPE: NODE, FILLABLE (node[mixer]{})
north west—east south	ADDER, TYPE: NODE, FILLABLE (node[adder]{})
	OSCILLATOR, TYPE: NODE, FILLABLE (node[oscillator]{})
	CIRCULATOR, TYPE: NODE, FILLABLE (node[circulator]{})
out2	WILKINSON DIVIDER, TYPE: NODE, FILLABLE (node[wilkinson]{})
	<pre>twoport: generic two port⁷, type: path-style, fillable , nodename: twoportshape.</pre>
	<pre>vco: vco, type: path-style, fillable , nodename: vcoshape.</pre>
	bandpass: bandpass, type: path-style, fillable, nodename: bandpassshape.
	<pre>bandstop: bandstop, type: path-style, fillable , nodename: bandstopshape.</pre>
	highpass: highpass, type: path-style, fillable, nodename: highpassshape.
<u></u>	lowpass: lowpass, type: path-style, fillable, nodename: lowpassshape.
A D	${\tt adc:}\ A/D\ converter,\ {\tt type:}\ {\tt path-style,}\ {\tt fillable}$, nodename: adcshape.
D A	${\tt dac}\colon {\rm D/A\ converter}, {\tt type}\colon {\tt path-style}, {\tt fillable}$, nodename: dacshape.

 $^{^7}$ To specify text to be put in the component: twoport[t=text]):

— DSP —	${\tt dsp} \colon {\rm DSP}, {\tt type} \colon {\tt path-style}, {\tt fillable}$, nodename: dspshape.
— FFT —	$\label{fft: FFT, type: path-style, fillable, nodename: fftshape.} % \begin{center} \begin{center} \textbf{fft: FFT, type: path-style, fillable, nodename: fftshape.} \end{center} % \begin{center} \begin{center} \textbf{fillable, nodename: fftshape.} \end{center} % \begin{center} \begin{center} fillable, nodename: $
	<pre>amp: amplifier, type: path-style, fillable , nodename: ampshape.</pre>
	$\begin{array}{lll} \textbf{vamp:} \ VGA, \ \textbf{type:} & \textbf{path-style, fillable,} \\ \textbf{nodename:} & \textbf{vampshape.} \end{array}$
	piattenuator: π attenuator, type: path-style, fillable , nodename: piattenuatorshape.
	vpiattenuator: var. π attenuator, type: path-style, fillable , nodename: vpiattenuatorshape.
	tattenuator: T attenuator, type: path-style, fillable, nodename: tattenuatorshape.
	<pre>vtattenuator: var. T attenuator, type: path-style, fillable , nodename: vtattenuatorshape.</pre>
$\boxed{ \varphi }$	<pre>phaseshifter: phase shifter, type: path-style, fillable , nodename: phaseshiftershape.</pre>
- X	<pre>vphaseshifter: var. phase shifter, type: path-style, fillable , nodename: vphaseshiftershape.</pre>
	<pre>detector: detector, type: path-style, fillable , nodename: detectorshape.</pre>
	Coupler, type: node (node[coupler]{})
	Coupler, 2, type: node (node[coupler2]{})

3.17.1 Blocks anchors

The ports of the mixer and adder can be addressed with numbers or west/south/east/north:



The Wilkinson divider has:

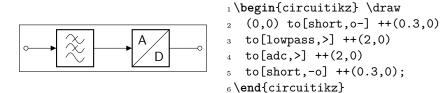


The couplers have:

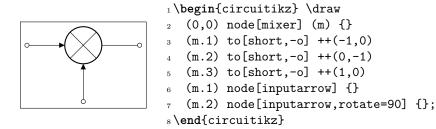
```
1 \begin{circuitikz} \draw
                      (0,0) node[coupler] (c) {\SI{10}{dB}}}
                      (c.1) to [short, -o] ++(-0.5, 0)
    10\,\mathrm{dB}
                      (c.2) to [short, -o] ++(0.5,0)
4
             3
                      (c.3) to [short, -o] ++(0.5, 0)
                      (c.4) to [short, -o] ++(-0.5,0)
                      (c.1) node[below left] {\texttt{1}}
                      (c.2) node[below right] {\texttt{2}}
             2
1
                      (c.3) node[above right] {\texttt{3}}
                      (c.4) node[above left] {\texttt{4}}
                   12 \end{circuitikz}
                   1\begin{circuitikz} \draw
                      (0,0) node[coupler2] (c) {\SI{3}{dB}}
                      (c.1) to [short, -o] ++(-0.5, 0)
     3 \, \mathrm{dB}
                      (c.2) to [short, -o] ++(0.5,0)
             3
4
                      (c.3) to [short, -o] ++(0.5, 0)
                      (c.4) to [short,-o] ++(-0.5,0)
                      (c.1) node[below left] {\texttt{1}}
                      (c.2) node[below right] {\texttt{2}}
             2
                      (c.3) node[above right] {\texttt{3}}
                      (c.4) node[above left] {\texttt{4}}}
                   12 \end{circuitikz}
```

3.17.2 Blocks customization

With the option > you can draw an arrow to the input of the block diagram symbols.



3.17.2.1 Multi ports Since inputs and outputs can vary, input arrows can be placed as nodes. Note that you have to rotate the arrow on your own:



3.17.2.2 Labels and custom twoport boxes Some twoports have the option to place a normal label (1=) and a inner label (t=).

```
LNA

1 \begin{circuitikz}
2 \ctikzset{bipoles/amp/width=0.9}
3 \draw (0,0) to[amp,t=LNA,1_=$F{=}0.9\,$dB,o-o] ++(3,0);
4 \end{circuitikz}
```

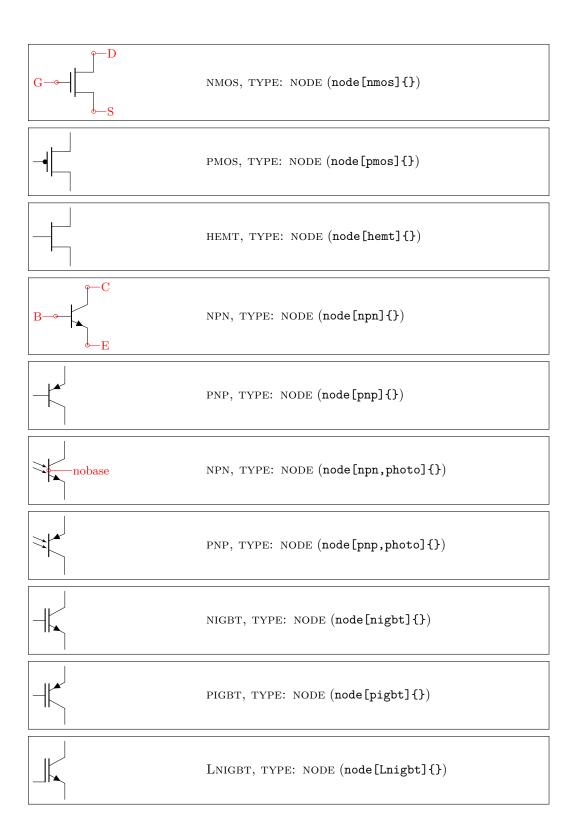
3.17.2.3 Box option Some devices have the possibility to add a box around them. The inner symbol scales down to fit inside the box.

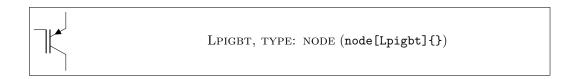
```
1\begin{circuitikz} \draw
2 (0,0) node[mixer,box,anchor=east] (m) {}
3 to[amp,box,>,-o] ++(2.5,0)
4 (m.west) node[inputarrow] {} to[short,-o]
++(-0.8,0)
5 (m.south) node[inputarrow,rotate=90] {} --
6 ++(0,-0.7) node[oscillator,box,anchor=north] {};
7\end{circuitikz}
```

3.17.2.4 Dash optional parts To show that a device is optional, you can dash it. The inner symbol will be kept with solid lines.

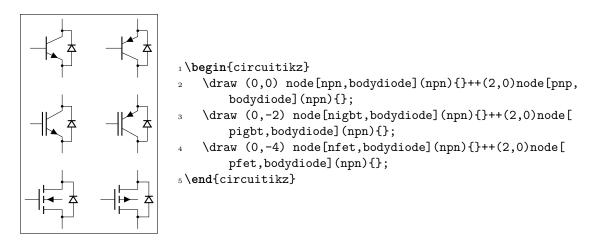
```
10 dB opt.
2 \draw (0,0) to[amp,l=\SI{10}{dB}] ++(2.5,0);
3 \draw[dashed] (2.5,0) to[lowpass,l=opt.]
++(2.5,0);
4 \end{circuitikz}
```

3.18 Transistors

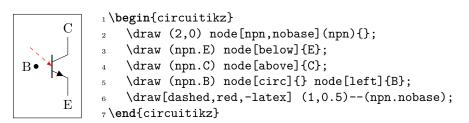




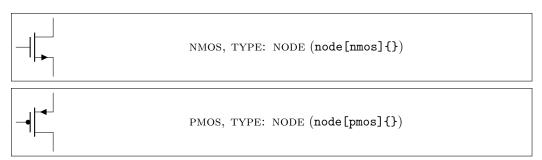
For all transistors a bodydiode (or freewheeling diode) can automatically be drawn. Just use the global option bodydiode, or for single transistors, the tikz-option bodydiode:



The Base/Gate connection of all transistors can be disable by using the options *nogate* or *nobase*, respectively. The Base/Gate anchors are floating, but there an additional anchor "nogate"/"nobase", which can be used to point to the unconnected base:



If the option arrowmos is used (or after the command \ctikzset{tripoles/mos style/arrows} is given), this is the output:



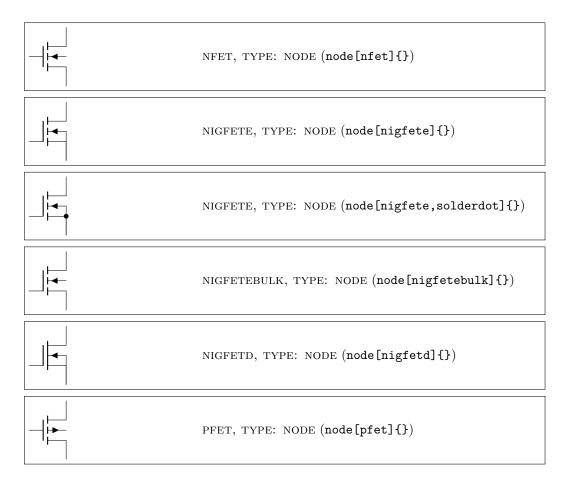
To draw the PMOS circle non-solid, use the option emptycircle or the command \ctikzset{tripoles/pmos style/emptycircle}.

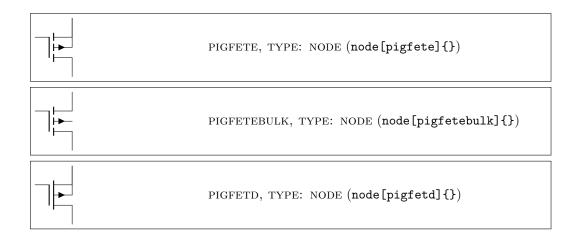
```
PMOS, TYPE: NODE (node[pmos,emptycircle]{})
```

If you prefer different position of the arrows in transistors and FETs, you can adjust them like this (it works for the other BJT-based transistors, too):

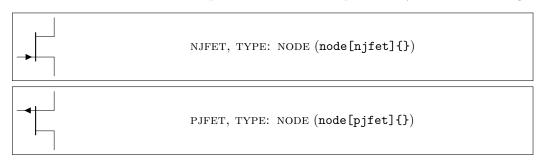
```
1 \begin{circuitikz}
2  \ctikzset{tripoles/mos style/arrows,
3  tripoles/npn/arrow pos=0.8,
4  tripoles/nmos/arrow pos=0.8,
5  tripoles/pmos/arrow pos=0.6, }
6  tripoles/pmos/arrow pos=0.6, }
7  \draw (0,0) node[npn, ](npn){};
8  \draw (2,0) node[pnp, ](npn){};
9  \draw (0,-2) node[nmos, ](npn){};
10  \draw (2,-2) node[pmos, ](npn){};
11 \end{circuitikz}
```

NFETS and PFETS have been incorporated based on code provided by Clemens Helfmeier and Theodor Borsche. Use the package options fetsolderdot/nofetsolderdot to enable/disable solderdot at some fet-transistors. Additionally, the solderdot option can be enabled/disabled for single transistors with the option "solderdot" and "nosolderdot", respectively.

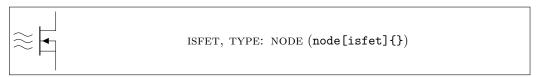




NJFET and PJFET have been incorporated based on code provided by Danilo Piazzalunga:

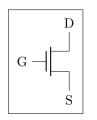


ISFET

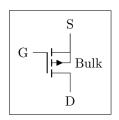


3.18.1 Transistors anchors

For NMOS, PMOS, NFET, NIGFETE, NIGFETD, PFET, PIGFETE, and PIGFETD transistors one has base, gate, source and drain anchors (which can be abbreviated with B, G, S and D):

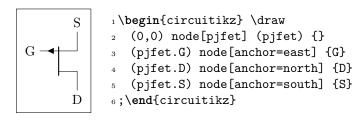


- 1\begin{circuitikz} \draw
- 2 (0,0) node[nmos] (mos) {}
- 3 (mos.gate) node[anchor=east] {G}
- 4 (mos.drain) node[anchor=south] {D}
- (mos.source) node[anchor=north] {S}
- 6;\end{circuitikz}

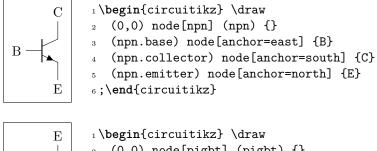


- 1\begin{circuitikz} \draw
- 2 (0,0) node[pigfete] (pigfete) {}
- 3 (pigfete.G) node[anchor=east] {G}
- 4 (pigfete.D) node[anchor=north] {D}
- 5 (pigfete.S) node[anchor=south] {S}
- 6 (pigfete.bulk) node[anchor=west] {Bulk}
- 7;\end{circuitikz}

Similarly NJFET and PJFET have gate, source and drain anchors (which can be abbreviated with G, S and D):



For NPN, PNP, NIGBT, and PIGBT transistors the anchors are base, emitter and collector anchors (which can be abbreviated with B, E and C):



```
E | 1\begin{circuitikz} \draw | 2 (0,0) node[pigbt] (pigbt) {} | 3 (pigbt.B) node[anchor=east] {B} | 4 (pigbt.C) node[anchor=north] {C} | 5 (pigbt.E) node[anchor=south] {E} | 6 ;\end{circuitikz}
```

Here is one composite example (please notice that the xscale=-1 style would also reflect the label of the transistors, so here a new node is added and its text is used, instead of that of pnp1):

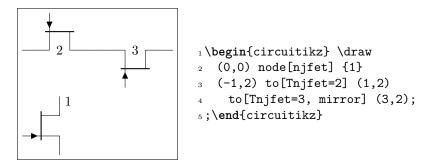
```
1 \begin{circuitikz} \draw
2 (0,0) node[pnp] (pnp2) {2}
3 (pnp2.B) node[pnp, xscale=-1, anchor=B] (pnp1) {}
4 (pnp1) node {1}
5 (pnp1.C) node[npn, anchor=C] (npn1) {}
6 (pnp2.C) node[npn, xscale=-1, anchor=C] (npn2) {}
7 (pnp1.E) -- (pnp2.E) (npn1.E) -- (npn2.E)
8 (pnp1.B) node[circ] {} |- (pnp2.C) node[circ] {}
9 ;\end{circuitikz}
```

Notice that the text labels of transistors are somewhat buggy. It is better to se explicit anchors to set transistor's names.

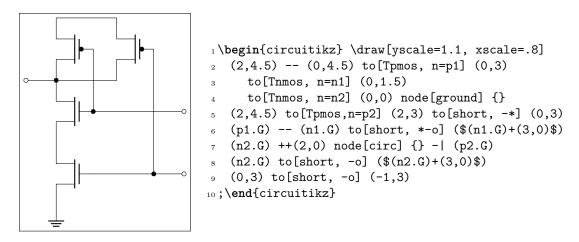
Similarly, transistors like other components can be reflected vertically:

3.18.2 Transistor paths

For syntactical convenience transistors can be placed using the normal path notation used for bipoles. The transitor type can be specified by simply adding a "T" (for transistor) in front of the node name of the transistor. It will be placed with the base/gate orthogonal to the direction of the path:

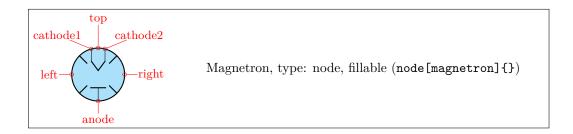


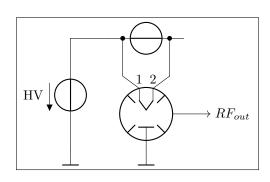
Access to the gate and/or base nodes can be gained by naming the transistors with the **n** or **name** path style:



The name property is available also for bipoles, although this is useful mostly for triac, potentiometer and thyristor (see 3.7).

3.19 Electronic Tubes

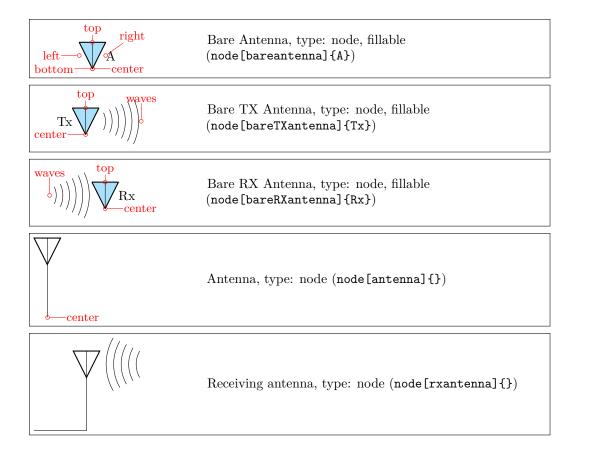




```
\begin{circuitikz}
\draw (0,-2)node[rground](gnd){} to[
   voltage source, v \le \{HV\}\} + (0,3)
    --++(1,0)to[V,n=DC]++(2,0);
\draw (2,-1) node[magnetron,scale=1](
   magn){};
\draw (DC.left)++(-0.2,0)to [short,*-]
   ++(0,-1) to [short] (magn.cathode1);
\draw (DC.right)++(0.2,0)to [short,*-]
    ++(0,-1) to [short] (magn.cathode2);
\draw (magn.anode) to [short] (magn.
    anode|-gnd) node[rground]{};
\draw (magn.cathode1)node[above]{$1$};
\draw (magn.cathode2)node[above]{$2$};
\draw[->](magn.east) --++(1,0)node[
   right] {$RF_{out}$};
\end{circuitikz}
```

3.20 RF components

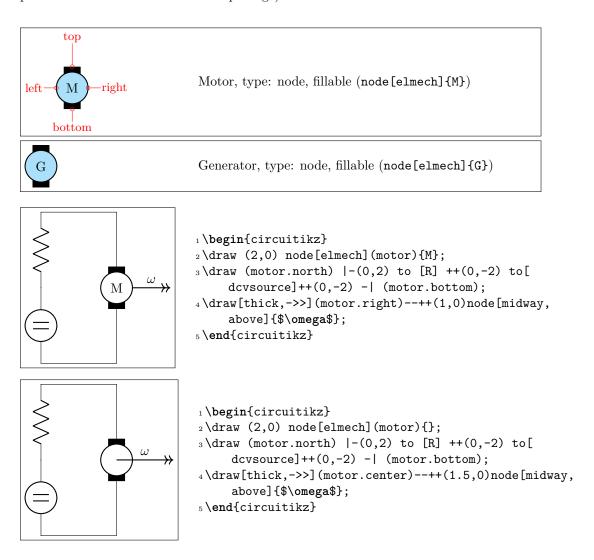
For the RF components, similarly to the grounds and supply rails, the center anchor is put on the connecting point of the symbol, so that you can use them directly in a path specification.



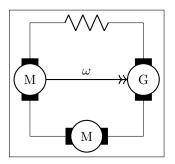
Transmitting antenna, type: node (node[txantenna]{})
Transmission line stub, type: node, fillable (node[tlinestub]{})
 TL: Transmission line, type: path-style, fillable, nodename: tlineshape. Aliases: transmission line, tline.
 match, type: node (node[match]{})

3.21 Electro-Mechanical Devices

The internal part of the motor and generator are, by default, filled white (to avoid compatibility problems with older versions of the package).



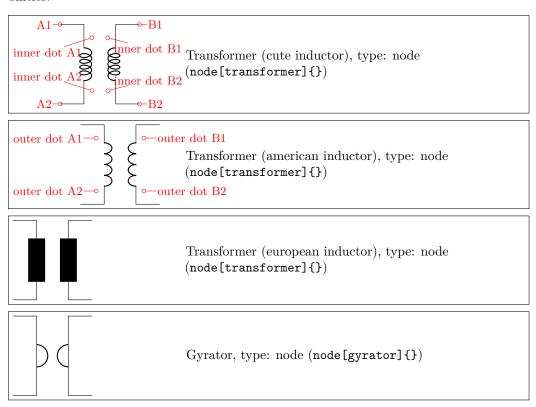
The symbols can also be used along a path, using the transistor-path-syntax(T in front of the shape name, see section 3.18.2). Don't forget to use parameter n to name the node and get acces to the anchors:



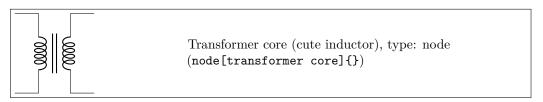
```
1 \begin{circuitikz}
2 \draw (0,0) to [Telmech=M,n=motor] ++(0,-3) to [
    Telmech=M] ++(3,0) to [Telmech=G,n=generator]
    ++(0,3) to [R] (0,0);
3 \draw[thick,->>] (motor.left)--(generator.left)node[
    midway,above]{$\omega$};
4 \end{circuitikz}
```

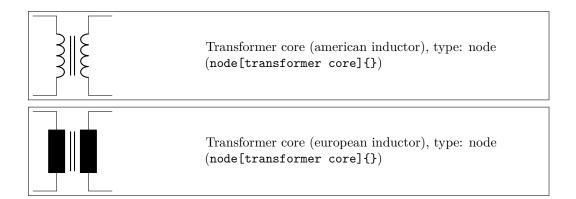
3.22 Double bipoles

Transformers automatically use the inductor shape currently selected. These are the three possibilities:



Transformers with core are also available:

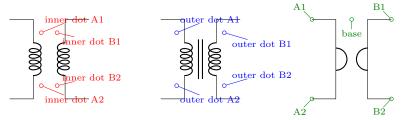




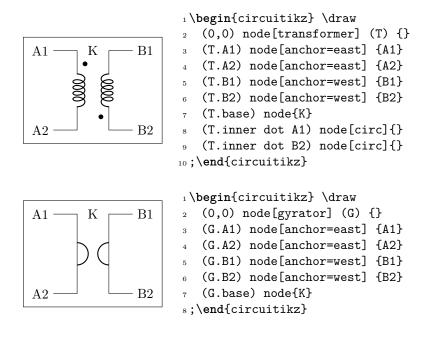
3.22.1 Double dipoles anchors

All the double bipoles/quadrupoles have the four anchors, two for each port. The first port, to the left, is port A, having the anchors A1 (up) and A2 (down); same for port B.

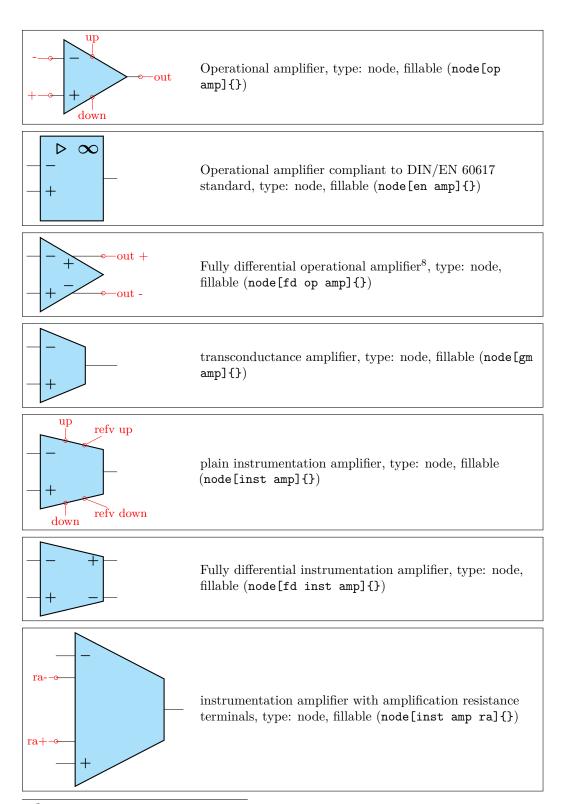
They also expose the base anchor, for labelling, and anchors for setting dots or signs to specify polarity. The set of anchors, to which the standard "geographical" north, north east, etc. is here:



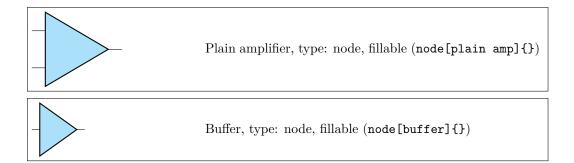
Alse the standard "geographical" north, north east, etc. are defined. A couple of examples follow:



3.23 Amplifiers

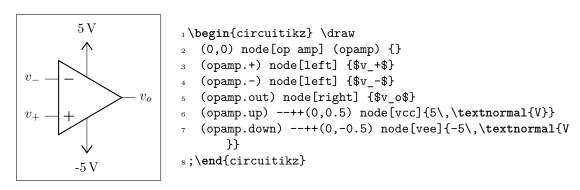


⁸Contributed by Kristofer M. Monisit.

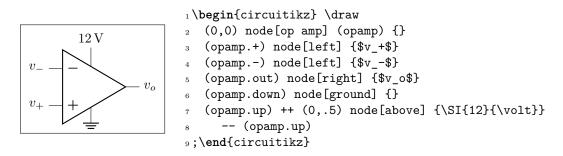


3.23.1 Amplifiers anchors

The op amp defines the inverting input (-), the non-inverting input (+) and the output (out) anchors:



There are also two more anchors defined, up and down, for the power supplies:

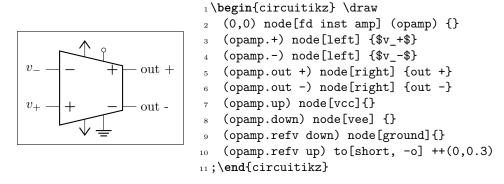


The fully differential op amp defines two outputs:

The instrumentation amplifier inst amp defines also references (normally you use the "down", unless you are flipping the component):

```
1 \begin{circuitikz} \draw
2 (0,0) node[inst amp] (opamp) {}
3 (opamp.+) node[left] {$v_+$}
4 (opamp.-) node[left] {$v_-$}
5 (opamp.out) node[right] {out}
6 (opamp.up) node[vcc] {}
7 (opamp.down) node[vee] {}
8 (opamp.refv down) node[ground] {}
9 (opamp.refv up) to[short, -o] ++(0,0.3)
10;\end{circuitikz}
```

The fully diffential instrumentation amplifier inst amp defines two outputs:

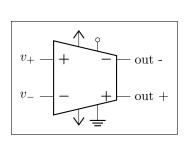


The instrumentation amplifier with resistance terminals (inst amp ra) defines also terminals to add an amplification resistor:

```
| \begin{circuitikz} \draw \\ 2 & (0,0) & node[inst amp ra] & (opamp) \{\} \\ 3 & (opamp.+) & node[left] \{\$v_-+\$\} \\ 4 & (opamp.out) & node[right] \{\$v_--\$\} \\ 5 & (opamp.out) & node[right] \{\$out\} \\ 6 & (opamp.up) & node[vec] \{\} \\ 7 & (opamp.down) & node[vee] \{\} \\ 8 & (opamp.refv down) & node[ground] \{\} \\ 9 & (opamp.refv up) & to[short, -o] & ++(0,0.3) \\ 10 & (opamp.ra-) & to[R] & (opamp.ra+) \\ 11 & \$\$\end{circuitikz}
```

3.23.2 Amplifiers cutomization

All these amplifier have the possibility to flip input and output (if needed) polarity. You can change polarity of the input with the noinv input down (default) or noinv input up key; and the output with noinv output up (default) or noinv output down key:



```
1 \begin{circuitikz} \draw
2 (0,0) node[fd inst amp,
3    noinv input up,
4    noinv output down] (opamp) {}
5    (opamp.+) node[left] {$v_+$}
6    (opamp.-) node[left] {$v_-$}
7    (opamp.out +) node[right] {out +}
8    (opamp.out -) node[right] {out -}
9    (opamp.up) node[vcc]{}
10    (opamp.down) node[vee] {}
11    (opamp.refv down) node[ground]{}
12    (opamp.refv up) to[short, -o] ++(0,0.3)
13; \end{circuitikz}
```

When you use the noinv input/output ... keys the anchors (+, -, out +, out -) will change with the effective position of the terminals. You have also the anchors in up, in down, out up, out down that will not change with the positive or negatie sign.

3.24 Support shapes and bipoles

Path style:

```
crossing: Jumper style non-contact crossing, type:
path-style, nodename: crossingshape. Aliases:
xing.
```

Node style:

•	Arrows (current and voltage), type: node (node[currarrow]{})
-	Arrow to draw at its tip, useful for block diagrams., type: node (node[inputarrow]{})
•	Connected terminal, type: node (node[circ]{})
0	Unconnected terminal, type: node (node[ocirc]{})
•	Diamond-style terminal, type: node (node[diamondpole]{})
	<pre>Jumper-style crossing node, type: node (node[jump crossing]{})</pre>
+	Plain style crossing node, type: node (node[plain crossing]{})
north west north top nort	h east
left west)))))) right east	Waves, type: node (node[waves]{})
south west bottom south	h east

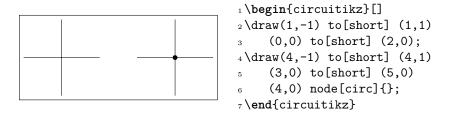
3.24.1 Terminal shapes

Since version 0.8.4, circ, ocirc, and diamondpole have all the standard geographical anchors, so you can do things like these:

```
1\begin{circuitikz}[american,]
2   \draw (0,-1) node[draw](R){R};
3   \draw (R.east) node[ocirc, right]{};
4\end{circuitikz}
```

3.24.2 Crossings

All circuit-drawing standards agree that to show a crossing without electric contact, a simple crossing of the wires suffices; the electrical contact must be explicitly marked with a filled dot.



However, sometime it is advisable to mark the non-contact situation more explicitly. To this end, you can use a path-style component called **crossing**:

```
1\begin{circuitikz}[]
2\draw(1,-1) to[short] (1,1) (0,0) to[crossing]
(2,0);
3\draw(4,-1) to[short] (4,1) (3,0) to[short]
(5,0)
4 (4,0) node[circ]{};
5\end{circuitikz}
```

That should suffice most of the time; the only problem is that the crossing jumper will be put in the center of the subpath where the to[crossing] is issued, so sometime a bit of trials and errors are needed to position it.

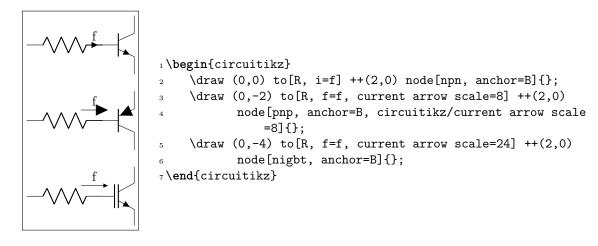
For a more powerful (and elegant) way you can use the crossing nodes:

Notice that the plain crossing and the jump crossing have a small gap in the straight wire, to enhance the effect of crossing (as a kind of shadow).

The size of the crossing elements can be changed with the key bipoles/crossing/size (default 0.2).

3.24.3 Arrows size

You can use the parameter current arrow scale to change the size of the arrows in various components and indicators; the normal value is 16, higher numbers give smaller arrows and so on. You need to use circuitikz/current arrow scale if you use it into a node.



3.25 Switches and buttons

Switches and button come in to-style (the simple ones and the pushbuttons), and as nodes.

3.25.1 Traditional switches

These are all of the to-style type:

X	switch: Switch, type: path-style, nodename: cspstshape. Aliases: spst.
	closing switch: Closing switch, type: path-style, nodename: cspstshape. Aliases: cspst.
	opening switch: Opening switch, type: path-style, nodename: ospstshape. Aliases: ospst.
	normal open switch: Normally open switch, type: path-style, nodename: nosshape. Aliases: nos.
	normal closed switch: Normally closed switch, type: path-style, nodename: ncsshape. Aliases: ncs.
	<pre>push button: Normally open push button, type: path-style, nodename: pushbuttonshape. Aliases: normally open push button, nopb.</pre>
	normally closed push button: Normally closed push button, type: path-style, nodename: ncpushbuttonshape. Aliases: ncpb.
	toggle switch: Toggle switch, type: path-style, nodename: toggleswitchshape.

While this is a node-style component

```
in—o—out 1
spdt, type: node (node[spdt]{})
```

3.25.2 Cute switches

These switches have been introduced after version 0.8.4, and they come in also in to-style and in node-style, but they are size-matched so that they can be used together in a seamless way.

The path element (to-style) are:

mid in out	cute closed switch: Cute closed switch, type: path-style, nodename: cuteclosedswitchshape. Aliases: ccsw.
	cute open switch: Cute open switch, type: path-style, nodename: cuteopenswitchshape. Aliases: cosw.
	cute closing switch: Cute closing switch, type: path-style, nodename: cuteclosingswitchshape. Aliases: ccgsw.
	cute opening switch: Cute opening switch, type: path-style, nodename: cuteopeningswitchshape. Aliases: cogsw.

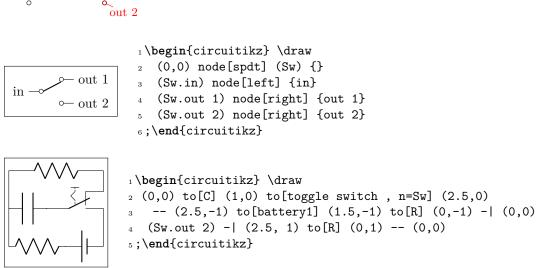
while the node-style components are the single-pole, double-throw (spdt) ones:

in—out 1 o—out 2	Cute spdt up, type: node (node[cute spdt up]{})
° ° ° ° ° ° ° ° ° ° ° ° ° ° ° ° ° ° °	Cute spdt mid, type: node (node[cute spdt mid]{})
mid	Cute spdt down, type: node (node[cute spdt down]{})
%	Cute spdt up with arrow, type: node (node[cute spdt up arrow]{})
\bigcirc	Cute spdt mid with arrow, type: node (node[cute spdt mid arrow]{})
\sim	Cute spdt down with arrow, type: node (node[cute spdt down arrow]{})

3.25.3 Switches anchors

The nodes-style switches have the following anchors:





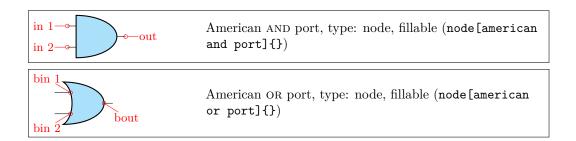
The mid anchor in the cute switches (both path- and node-style) can be used to combine switches to get more complex configurations:

```
1\begin{circuitikz}
2 \draw (0,1.4) node[cute spdt up](S1){};
3 \draw (0,0) node[cute spdt up](S2){};
4 \draw (0,-1) node[cuteclosedswitchshape, yscale=-1](S3){};
5 \draw [densely dashed] (S1.mid)--(S2.mid)--(S3.mid);
6 \end{circuitikz}
```

3.26 Logic gates

Logic gates, with two or more input, are supported. Albeit in principle these components are multipoles, the are considered tripoles here, for historical reasons (when they just had two inputs).

3.26.1 American Logic gates



American NOT port, type: node, fillable (node[american not port]{})
American NAND port, type: node, fillable (node[american nand port]{})
American NOR port, type: node, fillable (node[american nor port]{})
American XOR port, type: node, fillable (node[american xor port]{})
American XNOR port, type: node, fillable (node[american xnor port]{})

3.26.2 European Logic gates

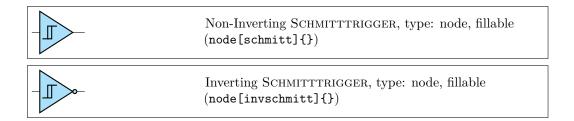
in 1—o— & —o—out	European AND port, type: node, fillable (node[european and port]{})
<u>≥</u> 1	European OR port, type: node, fillable (node[european or port]{})
1	European NOT port, type: node, fillable (node[european not port]{})
&	European NAND port, type: node, fillable (node[european nand port]{})
<u>≥1</u>	European NOR port, type: node, fillable (node[european nor port]{})
= 1	European XOR port, type: node, fillable (node[european xor port]{})
= 1	European XNOR port, type: node, fillable (node[european xnor port]{})

If (default behaviour) americanports option is active (or the style [american ports] is used), the shorthands and port, or port, not port, nand port, not port, xor port, and xnor port are equivalent to the american version of the respective logic port.

If otherwise europeanports option is active (or the style [european ports] is used), the shorthands and port, or port, not port, nand port, not port, xor port, and xnor port are equivalent to the european version of the respective logic port.

3.26.3 Special components

There is no "european" version of these symbols.



3.26.4 Logic port customization

As for most components, you can change the width and height of the ports; the thickness is given by the parameter tripoles/thickness (default 2):

```
1\tikz \draw (0,0) node[nand port] {}; \par
2\ctikzset{tripoles/american nand port/input height=.2}
3\ctikzset{tripoles/american nand port/port width=.4}
4\ctikzset{tripoles/thickness=4}
5\tikz \draw (0,0) node[nand port] {};
```

1 \begin{circuitikz}

This is especially useful if you have ports with more than two inputs, which are instantiated with the parameter number inputs:

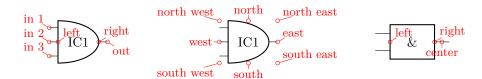
```
2\draw (0,3) node[american and port] (A) {P1};
3\begin{scope}
4 \ctikzset{tripoles/american or port/height=1.6}
5 \draw (A.out) -- ++(0.5,0)
6 node[american or port,
7 number inputs=5,
8 anchor=in 1] (B) {P2};
9\end{scope}
10\draw (0,1.5) node[american or port] (C) {P3};
11\draw (C.out) |- (B.in 2);
12\end{circuitikz}
```

You can tweak the appearance of american "or" family (or, nor, xor and xnor) ports, too, with the parameters inner (how much the base circle go "into" the shape, default 0.3) and angle (the angle at which the base starts, default 70).

```
1\tikz \draw (0,0) node[xnor port] {};
2\ctikzset{tripoles/american xnor port/inner=.7};
3\ctikzset{tripoles/american xnor port/angle=40};
4\tikz \draw (0,0) node[xnor port] {};
```

3.26.5 Logic port anchors

These are the anchors for logic ports:



You have also "border pin anchors":



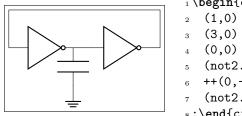
These anchors are especially useful if you want to negate inputs:

```
1\begin{circuitikz}
2\draw (0,3) node[american and port] (A) {P1};
3\node at (A.bin 1) [ocirc, left]{};
4\begin{scope}
5 \ctikzset{tripoles/american or port/height=1.6}
6 \draw (A.out) -- ++(0.5,0) node[american or port,
7 number inputs=5, anchor=in 1] (B) {P2};
8 \node at (B.bin 3) [ocirc, left]{};
9\end{scope}
10\draw (0,1.5) node[american or port] (C) {P3};
11\node at (C.bin 2) [ocirc, left]{};
12\draw (C.out) |- (B.in 2);
13\end{circuitikz}
```

As you can see, the center anchor is (for historic reasons) not in the center at all. You can fix this with the command \ctikzset{logic ports origin=center}:

```
1\begin{circuitikz}
         2\ctikzset{logic ports origin=center}
         3 \draw (0,0) node[and port] (myand) {}
            (myand.in 1) node[anchor=east] {1}
            (myand.in 2) node[anchor=east] {2}
            (myand.out) node[anchor=west] {3};
center
         7 \draw[<-] (myand.center) -- ++(1,-1)
            node{center};
         9 \end{circuitikz}
             1\begin{circuitikz} \draw
                (0,2) node[and port] (myand1) {}
                (0,0) node[and port] (myand2) {}
                (2,1) node[xnor port] (myxnor) {}
                (myand1.out) -| (myxnor.in 1)
                (myand2.out) -| (myxnor.in 2)
             7;\end{circuitikz}
```

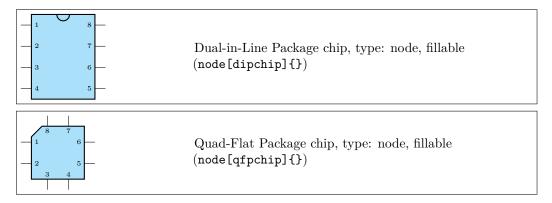
In the case of NOT, there are only in and out (although for compatibility reasons in 1 is still defined and equal to in):



```
1\begin{circuitikz} \draw
2  (1,0) node[not port] (not1) {}
3  (3,0) node[not port] (not2) {}
4  (0,0) -- (not1.in)
5  (not2.in) -- (not1.out)
6  ++(0,-1) node[ground] {} to[C] (not1.out)
7  (not2.out) -| (4,1) -| (0,0)
8 ;\end{circuitikz}
```

3.27 Chips

CircuiTikZ supports two types of variable-pin chips: DIP (Dual-in-Line Package) and QFP (Quad-Flat Package).



3.27.1 DIP and QFP chips customization

You can customize the DIP chip with the keys multipoles/dipchip/width (default 1.2) and multipoles/dipchip/pin spacing (default 0.4) that are expressed in fraction of basic lengths (see section 3.1.2). The height of the chip will be equal to half the numbers of pins multiplied by the spacing, plus one spacing for the borders. For the QFP chips, you can only chose the pin spacing with multipoles/qfpchip/pin spacing key.

The pins of the chip can be "hidden" (that is, just a spot in the border, optionally marked with a number) or "stick out" with a thin lead by setting multipoles/external pins width greater than 0 (default value is 0.2, so you'll have leads as shown above). Moreover, you can transform the thin lead into a pad by setting the key multipoles/external pad fraction to something different form 0 (default is 0); the value expresses the fraction of the pin spacing space that the pad will use on both sides of the pin.

The number of pins is settable with the key num pins. Please notice that the number of pins must be even for dipchips and multiple of 4 for qfpchips, otherwise havoc will ensue.

You can, if you want, avoid printing the numbers of the pin with hide numbers (default show numbers) if you prefer positioning them yourself (see the next section for the anchors you can use). The font used for the pins is adjustable with the key multipoles/font (default \tiny) For special use you can suppress the orientation mark with the key no topmark (default topmark).

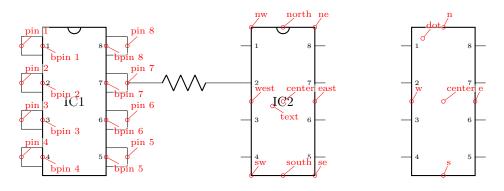
The line thickness of the main shape is controlled by multipoles/thickness (default 2) and the one of the external pins/pads with multipoles/external pins thickness (default 1).

```
\begin{circuitikz}
                      \ctikzset{multipoles/thickness=4}
                      \ctikzset{multipoles/external pins thickness=2}
RST
                      \draw (0,0) node[dipchip,
                        num pins=12,
                        hide numbers,
              6
  IC1
                        external pins width=0.3,
                        external pad fraction=4 ](C){IC1};
                      \draw (C.pin 1) -- ++(-0.5,0) to[R]
                        ++(0,-3) node[ground]{};
              10
                        \node [right, font=\tiny]
              11
                        at (C.bpin 1) {RST};
                   \end{circuitikz}
              \begin{circuitikz}
                  \draw (0,0) node[dipchip,
                   num pins=8,
IC1
                   external pins width=0.0](C){IC1};
                  \det (C.pin 1) -- ++(-0.5,0) to[R]
         5
                   ++(0,-1.5) node[ground]{};
              \end{circuitikz}
      П
                      \begin{circuitikz}
                         \ctikzset{multipoles/font={\color{red}\tiny}}
                         \draw (0,0) node[qfpchip,
                           num pins=16,
   IC1
                           external pad fraction=6](C){IC1};
                          \det (C.pin 1) -- ++(-0.5,0) to[R]
                 6
                           ++(0,-2) node[ground]{};
      \end{circuitikz}
```

3.27.2 Chips anchors

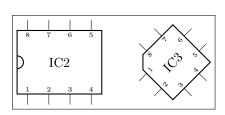
Chips have anchors on pins and global anchors for the main shape. The pin anchors to be used to connect wires to the chip are called pin 1, pin 2, ..., with just one space between pin and the number. Border pin anchors (bpin 1...) are always on the box border, and can be used to add numbers or whatever markings are needed. Obviously, in case of multipoles/external pins width equal to cero, border and normal pin anchors will coincide.

Additionally, you have geometrical anchors on the chip "box", see the following figure. The nodes are available with the full name (like north) and with the short abbreviations n, nw, w.... The dot anchor is useful to add a personalized marker if you use the no topmark key.



3.27.3 Chips rotation

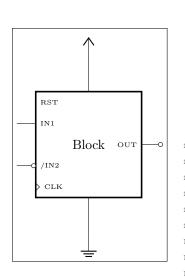
You can rotate chips, and normally the pin numbers are kept straight (option straight numbers, which is the default), but you can rotate them if you like with rotated numbers. Notice that the main label has to be (counter-)rotated manually in this case.



```
1 \begin{circuitikz}
2      \draw (0,0) node[dipchip,
3         rotate=90]{%
4       \rotatebox{-90}{IC2}};
5      \draw (3,0) node[qfpchip,
6         rotated numbers,
7         rotate=45]{IC3};
8 \end{circuitikz}
```

3.27.4 Chip special usage

You can use the chips to have special, personalized blocks. Look at the following example, which is easily put into a macro.



```
1 \begin{circuitikz}
     \ctikzset{multipoles/thickness=3}
     \ctikzset{multipoles/dipchip/width=2}
     \draw (0,0) node[dipchip,
        num pins=10, hide numbers, no topmark,
        external pins width=0](C){Block};
     \node [right, font=\tiny] at (C.bpin 1) {RST};
     \node [right, font=\tiny] at (C.bpin 2) {IN1};
     \node [right, font=\tiny] at (C.bpin 4) {/IN2};
     \node [left, font=\tiny] at (C.bpin 8) {OUT};
     \draw (C.bpin 2) -- ++ (-0.5,0) coordinate(extpin);
11
     \node [ocirc, anchor=0](notin2) at (C.bpin 4) {};
12
     \draw (notin2.180) -- (C.bpin 4 -| extpin);
     \draw (C.bpin 8) to[short,-o] ++(0.5,0);
     draw (C.bpin 5) ++(0,0.1) -- ++(0.1,-0.1)
15
        node[right, font=\tiny]{CLK} -- ++(-0.1,-0.1);
16
     \draw (C.n) -- ++(0,1) node[vcc]{};
     \draw (C.s) -- ++(0,-1) node[ground]{};
19 \end{circuitikz}
```

4 Labels and similar annotations

```
1\begin{circuitikz}
2 \draw (0,0) to[R, v=$v_1$] (2,0);
3\end{circuitikz}

1\begin{circuitikz}
2 \draw (0,0) to[R=$R_1$, i=$i_1$, v=$v_1$] (2,0);
3\end{circuitikz}

R_1 i_1 i_1 2 \draw (0,0) to[R=$R_1$, i=$i_1$, v=$v_1$] (2,0);
3\end{circuitikz}

1\begin{circuitikz}
2 \draw (0,0) to[R=$R_1$, i=$i_1$, v=$v_1$] (2,0);
3\end{circuitikz}
```

Long names/styles for the bipoles can be used:

4.1 Labels and Annotations

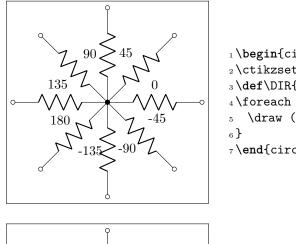
Since Version 0.7, beside the original label (l) option, there is a new option to place a second label, called annotation (a) at each bipole. Up to now this is a beta-test and there can be problems. For example, up to now this option is not compatible with the concurrent use of voltage labels.

The position of (a) and (l) labels can be adjusted with _ and , respectively.

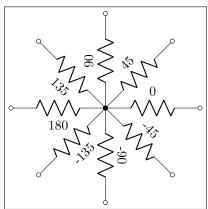
```
R_1 \longrightarrow \text{lbegin}\{\text{circuitikz}\}
2 \longrightarrow \text{draw } (0,0) \text{ to}[R, 1=\$R_1\$, a=1<\mathbb{n}\} \}
3 \longrightarrow \text{draw } (0,0) \text{ to}[R, 1=\$R_1\$, a=1<\mathbb{n}\} \}
4 \longrightarrow \text{draw } (0,0) \text{ to}[R, 1_=\$R_1\$, a^=1<\mathbb{n}\} \}
R_1 \longrightarrow \text{draw } (0,0) \text{ to}[R, 1_=\$R_1\$, a^=1<\mathbb{n}\} \}
3 \longrightarrow \text{draw } (0,0) \text{ to}[R, 1_=\$R_1\$, a^=1<\mathbb{n}\} \}
```

Caveat: notice that the way in which circuitikz processes the options, there will be problems if the label (or annotation, or voltage, or current) contains one of the characters = (equal) or , (comma), giving unexpected errors and wrong output. These two characters must be protected to the option parser using an \mbox{mbox} command, or redefining the characters with a $\mbox{T}_EX \mbox{def}$:

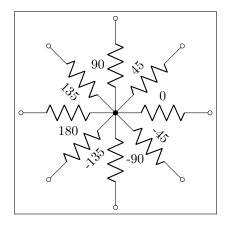
The default orientation of labels is controlled by the options smartlabels, rotatelabels and straightlabels (or the corresponding label/align keys). Here are examples to see the differences:



```
1\begin{circuitikz}
2\ctikzset{label/align = straight}
3\def\DIR{0,45,90,135,180,-90,-45,-135}
4\foreach \i in \DIR {
5 \draw (0,0) to[R=\i, *-o] (\i:2.5);
6}
7\end{circuitikz}
```



```
1 \begin{circuitikz}
2 \ctikzset{label/align = rotate}
3 \def\DIR{0,45,90,135,180,-90,-45,-135}
4 \foreach \i in \DIR {
5 \draw (0,0) to[R=\i, *-o] (\i:2.5);
6 }
7 \end{circuitikz}
```



```
1 \begin{circuitikz}
2 \ctikzset{label/align = smart}
3 \def\DIR{0,45,90,135,180,-90,-45,-135}
4 \foreach \i in \DIR {
5 \draw (0,0) to[R=\i, *-o] (\i:2.5);
6}
7 \end{circuitikz}
```

You also can use stacked (two lines) labels. The example should be self-explanatory: the two lines are specified as 12=line1 and line2. You can use the keys 12 halign to control horizontal position (left, center, right) and 12 valign to control the vertical one (bottom, ccenter, top).

```
\begin{circuitikz}[ american, ]
                            2
                                     % default is 12 halign=1, 12 valign=c
                            3
            R_{CC}
R_{CC}
                                     \draw (0,0) to [R, 12_=$R_{CC}$ and <math>SI{4.7}{k}
            4.7\,\mathrm{k}\Omega
                                                    , 12 valign=t] (2,0);
4.7 \,\mathrm{k}\Omega
                                          ohm},
                                     \draw (0,0) to [R, 12_=$R_{CC}$ and <math>SI{4.7}{k}
                                          ohm},
                                                                  ] (0,2);
             R_{CC}
  R_{CC}
                                     draw (0,0) to [R, 12_=$R_{CC}$ and <math>SI\{4.7\}\{k\}
             4.7\,\mathrm{k}\Omega
 4.7\,\mathrm{k}\Omega
                                          ohm}, 12 halign=c, 12 valign=b] (-2,0);
                                     \draw (0,0) to [R, 12_=$R_{CC}$ and <math>SI{4.7}{k}
                                          ohm}, 12 halign=r, 12 valign=c] (0, -2);
                                 \end{circuitikz}
                                 \begin{circuitikz}[ american, ]
 R_{CC}
                                     draw (0,0) to [R, 12^=$R_{CC}$ and <math>SI{4.7}{k}
              R_{CC}
 4.7 \,\mathrm{k}\Omega
                                          ohm}, 12 halign=c, 12 valign=b] (2,0);
             4.7 \,\mathrm{k}\Omega
                                     \draw (0,0) to [R, 12^=$R_{CC}$ and <math>SI_{4.7}_{k}
                                          ohm}, 12 halign=c, ] (0,2);
R_{CC}
                                     \draw (0,0) to [R, 12^=$R_{CC}$ and <math>SI{4.7}{k}
4.7\,\mathrm{k}\Omega
                                                  , 12 valign=t] (-2,0);
                                          ohm},
             R_{CC}
                                     \draw (0,0) to [R, 12^=$R_{CC}$ and <math>SI{4.7}{k}
                                          ohm}, 12 halign=c, 12 valign=t](0, -3);
                                 \end{circuitikz}
                            6
```

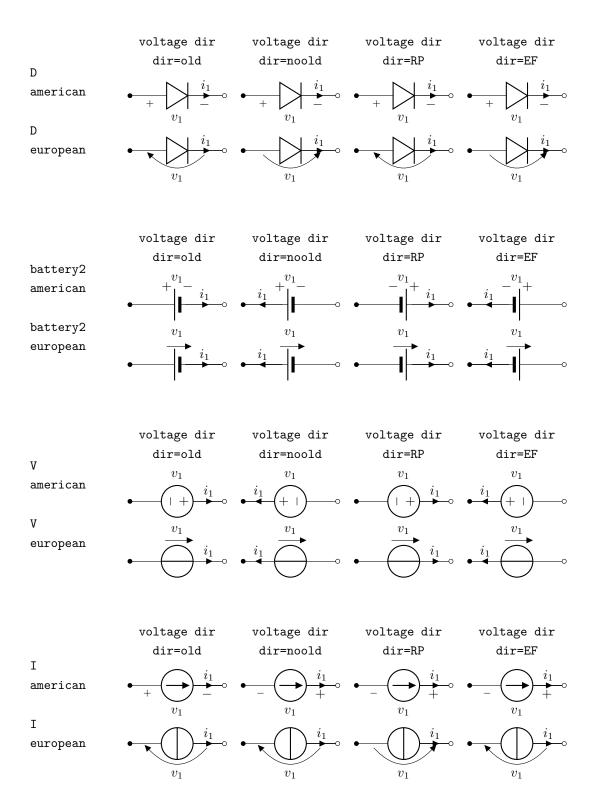
4.2 Currents and voltages

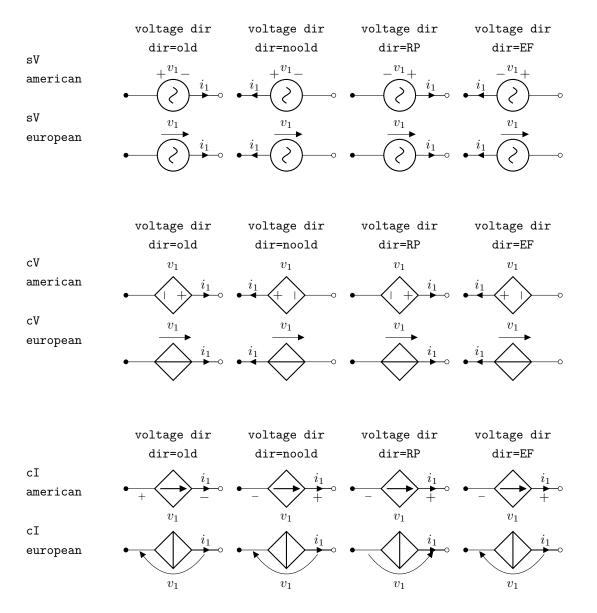
The default direction/sign for currents and voltages in the components is, unfortunately, not standard, and can change across country and sometime across different authors. This unfortunate situation created a bit of confusion in circuitikz across the versions, with several incompatible changes starting from version 0.5. From version 0.8.4 onward, the maintainers agreed a new policy for the directions of bipoles's voltages and currents, depending on 4 different possible options:

- oldvoltagedirection, or the key style voltage dir=old: Use old way of voltage direction having a difference between european and american direction, with wrong default labelling for batteries (it was the default before version 0.5);
- nooldvoltagedirection, or the key style voltage dir=noold: The standard from version 0.5 onward, utilize the (German?) standard of voltage arrows in the direction of electric fields (without fixing batteries);
- RPvoltages (meaning Rising Potential voltages), or the key style voltage dir=RP: the arrow is in direction of rising potential, like in oldvoltagedirections, but batteries and current sources are fixed so that they follow the passive/active standard: the default direction of v and i are chosen so that, when both values are positive:
 - in passive component, the element is dissipating power;
 - in active components (generators), the element is *generating power*.
- EFvoltages (meaning Electric Field voltages), or the key style voltage dir=EF: the arrow is in direction of the electric field, like in nooldvoltagedirections, but batteries are fixed;

The standard direction of currents, flows and voltages are changed by these options; notice that the default drops in case of passive and active elements is normally different. Take care that in the case of noold and EFvoltages also the currents can switch directions. It is much easier to understand the several behaviors by looking at the following examples, that have been generated by the code:

```
1\foreach\element in {R, C, D, battery2, V, I, sV, cV, cI}{%
     \noindent\ttfamily
     \begin{tabular}{p{2cm}}
3
        \element \\ american \\[15pt]
4
        \element \\ european \\
5
     \end{tabular}
6
     \foreach\mode in {old, noold, RP, EF} {
        \multicolumn{1}{c}{voltage dir} \\
            \multicolumn{1}{c}{dir=\mode} \\[4pt]
10
            \begin{tikzpicture}[
11
               american, voltage dir=\mode,
13
               draw (0,0) to[\ell, *-o, v=$v_1$, i=$i_1$, ] (2.5,0);
14
            \end{tikzpicture}\\
15
            \begin{tikzpicture}[
16
               european, voltage dir=\mode,
17
18
               draw (0,0) to[\ell, *-o, v=$v_1$, i=$i_1$, ] (2.5,0);
19
            \end{tikzpicture}
        \end{tabular}
21
        \medskip
22
     }
23
     \par
24
25 }
                voltage dir
                                 voltage dir
                                                  voltage dir
                                                                  voltage dir
                   dir=old
                                  dir=noold
                                                    dir=RP
                                                                     dir=EF
R
american
R
european
                                 voltage dir
                voltage dir
                                                  voltage dir
                                                                  voltage dir
                   dir=old
                                  dir=noold
                                                    dir=RP
                                                                     dir=EF
С
american
С
european
                                      v_1
```





Obviously, you normally use just one between current and flows, but anyway you can change direction of the voltages, currents and flows using the complete keys $i_>$, $i^<$, $i^>$, as shown in the following examples.

This manual has been typeset with the option RPvoltages.

4.3 Currents

In line (along the wire) currents are selected with $i_>$, $i^<$, $i^>$, and various simplification; the default position and direction is obtained with the key i=...

```
1 \begin{circuitikz}
                2 \draw (0,0) to[R, i_>=$i_1$] (2,0);
                3 \end{circuitikz}
                1 \begin{circuitikz}
                2 \draw (0,0) to[R, i^<=$i_1$] (2,0);</pre>
                3 \end{circuitikz}
                1 \begin{circuitikz}
                2 \draw (0,0) to[R, i_<=$i_1$] (2,0);</pre>
                3 \end{circuitikz}
                1 \begin{circuitikz}
                2 \draw (0,0) to[R, i>^=$i_1$] (2,0);
                3 \end{circuitikz}
                1 \begin{circuitikz}
                2 \draw (0,0) to[R, i>_=$i_1$] (2,0);
                3 \end{circuitikz}
                1 \begin{circuitikz}
                2 \draw (0,0) to[R, i<^=$i_1$] (2,0);</pre>
                3 \end{circuitikz}
                1\begin{circuitikz}
                2 \draw (0,0) to[R, i<_=$i_1$] (2,0);</pre>
                3 \end{circuitikz}
Also
                1 \begin{circuitikz}
                2 \draw (0,0) to[R, i<=$i_1$] (2,0);</pre>
                3 \end{circuitikz}
                1 \begin{circuitikz}
                2 \draw (0,0) to[R, i>=$i_1$] (2,0);
                3 \end{circuitikz}
                1\begin{circuitikz}
                2 \draw (0,0) to[R, i^=$i_1$] (2,0);
                3 \end{circuitikz}
                1 \begin{circuitikz}
                2 \draw (0,0) to[R, i_=$i_1$] (2,0);
                3 \end{circuitikz}
    10V
               1 \begin{circuitikz}
                   draw (0,0) to [V=10V, i_=$i_1$] (2,0);
               3\end{circuitikz}
```

```
10V
           1\begin{circuitikz}
              draw (0,0) to[V <= 10V, i_= i_1 (2,0);
           3\end{circuitikz}
10V
           1 \begin{circuitikz} [american]
              draw (0,0) to[V=10V, i_=$i_1$] (2,0);
           3 \end{circuitikz}
10V
           1 \begin{circuitikz}[american]
           2 \draw (0,0) to[V=10V,invert, i_=$i_1$] (2,0);
+ 1
           3 \end{circuitikz}
1A
           1 \begin{circuitikz} [american]
              draw (0,0) to[dcisource=1A, i_=$i_1$] (2,0);
           3\end{circuitikz}
1 A
           1 \begin{circuitikz}[american]
              \draw (0,0) to[dcisource=1A,invert, i_=$i_1$] (2,0);
           3 \end{circuitikz}
```

4.4 Flows

As an alternative for the current arrows, you can also use the following flows. They can also be used to indicate thermal or power flows. The syntax is pretty the same as for currents.

This is a new beta feature since version 0.8.3, therefore, please provide bugreports or hints to optimize this feature regarding placement and appearance! This means, that the appearance may change in the future!

```
1\begin{circuitikz}
2 \draw (0,0) to[R, f<_=$i_1$] (3,0);
3\end{circuitikz}

1\begin{circuitikz}
2 \draw (0,0) to[R, f>_=$i_1$] (3,0);
3\end{circuitikz}
```

4.5 Voltages

See introduction note at Currents (chapter 4.2, page 61)!

4.5.1 European style

The default, with arrows. Use option europeanvoltage or style [european voltages].

```
_{\text{1}} \verb| begin{circuitikz}| [european voltages]|
v_1
           2 \draw (0,0) to[R, v^>=$v_1$] (2,0);
           3 \end{circuitikz}
           1 \begin{circuitikz}[european voltages]
           2 \draw (0,0) to[R, v^<=$v_1$] (2,0);</pre>
           3\end{circuitikz}
           1 \begin{circuitikz}[european voltages]
           2 \draw (0,0) to[R, v_>=$v_1$] (2,0);
           3\end{circuitikz}
           1 \begin{circuitikz}[european voltages]
           draw (0,0) to[R, v_<=$v_1$] (2,0);</pre>
           3\end{circuitikz}
10V
           1\begin{circuitikz}
              draw (0,0) to [V=10V, i_=$i_1$] (2,0);
           3 \end{circuitikz}
10V
           1\begin{circuitikz}
               draw (0,0) to[V <= 10V, i_= i_1 (2,0);
           3\end{circuitikz}
           1 \begin{circuitikz}
               draw (0,0) to[I=1A, v_=$u_1$] (2,0);
           3 \end{circuitikz}
```

1 \begin{circuitikz}

3 \end{circuitikz}

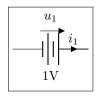
 $draw (0,0) to[I <= 1A, v_= u_1 (2,0);$



```
1\begin{circuitikz}
2 \draw (0,0) to[I=$~$,l=1A, v_=$u_1$] (2,0);
3\end{circuitikz}
```



```
1 \begin{circuitikz}
2 \draw (0,0) to[I,l=1A, v_=$u_1$] (2,0);
3 \end{circuitikz}
```



```
1 \begin{circuitikz}
2    \draw (0,0) to[battery,l_=1V, v=$u_1$, i=$i_1$] (2,0);
3 \end{circuitikz}
```

4.5.2 American style

Use option americanvoltage or set [american voltages].



```
1\begin{circuitikz}[american voltages]
2 \draw (0,0) to[R, v^>=$v_1$] (2,0);
3\end{circuitikz}
```



1\begin{circuitikz}[american voltages]
2 \draw (0,0) to[R, v^<=\$v_1\$] (2,0);
3\end{circuitikz}</pre>



1\begin{circuitikz}[american voltages]
2 \draw (0,0) to[R, v_>=\$v_1\$] (2,0);
3\end{circuitikz}



1\begin{circuitikz}[american voltages]
2 \draw (0,0) to[R, v_<=\$v_1\$] (2,0);
3\end{circuitikz}</pre>



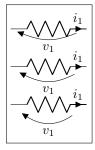
1\begin{circuitikz}[american]
2 \draw (0,0) to[I=1A, v_=\$u_1\$] (2,0);
3\end{circuitikz}



1\begin{circuitikz}[american]
2 \draw (0,0) to[I<=1A, v_=\$i_1\$] (2,0);
3\end{circuitikz}</pre>

4.5.3 Voltage position

It is possible to move away the arrows and the plus or minus signs with the key voltages shift (default value is 0, which gives the standard position):

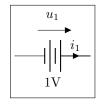


```
1 \begin{circuitikz}[]
2   \draw (0,0) to[R, v=$v_1$, i=$i_1$] (2,0);
3   \draw (0,-1) to[R, v=$v_1$, i=$i_1$,
4     voltage shift=0.5] (2,-1);
5   \draw (0,-2) to[R, v=$v_1$, i=$i_1$,
6     voltage shift=1.0, ] (2,-2);
7 \end{circuitikz}
```



```
1\begin{circuitikz}[american voltages, voltage shift=0.5]
2 \draw (0,0) to[R, v=$v_1$, i=$i_1$] (2,0);
3\end{circuitikz}
```

Notes that american voltage will not affect batteries.



```
begin{circuitikz}[voltage shift=0.5]
draw (0,0) to[battery,l_=1V, v=$u_1$, i=$i_1$] (2,0);
hend{circuitikz}
```



```
1 \begin{circuitikz}[american voltages, voltage shift=0.5]
2 \draw (0,0) to[battery,l_=1V, v=$u_1$, i=$i_1$] (2,0);
3 \end{circuitikz}
```

4.5.4 Global properties of voltages and currents



```
1\tikz \draw (0,0) to[R, v=1<\volt>] (2,0); \par
2\ctikzset{voltage/distance from node=.1}
3\tikz \draw (0,0) to[R, v=1<\volt>] (2,0);
```



```
1\tikz \draw (0,0) to[C, i=$\imath$] (2,0); \par
2\ctikzset{current/distance = .2}
3\tikz \draw (0,0) to[C, i=$\imath$] (2,0);
```

However, you can override the properties voltage/distance from node⁹, voltage/bump b¹⁰ and voltage/european label distance¹¹ on a per-component basis, in order to fine-tune the voltages:

⁹That is, how distant from the initial and final points of the path the arrow starts and ends.

 $^{^{10}\}mathrm{Controlling}$ how high the bump of the arrow is — how curved it is.

¹¹Controlling how distant from the bipole the voltage label will be.

```
1V 2V
```

```
1\tikz \draw (0,0) to[R, v=1<\volt>] (1.5,0)
2     to[C, v=2<\volt>] (3,0); \par
3\ctikzset{bipoles/capacitor/voltage/%
4     distance from node/.initial=.7}
5\tikz \draw (0,0) to[R, v=1<\volt>] (1.5,0)
6     to[C, v=2<\volt>] (3,0); \par
```

4.6 Nodes

```
1\begin{circuitikz}
   draw (0,0) to [R, o-o] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
   draw (0,0) to[R, -o] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
2 \draw (0,0) to[R, o-] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
2 \draw (0,0) to[R, *-*] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
2 \draw (0,0) to[R, -*] (2,0);
3\end{circuitikz}
1 \begin{circuitikz}
   draw (0,0) to[R, *-] (2,0);
3\end{circuitikz}
1 \begin{circuitikz}
   draw (0,0) to[R, d-d] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
2 \draw (0,0) to[R, -d] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
2 \draw (0,0) to[R, d-] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
2 \draw (0,0) to[R, o-*] (2,0);
3 \end{circuitikz}
```

```
| \begin{circuitikz}
| \draw (0,0) to[R, *-o] (2,0);
| \draw (0,0) to[R, *-o] (2,0);
| \draw (0,0) to[R, o-d] (2,0);
| \draw (0,0) to[R, o-d] (2,0);
| \draw (0,0) to[R, d-o] (2,0);
| \draw (0,0) to[R, d-o] (2,0);
| \draw (0,0) to[R, *-d] (2,0);
| \draw (0,0) to[R, *-d] (2,0);
| \draw (0,0) to[R, d-*] (2,0);
| \draw (
```

4.7 Special components

For some components label, current and voltage behave as one would expect:

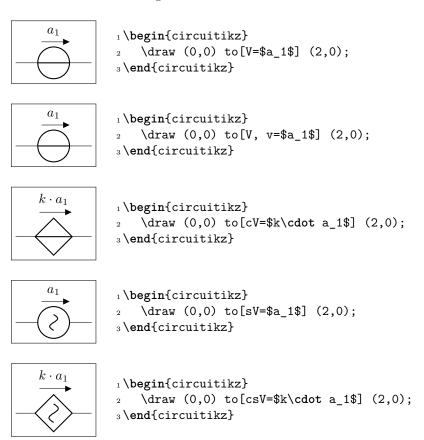
```
| \lambda \text{login} \{ \text{circuitikz} \} \\ 2 \ \draw (0,0) \to[I=\$a_1\$] (2,0); \\ 3 \end\{ \text{circuitikz} \} \\ 2 \ \draw (0,0) \to[I, i=\$a_1\$] (2,0); \\ 3 \end\{ \text{circuitikz} \} \\ 2 \ \draw (0,0) \to[cI=\$k\cdot a_1\$] (2,0); \\ 3 \end\{ \text{circuitikz} \} \\ 2 \ \draw (0,0) \to[sI=\$a_1\$] (2,0); \\ 3 \end\{ \text{circuitikz} \} \\ 2 \ \draw (0,0) \to[sI=\$a_1\$] (2,0); \\ 3 \end\{ \text{circuitikz} \} \\ 2 \ \draw (0,0) \to[sI=\$a_1\$] (2,0); \\ 3 \end\{ \text{circuitikz} \} \\ 2 \ \draw (0,0) \to[csI=\$k\cdot a_1\$] (2,0); \\ 3 \end\{ \text{circuitikz} \} \\ 2 \ \draw (0,0) \to[csI=\$k\cdot a_1\$] (2,0); \\ 3 \end\{ \text{circuitikz} \} \\ 2 \ \draw (0,0) \to[csI=\$k\cdot a_1\$] (2,0); \\ 3 \end\{ \text{circuitikz} \} \\ 2 \ \draw (0,0) \to[csI=\$k\cdot a_1\$] (2,0); \\ 3 \end\{ \text{circuitikz} \} \\ \text{circuitikz} \\ \text{codot a_1\$} \]
```

The following results from using the option americancurrent or using the style [american currents].

```
1\begin{circuitikz}[american currents]
2 \draw (0,0) to[I=$a_1$] (2,0);
3\end{circuitikz}
```

```
a_1 = \frac{a_1}{2} \cdot \frac{a_1}{3} \cdot \frac{a_1}{3} \cdot \frac{a_1}{3} \cdot \frac{k \cdot a_1}{3} \cdot \frac{k \cdot a_1}{3} \cdot \frac{k \cdot a_1}{3} \cdot \frac{a_1}{3} \cdot \frac{k \cdot a_1}{3} \cdot \frac{a_1}{3} \cdot \frac{a_1}
```

The same holds for voltage sources:



The following results from using the option americanvoltage or the style [american voltages].

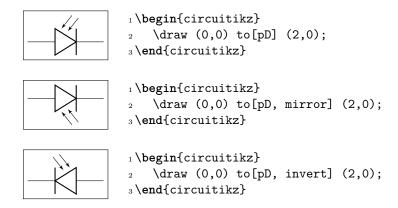
4.8 Integration with siunitx

If the option siunitx is active (and not in ConTeXt), then the following are equivalent:

```
1 \begin{circuitikz}
1\,\mathrm{k}\Omega
           2 \draw (0,0) to[R, l=1<\kilo\ohm>] (2,0);
           3 \end{circuitikz}
            1\begin{circuitikz}
1 \, \mathrm{k}\Omega
           ^{2} \draw (0,0) to [R, l=$\SI{1}{\kilo\ohm}$] (2,0);
            3 \end{circuitikz}
             1 \begin{circuitikz}
    1 \,\mathrm{mA}
             draw (0,0) to[R, i=1<\milli\ampere>] (2,0);
             3 \end{circuitikz}
             1 \begin{circuitikz}
             draw (0,0) to[R, i=$\SI{1}{\milli\ampere}$] (2,0);
             3 \end{circuitikz}
            1 \begin{circuitikz}
            draw (0,0) to[R, v=1<\volt>] (2,0);
           3 \end{circuitikz}
           1 \begin{circuitikz}
               \draw (0,0) to [R, v=$SI{1}{\volt}$] (2,0);
           3 \end{circuitikz}
```

4.9 Mirroring and Inverting

Bipole paths can also mirrored and inverted (or reverted) to change the drawing direction.



Placing labels, currents and voltages works also, please note, that mirroring and inverting does not incfluence the positioning of labels and voltages. Labels are by default above/right of the bipole and voltages below/left, respectively.

```
| \text{Total interpolation of the property of
```

4.10 Putting them together

```
1 kΩ
2 \draw (0,0) to [R=1<\kilo\ohm>,
3 i>_=1<\milli\ampere>, o-*] (3,0);
4 \end{circuitikz}

1 \begin{circuitikz}
2 \draw (0,0) to [D*, v=$v_D$,
3 i=1<\milli\ampere>, o-*] (3,0);
4 \end{circuitikz}
```

4.11 Line joins between Path Components

Line joins should be calculated correctly, if the were on the same path and if the path is not closed. For example, the following path is not closed correctly (-cycle does not work here!):

To correct the line ending, there are support shapes to fill the missing rectangle. They can be used like the support shapes(*,o,d) using a dot (.) on one or both ends of a component(have a look at the last resistor in this example:

5 Colors

5.1 Shape colors

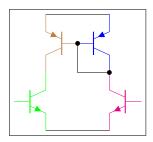
The color of the components is stored in the key $\circuitikzbasekey/color$. CircuiTikZ tries to follow the color set in TikZ, although sometimes it fails. If you change color in the picture, please do not use just the color name as a style, like [red], but rather assign the style [color=red].

Compare for instance

and

```
1\begin{circuitikz} \draw[color=red]
2 (0,2) node[and port] (myand1) {}
3 (0,0) node[and port] (myand2) {}
4 (2,1) node[xnor port] (myxnor) {}
5 (myand1.out) -| (myxnor.in 1)
6 (myand2.out) -| (myxnor.in 2)
7;\end{circuitikz}
```

One can of course change the color in medias res:



```
1 \begin{circuitikz} \draw
2  (0,0) node[pnp, color=blue] (pnp2) {}
3  (pnp2.B) node[pnp, xscale=-1, anchor=B, color=brown] (pnp1) {}
4  (pnp1.C) node[npn, anchor=C, color=green] (npn1) {}
5  (pnp2.C) node[npn, xscale=-1, anchor=C, color=magenta] (npn2) {}
6  (pnp1.E) -- (pnp2.E) (npn1.E) -- (npn2.E)
7  (pnp1.B) node[circ] {} |- (pnp2.C) node[circ] {}
8  ;\end{circuitikz}
```

The all-in-one stream of bipoles poses some challanges, as only the actual body of the bipole, and not the connecting lines, will be rendered in the specified color. Also, please notice the curly braces around the to:

Which, for some bipoles, can be frustrating:

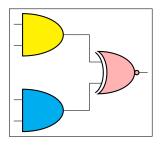
The only way out is to specify different paths:

```
1 \O \( \text{login} \) \( \text
```

And yes: this is a bug and *not* a feature...

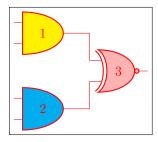
5.2 Fill colors

Since version 0.8.4, you can also fill most shapes with a color (the manual specifies which ones are fillable or not). The syntax is quite intuitive:



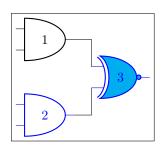
```
1 \begin{circuitikz} \draw
2    (0,2) node[and port, fill=yellow] (myand1) {}
3    (0,0) node[and port, fill=cyan] (myand2) {}
4    (2,1) node[xnor port,fill=red!30!white] (myxnor) {}
5    (myand1.out) -| (myxnor.in 1)
6    (myand2.out) -| (myxnor.in 2)
7 ;\end{circuitikz}
```

You can combine shape colors with fill colors, too, but you should use the draw color option style for this:

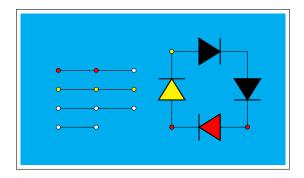


```
1 begin{circuitikz} \draw[color=red]
2    (0,2) node[and port, fill=yellow] (myand1) {1}
3    (0,0) node[and port, fill=cyan] (myand2) {2}
4    (2,1) node[xnor port,fill=red!30!white] (myxnor) {3}
5    (myand1.out) -| (myxnor.in 1)
6    (myand2.out) -| (myxnor.in 2)
7 ;\end{circuitikz}
```

This is because, as you can see from the following example in port 2, you can't specify both a fill and a color in the node (yes, it's a bug too, but it's quite complex to solve given the current circuitTikZ architecture). a workaround is shown in port 3:



Notice also that the connection point are always filled, although the color *tries* to follow the color of the filling of the component:



```
1\begin{circuitikz}
2  \fill[cyan] (0,3.0) rectangle (7,7);
3  \draw [fill=yellow, ] (4,4) to [D,o-o] ++(0,2) to [D*, fill=yellow] ++(2,0)
4  to[D*] ++(0,-2) to[D, fill=red, o-o] ++(-2,0);
5  \draw (1,4) node[ocirc]{} -- ++(1,0) node[ocirc]{};
6  \draw (1,4.5) to[short, o-o] ++(1,0) to[short, -o] ++(1,0);
7  \draw[fill=yellow] (1,5) to[short, o-o] ++(1,0) to[short, -o] ++(1,0);
8  \draw (1,5.5) to[short, fill=red, o-o] ++(1,0) to[short, -o] ++(1,0);
9 \end{circuitikz}
```

6 FAQ

Q: When using \tikzexternalize I get the following error:

! Emergency stop.

A: The TikZ manual states:

Furthermore, the library assumes that all LATEX pictures are ended with \end{tikzpicture}.

Just substitute every occurrence of the environment circuitikz with tikzpicture. They are actually pretty much the same.

Q: How do I draw the voltage between two nodes?

A: Between any two nodes there is an open circuit!

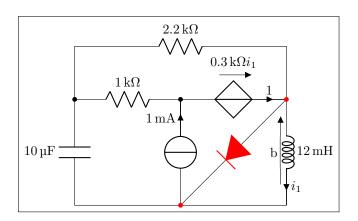
```
1 \begin{circuitikz} \draw
2 node[ocirc] (A) at (0,0) {}
3 node[ocirc] (B) at (2,1) {}
4 (A) to[open, v=$v$] (B)
5;\end{circuitikz}
```

Q: I cannot write to [R = \$R_1=12V\$] nor to [ospst = open, 3s]: I get errors.

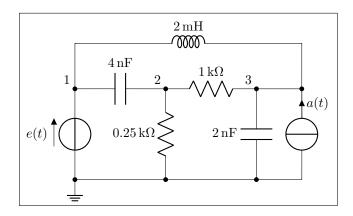
A: It is a limitation of the parser.

Use $\def{\eq}_{=}$ to [R = \$R_1\eq 12V\$] and to [ospst = open{,} 3s] instead; see caveat in section 4.1.

7 Examples

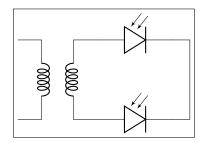


```
1\begin{circuitikz} [scale=1.4]\draw
2 (0,0) to[C, l=10<\micro\farad>] (0,2) -- (0,3)
3     to[R, l=2.2<\kilo\ohm>] (4,3) -- (4,2)
4     to[L, l=12<\milli\henry>, i=$i_1$,v=b] (4,0) -- (0,0)
5 (4,2) { to[D*, *-*, color=red] (2,0) }
6 (0,2) to[R, l=1<\kilo\ohm>, *-] (2,2)
7     to[cV, i=1,v=$\SI{.3}{\kilo\ohm} i_1$] (4,2)
8 (2,0) to[I, i=1<\milli\ampere>, -*] (2,2)
9;\end{circuitikz}
```



```
\begin{array}{c|c}
 & 10 \Omega \\
 & 20 \Omega \\
 & 5 \Omega \\
 & \overline{5} v_x
\end{array}
```

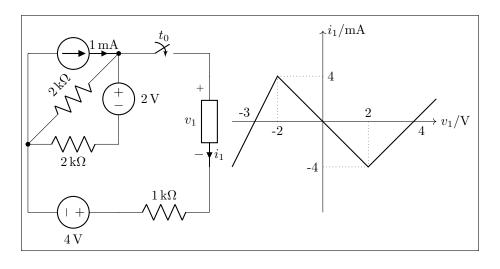
```
1\begin{circuitikz}[scale=1.2]\draw
2 (0,0) node[anchor=east] {B}
3          to[short, o-*] (1,0)
4          to[R=20<\ohm>, *-*] (1,2)
5          to[R=10<\ohm>, v=$v_x$] (3,2) -- (4,2)
6          to[cI=$\frac{\siemens}{5} v_x$, *-*] (4,0) -- (3,0)
7          to[R=5<\ohm>, *-*] (3,2)
8 (3,0) -- (1,0)
9 (1,2) to[short, -o] (0,2) node[anchor=east]{A}
10;\end{circuitikz}
```



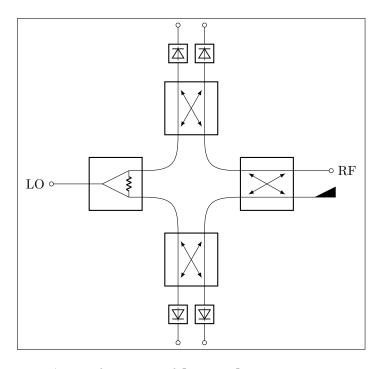
```
1\begin{circuitikz}[scale=1]\draw
2  (0,0) node[transformer] (T) {}
3   (T.B2) to[pD] ($(T.B2)+(2,0)$) -| (3.5, -1)
4   (T.B1) to[pD] ($(T.B1)+(2,0)$) -| (3.5, -1)
5;\end{circuitikz}
```

```
C_{d1}
C_{d1}
C_{d2}
C_{d2}
```

```
1\begin{circuitikz}[scale=1]\draw
2    (5,.5) node [op amp] (opamp) {}
3    (0,0) node [left] {$U_{we}$} to [R, l=$R_d$, o-*] (2,0)
4    to [R, l=$R_d$, *-*] (opamp.+)
5    to [C, l_=$C_{d2}$, *-] ($(opamp.+)+(0,-2)$) node [ground] {}
6    (opamp.out) |- (3.5,2) to [C, l_=$C_{d1}$, *-] (2,2) to [short] (2,0)
7    (opamp.-) -| (3.5,2)
8    (opamp.out) to [short, *-o] (7,.5) node [right] {$U_{wy}$}
9;\end{circuitikz}
```



```
1 \begin{circuitikz}[scale=1.2, american] \draw
   (0,2) to [I=1<\milli\ampere>] (2,2)
       to [R, 1_=2<\frac{hilo\hm>, *-*} (0,0)
        to [R, 1_=2<\kappa 0) (2,0)
        to [V, v_=2<\volt>] (2,2)
        to[cspst, l=$t_0$] (4,2) -- (4,1.5)
        to [generic, i=\$i_1\$, v=\$v_1\$] (4,-.5) -- (4,-1.5)
   (0,2) -- (0,-1.5) to[V, v_=4<\volt>] (2,-1.5)
       to [R, l=1<\kappa] (4,-1.5);
9
10
    \begin{scope}[xshift=6.5cm, yshift=.5cm]
11
    \draw [->] (-2,0) -- (2.5,0) node[anchor=west] {<math>v_1/\volt};
12
    \draw (-1,0) node[anchor=north] {-2} (1,0) node[anchor=south] {2}
14
         (0,1) node [anchor=west] {4} (0,-1) node [anchor=east] {-4}
15
         (2,0) node[anchor=north west] {4}
16
         (-1.5,0) node[anchor=south east] {-3};
17
    \frac{(-2,-1)}{(-1,1)} -- (1,-1) -- (2,0) -- (2.5,.5);
18
    \draw [dotted] (-1,1) -- (-1,0) (1,-1) -- (1,0)
19
         (-1,1) -- (0,1) (1,-1) -- (0,-1);
    \end{scope}
22 \end{circuitikz}
```



```
\begin{circuitikz}[scale=1]
1
        \ctikzset{bipoles/detector/width=.35}
2
        \ctikzset{quadpoles/coupler/width=1}
        \ctikzset{quadpoles/coupler/height=1}
        \ctikzset{tripoles/wilkinson/width=1}
        \ctikzset{tripoles/wilkinson/height=1}
        %\draw[help lines,red,thin,dotted] (0,-5) grid (5,5);
        \draw
         (-2,0) node[wilkinson](w1){}
9
         (2,0) node[coupler] (c1) {}
10
         (0,2) node[coupler,rotate=90] (c2) {}
11
         (0,-2) node[coupler,rotate=90] (c3) {}
12
         (w1.out1) .. controls ++(0.8,0) and ++(0,0.8) .. (c3.3)
13
         (w1.out2) .. controls ++(0.8,0) and ++(0,-0.8) .. (c2.4)
14
         (c1.1) .. controls ++(-0.8,0) and ++(0,0.8) .. (c3.2)
         (c1.4) .. controls ++(-0.8,0) and ++(0,-0.8) .. (c2.1)
16
         (w1.in) to [short, -o] ++(-1,0)
17
         (w1.in) node[left=30] {LO}
18
         (c1.2) node[match,yscale=1] {}
         (c1.3) to [short, -o] ++(1,0)
20
         (c1.3) node[right=30] {RF}
21
         (c2.3) to [detector, -0] ++(0,1.5)
22
         (c2.2) to [detector, -0] ++(0,1.5)
         (c3.1) to [detector, -0] ++(0, -1.5)
24
         (c3.4) to [detector,-o] ++(0,-1.5)
25
26
     \end{circuitikz}
```

```
1 \documentclass{standalone}

3 \usepackage{tikz}

\usetikzlibrary{circuits.ee.IEC}
\usetikzlibrary{positioning}

\usepackage[compatibility]{circuitikzgit}
\usepackage[compatibility]{circuitikz
```

8 Changelog

The major changes among the different circuitikz versions are listed here. See https://github.com/circuitikz/circuitikz/commits for a full list of changes.

- Version git (unreleased)
 - Fixed placement of straightlabels within 4th quadrant
 - Fixed straightvoltages at Diodes, varcap and some other components
 - Adjusted ground symbols to better match ISO standard
 - Fixed a bug about straightlabels (thanks to @fotesan)
 - Added Romano as contributor
 - Added a CONTRIBUTING file
 - Added new sources (cute european versions, noise sources)
 - Added new types of amplifiers, and option to flip inputs and outputs
 - Added bidirectional diodes (diac) thanks to Andre Lucas Chinazzo
 - Added L,R,C sensors (with european, american and cute variants)
 - Added stacked labels (thanks to the original work by Claudio Fiandrino)
 - Make the position of voltage symbols adjustable
 - Make the position of arrows in FETs and BJTs adjustable
 - Added the bulb symbol
 - Added options for solving the voltage direction problems.
 - Added chips (DIP, QFP) with a generic number of pins.
 - Added special anchors for transformers (and fixed the wrong center anchor)
 - Changed the logical port implementation to multiple inputs (thanks to John Kormylo)
 - Changed labels spacing so that they are independent on scale factor
 - Fixed the position of text labels in amplifiers
 - Added new switches.

- Added border anchors for the logic gates
- Added antennas with no wires, loudspeakers and microphone
- Make the components fillable
- Added the oscilloscope component

• Version 0.8.3 (2017-05-28)

- Removed unwanted lines at to-paths if the starting point is a node without a explicit anchor.
- Fixed scaling option, now all parts are scaled by bipoles/length
- Surge arrester appears no more if a to path is used without []-options
- Fixed current placement now possible with paths at an angle of around 280°
- Fixed voltage placement now possible with paths at an angle of around 280°
- Fixed label and annotation placement (at some angles position not changable)
- Adjustable default distance for straight-voltages: 'bipoles/voltage/straight label distance'
- Added Symbol for bandstop filter
- New annotation type to show flows using f=... like currents, can be used for thermal, power or current flows

• Version 0.8.2 (2017-05-01)

- Fixes pgfkeys error using alternatively specified mixed colors(see pgfplots manual section "4.7.5 Colors")
- Added new switches "ncs" and "nos"
- Reworked arrows at spst-switches
- Fixed direction of controlled american voltage source
- "v<=" and "i<=" do not rotate the sources anymore(see them as "counting direction indication", this can be different then the shape orientation); Use the option "invert" to change the direction of the source/appearance of the shape.
- current label "i=" can now be used independent of the regular label "l=" at current sources
- rewrite of current arrow placement. Current arrows can now also be rotated on zerolength paths
- New DIN/EN compliant operational amplifier symbol "en amp"

• Version 0.8.1 (2017-03-25)

- Fixed unwanted line through components if target coordinate is a name of a node
- Fixed position of labels with subscript letters.
- Absolute distance calculation in terms of ex at rotated labels
- Fixed label for transistor paths (no label drawn)

• Version 0.8 (2017-03-08)

- Allow use of voltage label at a [short]
- Correct line joins between path components (to[...])
- New Pole-shape .-. to fill perpendicular joins

- Fixed direction of controlled american current source
- Fixed incorrect scaling of magnetron
- Fixed: Number of american inductor coils not adjustable
- Fixed Battery Symbols and added new battery2 symbol
- Added non-inverting Schmitttrigger

• Version 0.7 (2016-09-08)

- Added second annotation label, showing, e.g., the value of an component
- Added new symbol: magnetron
- Fixed name conflict of diamond shape with tikz.shapes package
- Fixed varcap symbol at small scalings
- New packet-option "straightvoltages, to draw straight(no curved) voltage arrows
- New option "invert" to revert the node direction at paths
- Fixed american voltage label at special sources and battery
- Fixed/rotated battery symbol(longer lines by default positive voltage)
- New symbol Schmitttrigger

• Version 0.6 (2016-06-06)

- Added Mechanical Symbols (damper, mass, spring)
- Added new connection style diamond, use (d-d)
- Added new sources voosource and ioosource (double zero-style)
- All diode can now drawn in a stroked way, just use globel option "strokediode" or stroke instead of full/empty, or D-. Use this option for compliance with DIN standard EN-60617
- Improved Shape of Diodes:tunnel diode, Zener diode, schottky diode (bit longer lines at cathode)
- Reworked igbt: New anchors G,gate and new L-shaped form Lnigbt, Lpigbt
- Improved shape of all fet-transistors and mirrored p-chan fets as default, as pnp, pmos, pfet are already. This means a backward-incompatibility, but smaller code, because p-channels mosfet are by default in the correct direction(source at top). Just remove the 'yscale=-1' from your p-chan fets at old pictures.

• Version 0.5 (2016-04-24)

- new option boxed and dashed for hf-symbols
- new option solderdot to enable/disable solderdot at source port of some fets
- new parts: photovoltaic source, piezo crystal, electrolytic capacitor, electromechanical device(motor, generator)
- corrected voltage and current direction(option to use old behaviour)
- option to show body diode at fet transistors

• Version 0.4

- minor improvements to documentation
- comply with TDS
- merge high frequency symbols by Stefan Erhardt

- added switch (not opening nor closing)
- added solder dot in some transistors
- improved ConTeXt compatibility

• Version 0.3.1

- different management of color...
- fixed typo in documentation
- fixed an error in the angle computation in voltage and current routines
- fixed problem with label size when scaling a tikz picture
- added gas filled surge arrester
- added compatibility option to work with Tikz's own circuit library
- fixed infinite in arctan computation

• Version 0.3.0

- fixed gate node for a few transistors
- added mixer
- added fully differential op amp (by Kristofer M. Monisit)
- now general settings for the drawing of voltage can be overridden for specific components
- made arrows more homogeneous (either the current one, or latex' bt pgf)
- added the single battery cell
- added fuse and asymmetric fuse
- added toggle switch
- added varistor, photoresistor, thermocouple, push button
- added thermistor, thermistor ptc, thermistor ptc
- fixed misalignment of voltage label in vertical bipoles with names
- added isfet
- added noiseless, protective, chassis, signal and reference grounds (Luigi «Liverpool»)

• Version 0.2.4

- added square voltage source (contributed by Alistair Kwan)
- added buffer and plain amplifier (contributed by Danilo Piazzalunga)
- added squid and barrier (contributed by Cor Molenaar)
- added antenna and transmission line symbols contributed by Leonardo Azzinnari
- added the changeover switch spdt (suggestion of Fabio Maria Antoniali)
- rename of context.tex and context.pdf (thanks to Karl Berry)
- updated the email address
- in documentation, fixed wrong (non-standard) labelling of the axis in an example (thanks to prof. Claudio Beccaria)
- fixed scaling inconsistencies in quadrupoles
- fixed division by zero error on certain vertical paths
- introduced options straighlabels, rotatelabels, smartlabels

• Version 0.2.3

- fixed compatibility problem with label option from tikz
- Fixed resizing problem for shape ground
- Variable capacitor
- polarized capacitor
- ConTeXt support (read the manual!)
- nfet, nigfete, nigfetd, pfet, pigfete, pigfetd (contribution of Clemens Helfmeier and Theodor Borsche)
- njfet, pjfet (contribution of Danilo Piazzalunga)
- pigbt, nigbt
- backward incompatibility potentiometer is now the standard resistor-with-arrow-in-the-middle; the old potentiometer is now known as variable resistor (or vR), similarly to variable inductor and variable capacitor
- triac, thyristor, memristor
- new property "name" for bipoles
- fixed voltage problem for batteries in american voltage mode
- european logic gates
- $-\ backward\ incompatibility$ new american standard inductor. Old american inductor now called "cute inductor"
- backward incompatibility transformer now linked with the chosen type of inductor, and version with core, too. Similarly for variable inductor
- $-\ backward\ incompatibility$ styles for selecting shape variants now end are in the plural to avoid conflict with paths
- new placing option for some tripoles (mostly transistors)
- mirror path style
- Version 0.2.2 20090520
 - Added the shape for lamps.
 - Added options europeanresistor, europeaninductor, americanresistor and americaninductor, with corresponding styles.
 - FIXED: error in transistor arrow positioning and direction under negative xscale and yscale.
- Version 0.2.1 20090503
 - Op-amps added
 - added options arrowmos and noarrowmos, to add arrows to pmos and nmos
- Version 0.2 20090417 First public release on CTAN
 - Backward incompatibility: labels ending with : angle are not parsed for positioning anymore.
 - Full use of TikZ keyval features.
 - White background is not filled anymore: now the network can be drawn on a background picture as well.
 - Several new components added (logical ports, transistors, double bipoles, ...).
 - Color support.
 - Integration with {siunitx}.
 - Voltage, american style.
 - Better code, perhaps. General cleanup at the very least.
- Version 0.1 2007-10-29 First public release

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