

CircuiTikZ

version git:d280a18 (2019/03/27)

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March 27, 2019

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1 Introduction

1.1 About

CircuiTikZ was initiated by Massimo Redaelli in 2007, who was working as a research assistant at the Polytechnic University of Milan, Italy, and needed a tool for creating exercises and exams. After he left University in 2010 the development of CircuiTikZ slowed down, since L^AT_EX is mainly established in the academic world. In 2015 Stefan Lindner and Stefan Erhardt, both working as research assistants at the University of Erlangen-Nürnberg, Germany, joined the team and now maintain the project together with the initial author. In 2018 Romano Giannetti, full professor of Electronics at Comillas Pontifical University of Madrid, joined the team.

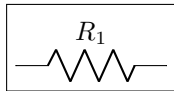
The use of CircuiTikZ is, of course, not limited to academic teaching. The package gets widely used by engineers for typesetting electronic circuits for articles and publications all over the world.

1.2 Loading the package

L ^A T _E X	ConT _E Xt ¹
<code>\usepackage{circuitikz}</code>	<code>\usemodule[circuitikz]</code>

TikZ will be automatically loaded.

CircuiTikZ commands are just TikZ commands, so a minimum usage example would be:



```
1\tikz \draw (0,0) to[R=$R_1$] (2,0);
```

1.3 Installing a new version of the package.

The stable version of the package should come with your L^AT_EX distribution. Downloading the files from CTAN and installing them locally is, unfortunately, a distribution-dependent task and sometime not so trivial. If you search for `local texmf tree` and the name of your distribution on <https://tex.stackexchange.com/> you will find a lot of hints.

Anyway, the easiest way of using whichever version of CircuiTikZ is to point to the github page <https://circuitikz.github.io/circuitikz/> of the project, and download the version you want. You will download a simple (bigish) file, called `circuitikz.sty`.

Now you can just put this file in your local `texmf` tree, if you have one, or simply adding it into the same directory where your main file resides, and then use

```
\usepackage[...options...]{circuitikzgit}
```

instead of `circuitikz`. This is also advantageous for “future resilience”; the authors try hard not to break backward compatibility with new versions, but sometime things happen.

1.4 Requirements

- `tikz`, version ≥ 3 ;
- `xstring`, not older than 2009/03/13;
- `siunitx`, if using `siunitx` option.

¹ConT_EXt support was added mostly thanks to Mojca Miklavac and Aditya Mahajan.

1.5 Incompatible packages

TikZ's own `circuit` library, which is based on `CircuitikZ`, (re?)defines several styles used by this library. In order to have them work together you can use the `compatibility` package option, which basically prefixes the names of all `CircuitikZ` `to[]` styles with an asterisk.

So, if loaded with said option, one must write `(0,0) to[*R] (2,0)` and, for transistors on a path, `(0,0) to[*Tnmos] (2,0)`, and so on (but `(0,0) node[nmos] {}`). See example at page 76.

1.6 License

Copyright © 2007–2019 Massimo Redaelli. This package is author-maintained. Permission is granted to copy, distribute and/or modify this software under the terms of the L^AT_EX Project Public License, version 1.3.1, or the GNU Public License. This software is provided ‘as is’, without warranty of any kind, either expressed or implied, including, but not limited to, the implied warranties of merchantability and fitness for a particular purpose.

1.7 Feedback

The easiest way to contact the authors is via the official Github repository: <https://github.com/circuitikz/circuitikz/issues>

1.8 Incompabilities between version

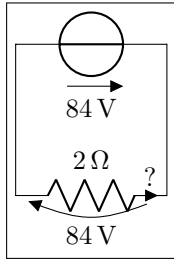
Here, we will provide a list of incompatibilities between different version of `circuitikz`. We will try to hold this list short, but sometimes it is easier to break with old syntax than including a lot of switches and compatibility layers. You can check the used version at your local installation using the macro `\pgfcircversion{}`.

- After v0.8.4: the parameters `tripoles/american` or `port/aaa, ...bbb, ...ccc` and `...ddd` are no longer used and are silently ignored; the same stands for `nor`, `xor`, and `xnor` ports.
- After v0.8.4: voltage and current directions/sign (plus and minus signs in case of `american voltages` and arrows in case of `european voltages` have been rationalized with a couple of new options (see details in section 4.2. The default case is still the same as v0.8.4.
- Since v0.8.2: voltage and current label directions(`v<=` / `i<=`) do NOT change the orientation of the drawn source shape anymore. Use the “invert” option to rotate the shape of the source. Furthermore, from this version on, the current label(`i=`) at current sources can be used independent of the regular label(`l=`).
- Since v0.7?: The label behaviour at mirrored bipoles has changes, this fixes the voltage drawing, but perhaps you have to adjust your label positions.
- Since v0.5.1: The parts `pfet`, `pigfete`, `pigfetebulk` and `pigfetd` are now mirrored by default. Please adjust your `yscale`-option to correct this.
- Since v0.5: New voltage counting direction, here exists an option to use the old behaviour

For older projects, you can use an older version locally using the git-version and picking the correct commit from the repository (branch `gh-pages`).

1.9 Package options

Circuit people are very opinionated about their symbols. In order to meet the individual gusto you can set a bunch of package options. The standard options are what the authors like, for example you get this:



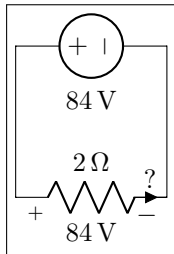
```

1 \begin{circuitikz}
2   \draw (0,0) to[R=2<\ohm>, i=?, v=84<\volt>] (2,0) --
3     (2,2) to[V<=84<\volt>] (0,2)
4     -- (0,0);
5 \end{circuitikz}

```

Feel free to load the package with your own cultural options:

\LaTeX	Con \TeX t
<code>\usepackage[american]{circuitikz}</code>	<code>\usemodule[circuitikz][american]</code>



```

1 \begin{circuitikz}
2   \draw (0,0) to[R=2<\ohm>, i=?, v=84<\volt>] (2,0) --
3     (2,2) to[V<=84<\volt>] (0,2)
4     -- (0,0);
5 \end{circuitikz}

```

Here is the list of all the options:

- **europeanvoltages**: uses arrows to define voltages, and uses european-style voltage sources;
- **straightvoltages**: uses arrows to define voltages, and uses straight voltage arrows;
- **americanvoltages**: uses $-$ and $+$ to define voltages, and uses american-style voltage sources;
- **europeancurrents**: uses european-style current sources;
- **americancurrents**: uses american-style current sources;
- **europeanresistors**: uses rectangular empty shape for resistors, as per european standards;
- **americanresistors**: uses zig-zag shape for resistors, as per american standards;
- **europeaninductors**: uses rectangular filled shape for inductors, as per european standards;
- **americaninductors**: uses "4-bumps" shape for inductors, as per american standards;
- **cuteinductors**: uses my personal favorite, "pig-tailed" shape for inductors;
- **americanports**: uses triangular logic ports, as per american standards;
- **europeanports**: uses rectangular logic ports, as per european standards;
- **americangfsgearrester**: uses round gas filled surge arresters, as per american standards;
- **europeangfsgearrester**: uses rectangular gas filled surge arresters, as per european standards;
- **european**: equivalent to **europeancurrents**, **europeanvoltages**, **europeanresistors**, **europeaninductors**, **europeanports**, **europeangfsgearrester**;
- **american**: equivalent to **americancurrents**, **americanvoltages**, **americanresistors**, **americaninductors**, **americanports**, **americangfsgearrester**;

- **siunitx**: integrates with **SIunitx** package. If labels, currents or voltages are of the form $\#1<\#2>$ then what is shown is actually $\backslash\mathrm{SI}\{\#1\}\{\#2\}$;
- **nosunitx**: labels are not interpreted as above;
- **fulldiode**: the various diodes are drawn *and* filled by default, i.e. when using styles such as `diode`, `D`, `sD`, ... Other diode styles can always be forced with e.g. `Do`, `D-`, ...
- **strokediode**: the various diodes are drawn *and* stroke by default, i.e. when using styles such as `diode`, `D`, `sD`, ... Other diode styles can always be forced with e.g. `Do`, `D*`, ...
- **emptydiode**: the various diodes are drawn *but not* filled by default, i.e. when using styles such as `D`, `sD`, ... Other diode styles can always be forced with e.g. `Do`, `D-`, ...
- **arrowmos**: pmos and nmos have arrows analogous to those of pnp and npn transistors;
- **noarrowmos**: pmos and nmos do not have arrows analogous to those of pnp and npn transistors;
- **fetbodydiode**: draw the body diode of a FET;
- **nofetbodydiode**: do not draw the body diode of a FET;
- **fetsolderdot**: draw solderdot at bulk-source junction of some transistors;
- **nofetsolderdot**: do not draw solderdot at bulk-source junction of some transistors;
- **emptypmoscircle**: the circle at the gate of a pmos transistor gets not filled;
- **lazymos**: draws lazy nmos and pmos transistors. Chip designers with huge circuits prefer this notation;
- **straightlabels**: labels on bipoles are always printed straight up, i.e. with horizontal baseline;
- **rotatelabels**: labels on bipoles are always printed aligned along the bipole;
- **smartlabels**: labels on bipoles are rotated along the bipoles, unless the rotation is very close to multiples of 90° ;
- **compatibility**: makes it possible to load **CircuitikZ** and **TikZ** circuit library together.
- **Voltage directions**: until v0.8.3, there was an error in the coherence between american and european voltages styles (see section 4.2 for the batteries. This has been fixed, but to guarantee backward compatibility and nasty surprises, the fix is available with new options:
 - **oldvoltagedirection**: Use old way of voltage direction having a difference between european and american direction, with wrong default labelling for batteries;
 - **nooldvoltagedirection**: The standard from 0.5 onward, utilize the (German?) standard of voltage arrows in the direction of electric fields (without fixing batteries);
 - **RPvoltages** (meaning Rising Potential voltages): the arrow is in direction of rising potential, like in **oldvoltagedirections**, but batteries and current sources are fixed to follow the passive/active standard;
 - **EFvoltages** (meaning Electric Field voltages): the arrow is in direction of the electric field, like in **nooldvoltagedirections**, but batteries are fixed;

If none of these option are given, the package will default to **nooldvoltagedirections**, but will give a warning. The behavior is also selectable circuit by circuit with the **voltage dir** style.

- `betterproportions`²: nicer proportions of transistors in comparison to resistors;

The old options in the singular (like `american voltage`) are still available for compatibility, but are discouraged.

Loading the package with no options is equivalent to the following options: `[nofetsolderdot, europeancurrents, europeanvoltages, americanports, americanresistors, cuteinductors, europeangfsurgearrester, nosiunitx, noarrowmos, smartlabels, nocompatibility]`.

In ConT_EXt the options are similarly specified: `current=european|american, voltage=european|american, resistor=american|european, inductor=cute|american|european, logic=american|european, siunitx=true|false, arrowmos=false|true`.

2 Tutorials

To draw a circuit, you have to load the `circuitikz` package; this can be done with

```
1 \usepackage[siunitx, RPvoltages]{circuitikz}
```

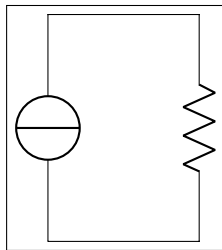
somewhere in your document preamble. It will load automatically the needed packages if not already done before.

2.1 Getting started with CircuiTikZ: a current shunt

Let's say we want to prepare a circuit to teach how a current shunt works; the idea is just draw a current generator, a couple of resistors in parallel, and the indication of currents and voltages for the discussion.

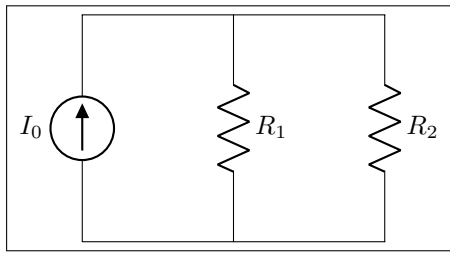
A circuit in CircuiTikZ is drawn into a `circuitikz` environment (which is really an alias for a `tikzpicture` one). In this first example we will use absolute coordinates. The electrical components can be divided in two big categories: the one that are bipoles and are placed along a path (also known as `to-style` component, for they're usage) and components that are nodes and can have any number of poles, or connections.

Let's start with the first type of components and build a basic mesh:



```
1 \begin{circuitikz}[]
2 \draw (0,0) to[isource] (0,3) -- (2,3)
3 to[R] (2,0) -- (0,0);
4 \end{circuitikz}
```

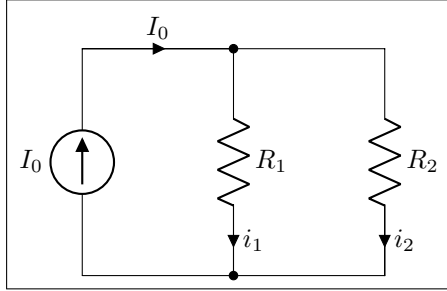
The symbol for the current source can surprise somebody; this is actually the european-style symbol, and the symbols chosen reflects the default options of loaded in the package (see section 1.9). Let's change the style for now (the author of the tutorial, Romano, is European but I have used since ever American style circuit, so...); and while at it, let add the other branch and some label.



```
1 \begin{circuitikz}[american]
2 \draw (0,0) to[isource, l=I_0] (0,3) --
3 (2,3)
4 to[R=$R_1$] (2,0) -- (0,0);
5 \draw (2,3) -- (4,3) to[R=$R_2$]
6 (4,0) -- (2,0);
7 \end{circuitikz}
```

²May change in the future!

You can use a single path or multiple path when drawing your circuit, it's just a question of style (but be aware that closing path could be non-trivial, see section 4.11), and you can use standard TikZ lines (–, |– or similar) for the wires. Nonetheless, sometime using the CircuiTikZ specific `short` component for the wires can be useful, because then we can add labels and nodes at it, like for example in the following circuit.

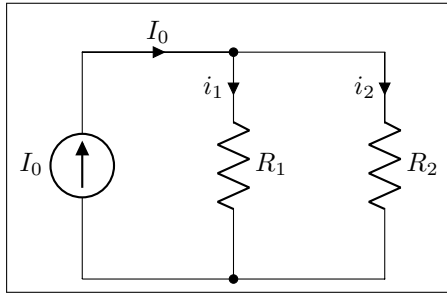


```

1 \begin{circuitikz}[american]
2   \draw (0,0) to[isource, l=$I_0$] (0,3)
3   to[short, -*, i=$I_0$] (2,3)
4   to[R=$R_1$, i=$i_1$] (2,0) -- (0,0);
5   \draw (2,3) -- (4,3)
6   to[R=$R_2$, i=$i_2$]
7   (4,0) to[short, -*] (2,0);
8 \end{circuitikz}

```

One of the problems with this circuit is that we would like to have the current in a different position, such as for example on the upper side of the resistors, so that the Kirchoff's Current Law at the node is better shown to students. No problem; as you can see in section 4.2 you can use the position specifier `<^_>` after the key `i`:

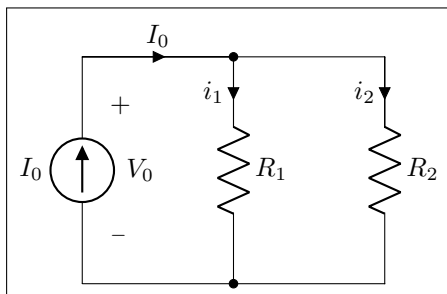


```

1 \begin{circuitikz}[american]
2   \draw (0,0) to[isource, l=$I_0$] (0,3)
3   to[short, -*, i=$I_0$] (2,3)
4   to[R=$R_1$, i>_=$i_1$] (2,0) -- (0,0);
5   \draw (2,3) -- (4,3)
6   to[R=$R_2$, i>_=$i_2$]
7   (4,0) to[short, -*] (2,0);
8 \end{circuitikz}

```

Finally, we would like to add voltages indication for carrying out the current formulas; as the default position of the voltage signs seems a bit cramped to me, I am adding the `voltage shift` parameter to make a bit more space for it...

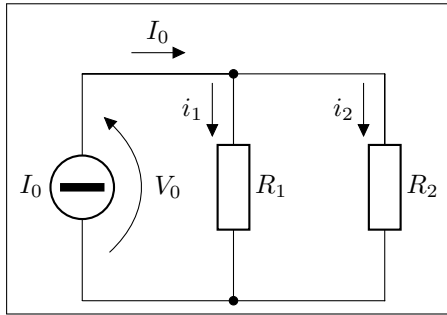


```

1 \begin{circuitikz}[american, voltage shift
2   =0.5]
3   \draw (0,0) to[isource, l=$I_0$, v=$V_0$]
4   (0,3)
5   to[short, -*, i=$I_0$] (2,3)
6   to[R=$R_1$, i>_=$i_1$] (2,0) -- (0,0);
7   \draw (2,3) -- (4,3)
8   to[R=$R_2$, i>_=$i_2$]
9   (4,0) to[short, -*] (2,0);
10 \end{circuitikz}

```

Et voilà! Remember that this is still \LaTeX , which means that you have done a description of your circuit, which is, in a lot of way, independent of the visualization of it. If you ever have to adapt the circuit to, say, a journal that force European style and flows instead of currents, you just change a couple of things and you have what seems a completely different diagram:

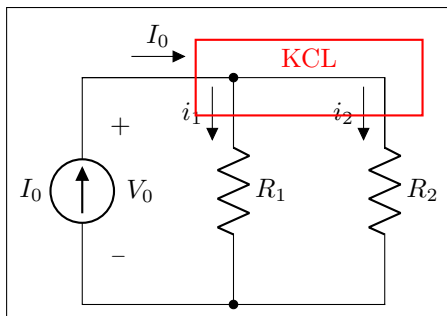


```

1 \begin{circuitikz}[european, voltage shift
  =0.5]
2   \draw (0,0) to[isourceC, l=$I_0$, v=$V_0$] (0,3)
3   to[short, -*, f=$I_0$] (2,3)
4   to[R=$R_1$, f>_=$i_1$] (2,0) -- (0,0);
5   \draw (2,3) -- (4,3)
6   to[R=$R_2$, f>_=$i_2$]
7   (4,0) to[short, -*] (2,0);
8 \end{circuitikz}

```

And finally, this is still TikZ, so that you can freely mix other graphics element to the circuit.



```

1 \begin{circuitikz}[american, voltage shift
  =0.5]
2   \draw (0,0) to[isource, l=$I_0$, v=$V_0$]
3   (0,3)
4   to[short, -*, f=$I_0$] (2,3)
5   to[R=$R_1$, f>_=$i_1$] (2,0) -- (0,0);
6   \draw (2,3) -- (4,3)
7   to[R=$R_2$, f>_=$i_2$]
8   (4,0) to[short, -*] (2,0);
9   \draw[red, thick] (1.5,2.5) rectangle
10  (4.5,3.5)
11  node[pos=0.5, above]{KCL};
12 \end{circuitikz}

```

2.2 A more complex tutorial: circuits Romano's style.

The idea is to draw a two-stage amplifier for a lesson, or exercise, on the different qualities of BJT and MOSFET transistors. Notice that this is a more “personal” tutorial, showing a way to draw circuits that is, in the author’s opinion, highly reusable and easy to do. The idea is using relative coordinates and named nodes as much as possible, so that changes in the circuit are easily done by changing keys numbers of position, and crucially, each block is reusable in other diagrams.

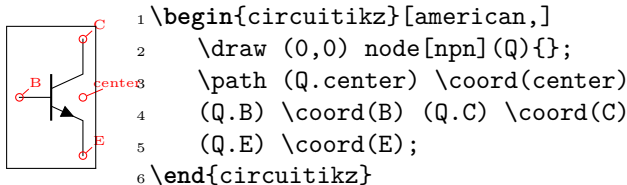
First of all, let’s define a handy function to show the position of nodes:

```

1 \def\coord(#1){coordinate(#1)}
2 \def\coord(#1){node[circle, red, draw, inner sep=1pt, pin={red, overlay, inner
  sep=0.5pt, font=\tiny, pin distance=0.1cm, pin edge={red, overlay
  ,}]45:#1}}{#1}{}}

```

The idea is that you can use `\coord()` instead of `coordinate()` in paths, and that will draw sort of *markers* showing them. For example:



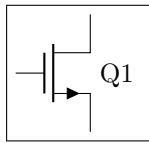
```

1 \begin{circuitikz}[american,]
2   \draw (0,0) node[npn] (Q){};
3   \path (Q.center) \coord(center)
4   (Q.B) \coord(B) (Q.C) \coord(C)
5   (Q.E) \coord(E);
6 \end{circuitikz}

```

After the circuit is drawn, simply commenting out the second definition of `\coord` will hide all the markers.

So let’s start with the first stage transistor; given that my preferred way of drawing a MOSFET is with arrows, I’ll start issuing the command `\ctikzset{tripoles/mos style/arrows}`:



```

1 \begin{circuitikz}[american,]
2 \ctikzset{tripoles/mos style/arrows}
3 \def\killdepth#1{\raisebox{0pt}{\height}[0pt]{#1}}
4 \draw (0,0) node[nmos] (Q1){};
5 \draw (Q1.center) node[right]{\killdepth{Q1}};
6 \end{circuitikz}

```

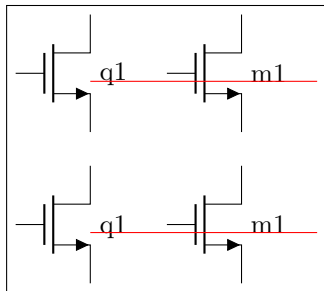
Another thing I like to modify with respect to the standard is the position of the arrows in transistors, which are normally midway the symbol. Issuing the following settings will move the arrows to the end or start of the corresponding pin.

```

1 \ctikzset{tripoles/mos style/arrows,
2 tripoles/npn/arrow pos=0.8,
3 tripoles/pnp/arrow pos=0.8,
4 tripoles/nmos/arrow pos=0.8,
5 tripoles/pmos/arrow pos=0.6, }

```

The tricky thing about `\killdepth{}` macro is a finicky details; I do not like the standard position of labels on transistors (which is near the collector/drain) so I plot the label at the right of the `center` anchor. Without the `\killdepth` macro, the labels of different transistor will be adjusted so that the center of the box is at the `center` anchor, and as an effect, labels with descenders (like Q) will have a different baseline than labels without. You can see this here (it's really subtle):

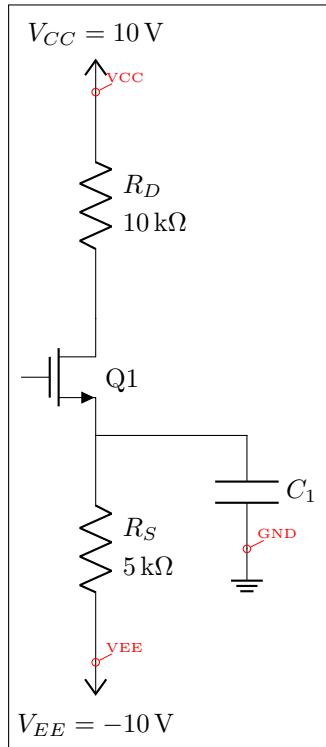


```

1 \begin{circuitikz}[american,]
2 \draw (0,0) node[nmos] (Q1){} ++(2,0) node[nmos] (M1){};
3 \draw (Q1.center) node[right]{q1};
4 \draw (M1.center) node[right]{m1};
5 \draw [red] (Q1.center) ++(0,-0.7ex) -- ++(3,0);
6 \draw (0,-2) node[nmos] (Q1){} ++(2,0) node[nmos] (M1){};
7 \draw (Q1.center) node[right]{\killdepth{q1}};
8 \draw (M1.center) node[right]{\killdepth{m1}};
9 \draw [red] (Q1.center) ++(0,-0.7ex) -- ++(3,0);
10 \end{circuitikz}

```

We will start connecting the first transistor with the power supply with a couple of resistors. Notice that I am naming the nodes `GND`, `VCC` and `VEE`, so that I can use the coordinates to have all the supply rails at the same vertical position (more on this later).

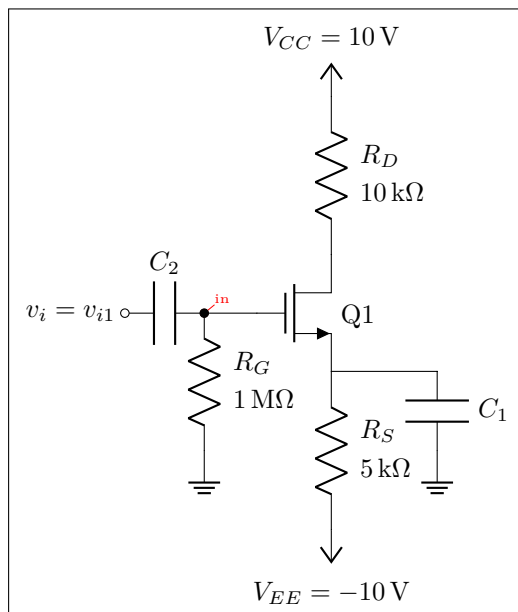


```

1 \begin{circuitikz}[american,]
2   \draw (0,0) node[nmos,](Q1){};
3   \draw (Q1.center) node[right]{\killdepth{Q1}};
4   \draw (Q1.S) to[R, l2^=$R_S$ and \SI{5}{k\ohm}]
      ++(0,-3)
5     node[vee](VEE){$V_{EE}=\SI{-10}{V}$};
6   \draw (Q1.D) to[R, l2_=$R_D$ and \SI{10}{k\ohm}]
      ++(0,3)
7     node[vcc](VCC){$V_{CC}=\SI{10}{V}$};
8   \draw (Q1.S) to[short] ++(2,0) to[C=$C_1$]
      ++(0,-1.5) node[ground](GND){};
9   \path (GND) \coord(GND) (VCC) \coord(VCC)
10    (VEE) \coord(VEE);
11 \end{circuitikz}

```

After that, let's add the input part. I will use a named node here, to refer to it to add the input source. Notice how the ground node is positioned: the coordinate (in | - GND) is the point with the horizontal coordinate of (in) and the horizontal one of (GND), lining it up with the ground of the capacitor C_1 .



```

1 \begin{circuitikz}[american, scale=0.7]
2   \draw (0,0) node[nmos,](Q1){};
3   \draw (Q1.center) node[right]
4     {\killdepth{Q1}};
5   \draw (Q1.S) to[R, l2^=$R_S$ and \SI
6     {5}{k\ohm}] ++(0,-3)
7     node[vee](VEE){$V_{EE}=\SI{-10}{V}$};
8   \draw (Q1.D) to[R, l2_=$R_D$ and \SI
9     {10}{k\ohm}] ++(0,3)
10    node[vcc](VCC){$V_{CC}=\SI{10}{V}$};
11   \draw (Q1.S) to[short] ++(2,0) to[C
12     =$C_1$] ++(0,-1.5) node[ground](
13     GND){};
14   \draw (Q1.G) to[short] ++(-1,0)
15     \coord (in) to[R, l2^=$R_G$ and \
16     SI{1}{M\ohm}]
17     (in | - GND) node[ground]{};
18   \draw (in) to[C, l_=$C_2$,*-o]
19     ++(-1.5,0) node[left](vi1){$v_i=
20     v_{i1}$};
21 \end{circuitikz}

```

Notice that the only absolute coordinate here is the first one, (0,0); so the elements are

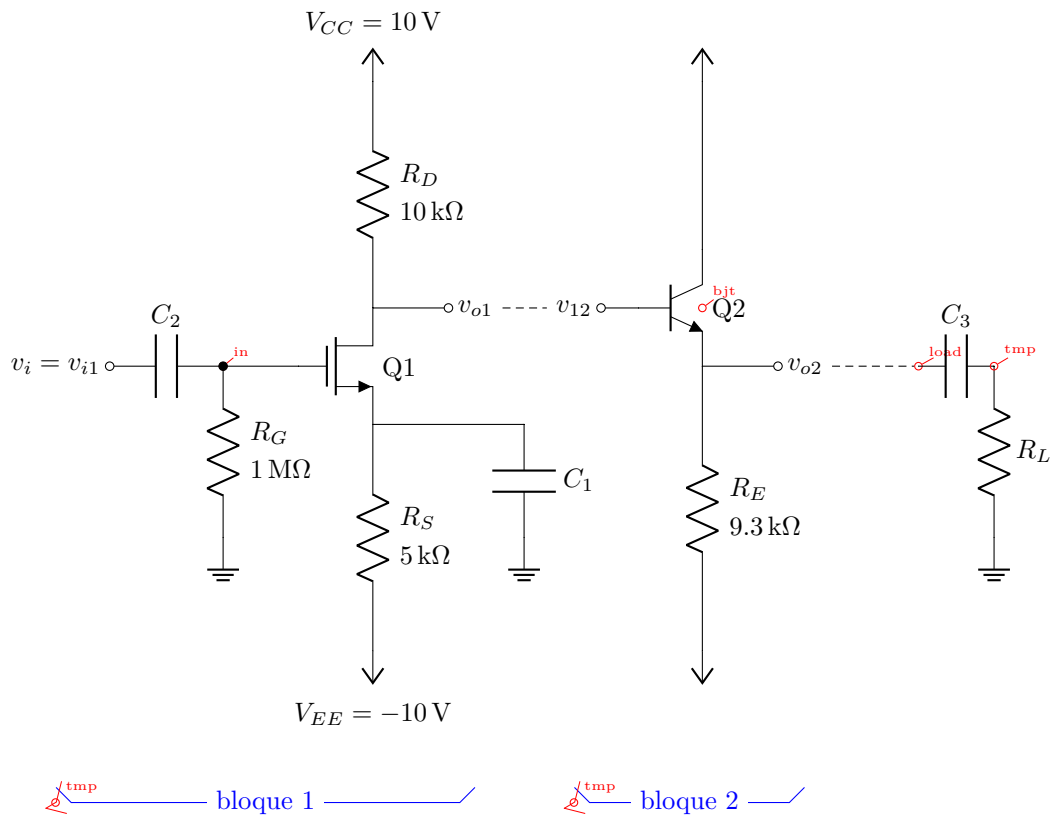
connected with relative movements and can be moved by just changing one number (for example, changing the `to[C=C_1] ++(0,-1.5)` will move *all* the grounds down).

This is the final circuit, with the nodes still marked:

```

1 \tikzset{blockdef/.style={%
2   {Straight Barb[harpoon, reversed, right, length=0.2cm]}--{Straight Barb[
3     harpoon, reversed, left, length=0.2cm]},
4   blue, %densely dotted,
5 }}
6 \def\killdepth#1{{\raisebox{0pt}{\height}[0pt]{#1}}}
7 \def\coord(#1){coordinate(#1)}
8 \def\coord(#1){node[circle, red, draw, inner sep=1pt, pin={red, overlay, inner
9   sep=0.5pt, font=\tiny, pin distance=0.1cm, pin edge={red, overlay
10    },]45:#1}]{#1}}
11 \begin{circuitikz}[american, ]
12   \draw (0,0) node[nmos,](Q1){};
13   \draw (Q1.center) node[right]{\killdepth{Q1}};
14   \draw (Q1.S) to[R, l2^=$R_S$ and \SI{5}{k\ohm}] ++(0,-3) node[vee](VEE){$V_{EE}=\SI{-10}{V}$}; %define VEE level
15   \draw (Q1.S) to[short] ++(2,0) to[C=$C_1$] ++(0,-1.5) node[ground](GND){};
16   \draw (Q1.G) to[short] ++(-1,0) \coord(in) to[R, l2^=$R_G$ and \SI{1}{M\ohm}] (in |- GND) node[ground]{};
17   \draw (in) to[C, l_=$C_2$, *-o] ++(-1.5,0) node[left](vi1){$v_i=v_{i1}$};
18   \draw (Q1.D) to[R, l2_=$R_D$ and \SI{10}{k\ohm}] ++(0,3) node[vcc](VCC){$V_{CC}=\SI{10}{V}$};
19   \draw (Q1.D) to[short, -o] ++(1,0) node[right](vo1){$v_{o1}$};
20   %
21   \path (vo1) -- ++(3,0) \coord(bjt);
22   %
23   \draw (bjt) node[npn,](Q2){};
24   \draw (Q2.center) node[right]{\killdepth{Q2}};
25   \draw (Q2.B) to[short, -o] ++(-0.5,0) node[left](vi2){$v_{i2}$};
26   \draw (Q2.E) to[R, l2^=$R_E$ and \SI{9.3}{k\ohm}] (Q2.E |- VEE) node[vee]{};
27   \draw (Q2.E) to[short, -o] ++(1,0) node[right](vo2){$v_{o2}$};
28   \draw (Q2.C) to[short] (Q2.C |- VCC) node[vcc]{};
29   %
30   \path (vo2) ++(1.5,0) \coord(load);
31   \draw (load) to[C=$C_3$] ++(1,0) \coord(tmp) to[R=$R_L$] (tmp |- GND) node[ground]{};
32   \draw [densely dashed] (vo2) -- (load);
33   %
34   \draw [densely dashed] (vo1) -- (vi2);
35   %
36   \draw [blockdef] (vi1|-VEE) ++(0,-2) \coord(tmp)
37     -- node[midway, fill=white]{bloque 1} (vo1|- tmp);
38   \draw [blockdef] (vi2|-VEE) ++(0,-2) \coord(tmp)
39     -- node[midway, fill=white]{bloque 2} (vo2|- tmp);
40 \end{circuitikz}

```



3 The components

Components in CircuiTikZ come in two forms: a path-style form, to be used in `to` path specifications, and node-style, which will be instantiated by a `node` specification.

3.1 Path-style components

The path-style components are used as in the following way:

```

1 \begin{circuitikz}
2 \draw (0,0) to[#1=#2, #options] (2,0);
3 \end{circuitikz}
```

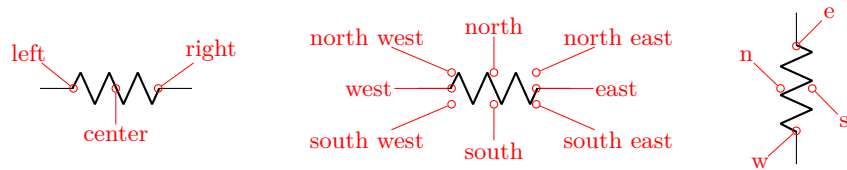
where `#1` is the name of the component, `#2` is an (optional) label, and `options` are optional labels, annotations, style specifier that will be explained in the rest of the manual.

Transistors and some other node-style components can also be placed using the syntax for bipoles. See section 3.6.1.2.

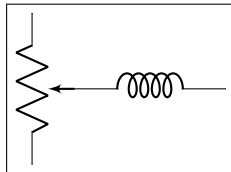
Most path-style components can be used as a node-style components; to access them, you add a `shape` to the main name of component (for example, `diodeshape`). Such a “node name” is specified in the description of each component.

3.1.1 Anchors

Normally, path-style component do not need anchors, although they have them for your to use. You have the basic “geographical” anchors (defined for an horizontal element):



In the case of bipoles, also shortened geographical anchors exists. In the description, it will be shown when a bipole has additional anchors. To use the anchors, just give a name to the bipole element.

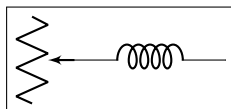


```

1 \begin{circuitikz}
2   \draw (0,0) to[potentiometer, name=P, mirror] ++(0,2);
3   \draw (P.wiper) to[L] ++(2,0);
4 \end{circuitikz}

```

Alternatively, that you can use the shape form, and then use the `left` and `right` anchors to do your connections.



```

1 \begin{circuitikz}
2   \draw (0,0) node[potentiometershape, rotate=-90] (P){};
3   \draw (P.wiper) to[L] ++(2,0);
4 \end{circuitikz}

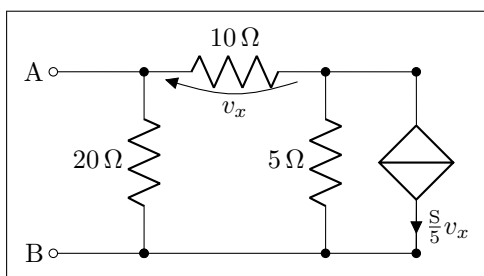
```

3.1.2 Customization

Pretty much all CircuitikZ relies heavily on `pgfkeys` for value handling and configuration. Indeed, at the beginning of `circuitikz.sty` and in the file `pfgcirc.define.tex` a series of key definitions can be found that modify all the graphical characteristics of the package.

All can be varied using the `\ctikzset` command, anywhere in the code.

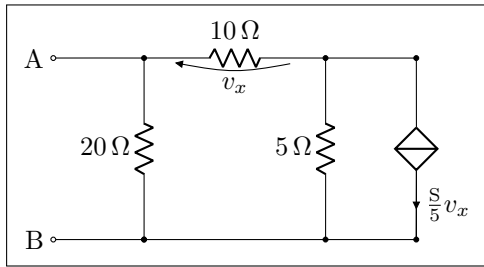
3.1.2.1 Components size Perhaps the most important parameter is `\circuitikzbasekey/bipoles/length` (default 1.4cm), which can be interpreted as the length of a resistor (including reasonable connections): all other lengths are relative to this value. For instance:



```

1 \ctikzset{bipoles/length=1.4cm}
2 \begin{circuitikz}[scale=1.2]\draw
3   (0,0) node[anchor=east] {B}
4     to[short, o-*] (1,0)
5     to[R=20<\ohm>, *-] (1,2)
6     to[R=10<\ohm>, v=$v_x$] (3,2) -- (4,2)
7     to[cI=$\frac{\si{siemens}}{5} v_x$, *-] (4,0) -- (3,0)
8     to[R=5<\ohm>, *-] (3,2)
9   (3,0) -- (1,0)
10  (1,2) to[short, -o] (0,2) node[anchor=east]{A}
11 \end{circuitikz}

```



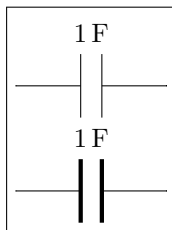
```

1 \ctikzset{bipoles/length=.8cm}
2 \begin{circuitikz}[scale=1.2]\draw
3   (0,0) node[anchor=east] {B}
4     to[short, o-*] (1,0)
5     to[R=20<\ohm>, *-] (1,2)
6     to[R=10<\ohm>, v=$v_x$] (3,2) -- (4,2)
7     to[cI=$\frac{\siemens}{5} v_x$, *-] (4,0) -- (3,0)
8     to[R=5<\ohm>, *-] (3,2)
9   (3,0) -- (1,0)
10  (1,2) to[short, -o] (0,2) node[anchor=east]{A}
11 \end{circuitikz}

```

3.1.2.2 Thickness of the lines (globally)

You can change the thickness of the components lines with the parameter `bipoles/thickness` (default 2). The number is relative to the thickness of the normal lines leading to the component.



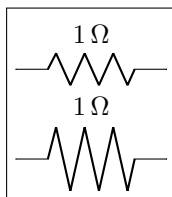
```

1 \ctikzset{bipoles/thickness=1}
2 \tikz \draw (0,0) to[C=1<\farad>] (2,0); \par
3 \ctikzset{bipoles/thickness=4}
4 \tikz \draw (0,0) to[C=1<\farad>] (2,0);

```

3.1.2.3 Shape of the components (on a per-component-class basis)

The shape of the components are adjustable with a lot of parameters; in this manual we will comment the main ones, but you can look into the source files specified above to find more.



```

1 \tikz \draw (0,0) to[R=1<\ohm>] (2,0); \par
2 \ctikzset{bipoles/resistor/height=.6}
3 \tikz \draw (0,0) to[R=1<\ohm>] (2,0);

```

3.1.3 Descriptions

The typical entry in the component list will be like this:

	<code>resistor</code> , type: <code>path-style</code> , nodename: <code>resistorshape</code> . Aliases: <code>R</code> , <code>american resistor</code> .
	<code>pR</code> , type: <code>path-style</code> , nodename: <code>potentiometershape</code> . Aliases: <code>pR</code> , <code>american potentiometer</code> .

where you have all the needed information about the bipole, with also no-standard anchors.

3.2 Node-style components

Node-style components (monopoles, multipoles) can be drawn at a specified point with this syntax, where #1 is the name of the component:

```

1 \begin{circuitikz}
2   \draw (0,0) node[#1,#2] (#3) {#4};
3 \end{circuitikz}

```

Explanation of the parameters:

#1: component name³ (mandatory)

#2: list of comma separated options (optional)

#3: name of an anchor (optional)

#4: text written to the text anchor of the component (optional)

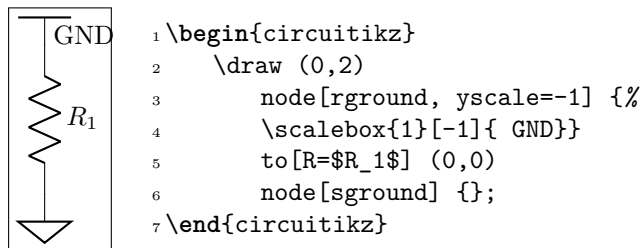
Most path-style components can be used as a node-style components; to access them, you add a **shape** to the main name of component (for example, **diodeshape**). Such a “node name” is specified in the description of each component.

Notice: Nodes must have curly brackets at the end, even when empty. An optional anchor (#3) can be defined within round brackets to be addressed again later on. And please don’t forget the semicolon to terminate the `\draw` command.

Also notice: If using the `\tikzexternalize` feature, as of TikZ 2.1 all pictures must end with `\end{tikzpicture}`. Thus you *cannot* use the `circuitikz` environment. Which is ok: just use the environment `tikzpicture`: everything will work there just fine.

3.2.1 Mirroring and flipping

Mirroring and flipping of node components is obtained by using the TikZ keys `xscale` and `yscale`. Notice that this parameters affect also text labels, so they need to be un-scaled by hand.

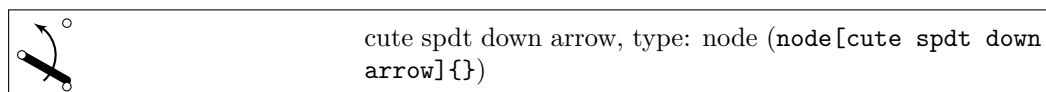


3.2.2 Anchors

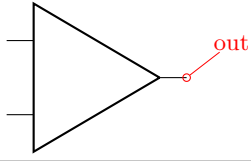
Node components anchors are variable across the various kind of components, so they will be described better after each category is presented in the manual.

3.2.3 Descriptions

The typical entry in the component list will be like this:










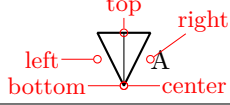
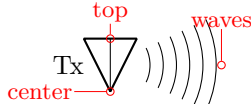
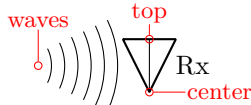
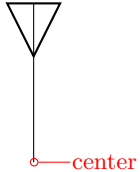
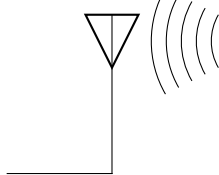
³For using bipoles as nodes, the name of the node is `#1shape`.

	plain amp, type: node (node[plain amp]{})
---	---

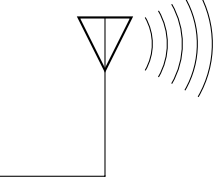
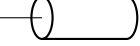



All the shapes defined by CircuiTikZ. These are all **pgf** nodes, so they are usable in both **pgf** and **TikZ**.

3.3 Monopoles

For the monopoles, the **center** anchor is put on the connecting point of the symbol, so that you can use them directly in a **path** specification.


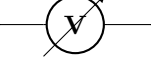

	ground, type: node (node[ground]{})
	rground, type: node (node[rground]{})
	sground, type: node (node[sground]{})
	tground, type: node (node[tground]{})
	nground, type: node (node[nground]{})
	pground, type: node (node[pground]{})
	cground, type: node (node[cground]{})
	bareantenna, type: node (node[bareantenna]{A})
	bareTXantenna, type: node (node[bareTXantenna]{Tx})
	bareRXantenna, type: node (node[bareRXantenna]{Rx})
	antenna, type: node (node[antenna]{})
	rxantenna, type: node (node[rxantenna]{})

³These last three were contributed by Luigi «Liverpool»



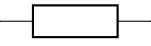
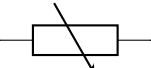
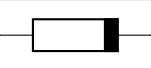

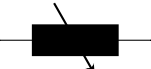
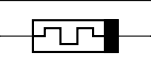
	<code>txantenna</code> , type: node (node[txantenna]{})
	<code>tlinestub</code> , type: node (node[tlinestub]{})
	<code>vcc</code> , type: node (node[vcc]{})
	<code>vee</code> , type: node (node[vee]{})
	<code>match</code> , type: node (node[match]{})

3.4 Bipoles



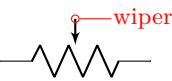
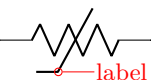
3.4.1 Instruments

	<code>ammeter</code> , type: path-style , nodename: ammetershape.
	<code>voltmeter</code> , type: path-style , nodename: voltmetershape.
	<code>ohmmeter</code> , type: path-style , nodename: ohmmetershape.


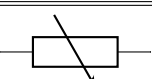
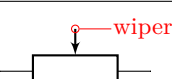
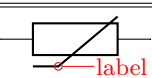
3.4.2 Resistive bipoles

	<code>short</code> , type: path-style , nodename: shortshape.
	<code>open</code> , type: path-style , nodename: openshape.
	<code>generic</code> , type: path-style , nodename: genericshape.
	<code>tgeneric</code> , type: path-style , nodename: tgenericshape.
	<code>ageneric</code> , type: path-style , nodename: agenericshape.
	<code>fullgeneric</code> , type: path-style , nodename: fullgenericshape.
	<code>tfullgeneric</code> , type: path-style , nodename: tfullgenericshape.
	<code>memristor</code> , type: path-style , nodename: memristorshape. Aliases: Mr.

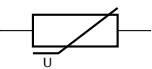
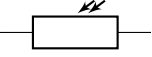

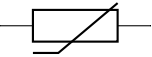


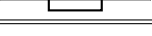
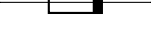
If (default behaviour) `americanresistors` option is active (or the style `[american resistors]` is used), the resistor is displayed as follows:

	<code>R</code> , type: path-style, nodename: <code>resistorshape</code> . Aliases: american resistor.
	<code>vR</code> , type: path-style, nodename: <code>vresistorshape</code> . Aliases: variable american resistor.
	<code>pR</code> , type: path-style, nodename: <code>potentiometershape</code> . Aliases: american potentiometer.
	<code>sR</code> , type: path-style, nodename: <code>resistivesensshape</code> . Aliases: american resistive sensor.

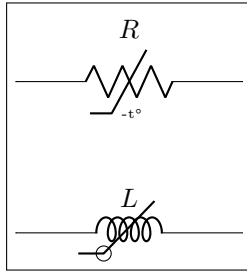
If instead `europeanresistors` option is active (or the style `[european resistors]` is used), the resistors, variable resistors and potentiometers are displayed as follows:

	<code>R</code> , type: path-style, nodename: <code>genericshape</code> . Aliases: european resistor.
	<code>vR</code> , type: path-style, nodename: <code>tgenericshape</code> . Aliases: variable european resistor.
	<code>pR</code> , type: path-style, nodename: <code>genericpotentiometershape</code> . Aliases: european potentiometer.
	<code>sR</code> , type: path-style, nodename: <code>thermistorshape</code> . Aliases: european resistive sensor.

Other miscellaneous resistor-like devices:

	<code>varistor</code> , type: path-style , nodename: <code>varistorshape</code> .
	<code>phR</code> , type: path-style, nodename: <code>photoresistorshape</code> . Aliases: photoresistor.
	<code>thermocouple</code> , type: path-style , nodename: <code>thermocoupleshape</code> .
	<code>thR</code> , type: path-style, nodename: <code>thermistorshape</code> . Aliases: thermistor.
	<code>thRp</code> , type: path-style, nodename: <code>thermistorptcshape</code> . Aliases: thermistor ptc.
	<code>thRn</code> , type: path-style, nodename: <code>thermistorntcshape</code> . Aliases: thermistor ntc.
	<code>fuse</code> , type: path-style , nodename: <code>fuseshape</code> .
	<code>afuse</code> , type: path-style , nodename: <code>afuseshape</code> . Aliases: asymmetric fuse.

3.4.2.1 Generic sensors anchors Generic sensors have an extra label to help positioning the type of dependence, if needed:



```

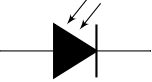
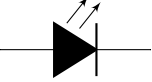

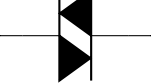
1 \begin{circuitikz}
2   \draw (0,2) to[sR, l=$R$, name=mySR] ++(3,0);
3   \node [font=\tiny, right] at(mySR.label) {-t\si{\degree}};
4   \draw (0,0) to[sL, l=$L$, name=mySL] ++(3,0);
5   \node [draw, circle, inner sep=2pt] at(mySL.label) {};
6 \end{circuitikz}

```

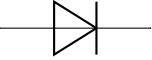



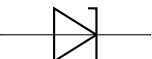
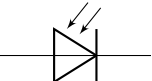
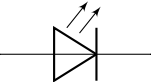

The anchor is positioned just on the corner of the segmented line crossing the component.

3.4.3 Diodes and such

	empty diode, type: path-style, nodename: emptydiodeshape. Aliases: Do.
	empty Schottky diode, type: path-style, nodename: emptysdiodeshape. Aliases: sDo.
	empty Zener diode, type: path-style, nodename: emptyzdiodeshape. Aliases: zDo.
	empty ZZener diode, type: path-style, nodename: emptyzzdiodeshape. Aliases: zzDo.
	empty tunnel diode, type: path-style, nodename: emptytdiodeshape. Aliases: tDo.
	empty photodiode, type: path-style, nodename: emptypdiodeshape. Aliases: pDo.
	empty led, type: path-style, nodename: emptylediodeshape. Aliases: leDo.
	empty varcap, type: path-style, nodename: emptyvarcapshape. Aliases: VCo.
	empty bidirectionaldiode, type: path-style, nodename: emptybidirectionaldiodeshape. Aliases: biDo.
	full diode, type: path-style, nodename: fulldiodeshape. Aliases: D*.
	full Schottky diode, type: path-style, nodename: fullsdiodeshape. Aliases: sD*.
	full Zener diode, type: path-style, nodename: fullzdiodeshape. Aliases: zD*.
	full ZZener diode, type: path-style, nodename: fullzzdiodeshape. Aliases: zzD*.
	full tunnel diode, type: path-style, nodename: fulltdiodeshape. Aliases: tD*.

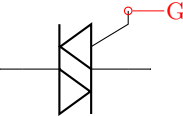
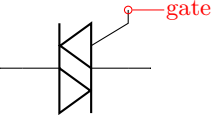
	<code>full photodiode</code> , type: path-style, nodename: <code>fullpdiodeshape</code> . Aliases: <code>pD*</code> .
	<code>full led</code> , type: path-style, nodename: <code>fulllediodeshape</code> . Aliases: <code>leD*</code> .
	<code>full varcap</code> , type: path-style, nodename: <code>fullvarcapshape</code> . Aliases: <code>VC*</code> .
	<code>full bidirectionaldiode</code> , type: path-style, nodename: <code>fullbidirectionaldiodeshape</code> . Aliases: <code>biD*</code> .

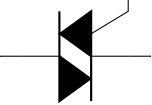
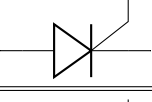
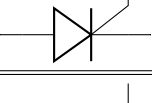
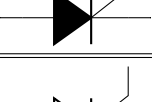
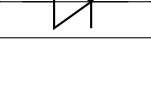
These shapes has no exact node-style counterpart, because the stroke line is build upon the empty variants:

	<code>stroke diode</code> , type: path-style, nodename: <code>emptydiodeshape</code> . Aliases: <code>D-</code> .
	<code>stroke Schottky diode</code> , type: path-style, nodename: <code>emptysdiodeshape</code> . Aliases: <code>sD-</code> .
	<code>stroke Zener diode</code> , type: path-style, nodename: <code>emptyzdiodeshape</code> . Aliases: <code>zD-</code> .
	<code>stroke ZZener diode</code> , type: path-style, nodename: <code>emptyzzdiodeshape</code> . Aliases: <code>zzD-</code> .
	<code>stroke tunnel diode</code> , type: path-style, nodename: <code>emptytdiodeshape</code> . Aliases: <code>tD-</code> .
	<code>stroke photodiode</code> , type: path-style, nodename: <code>emptypdiodeshape</code> . Aliases: <code>pD-</code> .
	<code>stroke led</code> , type: path-style, nodename: <code>emptylediodeshape</code> . Aliases: <code>leD-</code> .
	<code>stroke varcap</code> , type: path-style, nodename: <code>emptyvarcapshape</code> . Aliases: <code>VC-</code> .

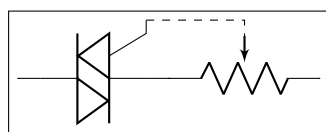
3.4.4 Tripole-like diodes

The following tripoles are entered with the usual command of the form

	<code>triac</code> , type: path-style, nodename: <code>emptytriacshape</code> . Aliases: <code>Tr</code> .
	<code>empty triac</code> , type: path-style, nodename: <code>emptytriacshape</code> . Aliases: <code>Tro</code> .

	<code>full triac</code> , type: path-style, nodename: <code>fulltriacshape</code> . Aliases: <code>Tr*</code> .
	<code>thyristor</code> , type: path-style, nodename: <code>emptythyristorshape</code> . Aliases: <code>Ty</code> .
	<code>empty thyristor</code> , type: path-style, nodename: <code>emptythyristorshape</code> . Aliases: <code>Tyo</code> .
	<code>full thyristor</code> , type: path-style, nodename: <code>fullthyristorshape</code> . Aliases: <code>Ty*</code> .
	<code>stroke thyristor</code> , type: path-style, nodename: <code>emptythyristorshape</code> . Aliases: <code>Ty-</code> .

3.4.4.1 Triacs anchors When inserting a thyristor, a triac or a potentiometer, one needs to refer to the third node—gate (gate or G) for the former two; wiper (wiper or W) for the latter one. This is done by giving a name to the bipole:

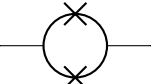
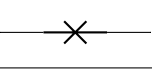
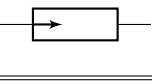
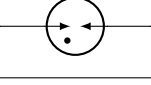


```

1 \begin{circuitikz} \draw
2   (0,0) to[Tr, n=TRI] (2,0)
3     to[pR, n=POT] (4,0);
4   \draw[dashed] (TRI.G) -| (POT.wiper)
5 ;\end{circuitikz}

```

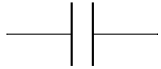
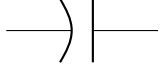

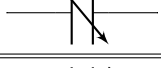

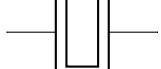
The package options `fulldiode`, `strokediode`, and `emptydiode` (and the styles `[full diodes]`, `[stroke diodes]`, and `[empty diodes]`) define which shape will be used by abbreviated commands such that `D`, `sD`, `zD`, `zzD`, `tD`, `pD`, `leD`, `VC`, `Ty`, `Tr` (no stroke symbol available!).

	<code>squid</code> , type: path-style , nodename: <code>squidshape</code> .
	<code>barrier</code> , type: path-style , nodename: <code>barriershape</code> .
	<code>european gas filled surge arrester</code> , type: path-style , nodename: <code>european gas filled surge arrestershape</code> .
	<code>american gas filled surge arrester</code> , type: path-style , nodename: <code>american gas filled surge arrestershape</code> .

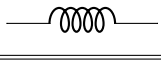
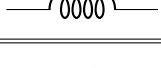
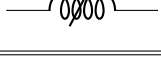
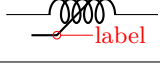
If (default behaviour) `europengfsurgearrester` option is active (or the style `[european gas filled surge arrester]` is used), the shorthands `gas filled surge arrester` and `gf surge arrester` are equivalent to the european version of the component.

If otherwise `americangfsurgearrester` option is active (or the style `[american gas filled surge arrester]` is used), the shorthands `gas filled surge arrester` and `gf surge arrester` are equivalent to the american version of the component.



3.4.5 Basic dynamical bipoles


	<code>capacitor</code> , type: <code>path-style</code> , nodename: <code>capacitorshape</code> . Aliases: <code>C</code> .
	<code>polar capacitor</code> , type: <code>path-style</code> , nodename: <code>polarcapacitorshape</code> . Aliases: <code>pC</code> .
	<code>ecapacitor</code> , type: <code>path-style</code> , nodename: <code>ecapacitorshape</code> . Aliases: <code>eC</code> , <code>elko</code> .
	<code>variable capacitor</code> , type: <code>path-style</code> , nodename: <code>vcapacitorshape</code> . Aliases: <code>vC</code> .
	<code>capacitive sensor</code> , type: <code>path-style</code> , nodename: <code>capacitivesensshape</code> . Aliases: <code>sC</code> .
	<code>piezoelectric</code> , type: <code>path-style</code> , nodename: <code>piezoelectricshape</code> . Aliases: <code>PZ</code> .

If (default behaviour) `cuteinductors` option is active (or the style `[cute inductors]` is used), the inductors are displayed as follows:


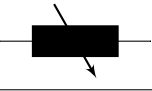
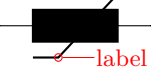
	<code>L</code> , type: <code>path-style</code> , nodename: <code>cuteinductorshape</code> . Aliases: <code>cute inductor</code> .
	<code>cute choke</code> , type: <code>path-style</code> , nodename: <code>cutechokesshape</code> .
	<code>vL</code> , type: <code>path-style</code> , nodename: <code>vcuteinductorshape</code> . Aliases: <code>variable cute inductor</code> .
	<code>sL</code> , type: <code>path-style</code> , nodename: <code>scuteinductorshape</code> . Aliases: <code>cute inductive sensor</code> .

If `americaninductors` option is active (or the style `[american inductors]` is used), the inductors are displayed as follows:

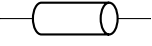
	<code>L</code> , type: <code>path-style</code> , nodename: <code>americaninductorshape</code> . Aliases: <code>american inductor</code> .
	<code>vL</code> , type: <code>path-style</code> , nodename: <code>vamericaninductorshape</code> . Aliases: <code>variable american inductor</code> .

	<code>sL</code> , type: path-style, nodename: <code>samericaninductorshape</code> . Aliases: american inductive sensor.
---	---

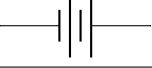
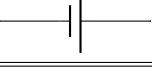

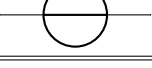
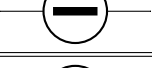
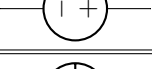
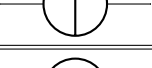
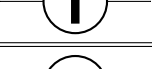

Finally, if `europeaninductors` option is active (or the style `[european inductors]` is used), the inductors are displayed as follows:

	<code>L</code> , type: path-style, nodename: <code>fullgenericshape</code> . Aliases: european inductor.
	<code>vL</code> , type: path-style, nodename: <code>tfullgenericshape</code> . Aliases: variable european inductor.
	<code>sL</code> , type: path-style, nodename: <code>sfullgenericshape</code> . Aliases: european inductive sensor.

There is also a transmission line:

	<code>TL</code> , type: path-style, nodename: <code>tlineshape</code> . Aliases: transmission line, <code>tline</code> .
---	--

3.4.6 Stationary sources

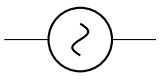
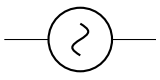
	<code>battery</code> , type: path-style , nodename: <code>batteryshape</code> .
	<code>battery1</code> , type: path-style , nodename: <code>battery1shape</code> .
	<code>battery2</code> , type: path-style , nodename: <code>battery2shape</code> .
	<code>european voltage source</code> , type: path-style, nodename: <code>vsourceshape</code> .
	<code>cute european voltage source</code> , type: path-style, nodename: <code>vsourceshape</code> . Aliases: <code>vsourceshape</code> , <code>ceV</code> .
	<code>american voltage source</code> , type: path-style, nodename: <code>vsourceshape</code> .
	<code>european current source</code> , type: path-style, nodename: <code>isourceshape</code> .
	<code>cute european current source</code> , type: path-style, nodename: <code>isourceshape</code> . Aliases: <code>isourceshape</code> , <code>ceI</code> .
	<code>american current source</code> , type: path-style, nodename: <code>isourceshape</code> .

If (default behaviour) `europeancurrents` option is active (or the style `[european currents]` is used), the shorthands `current source`, `isource`, and `I` are equivalent to `european current source`. Otherwise, if `americancurrents` option is active (or the style `[american currents]` is used) they are equivalent to `american current source`.

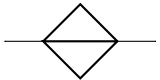

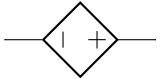
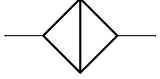
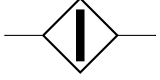
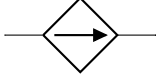
Similarly, if (default behaviour) `europeanvoltages` option is active (or the style `[european voltages]` is used), the shorthands `voltage source`, `vsource`, and `V` are equivalent to `european voltage source`. Otherwise, if `americanvoltages` option is active (or the style `[american voltages]` is used) they are equivalent to `american voltage source`.

3.4.7 Sinusoidal sources

Here because I was asked for them. But how do you distinguish one from the other?!

	<code>sinusoidal voltage source</code> , type: <code>path-style</code> , nodename: <code>vsourcesinshape</code> . Aliases: <code>vsourcesin</code> , <code>sV</code> .
	<code>sinusoidal current source</code> , type: <code>path-style</code> , nodename: <code>isourcesinshape</code> . Aliases: <code>isourcesin</code> , <code>sI</code> .

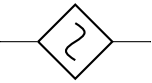
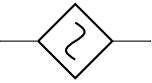
3.4.8 Controlled sources

	<code>europ^ean controlled voltage source</code> , type: <code>path-style</code> , nodename: <code>cvsourceCshape</code> .
	<code>cute europ^ean controlled voltage source</code> , type: <code>path-style</code> , nodename: <code>cvsourceCshape</code> . Aliases: <code>cvsourceC</code> , <code>cceV</code> .
	<code>american controlled voltage source</code> , type: <code>path-style</code> , nodename: <code>cvsourceAMshape</code> .
	<code>europ^ean controlled current source</code> , type: <code>path-style</code> , nodename: <code>cisourceCshape</code> .
	<code>cute europ^ean controlled current source</code> , type: <code>path-style</code> , nodename: <code>cisourceCshape</code> . Aliases: <code>cisourceC</code> , <code>cceI</code> .
	<code>american controlled current source</code> , type: <code>path-style</code> , nodename: <code>cisourceAMshape</code> .

If (default behaviour) `europeancurrents` option is active (or the style `[european currents]` is used), the shorthands `controlled current source`, `cisource`, and `cI` are equivalent to `european controlled current source`. Otherwise, if `americancurrents` option is active (or the style `[american currents]` is used) they are equivalent to `american controlled current source`.

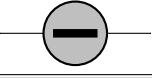
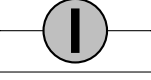
Similarly, if (default behaviour) `europeanvoltages` option is active (or the style `[european voltages]` is used), the shorthands `controlled voltage source`, `cvsource`,

and `cV` are equivalent to european controlled voltage source. Otherwise, if `americanvoltages` option is active (or the style `[american voltages]` is used) they are equivalent to american controlled voltage source.

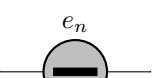
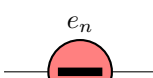
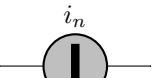
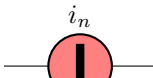
	controlled sinusoidal voltage source, type: path-style, nodename: cvsourcesinshape. Aliases: controlled vsourcesin, cvsourcesin, csV.
	controlled sinusoidal current source, type: path-style, nodename: cisourcesinshape. Aliases: controlled isourcesin, cisourcesin, csI.

3.4.9 Noise sources

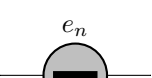

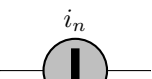

In this case, the “direction” of the source has no sense.

	noise voltage source, type: path-style, nodename: vsourcenNshape. Aliases: vsourcenN, nV.
	noise current source, type: path-style, nodename: isourcenNshape. Aliases: isourcenN, nI.

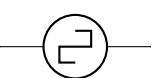
You can change the fill color with the key `circuitikz/bipoles/noise sources/fillcolor`:


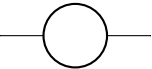
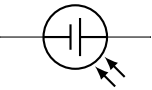
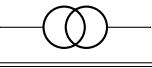
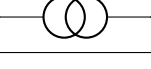
		1 <code>\begin{circuitikz}</code>
		2 <code>\draw(0,0) to [nV, l=\$e_n\$] ++(2,0);</code>
		3 <code>\draw(0,-2) to [nI, l=\$i_n\$] ++(2,0);</code>
		4 <code>\begin{scope}[circuitikz/bipoles/noise</code>
		5 <code>\draw(3,0) to [nV, l=\$e_n\$] ++(2,0);</code>
		6 <code>\draw(3,-2) to [nI, l=\$i_n\$] ++(2,0);</code>
		7 <code>\end{scope}</code>
		8 <code>\end{circuitikz}</code>

If you prefer a patterned noise generator (similar to the one you draw by hand) you can use the fake color `dashed`:


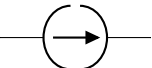
		1 <code>\begin{circuitikz}</code>
		2 <code>\draw(0,0) to [nV, l=\$e_n\$] ++(2,0);</code>
		3 <code>\draw(0,-2) to [nI, l=\$i_n\$] ++(2,0);</code>
		4 <code>\begin{scope}[circuitikz/bipoles/noise</code>
		5 <code>\draw(3,0) to [nV, l=\$e_n\$] ++(2,0);</code>
		6 <code>\draw(3,-2) to [nI, l=\$i_n\$] ++(2,0);</code>
		7 <code>\end{scope}</code>
		8 <code>\end{circuitikz}</code>

3.4.10 Special sources

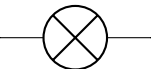

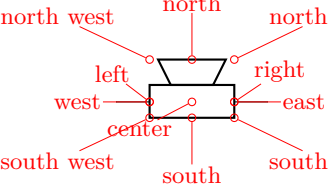
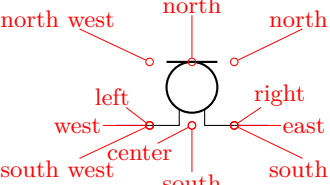
	square voltage source, type: path-style, nodename: vsourcesquashape. Aliases: vsourcesquare, sqV.
---	---

	<code>vsource</code> <code>tri</code> , type: path-style , nodename: <code>vsource</code> <code>tri</code> <code>shape</code> . Aliases: <code>tV</code> .
	<code>esource</code> , type: path-style , nodename: <code>esource</code> <code>shape</code> .
	<code>pvsources</code> , type: path-style , nodename: <code>pvsources</code> <code>shape</code> .
	<code>ioosources</code> , type: path-style, nodename: <code>oosources</code> <code>shape</code> .
	<code>voosources</code> , type: path-style, nodename: <code>oosources</code> <code>shape</code> .

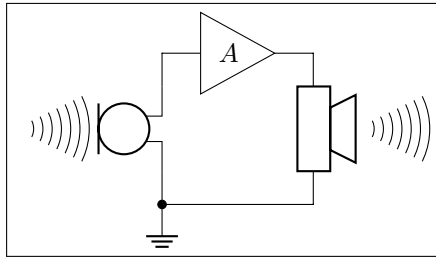
3.4.11 DC sources

	<code>dcvsource</code> , type: path-style , nodename: <code>dcvsource</code> <code>shape</code> .
	<code>dcisources</code> , type: path-style , nodename: <code>dcisources</code> <code>shape</code> .

3.4.12 Other bipoles

	<code>lamp</code> , type: path-style , nodename: <code>lamp</code> <code>shape</code> .
	<code>bulb</code> , type: path-style , nodename: <code>bulb</code> <code>shape</code> .
	<code>loudspeakers</code> , type: path-style , nodename: <code>loudspeakers</code> <code>shape</code> .
	<code>mic</code> , type: path-style , nodename: <code>mic</code> <code>shape</code> .

You can use microphones and loudspeakers with `waves` (see section 3.9) too:



```

1  \begin{circuitikz}
2      \draw (0,0) to[mic, name=M] ++(0,2)
3      to[amp, t=$A$] ++(2,0)
4      to[loudspeaker, name=L] ++(0,-2)
5      to[short, -*] (0,0) node[ground]{};
6      \node [waves, scale=0.7, left=5pt]
7          at(M.north) {};
8      \node [waves, scale=0.7, right]
9          at(L.north) {};
10 \end{circuitikz}

```

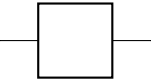

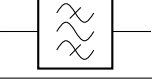







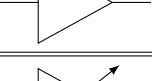
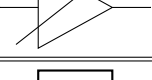
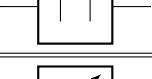
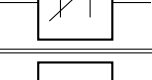
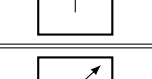

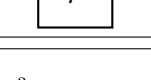
3.4.13 Mechanical Analogy


	<code>damper</code> , type: path-style , nodename: dampershape.
	<code>spring</code> , type: path-style , nodename: springshape.
	<code>mass</code> , type: path-style , nodename: massshape.



3.5 Block diagram components

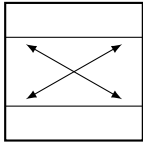
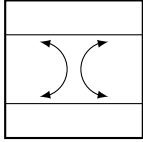
Contributed by Stefan Erhardt.

	<code>mixer</code> , type: node (<code>node[mixer]{} </code>)
	<code>adder</code> , type: node (<code>node[adder]{} </code>)
	<code>oscillator</code> , type: node (<code>node[oscillator]{} </code>)
	<code>circulator</code> , type: node (<code>node[circulator]{} </code>)
	<code>wilkinson</code> , type: node (<code>node[wilkinson]{} </code>)

	<code>twoport</code> , type: path-style , nodename: twoportshape.
	<code>vco</code> , type: path-style , nodename: vcoshape.
	<code>bandpass</code> , type: path-style , nodename: bandpassshape.
	<code>bandstop</code> , type: path-style , nodename: bandstopshape.
	<code>highpass</code> , type: path-style , nodename: highpassshape.
	<code>lowpass</code> , type: path-style , nodename: lowpassshape.
	<code>adc</code> , type: path-style , nodename: adcshape.
	<code>dac</code> , type: path-style , nodename: dacshape.
	<code>dsp</code> , type: path-style , nodename: dspshape.
	<code>fft</code> , type: path-style , nodename: fftshape.
	<code>amp</code> , type: path-style , nodename: ampshape.
	<code>vamp</code> , type: path-style , nodename: vampshape.
	<code>piattenuator</code> , type: path-style , nodename: piattenuatorshape.
	<code>vpiattenuator</code> , type: path-style , nodename: vpiattenuatorshape.
	<code>tattenuator</code> , type: path-style , nodename: tattenuatorshape.
	<code>vtattenuator</code> , type: path-style , nodename: vtattenuatorshape.
	<code>phaseshifter</code> , type: path-style , nodename: phaseshiftershape.

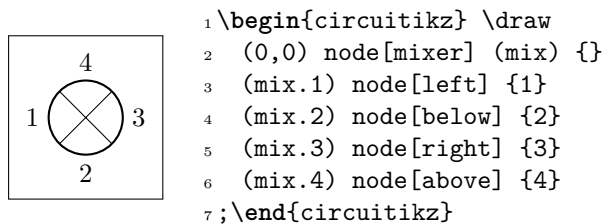
³To specify text to be put in the component: `twoport[t=text]`: 

	<code>vphaseshifter</code> , type: path-style , nodename: <code>vphaseshiftershape</code> .
	<code>detector</code> , type: path-style , nodename: <code>detectorshape</code> .

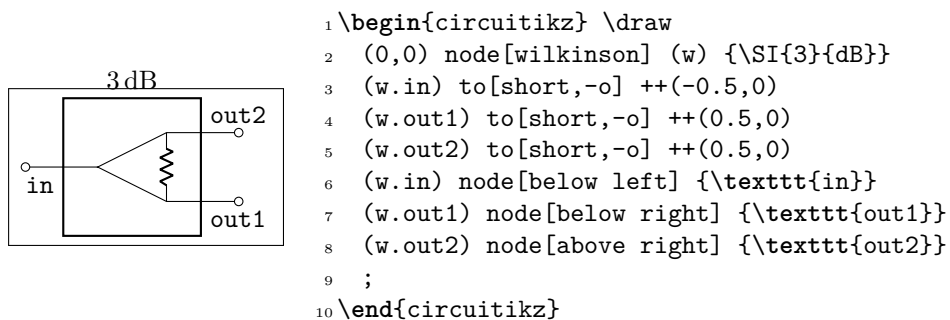
	<code>coupler</code> , type: node (node[<code>coupler</code>]{})
	<code>coupler2</code> , type: node (node[<code>coupler2</code>]{})

3.5.1 Blocks anchors

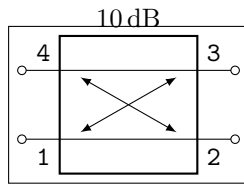
The ports of the mixer and adder can be addressed with numbers or `west/south/east/north`:



The Wilkinson divider has:



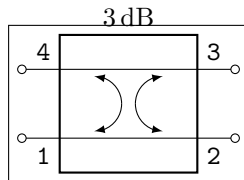
The couplers have:



```

1 \begin{circuitikz} \draw
2   (0,0) node[coupler] (c) {\SI{10}{dB}}
3   (c.1) to[short,-o] ++(-0.5,0)
4   (c.2) to[short,-o] ++(0.5,0)
5   (c.3) to[short,-o] ++(0.5,0)
6   (c.4) to[short,-o] ++(-0.5,0)
7   (c.1) node[below left] {\texttt{1}}
8   (c.2) node[below right] {\texttt{2}}
9   (c.3) node[above right] {\texttt{3}}
10  (c.4) node[above left] {\texttt{4}}
11 ;
12 \end{circuitikz}

```



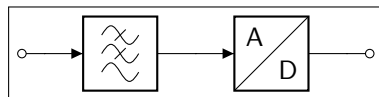
```

1 \begin{circuitikz} \draw
2   (0,0) node[coupler2] (c) {\SI{3}{dB}}
3   (c.1) to[short,-o] ++(-0.5,0)
4   (c.2) to[short,-o] ++(0.5,0)
5   (c.3) to[short,-o] ++(0.5,0)
6   (c.4) to[short,-o] ++(-0.5,0)
7   (c.1) node[below left] {\texttt{1}}
8   (c.2) node[below right] {\texttt{2}}
9   (c.3) node[above right] {\texttt{3}}
10  (c.4) node[above left] {\texttt{4}}
11 ;
12 \end{circuitikz}

```

3.5.2 Blocks customization

With the option `>` you can draw an arrow to the input of the block diagram symbols.

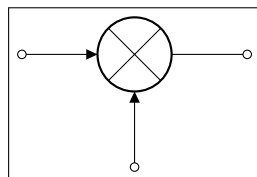


```

1 \begin{circuitikz} \draw
2   (0,0) to[short,o-] ++(0.3,0)
3   to[lowpass,>] ++(2,0)
4   to[adc,>] ++(2,0)
5   to[short,-o] ++(0.3,0);
6 \end{circuitikz}

```

3.5.2.1 Multi ports Since inputs and outputs can vary, input arrows can be placed as nodes. Note that you have to rotate the arrow on your own:

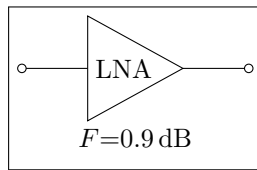


```

1 \begin{circuitikz} \draw
2   (0,0) node[mixer] (m) {}
3   (m.1) to[short,-o] ++(-1,0)
4   (m.2) to[short,-o] ++(0,-1)
5   (m.3) to[short,-o] ++(1,0)
6   (m.1) node[inputarrow] {}
7   (m.2) node[inputarrow,rotate=90] {};
8 \end{circuitikz}

```

3.5.2.2 Labels and custom twoport boxes Some twoports have the option to place a normal label (`l=`) and a inner label (`t=`).

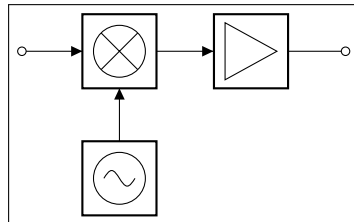


```

1 \begin{circuitikz}
2   \ctikzset{bipoles/amp/width=0.9}
3   \draw (0,0) to[amp,t=LNA,l_=$F{=}0.9\,$dB,o-o] ++(3,0);
4 \end{circuitikz}

```

3.5.2.3 Box option Some devices have the possibility to add a box around them. The inner symbol scales down to fit inside the box.

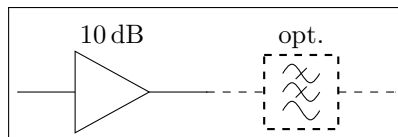


```

1 \begin{circuitikz} \draw
2   (0,0) node[mixer,box,anchor=west] (m) {}
3   to[amp,box,>,-o] ++(2.5,0)
4   (m.west) node[inputarrow] {} to[short,-o]
5   ++(-0.8,0)
6   (m.south) node[inputarrow,rotate=90] {} --
7   ++(0,-0.7) node[oscillator,box,anchor=north] {};
8 \end{circuitikz}

```

3.5.2.4 Dash optional parts To show that a device is optional, you can dash it. The inner symbol will be kept with solid lines.



```

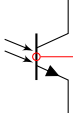
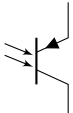
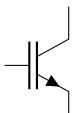
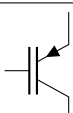
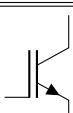

1 \begin{circuitikz}
2   \draw (0,0) to[amp,l=\SI{10}{dB}] ++(2.5,0);
3   \draw[dashed] (2.5,0) to[lowpass,l=opt.]
4   ++(2.5,0);
5 \end{circuitikz}

```

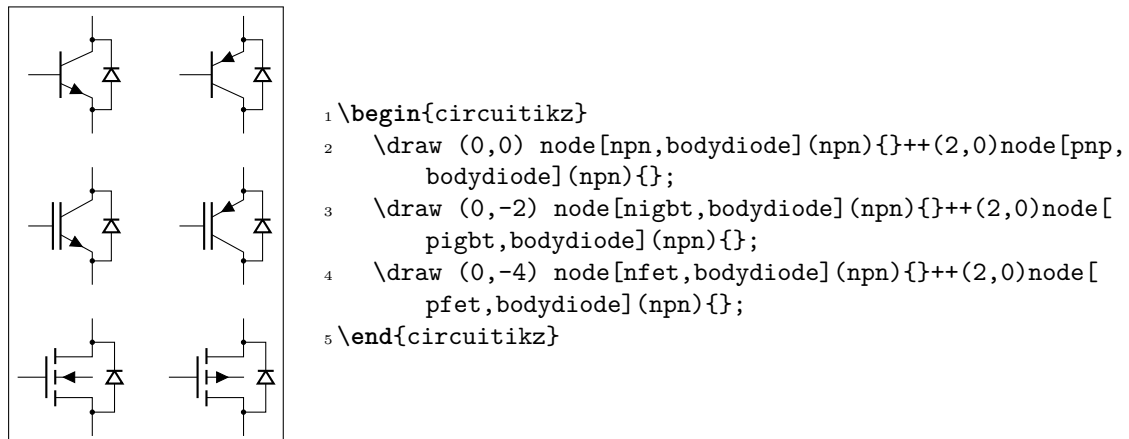
3.6 Tripoles

3.6.1 Transistors

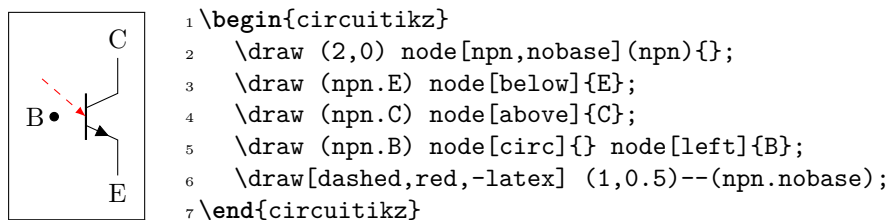
	nmos, type: node (node[nmos]{})
	pmos, type: node (node[pmos]{})
	hemt, type: node (node[hemt]{})
	nnp, type: node (node[nnp]{})
	pnp, type: node (node[pnp]{})

	<code>nnp,photo, type: node (node[nnp,photo]{})</code>
	<code>pnnp,photo, type: node (node[pnp,photo]{})</code>
	<code>nigt, type: node (node[nigt]{})</code>
	<code>pigt, type: node (node[pigt]{})</code>
	<code>Lnigt, type: node (node[Lnigt]{})</code>
	<code>Lpigt, type: node (node[Lpigt]{})</code>

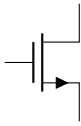
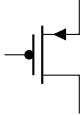
For all transistors a bodydiode (or freewheeling diode) can automatically be drawn. Just use the global option `bodydiode`, or for single transistors, the `tikz`-option `bodydiode`:



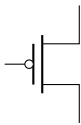
The Base/Gate connection of all transistors can be disabled by using the options `nogate` or `nobase`, respectively. The Base/Gate anchors are floating, but there is an additional anchor "no-gate"/"nobase", which can be used to point to the unconnected base:



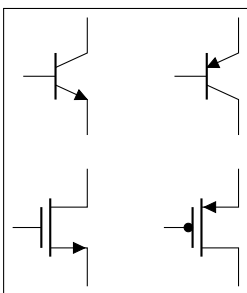
If the option `arrowmos` is used (or after the command `\ctikzset{tripoles/mos style/arrows}` is given), this is the output:

	<code>nmos, type: node (node[nmos]{})</code>
	<code>pmos, type: node (node[pmos]{})</code>

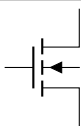
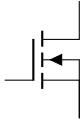
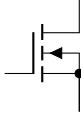
To draw the PMOS circle non-solid, use the option `emptycircle` or the command `\ctikzset{tripoles/pmos style/emptycircle}`.

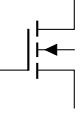
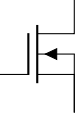
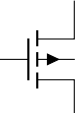
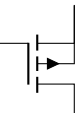
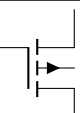
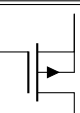
	<code>pmos,emptycircle, type: node (node[pmos,emptycircle]{})</code>
---	--

If you prefer different position of the arrows in transistors and FETs, you can adjust them like this (it works for the other BJT-based transistors, too):

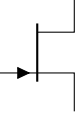
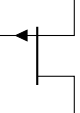
	<code>1 \begin{circuitikz}</code>
	<code>2 \ctikzset{tripoles/mos style/arrows,</code>
	<code>3 tripoles/npn/arrow pos=0.8,</code>
	<code>4 tripoles/pnp/arrow pos=0.8,</code>
	<code>5 tripoles/nmos/arrow pos=0.8,</code>
	<code>6 tripoles/pmos/arrow pos=0.6, }</code>
	<code>7 \draw (0,0) node[npn,](nnp){};</code>
	<code>8 \draw (2,0) node[pnp,](npn){};</code>
	<code>9 \draw (0,-2) node[nmos,](nnp){};</code>
	<code>10 \draw (2,-2) node[pmos,](npn){};</code>
	<code>11 \end{circuitikz}</code>

NFETs and PFETs have been incorporated based on code provided by Clemens Helfmeier and Theodor Borsche. Use the package options `fetsolderdot`/`nofetsolderdot` to enable/disable solderdot at some fet-transistors. Additionally, the solderdot option can be enabled/disabled for single transistors with the option `"solderdot"` and `"nosolderdot"`, respectively.

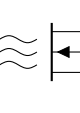
	<code>nfet, type: node (node[nfet]{})</code>
	<code>nigfete, type: node (node[nigfete]{})</code>
	<code>nigfete,solderdot, type: node (node[nigfete,solderdot]{})</code>

	nigfetebulk, type: node (node[nigfetebulk]{})
	nigfetd, type: node (node[nigfetd]{})
	pfet, type: node (node[pfet]{})
	pigfete, type: node (node[pigfete]{})
	pigfetebulk, type: node (node[pigfetebulk]{})
	pigfetd, type: node (node[pigfetd]{})

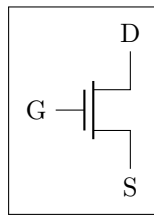
NJFET and PJFET have been incorporated based on code provided by Danilo Piazzalunga:

	njfet, type: node (node[njfet]{})
	pjfet, type: node (node[pjfet]{})

ISFET

	isfet, type: node (node[isfet]{})
---	-----------------------------------

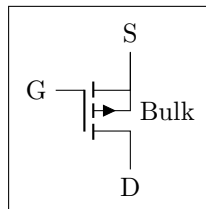
3.6.1.1 Transistors anchors For NMOS, PMOS, NFET, NIGFETE, NIGFETD, PFET, PIGFETE, and PIGFETD transistors one has **base**, **gate**, **source** and **drain** anchors (which can be abbreviated with B, G, S and D):



```

1 \begin{circuitikz} \draw
2   (0,0) node[nmos] (mos) {}
3   (mos.gate) node[anchor=east] {G}
4   (mos.drain) node[anchor=south] {D}
5   (mos.source) node[anchor=north] {S}
6 ;\end{circuitikz}

```

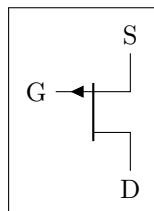


```

1 \begin{circuitikz} \draw
2   (0,0) node[pigfete] (pigfete) {}
3   (pigfete.G) node[anchor=east] {G}
4   (pigfete.D) node[anchor=north] {D}
5   (pigfete.S) node[anchor=south] {S}
6   (pigfete.bulk) node[anchor=west] {Bulk}
7 ;\end{circuitikz}

```

Similarly NJFET and PJFET have **gate**, **source** and **drain** anchors (which can be abbreviated with G, S and D):

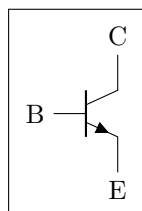


```

1 \begin{circuitikz} \draw
2   (0,0) node[pjfet] (pjfet) {}
3   (pjfet.G) node[anchor=east] {G}
4   (pjfet.D) node[anchor=north] {D}
5   (pjfet.S) node[anchor=south] {S}
6 ;\end{circuitikz}

```

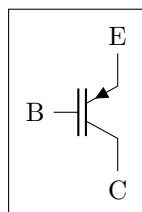
For NPN, PNP, NIGBT, and PIGBT transistors the anchors are **base**, **emitter** and **collector** anchors (which can be abbreviated with B, E and C):



```

1 \begin{circuitikz} \draw
2   (0,0) node[npn] (npn) {}
3   (npn.base) node[anchor=east] {B}
4   (npn.collector) node[anchor=south] {C}
5   (npn.emitter) node[anchor=north] {E}
6 ;\end{circuitikz}

```

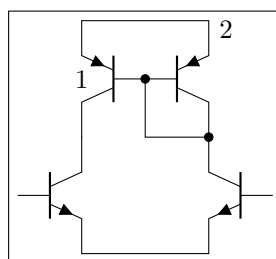


```

1 \begin{circuitikz} \draw
2   (0,0) node[pigbt] (pigbt) {}
3   (pigbt.B) node[anchor=east] {B}
4   (pigbt.C) node[anchor=north] {C}
5   (pigbt.E) node[anchor=south] {E}
6 ;\end{circuitikz}

```

Here is one composite example (please notice that the `xscale=-1` style would also reflect the label of the transistors, so here a new node is added and its text is used, instead of that of `npn1`):



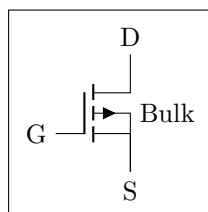
```

1 \begin{circuitikz} \draw
2   (0,0) node[npn] (npn2) {2}
3   (npn2.B) node[npn, xscale=-1, anchor=B] (npn1) {}
4   (npn1) node {1}
5   (npn1.C) node[npn, anchor=C] (npn1) {}
6   (npn2.C) node[npn, xscale=-1, anchor=C] (npn2) {}
7   (npn1.E) -- (npn2.E) (npn1.E) -- (npn2.E)
8   (npn1.B) node[circ] {} |- (npn2.C) node[circ] {}
9 ;\end{circuitikz}

```

Notice that the text labels of transistors are somewhat buggy. It is better to use explicit anchors to set transistor's names.

Similarly, transistors like other components can be reflected vertically:

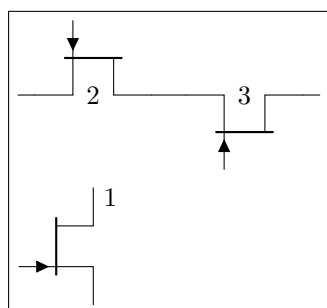


```

1 \begin{circuitikz} \draw
2 (0,0) node[pigfete, yscale=-1] (pigfete) {}
3 (pigfete.bulk) node[anchor=west] {Bulk}
4 (pigfete.G) node[anchor=west] {G}
5 (pigfete.D) node[anchor=south] {D}
6 (pigfete.S) node[anchor=north] {S}
7 ;\end{circuitikz}

```

3.6.1.2 Transistor paths For syntactical convenience transistors can be placed using the normal path notation used for bipoles. The transistor type can be specified by simply adding a “T” (for transistor) in front of the node name of the transistor. It will be placed with the base/gate orthogonal to the direction of the path:

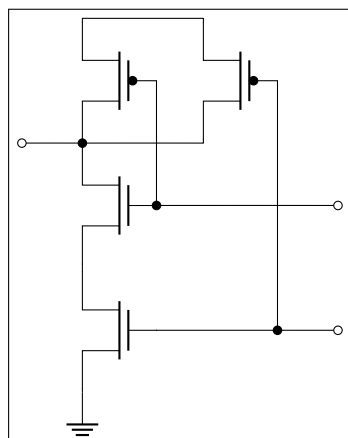


```

1 \begin{circuitikz} \draw
2 (0,0) node[njfet] {1}
3 (-1,2) to[Tnjfet=2] (1,2)
4 to[Tnjfet=3, mirror] (3,2);
5 ;\end{circuitikz}

```

Access to the gate and/or base nodes can be gained by naming the transistors with the **n** or **name** path style:



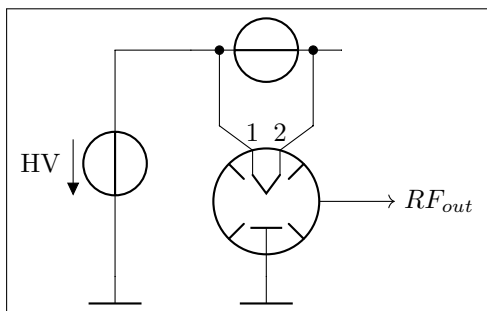
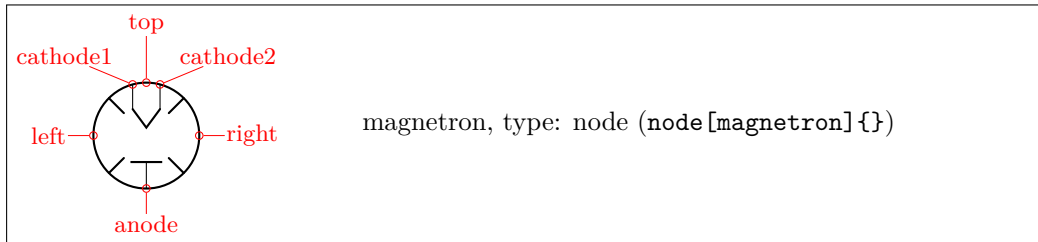
```

1 \begin{circuitikz} \draw[yscale=1.1, xscale=.8]
2 (2,4.5) -- (0,4.5) to[Tpmos, n=p1] (0,3)
3 to[Tnmos, n=n1] (0,1.5)
4 to[Tnmos, n=n2] (0,0) node[ground] {}
5 (2,4.5) to[Tpmos,n=p2] (2,3) to[short, -*] (0,3)
6 (p1.G) -- (n1.G) to[short, *-o] ($(n1.G)+(3,0)$)
7 (n2.G) ++(2,0) node[circ] {} -| (p2.G)
8 (n2.G) to[short, -o] ($(n2.G)+(3,0)$)
9 (0,3) to[short, -o] (-1,3)
10 ;\end{circuitikz}

```

The **name** property is available also for bipoles, although this is useful mostly for triac, potentiometer and thyristor (see 3.4.4).

3.6.2 Electronic Tubes

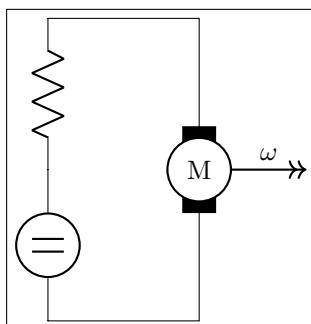
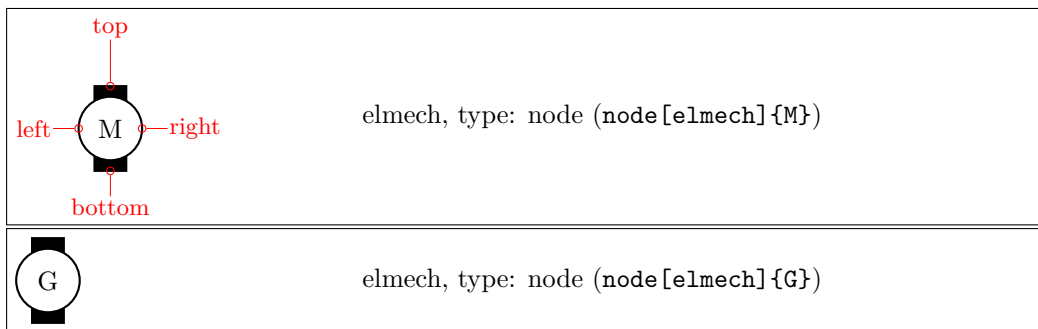


```

1 \begin{circuitikz}
2 \draw (0,-2)node[rground](gnd){} to[
   voltage source,v<={HV}]++(0,3)
   --++(1,0)to[V,n=DC]++(2,0);
3 \draw (2,-1) node[magnetron,scale=1](
   magn){};
4 \draw (DC.left)++(-0.2,0)to [short,*-]
   ++(0,-1) to [short] (magn.cathode1);
5 \draw (DC.right)++(0.2,0)to [short,*-]
   ++(0,-1) to [short] (magn.cathode2);
6 \draw (magn.anode) to [short] (magn.
   anode|-gnd) node[rground]{};
7 \draw (magn.cathode1)node[above]{$1$};
8 \draw (magn.cathode2)node[above]{$2$};
9 \draw[->](magn.east) --++(1,0)node[
   right]{$RF_{out}$};
10 \end{circuitikz}

```

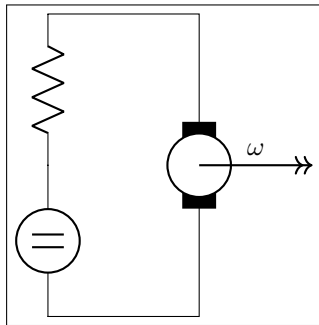
3.6.3 Electro-Mechanical Devices



```

1 \begin{circuitikz}
2 \draw (2,0) node[elmech](motor){M};
3 \draw (motor.north) |-(0,2) to [R] ++(0,-2) to[
   dcvsource]++(0,-2) -| (motor.bottom);
4 \draw[thick,->>](motor.right)---++(1,0)node[midway,
   above]{$\omega$};
5 \end{circuitikz}

```

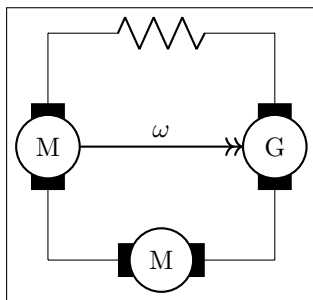


```

1 \begin{circuitikz}
2 \draw (2,0) node[elmech](motor){};
3 \draw (motor.north) |-(0,2) to [R] ++(0,-2) to [
    dcvsource]++(0,-2) -| (motor.bottom);
4 \draw[thick,->>] (motor.center)--++(1.5,0)node[midway,
    above]{\omega};
5 \end{circuitikz}

```

The symbols can also be used along a path, using the transistor-path-syntax (T in front of the shape name, see section 3.6.1.2). Don't forget to use parameter n to name the node and get access to the anchors:



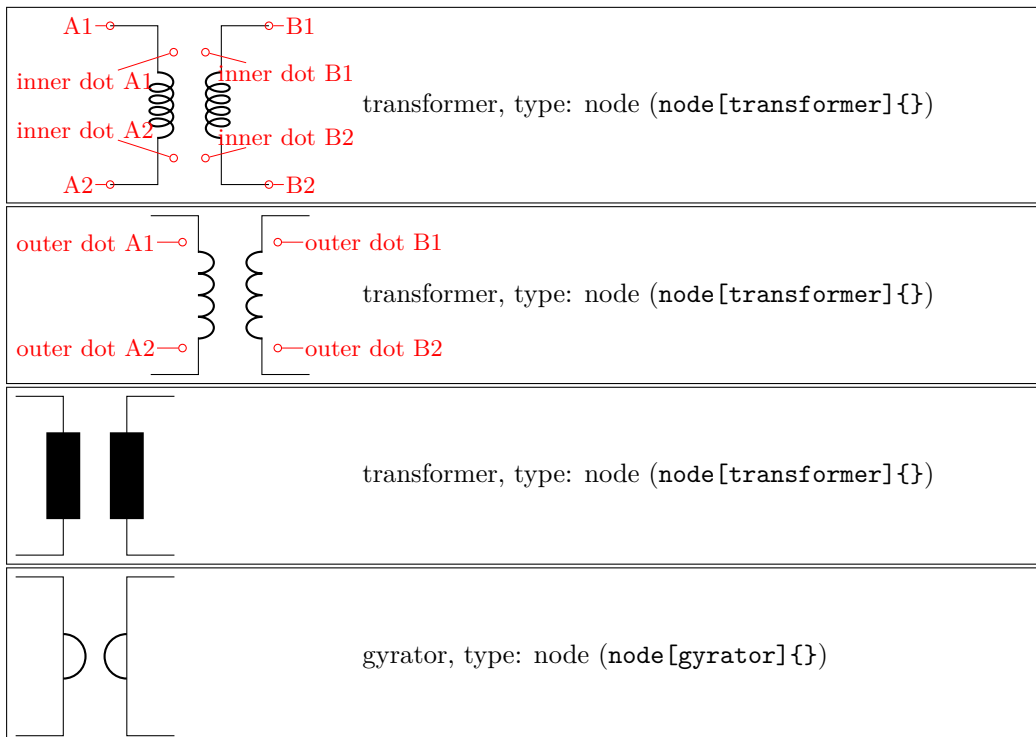
```

1 \begin{circuitikz}
2 \draw (0,0) to [Telmech=M,n=motor] ++(0,-3) to [
    Telmech=M] ++(3,0) to [Telmech=G,n=generator]
    ++(0,3) to [R] (0,0);
3 \draw[thick,->>] (motor.left)--(generator.left)node[
    midway,above]{\omega};
4 \end{circuitikz}

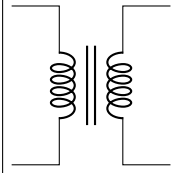
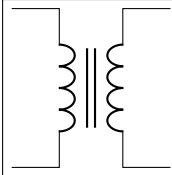
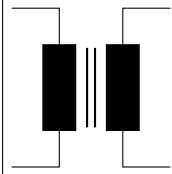
```

3.7 Double bipoles

Transformers automatically use the inductor shape currently selected. These are the three possibilities:



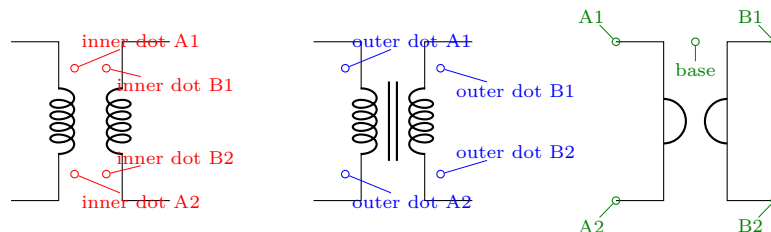
Transformers with core are also available:

	transformer core, type: node (node[transformer core]{})
	transformer core, type: node (node[transformer core]{})
	transformer core, type: node (node[transformer core]{})

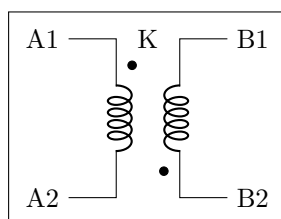
3.7.1 Double dipoles anchors

All the double bipoles/quadrupoles have the four anchors, two for each port. The first port, to the left, is port **A**, having the anchors **A1** (up) and **A2** (down); same for port **B**.

They also expose the **base** anchor, for labelling, and anchors for setting dots or signs to specify polarity. The set of anchors, to which the standard “geographical” **north**, **north east**, etc. is here:



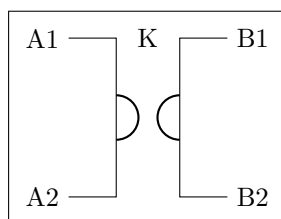
Also the standard “geographical” **north**, **north east**, etc. are defined. A couple of examples follow:



```

1 \begin{circuitikz} \draw
2   (0,0) node[transformer] (T) {}
3   (T.A1) node[anchor=east] {A1}
4   (T.A2) node[anchor=east] {A2}
5   (T.B1) node[anchor=west] {B1}
6   (T.B2) node[anchor=west] {B2}
7   (T.base) node{K}
8   (T.inner dot A1) node[circ]{}
9   (T.inner dot B2) node[circ]{}
10 ;\end{circuitikz}

```



```

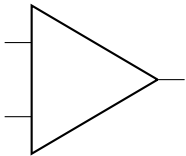
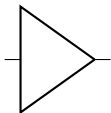
1 \begin{circuitikz} \draw
2   (0,0) node[gyrator] (G) {}
3   (G.A1) node[anchor=east] {A1}
4   (G.A2) node[anchor=east] {A2}
5   (G.B1) node[anchor=west] {B1}
6   (G.B2) node[anchor=west] {B2}
7   (G.base) node{K}
8 ;\end{circuitikz}

```

3.8 Amplifiers

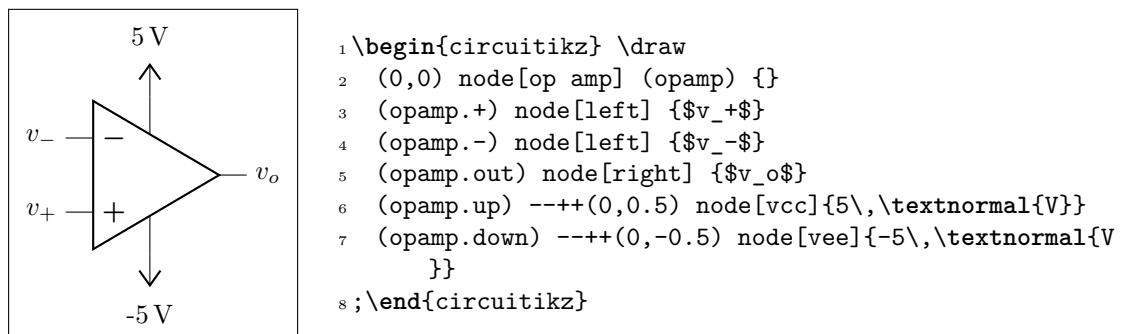
	op amp, type: node (node[op amp]{})
	en amp, type: node (node[en amp]{})
	fd op amp, type: node (node[fd op amp]{})
	gm amp, type: node (node[gm amp]{})
	inst amp, type: node (node[inst amp]{})
	fd inst amp, type: node (node[fd inst amp]{})
	inst amp ra, type: node (node[inst amp ra]{})

³Contributed by Kristofer M. Monisit.

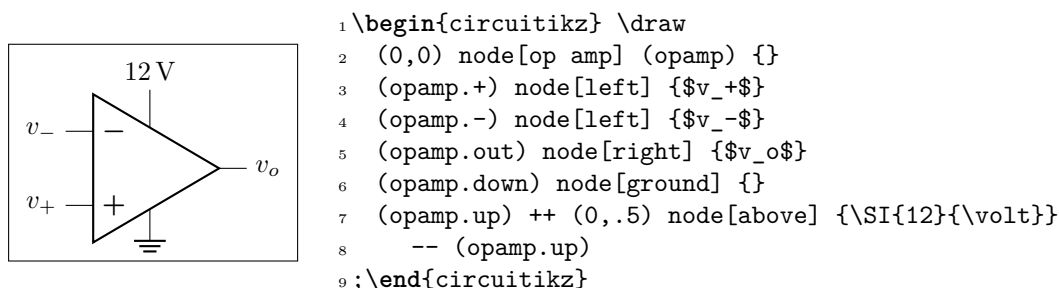
	plain amp, type: node (node[plain amp]{})
	buffer, type: node (node[buffer]{})

3.8.1 Amplifiers anchors

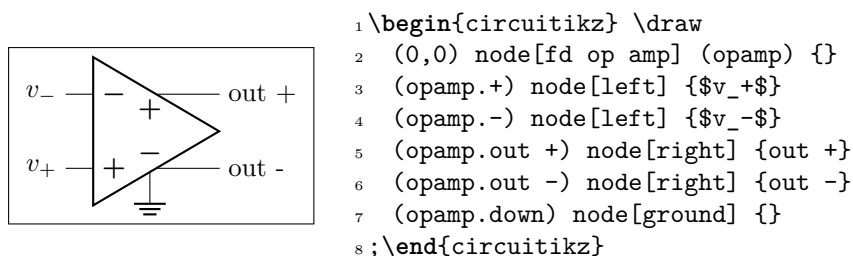
The op amp defines the inverting input (-), the non-inverting input (+) and the output (out) anchors:



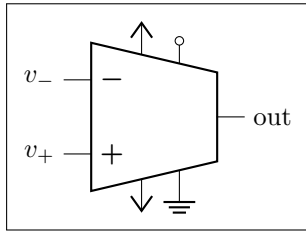
There are also two more anchors defined, up and down, for the power supplies:



The fully differential op amp defines two outputs:



The instrumentation amplifier inst amp defines also references (normally you use the "down", unless you are flipping the component):

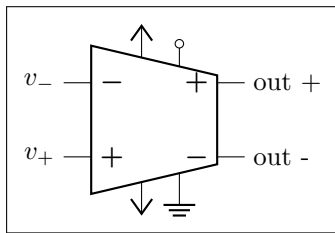


```

1 \begin{circuitikz} \draw
2   (0,0) node[inst amp] (opamp) {}
3   (opamp.+) node[left] {$v_+$}
4   (opamp.-) node[left] {$v_-$}
5   (opamp.out) node[right] {out}
6   (opamp.up) node[vcc]{}
7   (opamp.down) node[vee] {}
8   (opamp.refv down) node[ground]{}
9   (opamp.refv up) to[short, -o] ++(0,0.3)
10 ;\end{circuitikz}

```

The fully differential instrumentation amplifier inst amp defines two outputs:

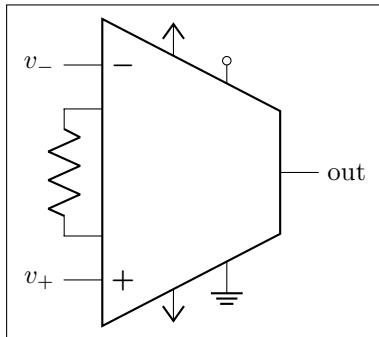


```

1 \begin{circuitikz} \draw
2   (0,0) node[fd inst amp] (opamp) {}
3   (opamp.+) node[left] {$v_+$}
4   (opamp.-) node[left] {$v_-$}
5   (opamp.out +) node[right] {out +}
6   (opamp.out -) node[right] {out -}
7   (opamp.up) node[vcc]{}
8   (opamp.down) node[vee] {}
9   (opamp.refv down) node[ground]{}
10  (opamp.refv up) to[short, -o] ++(0,0.3)
11 ;\end{circuitikz}

```

The instrumentation amplifier with resistance terminals (inst amp ra) defines also terminals to add an amplification resistor:



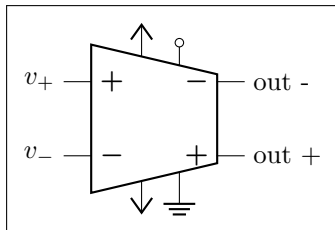
```

1 \begin{circuitikz} \draw
2   (0,0) node[inst amp ra] (opamp) {}
3   (opamp.+) node[left] {$v_+$}
4   (opamp.-) node[left] {$v_-$}
5   (opamp.out) node[right] {out}
6   (opamp.up) node[vcc]{}
7   (opamp.down) node[vee] {}
8   (opamp.refv down) node[ground]{}
9   (opamp.refv up) to[short, -o] ++(0,0.3)
10  (opamp.ra-) to[R] (opamp.ra+)
11 ;\end{circuitikz}

```

3.8.2 Amplifiers customization

All these amplifier have the possibility to flip input and output (if needed) polarity. You can change polarity of the input with the `noinv input down` (default) or `noinv input up` key; and the output with `noinv output up` (default) or `noinv output down` key:



```

1 \begin{circuitikz} \draw
2   (0,0) node[fd inst amp,
3     noinv input up,
4     noinv output down] (opamp) {}
5   (opamp.+) node[left] {$v_+$}
6   (opamp.-) node[left] {$v_-$}
7   (opamp.out +) node[right] {out +}
8   (opamp.out -) node[right] {out -}
9   (opamp.up) node[vcc]{}
10  (opamp.down) node[vee] {}
11  (opamp.refv down) node[ground]{}
12  (opamp.refv up) to[short, -o] ++(0,0.3)
13 \end{circuitikz}

```

When you use the `noinv input/output ...` keys the anchors (+, -, out +, out -) will change with the effective position of the terminals. You have also the anchors `in up`, `in down`, `out up`, `out down` that will not change with the positive or negative sign.

3.9 Support shapes and bipoles

Path style:

	<code>crossing</code> , type: <code>path-style</code> , nodename: <code>crossingshape</code> . Aliases: <code>xing</code> .
--	---

Node style:

►	<code>curarrow</code> , type: node (<code>node[curarrow]{}</code>)
►	<code>inputarrow</code> , type: node (<code>node[inputarrow]{}</code>)
•	<code>circ</code> , type: node (<code>node[circ]{}</code>)
◦	<code>ocirc</code> , type: node (<code>node[ocirc]{}</code>)
◆	<code>diamondpole</code> , type: node (<code>node[diamondpole]{}</code>)
⤿	<code>jump crossing</code> , type: node (<code>node[jump crossing]{}</code>)
+	<code>plain crossing</code> , type: node (<code>node[plain crossing]{}</code>)

3.9.1 Terminal shapes

Since version 0.8.4, `circ`, `ocirc` , and `diamondpole` have all the standard geographical anchors, so you can do things like these:

```

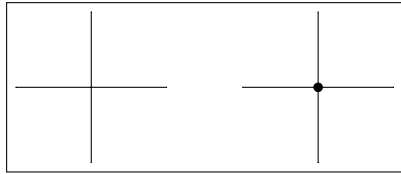
1 \begin{circuitikz}[american,]
2   \draw (0,-1) node[draw] (R){R};
3   \draw (R.east) node[ocirc, right]{};
4 \end{circuitikz}

```



3.9.2 Crossings

All circuit-drawing standards agree that to show a crossing without electric contact, a simple crossing of the wires suffices; the electrical contact must be explicitly marked with a filled dot.

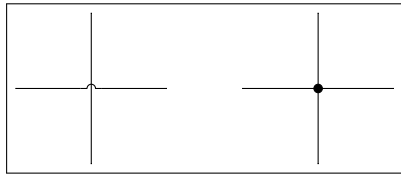


```

1 \begin{circuitikz}[]
2 \draw(1,-1) to[short] (1,1)
3   (0,0) to[short] (2,0);
4 \draw(4,-1) to[short] (4,1)
5   (3,0) to[short] (5,0)
6   (4,0) node[circ]{};
7 \end{circuitikz}

```

However, sometime it is advisable to mark the non-contact situation more explicitly. To this end, you can use a path-style component called `crossing`:



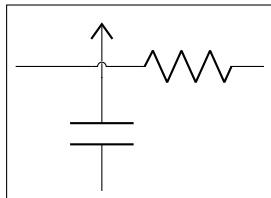
```

1 \begin{circuitikz}[]
2 \draw(1,-1) to[short] (1,1) (0,0) to[crossing]
3   (2,0);
4 \draw(4,-1) to[short] (4,1) (3,0) to[short]
5   (5,0)
6   (4,0) node[circ]{};
7 \end{circuitikz}

```

That should suffice most of the time; the only problem is that the crossing jumper will be put in the center of the subpath where the `to[crossing]` is issued, so sometime a bit of trials and errors are needed to position it.

For a more powerful (and elegant) way you can use the crossing nodes:



```

1 \begin{circuitikz}[]
2   \node at (1,1)[jump crossing] (X){};
3   \draw (X.west) -- ++(-1,0);
4   \draw (X.east) to[R] ++(2,0);
5   \draw (X.north) node[vcc]{};
6   \draw (X.south) to[C] ++(0,-1.5);
7 \end{circuitikz}

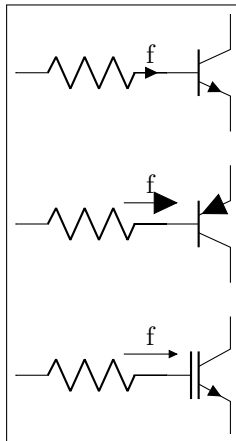
```

Notice that the `plain crossing` and the `jump crossing` have a small gap in the straight wire, to enhance the effect of crossing (as a kind of shadow).

The size of the crossing elements can be changed with the key `bipoles/crossing/size` (default 0.2).

3.9.3 Arrows size

You can use the parameter `current arrow scale` to change the size of the arrows in various components and indicators; the normal value is 16, higher numbers give smaller arrows and so on. You need to use `circuitikz/current arrow scale` if you use it into a node.



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i=f] ++(2,0) node[npn, anchor=B]{};
3   \draw (0,-2) to[R, f=f, current arrow scale=8] ++(2,0)
4     node[pnp, anchor=B, circuitikz/current arrow scale
5       =8]{};
6   \draw (0,-4) to[R, f=f, current arrow scale=24] ++(2,0)
7     node[night, anchor=B]{};
8 \end{circuitikz}

```

3.10 Switches and buttons

Switches and button come in to-style (the simple ones and the pushbuttons), and as nodes.

3.10.1 Traditional switches

These are all of the to-style type:

	switch , type: path-style, nodename: cspstshape. Aliases: spst.
	closing switch , type: path-style, nodename: cspstshape. Aliases: cspst.
	opening switch , type: path-style, nodename: ospstshape. Aliases: ospst.
	normal open switch , type: path-style, nodename: nosshape. Aliases: nos.
	normal closed switch , type: path-style, nodename: ncshape. Aliases: ncs.
	push button , type: path-style, nodename: pushbuttonshape. Aliases: normally open push button, nopb.
	normally closed push button , type: path-style, nodename: ncpushbuttonshape. Aliases: ncpb.
	toggle switch , type: path-style, nodename: toggleswitchshape.

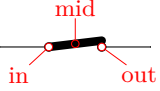



While this is a node-style component

	spdt , type: node (node[spdt]{})
--	---

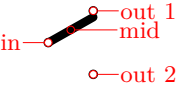

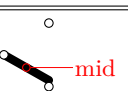



3.10.2 Cute switches

These switches have been introduced after version 0.8.4, and they come in also in to-style and in node-style, but they are size-matched so that they can be used together in a seamless way.

The path element (to-style) are:

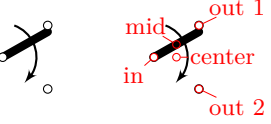
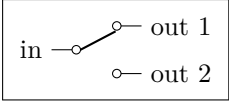
	<code>cute closed switch</code> , type: path-style, nodename: <code>cuteclosedswitchshape</code> . Aliases: <code>ccsw</code> .
	<code>cute open switch</code> , type: path-style, nodename: <code>cuteopenswitchshape</code> . Aliases: <code>cosw</code> .
	<code>cute closing switch</code> , type: path-style, nodename: <code>cuteclosingswitchshape</code> . Aliases: <code>cogsw</code> .
	<code>cute opening switch</code> , type: path-style, nodename: <code>cuteopeningswitchshape</code> . Aliases: <code>cogsw</code> .

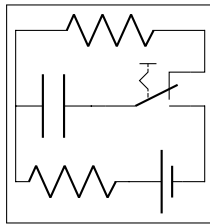
while the node-style components are the single-pole, double-throw (spdt) ones:

	<code>cute spdt up</code> , type: node (node[<code>cute spdt up</code>]{})
	<code>cute spdt mid</code> , type: node (node[<code>cute spdt mid</code>]{})
	<code>cute spdt down</code> , type: node (node[<code>cute spdt down</code>]{})
	<code>cute spdt up arrow</code> , type: node (node[<code>cute spdt up arrow</code>]{})
	<code>cute spdt mid arrow</code> , type: node (node[<code>cute spdt mid arrow</code>]{})
	<code>cute spdt down arrow</code> , type: node (node[<code>cute spdt down arrow</code>]{})

3.10.3 Switches anchors

The nodes-style switches have the following anchors:

	
	<pre> 1 \begin{circuitikz} \draw 2 (0,0) node[spdt] (Sw) {} 3 (Sw.in) node[left] {in} 4 (Sw.out 1) node[right] {out 1} 5 (Sw.out 2) node[right] {out 2} 6 ;\end{circuitikz} </pre>

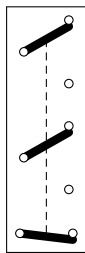


```

1 \begin{circuitikz} \draw
2 (0,0) to[C] (1,0) to[toggle switch , n=Sw] (2.5,0)
3 -- (2.5,-1) to[battery1] (1.5,-1) to[R] (0,-1) -| (0,0)
4 (Sw.out 2) -| (2.5, 1) to[R] (0,1) -- (0,0)
5 ;\end{circuitikz}

```

The `mid` anchor in the cute switches (both path- and node-style) can be used to combine switches to get more complex configurations:



```

1 \begin{circuitikz}
2 \draw (0,1.4) node[cute spdt up] (S1){};
3 \draw (0,0) node[cute spdt up] (S2){};
4 \draw (0,-1) node[cuteclosedswitchshape, yscale=-1] (S3){};
5 \draw [densely dashed] (S1.mid)--(S2.mid)--(S3.mid);
6 \end{circuitikz}

```

3.11 Logic gates

Logic gates, with two or more input, are supported. Albeit in principle these components are multipoles, they are considered tripoles here, for historical reasons (when they just had two inputs).

3.11.1 American Logic gates

	american and port, type: node (node[american and port]{})
	american or port, type: node (node[american or port]{})
	american not port, type: node (node[american not port]{})
	american nand port, type: node (node[american nand port]{})
	american nor port, type: node (node[american nor port]{})
	american xor port, type: node (node[american xor port]{})
	american xnor port, type: node (node[american xnor port]{})

3.11.2 European Logic gates

	europaean and port, type: node (node[europaean and port]{})
	europaean or port, type: node (node[europaean or port]{})
	europaean not port, type: node (node[europaean not port]{})
	europaean nand port, type: node (node[europaean nand port]{})
	europaean nor port, type: node (node[europaean nor port]{})
	europaean xor port, type: node (node[europaean xor port]{})
	europaean xnor port, type: node (node[europaean xnor port]{})

If (default behaviour) `americanports` option is active (or the style `[american ports]` is used), the shorthands `and port`, `or port`, `not port`, `nand port`, `not port`, `xor port`, and `xnor port` are equivalent to the american version of the respective logic port.

If otherwise `europaeanports` option is active (or the style `[europaean ports]` is used), the shorthands `and port`, `or port`, `not port`, `nand port`, `not port`, `xor port`, and `xnor port` are equivalent to the europaean version of the respective logic port.

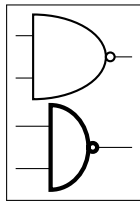
3.11.3 Special components

There is no “europaean” version of these symbols.

	schmitt, type: node (node[schmitt]{})
	invschmitt, type: node (node[invschmitt]{})

3.11.4 Logic port customization

As for most components, you can change the width and height of the ports; the thickness is given by the parameter `tripoles/thickness` (default 2):

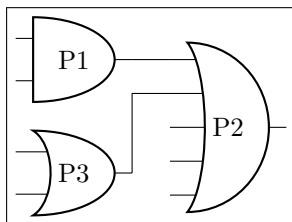


```

1\tikz \draw (0,0) node[nand port] {}; \par
2\ctikzset{tripoles/american nand port/input height=.2}
3\ctikzset{tripoles/american nand port/port width=.4}
4\ctikzset{tripoles/thickness=4}
5\tikz \draw (0,0) node[nand port] {};

```

This is especially useful if you have ports with more than two inputs, which are instantiated with the parameter `number inputs` :

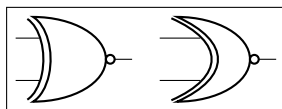


```

1\begin{circuitikz}
2\draw (0,3) node[american and port] (A) {P1};
3\begin{scope}
4\ctikzset{tripoles/american or port/height=1.6}
5\draw (A.out) -- ++(0.5,0)
6node[american or port,
7number inputs=5,
8anchor=in 1] (B) {P2};
9\end{scope}
10\draw (0,1.5) node[american or port] (C) {P3};
11\draw (C.out) |- (B.in 2);
12\end{circuitikz}

```

You can tweak the appearance of american “or” family (or, nor, xor and xnor) ports, too, with the parameters `inner` (how much the base circle go “into” the shape, default 0.3) and `angle` (the angle at which the base starts, default 70).



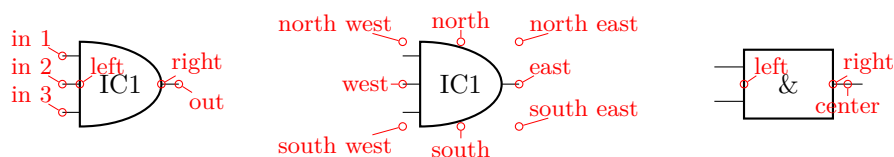
```

1\tikz \draw (0,0) node[xnor port] {};
2\ctikzset{tripoles/american xnor port/inner=.7}
3\ctikzset{tripoles/american xnor port/angle=40}
4\tikz \draw (0,0) node[xnor port] {};

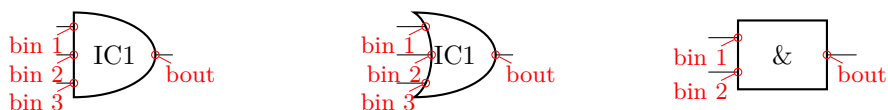
```

3.11.5 Logic port anchors

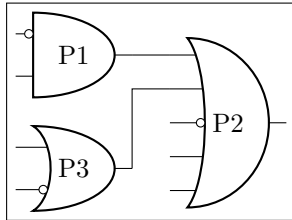
These are the anchors for logic ports:



You have also “border pin anchors”:



These anchors are especially useful if you want to negate inputs:

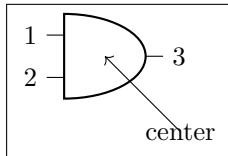


```

1 \begin{circuitikz}
2 \draw (0,3) node[american and port] (A) {P1};
3 \node at (A.bin 1) [ocirc, left]{} ;
4 \begin{scope}
5   \ctikzset{tripoles/american or port/height=1.6}
6   \draw (A.out) -- ++(0.5,0) node[american or port,
7     number inputs=5, anchor=in 1] (B) {P2};
8   \node at (B.bin 3) [ocirc, left]{} ;
9 \end{scope}
10 \draw (0,1.5) node[american or port] (C) {P3};
11 \node at (C.bin 2) [ocirc, left]{} ;
12 \draw (C.out) |- (B.in 2);
13 \end{circuitikz}

```

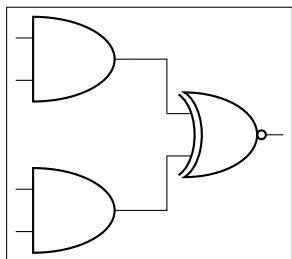
As you can see, the `center` anchor is (for historic reasons) not in the center at all. You can fix this with the command `\ctikzset{logic ports origin=center}`:



```

1 \begin{circuitikz}
2 \ctikzset{logic ports origin=center}
3 \draw (0,0) node[and port] (myand) {}
4 (myand.in 1) node[anchor=east] {1}
5 (myand.in 2) node[anchor=east] {2}
6 (myand.out) node[anchor=west] {3};
7 \draw[<-] (myand.center) -- ++(1,-1)
8   node{center};
9 \end{circuitikz}

```

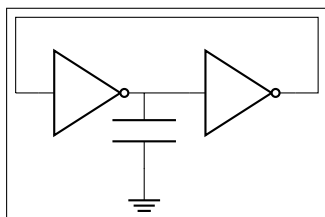


```

1 \begin{circuitikz} \draw
2 (0,2) node[and port] (myand1) {}
3 (0,0) node[and port] (myand2) {}
4 (2,1) node[xnor port] (myxnor) {}
5 (myand1.out) -| (myxnor.in 1)
6 (myand2.out) -| (myxnor.in 2)
7 ;\end{circuitikz}

```

In the case of NOT, there are only `in` and `out` (although for compatibility reasons `in 1` is still defined and equal to `in`):



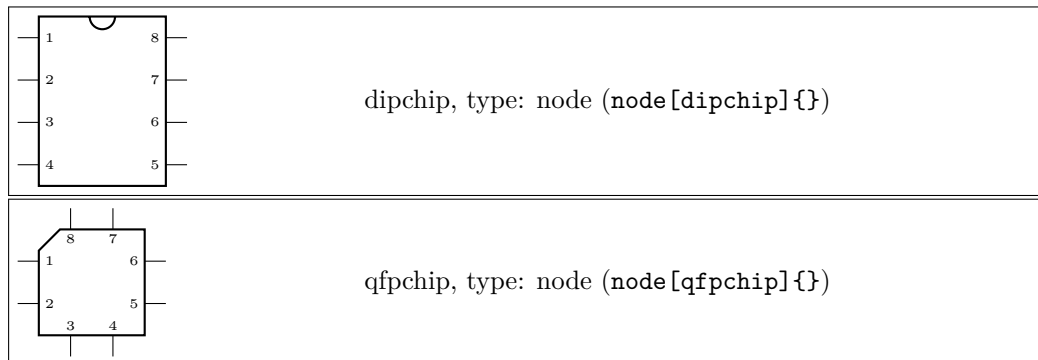
```

1 \begin{circuitikz} \draw
2 (1,0) node[not port] (not1) {}
3 (3,0) node[not port] (not2) {}
4 (0,0) -- (not1.in)
5 (not2.in) -- (not1.out)
6 ++(0,-1) node[ground] {} to[C] (not1.out)
7 (not2.out) -| (4,1) -| (0,0)
8 ;\end{circuitikz}

```

3.12 Chips

CircuitikZ supports two types of variable-pin chips: DIP (Dual-in-Line Package) and QFP (Quad-Flat Package).



3.12.1 DIP and QFP chips customization

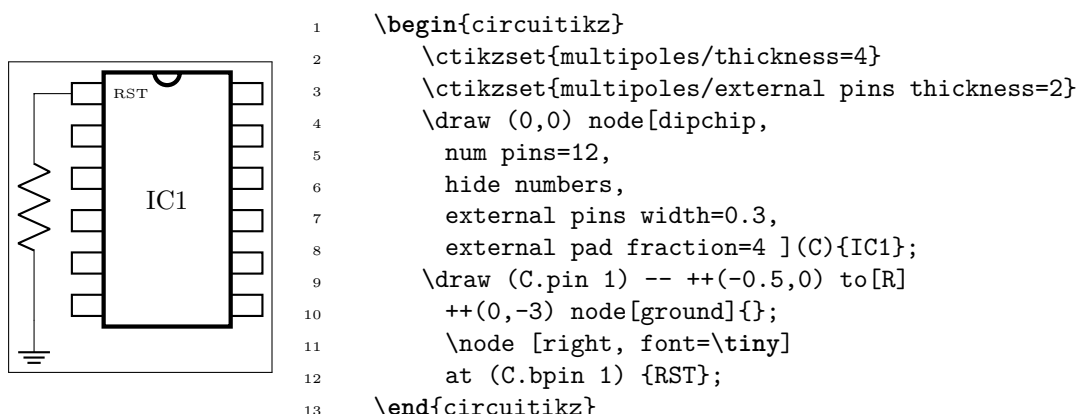
You can customize the DIP chip with the keys `multipoles/dipchip/width` (default 1.2) and `multipoles/dipchip/pin spacing` (default 0.4) that are expressed in fraction of basic lengths (see section 3.1.2). The height of the chip will be equal to half the numbers of pins multiplied by the spacing, plus one spacing for the borders. For the QFP chips, you can only chose the pin spacing with `multipoles/qfpchip/pin spacing` key.

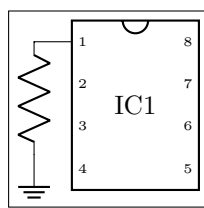
The pins of the chip can be “hidden” (that is, just a spot in the border, optionally marked with a number) or “stick out” with a thin lead by setting `multipoles/external pins width` greater than 0 (default value is 0.2, so you’ll have leads as shown above). Moreover, you can transform the thin lead into a pad by setting the key `multipoles/external pad fraction` to something different from 0 (default is 0); the value expresses the fraction of the pin spacing space that the pin will use on both sides of the pin.

The number of pins is setttable with the key `num pins`. **Please notice** that the number of pins **must** be *even* for `dipchips` and *multiple of 4* for `qfpchips`, otherwise havoc will ensue.

You can, if you want, avoid printing the numbers of the pin with `hide numbers` (default `show numbers`) if you prefer positioning them yourself (see the next section for the anchors you can use). The font used for the pins is adjustable with the key `multipoles/font` (default `\tiny`) For special use you can suppress the orientation mark with the key `no topmark` (default `topmark`).

The line thickness of the main shape is controlled by `multipoles/thickness` (default 2) and the one of the external pins/pads with `multipoles/external pins thickness` (default 1).

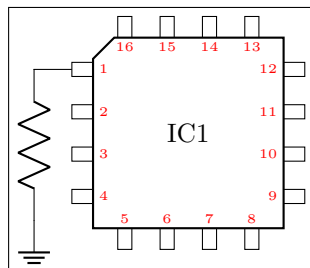




```

1 \begin{circuitikz}
2   \draw (0,0) node[dipchip,
3     num pins=8,
4     external pins width=0.0](C){IC1};
5   \draw (C.pin 1) -- ++(-0.5,0) to[R]
6     ++(0,-1.5) node[ground]{};
7 \end{circuitikz}

```



```

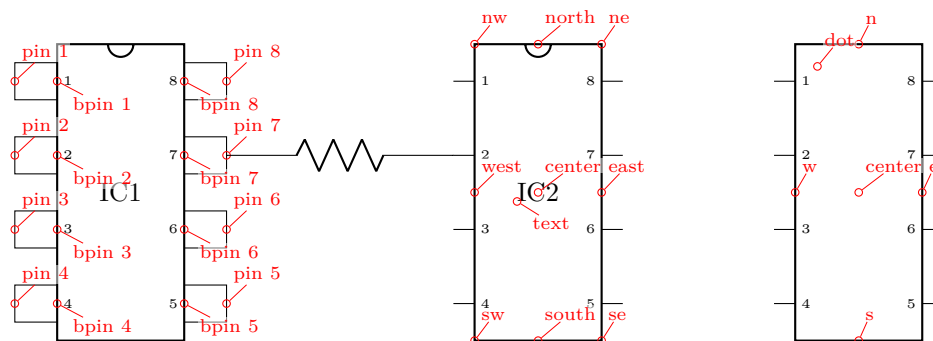
1 \begin{circuitikz}
2   \ctikzset{multipoles/font={\color{red}\tiny}}
3   \draw (0,0) node[qfpchip,
4     num pins=16,
5     external pad fraction=6](C){IC1};
6   \draw (C.pin 1) -- ++(-0.5,0) to[R]
7     ++(0,-2) node[ground]{};
8 \end{circuitikz}

```

3.12.2 Chips anchors

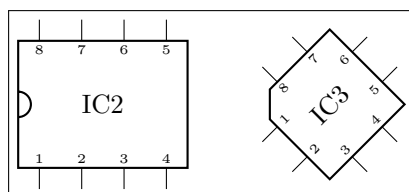
Chips have anchors on pins and global anchors for the main shape. The pin anchors to be used to connect wires to the chip are called `pin 1`, `pin 2`, ..., with just one space between `pin` and the number. Border pin anchors (`bpin 1...`) are always on the box border, and can be used to add numbers or whatever markings are needed. Obviously, in case of `multipoles/external pins width` equal to zero, border and normal pin anchors will coincide.

Additionally, you have geometrical anchors on the chip “box”, see the following figure. The nodes are available with the full name (like `north`) and with the short abbreviations `n`, `nw`, `w...`. The `dot` anchor is useful to add a personalized marker if you use the `no topmark` key.



3.12.3 Chips rotation

You can rotate chips, and normally the pin numbers are kept straight (option `straight numbers`, which is the default), but you can rotate them if you like with `rotated numbers`. Notice that the main label has to be (counter-)rotated manually in this case.



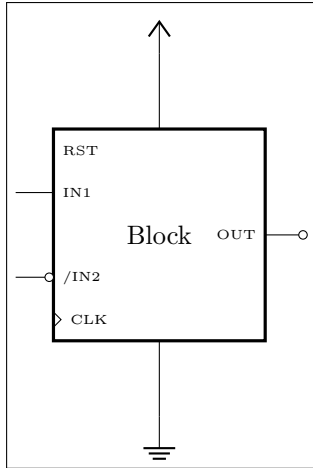
```

1 \begin{circuitikz}
2   \draw (0,0) node[dipchip,
3     rotate=90]{%
4     \rotatebox{-90}{IC2}};
5   \draw (3,0) node[qfpchip,
6     rotated numbers,
7     rotate=45]{IC3};
8 \end{circuitikz}

```

3.12.4 Chip special usage

You can use the chips to have special, personalized blocks. Look at the following example, which is easily put into a macro.

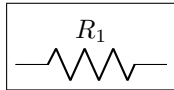


```

1 \begin{circuitikz}
2   \ctikzset{multipoles/thickness=3}
3   \ctikzset{multipoles/dipchip/width=2}
4   \draw (0,0) node[dipchip,
5     num pins=10, hide numbers, no topmark,
6     external pins width=0](C){Block};
7   \node [right, font=\tiny] at (C.bpin 1) {RST};
8   \node [right, font=\tiny] at (C.bpin 2) {IN1};
9   \node [right, font=\tiny] at (C.bpin 4) {/IN2};
10  \node [left, font=\tiny] at (C.bpin 8) {OUT};
11  \draw (C.bpin 2) -- ++(-0.5,0) coordinate(extpin);
12  \node [ocirc, anchor=0](notin2) at (C.bpin 4) {};
13  \draw (notin2.180) -- (C.bpin 4 -| extpin);
14  \draw (C.bpin 8) to[short,-o] ++(0.5,0);
15  \draw (C.bpin 5) ++(0,0.1) -- ++(0.1,-0.1)
16    node[right, font=\tiny]{CLK} -- ++(-0.1,-0.1);
17  \draw (C.n) -- ++(0,1) node[vcc]{};
18  \draw (C.s) -- ++(0,-1) node[ground]{};
19 \end{circuitikz}

```

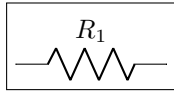
4 Labels and similar annotations



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, l=$R_1$] (2,0);
3 \end{circuitikz}

```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R=$R_1$] (2,0);
3 \end{circuitikz}

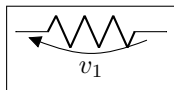
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i=$i_1$] (2,0);
3 \end{circuitikz}

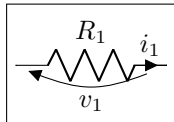
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, v=$v_1$] (2,0);
3 \end{circuitikz}

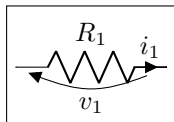
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R=$R_1$, i=$i_1$, v=$v_1$] (2,0);
3 \end{circuitikz}

```

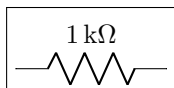


```

1 \begin{circuitikz}
2   \draw (0,0) to[R=$R_1$, i=$i_1$, v=$v_1$] (2,0);
3 \end{circuitikz}

```

Long names/styles for the bipoles can be used:



```

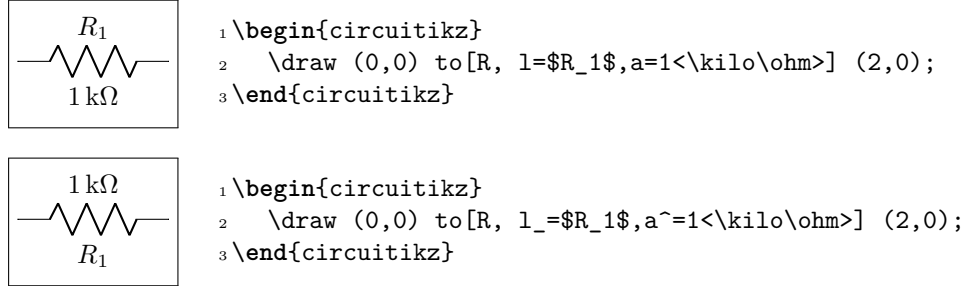
1 \begin{circuitikz}\draw
2   (0,0) to[resistor=1<kilo\ohm>] (2,0)
3;\end{circuitikz}

```

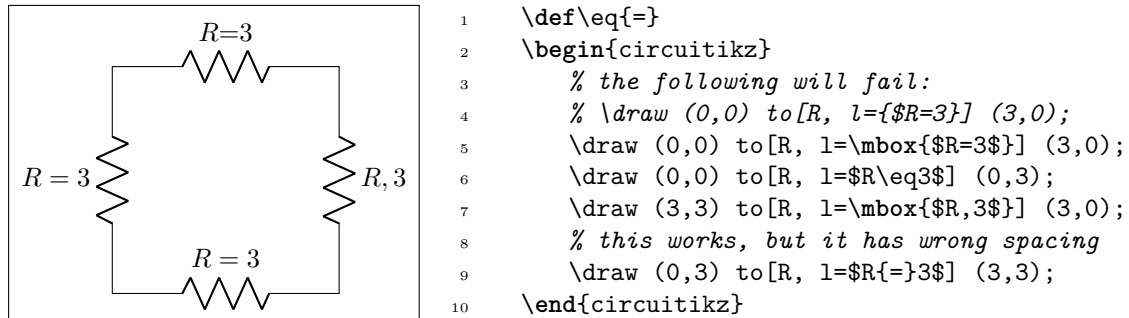
4.1 Labels and Annotations

Since Version 0.7, beside the original label (l) option, there is a new option to place a second label, called annotation (a) at each bipole. Up to now this is a beta-test and there can be problems. For example, up to now this option is not compatible with the concurrent use of voltage labels.

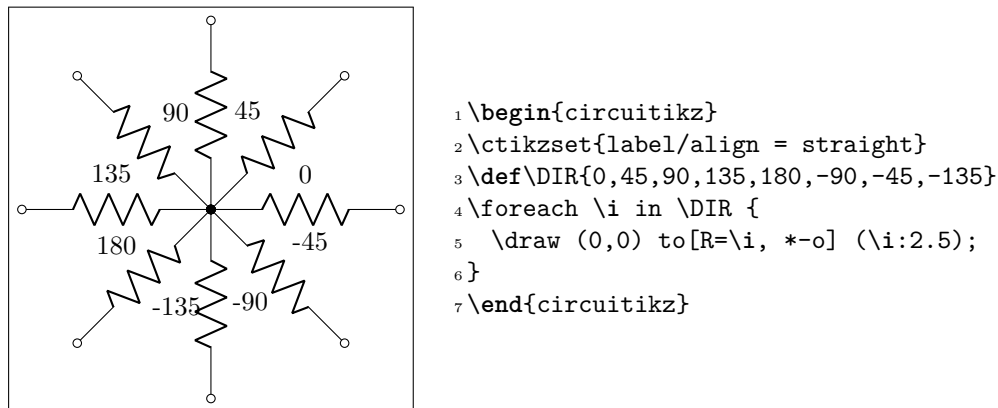
The position of (a) and (l) labels can be adjusted with `_` and `^`, respectively.

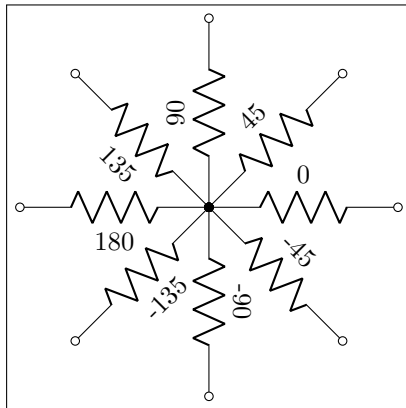


Caveat: notice that the way in which `circuitikz` processes the options, there will be problems if the label (or annotation, or voltage, or current) contains one of the characters `=` (equal) or `,` (comma), giving unexpected errors and wrong output. These two characters must be protected to the option parser using an `\mbox` command, or redefining the characters with a `TeX \def`:



The default orientation of labels is controlled by the options `smartlabels`, `rotatelabels` and `straightlabels` (or the corresponding `label/align` keys). Here are examples to see the differences:

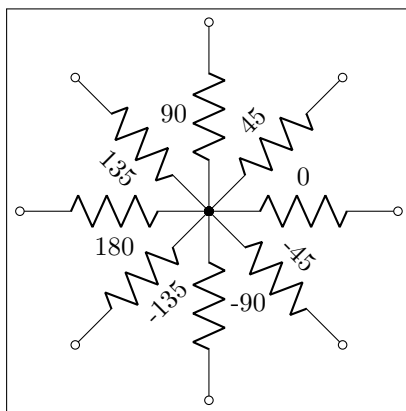




```

1 \begin{circuitikz}
2 \ctikzset{label/align = rotate}
3 \def\DIR{0,45,90,135,180,-90,-45,-135}
4 \foreach \i in \DIR {
5   \draw (0,0) to[R=\i, *-o] (\i:2.5);
6 }
7 \end{circuitikz}

```

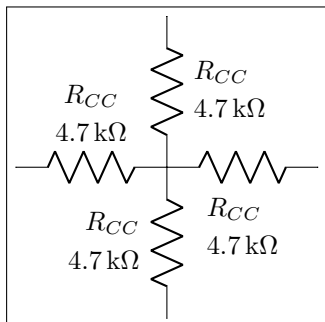


```

1 \begin{circuitikz}
2 \ctikzset{label/align = smart}
3 \def\DIR{0,45,90,135,180,-90,-45,-135}
4 \foreach \i in \DIR {
5   \draw (0,0) to[R=\i, *-o] (\i:2.5);
6 }
7 \end{circuitikz}

```

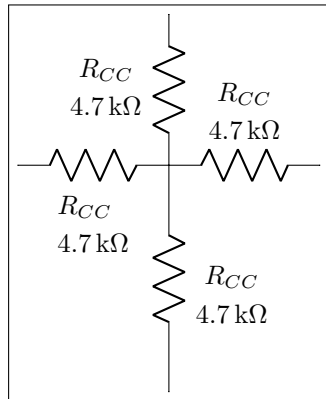
You also can use stacked (two lines) labels. The example should be self-explanatory: the two lines are specified as `l2=line1` and `line2`. You can use the keys `l2 halign` to control horizontal position (left, center, right) and `l2 valign` to control the vertical one (bottom, center, top).



```

1 \begin{circuitikz}[ american, ]
2 %
3 % default is l2 halign=l, l2 valign=c
4 %
5 \draw (0,0) to[R, l2=$R_{CC}$ and \SI{4.7}{k\
6   ohm}, , l2 valign=t] (2,0);
7 \draw (0,0) to[R, l2=$R_{CC}$ and \SI{4.7}{k\
8   ohm}, , ] (0,2);
9 \draw (0,0) to[R, l2=$R_{CC}$ and \SI{4.7}{k\
10  ohm}, l2 halign=c, l2 valign=b] (-2,0);
11 \draw (0,0) to[R, l2=$R_{CC}$ and \SI{4.7}{k\
12  ohm}, l2 halign=r, l2 valign=c] (0, -2);
13 \end{circuitikz}

```



```

1 \begin{circuitikz}[ american, ]
2   \draw (0,0) to[R, l2^=$R_{CC}$ and \SI{4.7}{k\
      ohm}, l2 halign=c, l2 valign=b] (2,0);
3   \draw (0,0) to[R, l2^=$R_{CC}$ and \SI{4.7}{k\
      ohm}, l2 halign=c, ] (0,2);
4   \draw (0,0) to[R, l2^=$R_{CC}$ and \SI{4.7}{k\
      ohm}, , l2 valign=t] (-2,0);
5   \draw (0,0) to[R, l2^=$R_{CC}$ and \SI{4.7}{k\
      ohm}, l2 halign=c, l2 valign=t](0, -3);
6 \end{circuitikz}

```

4.2 Currents and voltages

The default direction/sign for currents and voltages in the components is, unfortunately, not standard, and can change across country and sometime across different authors. This unfortunate situation created a bit of confusion in `circuitikz` across the versions, with several incompatible changes starting from version 0.5. From version 0.8.4 onward, the maintainers agreed a new policy for the directions of bipoles's voltages and currents, depending on 4 different possible options:

- `oldvoltagedirection`, or the key style `voltage dir=old`: Use old way of voltage direction having a difference between european and american direction, with wrong default labelling for batteries (it was the default before version 0.5);
- `nooldvoltagedirection`, or the key style `voltage dir=noold`: The standard from version 0.5 onward, utilize the (German?) standard of voltage arrows in the direction of electric fields (without fixing batteries);
- `RPvoltages` (meaning Rising Potential voltages), or the key style `voltage dir=RP`: the arrow is in direction of rising potential, like in `oldvoltagedirections`, but batteries and current sources are fixed so that they follow the passive/active standard: the default direction of v and i are chosen so that, when both values are positive:
 - in passive component, the element is *dissipating power*;
 - in active components (generators), the element is *generating power*.
- `EFvoltages` (meaning Electric Field voltages), or the key style `voltage dir=EF`: the arrow is in direction of the electric field, like in `nooldvoltagedirections`, but batteries are fixed;

The standard direction of currents, flows and voltages are changed by these options; notice that the default drops in case of passive and active elements is normally different. Take care that in the case of `noold` and `EFvoltages` also the currents can switch directions. It is much easier to understand the several behaviors by looking at the following examples, that have been generated by the code:

```

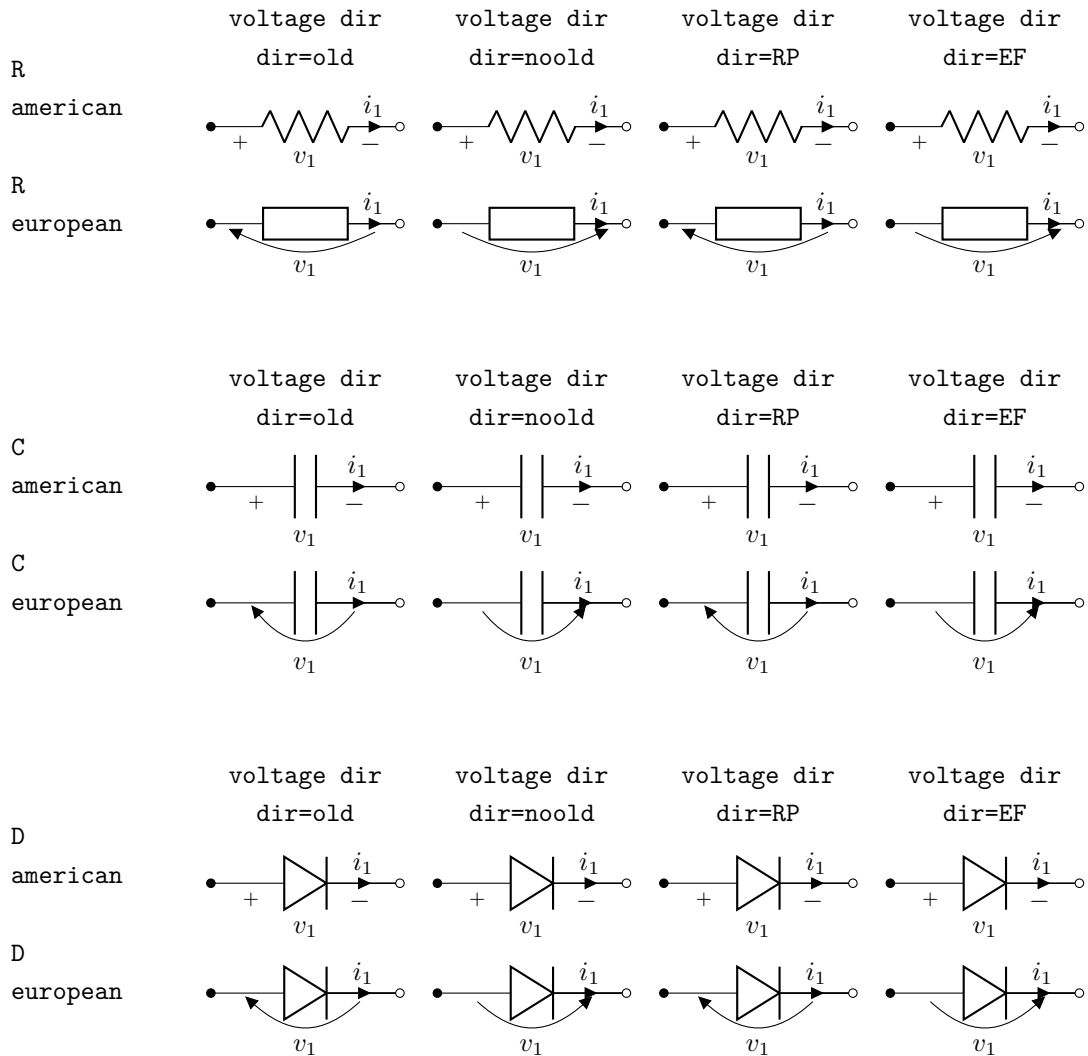
1 \foreach\element in {R, C, D, battery2, V, I, sV, cV, cI}{%
2   \noindent\ttfamily
3   \begin{tabular}{p{2cm}}
4     \element \\\ american \\\[15pt]
5     \element \\\ european \\\
6   \end{tabular}
7   \foreach\mode in {old, noold, RP, EF} {
8     \begin{tabular}{@{}l@{}}
9       \multicolumn{1}{c}{voltage dir} \\\
10      \multicolumn{1}{c}{dir=\mode} \\\[4pt]

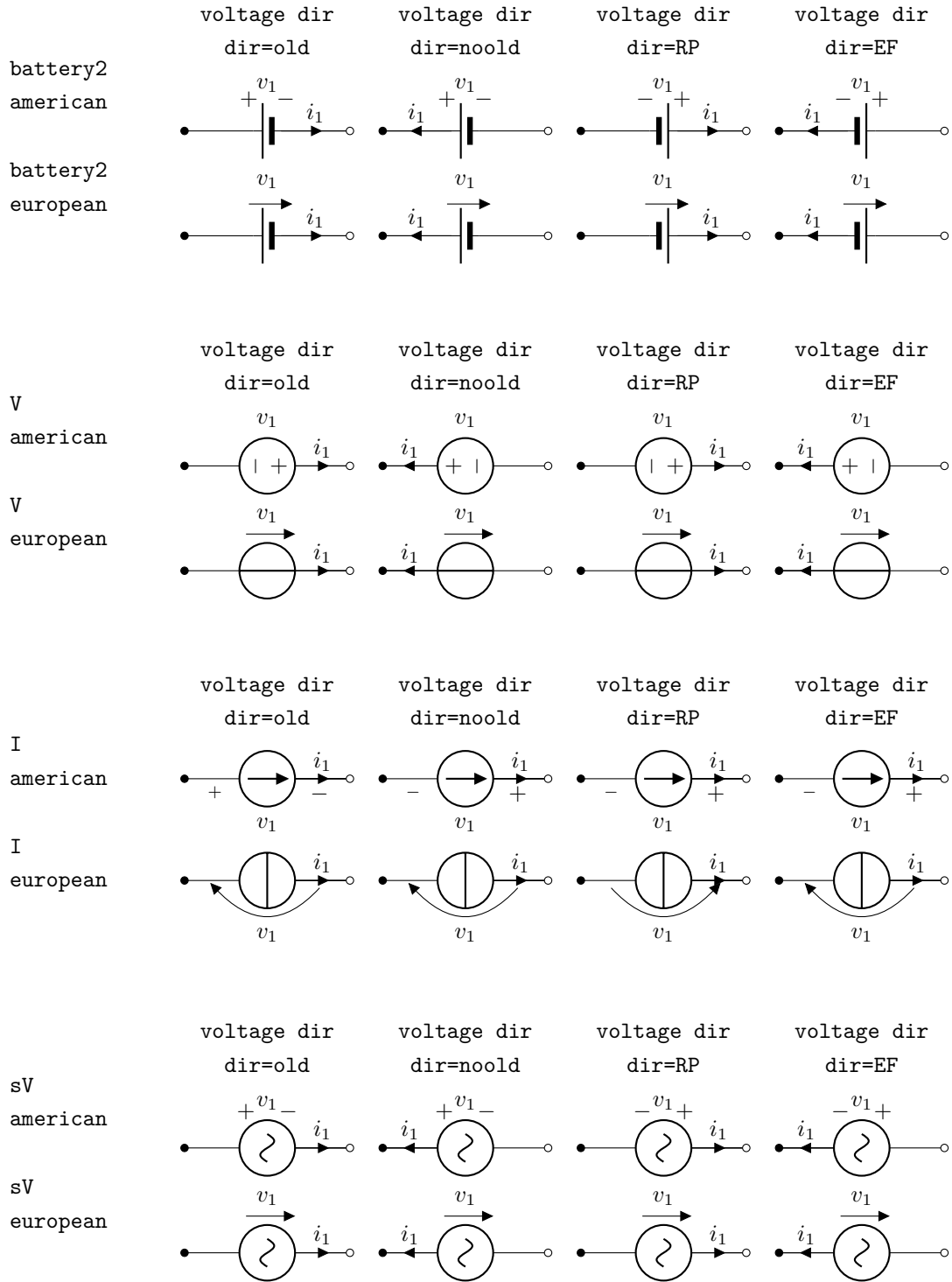
```

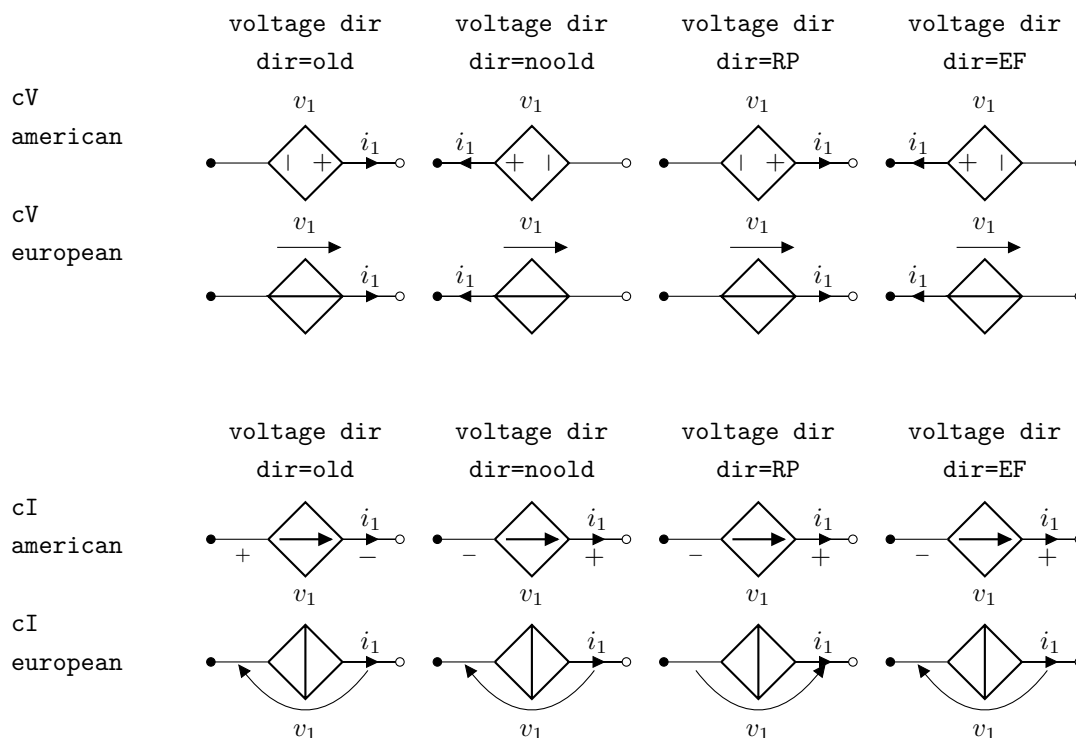
```

11 \begin{tikzpicture}[
12     american, voltage dir=\mode,
13 ]
14 \draw (0,0) to[\element, *-o, v=$v_1$, i=$i_1$, ] (2.5,0);
15 \end{tikzpicture}\\
16 \begin{tikzpicture}[
17     european, voltage dir=\mode,
18 ]
19 \draw (0,0) to[\element, *-o, v=$v_1$, i=$i_1$, ] (2.5,0);
20 \end{tikzpicture}
21 \end{tabular}
22 \medskip
23 }
24 \par
25 }

```







Obviously, you normally use just one between current and flows, but anyway you can change direction of the voltages, currents and flows using the complete keys `i_>`, `i^<`, `i>_`, `i>^`, as shown in the following examples.

This manual has been typeset with the option `RPvoltages`.

4.3 Currents

Inline (along the wire) currents are selected with `i_>`, `i^<`, `i>_`, `i>^`, and various simplification; the default position and direction is obtained with the key `i=...`



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i^>=$i_1$] (2,0);
3 \end{circuitikz}

```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i_>=$i_1$] (2,0);
3 \end{circuitikz}

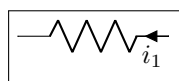
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i^<=$i_1$] (2,0);
3 \end{circuitikz}

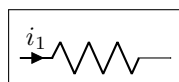
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i_<=$i_1$] (2,0);
3 \end{circuitikz}

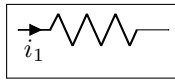
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i^>=$i_1$] (2,0);
3 \end{circuitikz}

```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i>_=$i_1$] (2,0);
3 \end{circuitikz}

```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i<^=$i_1$] (2,0);
3 \end{circuitikz}

```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i<_=$i_1$] (2,0);
3 \end{circuitikz}

```

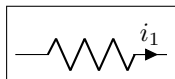
Also



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i<_=$i_1$] (2,0);
3 \end{circuitikz}

```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i>_=$i_1$] (2,0);
3 \end{circuitikz}

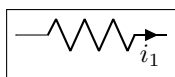
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i^=$i_1$] (2,0);
3 \end{circuitikz}

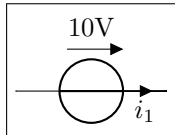
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i_=$i_1$] (2,0);
3 \end{circuitikz}

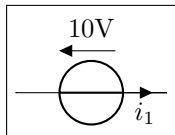
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[V=10V, i_=$i_1$] (2,0);
3 \end{circuitikz}

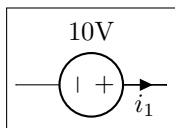
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[V<=10V, i_=$i_1$] (2,0);
3 \end{circuitikz}

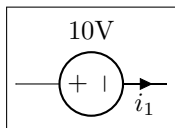
```



```

1 \begin{circuitikz}[american]
2   \draw (0,0) to[V=10V, i_=$i_1$] (2,0);
3 \end{circuitikz}

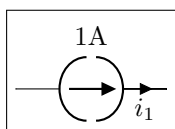
```



```

1 \begin{circuitikz}[american]
2   \draw (0,0) to[V=10V,invert, i_=$i_1$] (2,0);
3 \end{circuitikz}

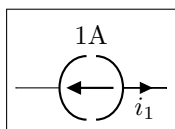
```



```

1 \begin{circuitikz}[american]
2   \draw (0,0) to[dcisource=1A, i_=$i_1$] (2,0);
3 \end{circuitikz}

```



```

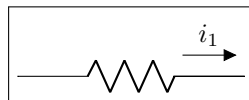
1 \begin{circuitikz}[american]
2   \draw (0,0) to[dcisource=1A,invert, i_=$i_1$] (2,0);
3 \end{circuitikz}

```

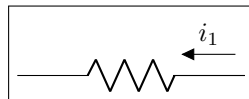
4.4 Flows

As an alternative for the current arrows, you can also use the following flows. They can also be used to indicate thermal or power flows. The syntax is pretty the same as for currents.

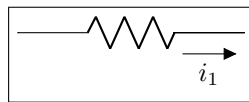
This is a new beta feature since version 0.8.3, therefore, please provide bugreports or hints to optimize this feature regarding placement and appearance! This means, that the appearance may change in the future!



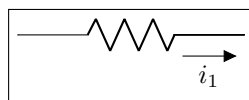
```
1 \begin{circuitikz}
2   \draw (0,0) to[R, f=$i_1$] (3,0);
3 \end{circuitikz}
```



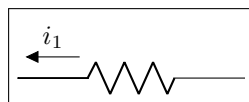
```
1 \begin{circuitikz}
2   \draw (0,0) to[R, f<=$i_1$] (3,0);
3 \end{circuitikz}
```



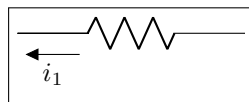
```
1 \begin{circuitikz}
2   \draw (0,0) to[R, f_=$i_1$] (3,0);
3 \end{circuitikz}
```



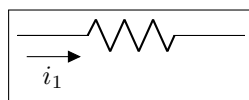
```
1 \begin{circuitikz}
2   \draw (0,0) to[R, f_>=$i_1$] (3,0);
3 \end{circuitikz}
```



```
1 \begin{circuitikz}
2   \draw (0,0) to[R, f^<=$i_1$] (3,0);
3 \end{circuitikz}
```



```
1 \begin{circuitikz}
2   \draw (0,0) to[R, f<_=$i_1$] (3,0);
3 \end{circuitikz}
```



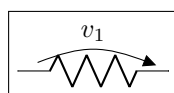
```
1 \begin{circuitikz}
2   \draw (0,0) to[R, f>_=$i_1$] (3,0);
3 \end{circuitikz}
```

4.5 Voltages

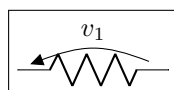
See introduction note at Currents (chapter 4.2, page 56)!

4.5.1 European style

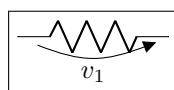
The default, with arrows. Use option `europeanvoltage` or style `[european voltages]`.



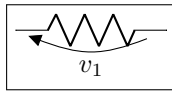
```
1 \begin{circuitikz}[european voltages]
2   \draw (0,0) to[R, v^>=$v_1$] (2,0);
3 \end{circuitikz}
```



```
1 \begin{circuitikz}[european voltages]
2   \draw (0,0) to[R, v^<=$v_1$] (2,0);
3 \end{circuitikz}
```



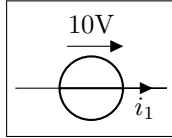
```
1 \begin{circuitikz}[european voltages]
2   \draw (0,0) to[R, v_>=$v_1$] (2,0);
3 \end{circuitikz}
```



```

1 \begin{circuitikz}[european voltages]
2   \draw (0,0) to[R, v_<=$v_1$] (2,0);
3 \end{circuitikz}

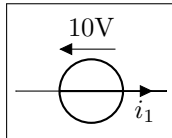
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[V=10V, i_=$i_1$] (2,0);
3 \end{circuitikz}

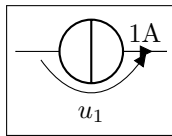
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[V<=10V, i_=$i_1$] (2,0);
3 \end{circuitikz}

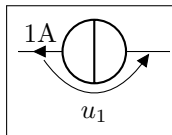
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[I=1A, v_=$u_1$] (2,0);
3 \end{circuitikz}

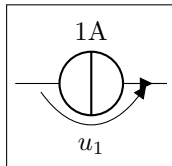
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[I<=1A, v_=$u_1$] (2,0);
3 \end{circuitikz}

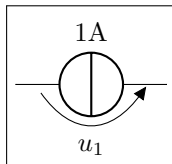
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[I=~$,l=1A, v_=$u_1$] (2,0);
3 \end{circuitikz}

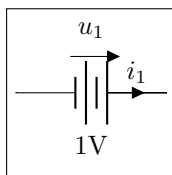
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[I,l=1A, v_=$u_1$] (2,0);
3 \end{circuitikz}

```



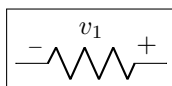
```

1 \begin{circuitikz}
2   \draw (0,0) to[battery,l=1V, v=$u_1$, i=$i_1$] (2,0);
3 \end{circuitikz}

```

4.5.2 American style

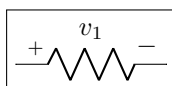
Use option `americanvoltage` or set `[american voltages]`.



```

1 \begin{circuitikz}[american voltages]
2   \draw (0,0) to[R, v^>=$v_1$] (2,0);
3 \end{circuitikz}

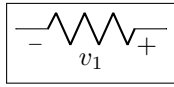
```



```

1 \begin{circuitikz}[american voltages]
2   \draw (0,0) to[R, v^<=$v_1$] (2,0);
3 \end{circuitikz}

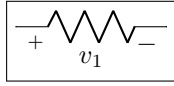
```

```

1 \begin{circuitikz}[american voltages]
2   \draw (0,0) to[R, v_>=$v_1$] (2,0);
3 \end{circuitikz}

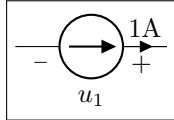
```



```

1 \begin{circuitikz}[american voltages]
2   \draw (0,0) to[R, v_<=$v_1$] (2,0);
3 \end{circuitikz}

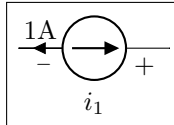
```



```

1 \begin{circuitikz}[american]
2   \draw (0,0) to[I=1A, v_=$u_1$] (2,0);
3 \end{circuitikz}

```



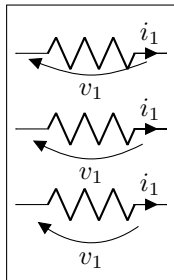
```

1 \begin{circuitikz}[american]
2   \draw (0,0) to[I<=1A, v_=$i_1$] (2,0);
3 \end{circuitikz}

```

4.5.3 Voltage position

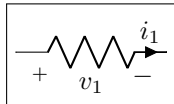
It is possible to move away the arrows and the plus or minus signs with the key `voltages shift` (default value is 0, which gives the standard position):



```

1 \begin{circuitikz}[]
2   \draw (0,0) to[R, v=$v_1$, i=$i_1$] (2,0);
3   \draw (0,-1) to[R, v=$v_1$, i=$i_1$,
4     voltage shift=0.5] (2,-1);
5   \draw (0,-2) to[R, v=$v_1$, i=$i_1$,
6     voltage shift=1.0, ] (2,-2);
7 \end{circuitikz}

```

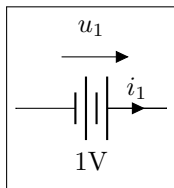


```

1 \begin{circuitikz}[american voltages, voltage shift=0.5]
2   \draw (0,0) to[R, v=$v_1$, i=$i_1$] (2,0);
3 \end{circuitikz}

```

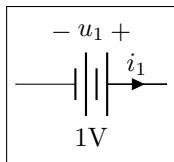
Notes that `american voltage` will not affect batteries.



```

1   \begin{circuitikz}[voltage shift=0.5]
2     \draw (0,0) to[battery,l=1V, v=$u_1$, i=$i_1$] (2,0);
3 \end{circuitikz}

```

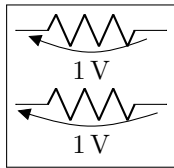


```

1 \begin{circuitikz}[american voltages, voltage shift=0.5]
2   \draw (0,0) to[battery,l=1V, v=$u_1$, i=$i_1$] (2,0);
3 \end{circuitikz}

```

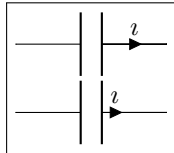
4.5.4 Global properties of voltages and currents



```

1\tikz \draw (0,0) to[R, v=1<\volt>] (2,0); \par
2\ctikzset{voltage/distance from node=.1}
3\tikz \draw (0,0) to[R, v=1<\volt>] (2,0);

```

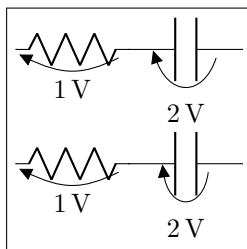


```

1\tikz \draw (0,0) to[C, i=${\imath}$] (2,0); \par
2\ctikzset{current/distance = .2}
3\tikz \draw (0,0) to[C, i=${\imath}$] (2,0);

```

However, you can override the properties `voltage/distance from node`⁴, `voltage/bump b`⁵ and `voltage/european label distance`⁶ on a per-component basis, in order to fine-tune the voltages:



```

1\tikz \draw (0,0) to[R, v=1<\volt>] (1.5,0)
2      to[C, v=2<\volt>] (3,0); \par
3\ctikzset{bipoles/capacitor/voltage/%
4      distance from node/.initial=.7}
5\tikz \draw (0,0) to[R, v=1<\volt>] (1.5,0)
6      to[C, v=2<\volt>] (3,0); \par

```

4.6 Nodes



```

1\begin{circuitikz}
2  \draw (0,0) to[R, o-o] (2,0);
3\end{circuitikz}

```



```

1\begin{circuitikz}
2  \draw (0,0) to[R, -o] (2,0);
3\end{circuitikz}

```



```

1\begin{circuitikz}
2  \draw (0,0) to[R, o-] (2,0);
3\end{circuitikz}

```



```

1\begin{circuitikz}
2  \draw (0,0) to[R, *-] (2,0);
3\end{circuitikz}

```



```

1\begin{circuitikz}
2  \draw (0,0) to[R, -*] (2,0);
3\end{circuitikz}

```



```

1\begin{circuitikz}
2  \draw (0,0) to[R, *-] (2,0);
3\end{circuitikz}

```



```

1\begin{circuitikz}
2  \draw (0,0) to[R, d-d] (2,0);
3\end{circuitikz}

```

⁴That is, how distant from the initial and final points of the path the arrow starts and ends.

⁵Controlling how high the bump of the arrow is — how curved it is.

⁶Controlling how distant from the bipole the voltage label will be.



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, -d] (2,0);
3 \end{circuitikz}

```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, d-] (2,0);
3 \end{circuitikz}

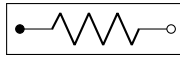
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, o-*] (2,0);
3 \end{circuitikz}

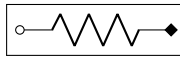
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, *-o] (2,0);
3 \end{circuitikz}

```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, o-d] (2,0);
3 \end{circuitikz}

```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, d-o] (2,0);
3 \end{circuitikz}

```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, *-d] (2,0);
3 \end{circuitikz}

```



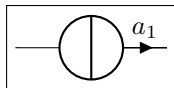
```

1 \begin{circuitikz}
2   \draw (0,0) to[R, d-*] (2,0);
3 \end{circuitikz}

```

4.7 Special components

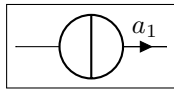
For some components label, current and voltage behave as one would expect:



```

1 \begin{circuitikz}
2   \draw (0,0) to[I=$a_1$] (2,0);
3 \end{circuitikz}

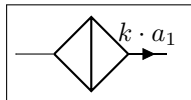
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[I, i=$a_1$] (2,0);
3 \end{circuitikz}

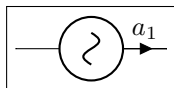
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[cI=$k \cdot a_1$] (2,0);
3 \end{circuitikz}

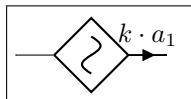
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[sI=$a_1$] (2,0);
3 \end{circuitikz}

```

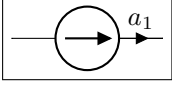
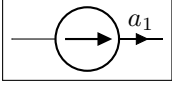
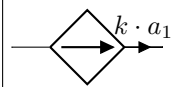
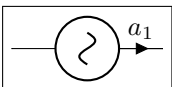
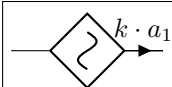


```

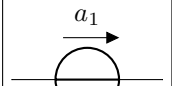
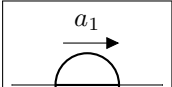
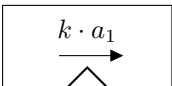
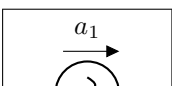
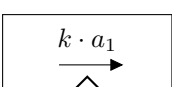
1 \begin{circuitikz}
2   \draw (0,0) to[csI=$k \cdot a_1$] (2,0);
3 \end{circuitikz}

```

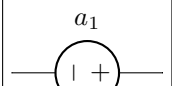
The following results from using the option `americancurrent` or using the style `[american currents]`.

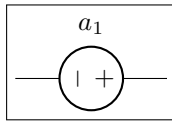
	<pre> 1 \begin{circuitikz}[american currents] 2 \draw (0,0) to[I=\$a_1\$] (2,0); 3 \end{circuitikz} </pre>
	<pre> 1 \begin{circuitikz}[american currents] 2 \draw (0,0) to[I, i=\$a_1\$] (2,0); 3 \end{circuitikz} </pre>
	<pre> 1 \begin{circuitikz}[american currents] 2 \draw (0,0) to[cI=\$k \cdot a_1\$] (2,0); 3 \end{circuitikz} </pre>
	<pre> 1 \begin{circuitikz}[american currents] 2 \draw (0,0) to[sI=\$a_1\$] (2,0); 3 \end{circuitikz} </pre>
	<pre> 1 \begin{circuitikz}[american currents] 2 \draw (0,0) to[csI=\$k \cdot a_1\$] (2,0); 3 \end{circuitikz} </pre>

The same holds for voltage sources:

	<pre> 1 \begin{circuitikz} 2 \draw (0,0) to[V=\$a_1\$] (2,0); 3 \end{circuitikz} </pre>
	<pre> 1 \begin{circuitikz} 2 \draw (0,0) to[V, v=\$a_1\$] (2,0); 3 \end{circuitikz} </pre>
	<pre> 1 \begin{circuitikz} 2 \draw (0,0) to[cV=\$k \cdot a_1\$] (2,0); 3 \end{circuitikz} </pre>
	<pre> 1 \begin{circuitikz} 2 \draw (0,0) to[sV=\$a_1\$] (2,0); 3 \end{circuitikz} </pre>
	<pre> 1 \begin{circuitikz} 2 \draw (0,0) to[csV=\$k \cdot a_1\$] (2,0); 3 \end{circuitikz} </pre>

The following results from using the option `americanvoltage` or the style `[american voltages]`.

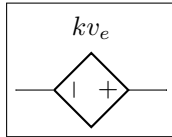
	<pre> 1 \begin{circuitikz}[american voltages] 2 \draw (0,0) to[V=\$a_1\$] (2,0); 3 \end{circuitikz} </pre>
---	--



```

1 \begin{circuitikz}[american voltages]
2   \draw (0,0) to[V, v=$a_1$] (2,0);
3 \end{circuitikz}

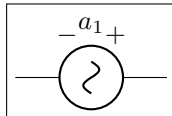
```



```

1 \begin{circuitikz}[american voltages]
2   \draw (0,0) to[cV=$k v_e$] (2,0);
3 \end{circuitikz}

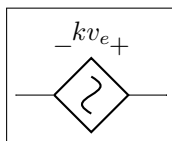
```



```

1 \begin{circuitikz}[american voltages]
2   \draw (0,0) to[sV=$a_1$] (2,0);
3 \end{circuitikz}

```



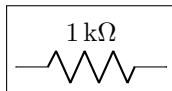
```

1 \begin{circuitikz}[american voltages]
2   \draw (0,0) to[csV=$k v_e$] (2,0);
3 \end{circuitikz}

```

4.8 Integration with siunitx

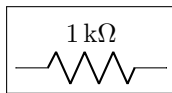
If the option `siunitx` is active (and *not* in `ConTEXt`), then the following are equivalent:



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, l=1<\kilo\ohm>] (2,0);
3 \end{circuitikz}

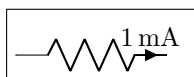
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, l=$\SI{1}{\kilo\ohm}$] (2,0);
3 \end{circuitikz}

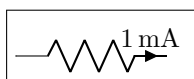
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i=1<\milli\ampere>] (2,0);
3 \end{circuitikz}

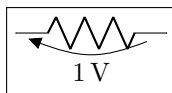
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, i=$\SI{1}{\milli\ampere}$] (2,0);
3 \end{circuitikz}

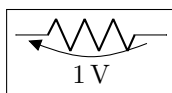
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[R, v=1<\volt>] (2,0);
3 \end{circuitikz}

```



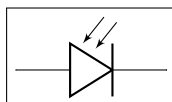
```

1 \begin{circuitikz}
2   \draw (0,0) to[R, v=$\SI{1}{\volt}$] (2,0);
3 \end{circuitikz}

```

4.9 Mirroring and Inverting

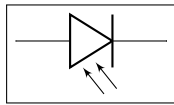
Bipole paths can also mirrored and inverted (or reverted) to change the drawing direction.



```

1 \begin{circuitikz}
2   \draw (0,0) to[pD] (2,0);
3 \end{circuitikz}

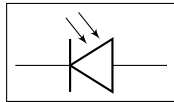
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[pD, mirror] (2,0);
3 \end{circuitikz}

```

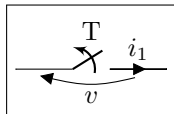


```

1 \begin{circuitikz}
2   \draw (0,0) to[pD, invert] (2,0);
3 \end{circuitikz}

```

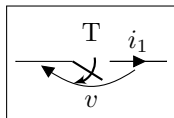
Placing labels, currents and voltages works also, please note, that mirroring and inverting does not influence the positioning of labels and voltages. Labels are by default above/right of the bipole and voltages below/left, respectively.



```

1 \begin{circuitikz}
2   \draw (0,0) to[ospst=T, i=$i_1$, v=$v$] (2,0);
3 \end{circuitikz}

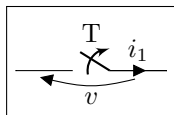
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[ospst=T, mirror, i=$i_1$, v=$v$] (2,0);
3 \end{circuitikz}

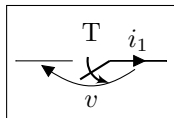
```



```

1 \begin{circuitikz}
2   \draw (0,0) to[ospst=T, invert, i=$i_1$, v=$v$] (2,0);
3 \end{circuitikz}

```

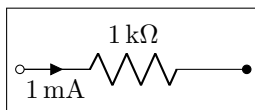


```

1 \begin{circuitikz}
2   \draw (0,0) to[ospst=T,mirror,invert, i=$i_1$, v=$v$] (2,0);
3 \end{circuitikz}

```

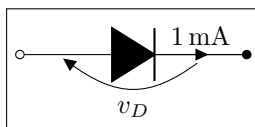
4.10 Putting them together



```

1 \begin{circuitikz}
2   \draw (0,0) to[R=1<\kilo\ohm>,
3     i>_ =1<\milli\ampere>, o-*] (3,0);
4 \end{circuitikz}

```



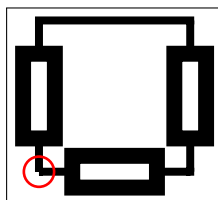
```

1 \begin{circuitikz}
2   \draw (0,0) to[D*, v=$v_D$,
3     i=1<\milli\ampere>, o-*] (3,0);
4 \end{circuitikz}

```

4.11 Line joins between Path Components

Line joins should be calculated correctly, if the were on the same path and if the path is not closed. For example, the following path is not closed correctly(*-cycle* does not work here!):

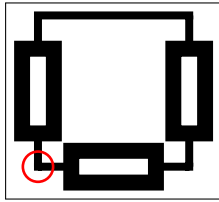


```

1 \begin{tikzpicture}[line width=3pt,european]
2   \draw (0,0) to[R]++(2,0)to[R]++(0,2)
3     ---++(-2,0)to[R]++(0,-2);
4   \draw[red,line width=1pt] circle(2mm);
5 \end{tikzpicture}

```

To correct the line ending, there are support shapes to fill the missing rectangle. They can be used like the support shapes(*,o,d) using a dot (.) on one or both ends of a component(have a look at the last resistor in this example:



```

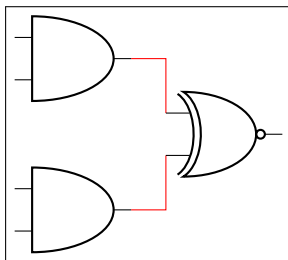
1 \begin{tikzpicture}[line width=3pt,european]
2 \draw (0,0) to[R]++(2,0)to[R]++(0,2)
3   --++(-2,0)to[R,-.]++(0,-2);
4 \draw[red,line width=1pt] circle(2mm);
5 \end{tikzpicture}

```

5 Colors

The color of the components is stored in the key `\circuitikzbasekey/color`. CircuiTikZ tries to follow the color set in TikZ, although sometimes it fails. If you change color in the picture, please do not use just the color name as a style, like `[red]`, but rather assign the style `[color=red]`.

Compare for instance

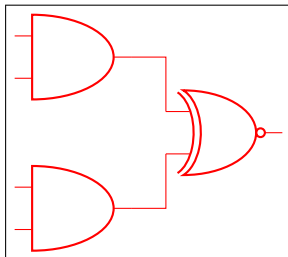


```

1 \begin{circuitikz} \draw[red]
2   (0,2) node[and port] (myand1) {}
3   (0,0) node[and port] (myand2) {}
4   (2,1) node[xnor port] (myxnor) {}
5   (myand1.out) -| (myxnor.in 1)
6   (myand2.out) -| (myxnor.in 2)
7 ;\end{circuitikz}

```

and

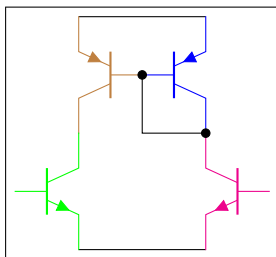


```

1 \begin{circuitikz} \draw[color=red]
2   (0,2) node[and port] (myand1) {}
3   (0,0) node[and port] (myand2) {}
4   (2,1) node[xnor port] (myxnor) {}
5   (myand1.out) -| (myxnor.in 1)
6   (myand2.out) -| (myxnor.in 2)
7 ;\end{circuitikz}

```

One can of course change the color *in medias res*:

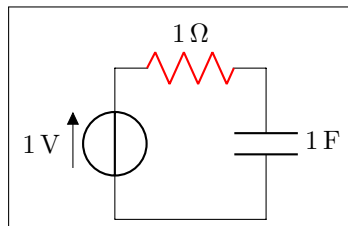


```

1 \begin{circuitikz} \draw
2   (0,0) node[pnp, color=blue] (pnp2) {}
3   (pnp2.B) node[pnp, xscale=-1, anchor=B, color=brown] (pnp1) {}
4   (pnp1.C) node[npn, anchor=C, color=green] (npn1) {}
5   (pnp2.C) node[npn, xscale=-1, anchor=C, color=magenta] (npn2) {}
6   (pnp1.E) -- (pnp2.E) (npn1.E) -- (npn2.E)
7   (pnp1.B) node[circ] {} |- (pnp2.C) node[circ] {}
8 ;\end{circuitikz}

```

The all-in-one stream of bipoles poses some challenges, as only the actual body of the bipole, and not the connecting lines, will be rendered in the specified color. Also, please notice the curly braces around the `to`:

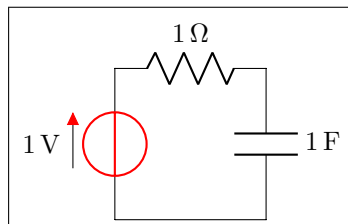


```

1 \begin{circuitikz} \draw
2   (0,0) to[V=1<\volt>] (0,2)
3     { to[R=1<\ohm>, color=red] (2,2) }
4       to[C=1<\farad>] (2,0) -- (0,0)
5 ;\end{circuitikz}

```

Which, for some bipoles, can be frustrating:

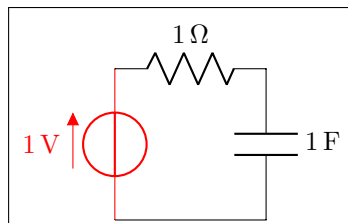


```

1 \begin{circuitikz} \draw
2   (0,0){to[V=1<\volt>, color=red] (0,2) }
3     to[R=1<\ohm>] (2,2)
4     to[C=1<\farad>] (2,0) -- (0,0)
5 ;\end{circuitikz}

```

The only way out is to specify different paths:



```

1 \begin{circuitikz} \draw[color=red]
2   (0,0) to[V=1<\volt>, color=red] (0,2);
3   \draw (0,2) to[R=1<\ohm>] (2,2)
4     to[C=1<\farad>] (2,0) -- (0,0)
5 ;\end{circuitikz}

```

And yes: this is a bug and *not* a feature...

6 FAQ

Q: When using `\tikzexternalize` I get the following error:

! Emergency stop.

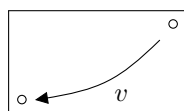
A: The TikZ manual states:

Furthermore, the library assumes that all \LaTeX pictures are ended with `\end{tikzpicture}`.

Just substitute every occurrence of the environment `circuitikz` with `tikzpicture`. They are actually pretty much the same.

Q: How do I draw the voltage between two nodes?

A: Between any two nodes there is an open circuit!



```

1 \begin{circuitikz} \draw
2   node[ocirc] (A) at (0,0) {}
3   node[ocirc] (B) at (2,1) {}
4   (A) to[open, v=$v$] (B)
5 ;\end{circuitikz}

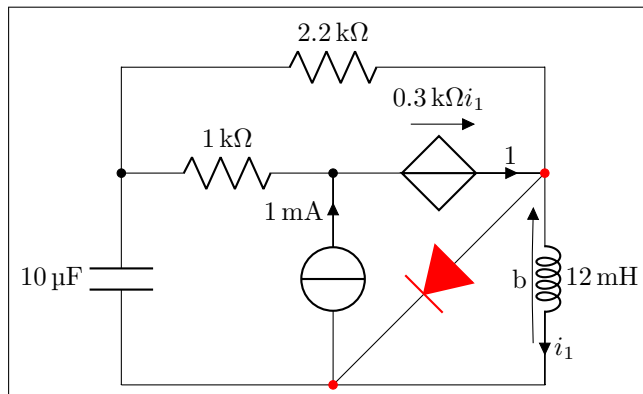
```

Q: I cannot write `to[R = \$R_1=12V\$]` nor `to[ospst = open, 3s]`: I get errors.

A: It is a limitation of the parser.

Use `\def{\eq}{=}` to `to[R = \$R_1\eq 12V\$]` and `to[ospst = open{,} 3s]` instead; see caveat in section 4.1.

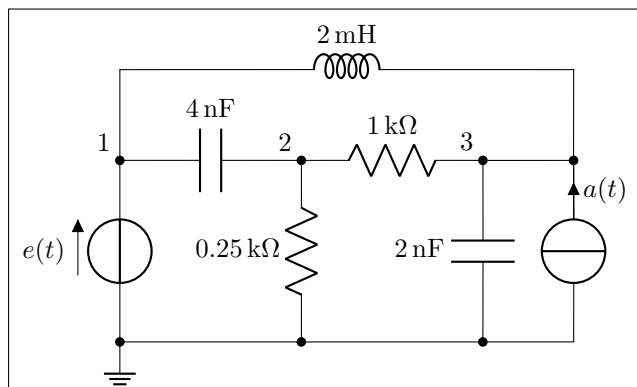
7 Examples



```

1 \begin{circuitikz}[scale=1.4]\draw
2 (0,0) to[C, l=10<\micro\farad>] (0,2) -- (0,3)
3     to[R, l=2.2<\kilo\ohm>] (4,3) -- (4,2)
4     to[L, l=12<\milli\henry>, i=$i_1$,v=b] (4,0) -- (0,0)
5 (4,2) { to[D*, **-, color=red] (2,0) }
6 (0,2) to[R, l=1<\kilo\ohm>, *-] (2,2)
7     to[cV, i=1,v=$\SI{.3}{\kilo\ohm} i_1$] (4,2)
8 (2,0) to[I, i=1<\milli\ampere>, -*] (2,2)
9;\end{circuitikz}

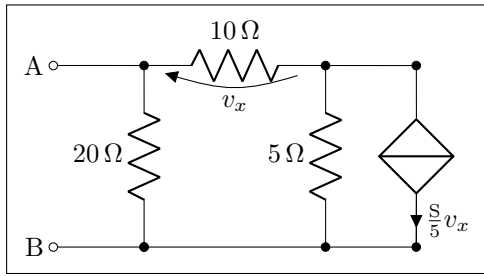
```



```

1 \begin{circuitikz}[scale=1.2]\draw
2 (0,0) node[ground] {}
3     to[V=$e(t)$, **] (0,2) to[C=4<\nano\farad>] (2,2)
4     to[R, l_=.25<\kilo\ohm>, **] (2,0)
5 (2,2) to[R=1<\kilo\ohm>] (4,2)
6     to[C, l_=2<\nano\farad>, **] (4,0)
7 (5,0) to[I, i_=$a(t)$, -*] (5,2) -- (4,2)
8 (0,0) -- (5,0)
9 (0,2) -- (0,3) to[L, l=2<\milli\henry>] (5,3) -- (5,2)
10
11 {[anchor=south east] (0,2) node {1} (2,2) node {2} (4,2) node {3}}
12;\end{circuitikz}

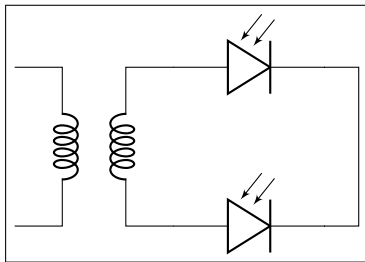
```



```

1 \begin{circuitikz}[scale=1.2]\draw
2   (0,0) node[anchor=east] {B}
3     to[short, o-*] (1,0)
4     to[R=20<\ohm>, *-] (1,2)
5     to[R=10<\ohm>, v=v_x$] (3,2) -- (4,2)
6     to[cI=$\frac{\siemens}{5} v_x$, *-] (4,0) -- (3,0)
7     to[R=5<\ohm>, *-] (3,2)
8   (3,0) -- (1,0)
9   (1,2) to[short, -o] (0,2) node[anchor=east]{A}
10;\end{circuitikz}

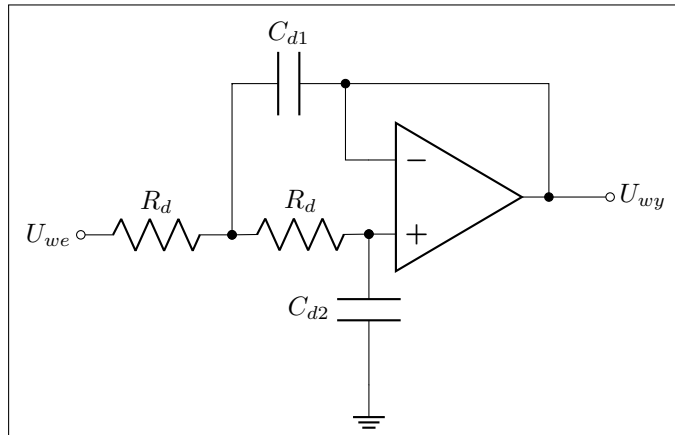
```



```

1 \begin{circuitikz}[scale=1]\draw
2   (0,0) node[transformer] (T) {}
3   (T.B2) to[pD] ($(T.B2)+(2,0)$) -| (3.5, -1)
4   (T.B1) to[pD] ($(T.B1)+(2,0)$) -| (3.5, -1)
5;\end{circuitikz}

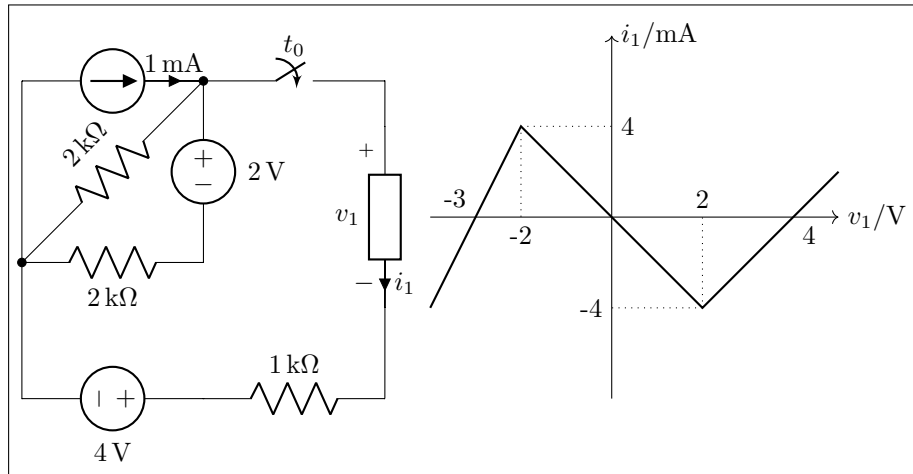
```



```

1 \begin{circuitikz}[scale=1]\draw
2   (5,.5) node [op amp] (opamp) {}
3   (0,0) node [left] {$U_{we}$} to [R, l=$R_d$, o-*] (2,0)
4   to [R, l=$R_d$, *-*] (opamp.+)
5   to [C, l=$C_{d2}$, *-*] ($(opamp.+) + (0,-2)$) node [ground] {}
6   (opamp.out) |- (3.5,2) to [C, l=$C_{d1}$, *-*] (2,2) to [short] (2,0)
7   (opamp.-) -| (3.5,2)
8   (opamp.out) to [short, *-o] (7,.5) node [right] {$U_{wy}$}
9 ;\end{circuitikz}

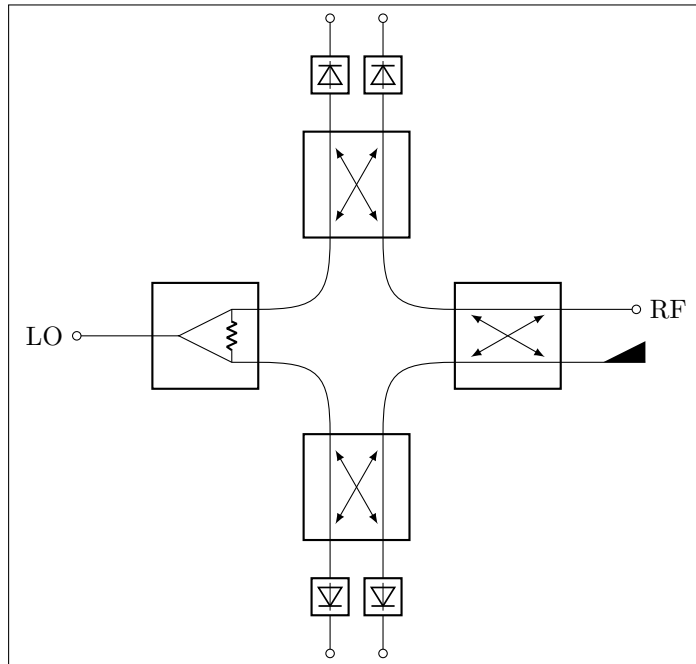
```



```

1 \begin{circuitikz}[scale=1.2, american]\draw
2   (0,2) to[I=1<\milli\ampere>] (2,2)
3     to[R, l=2<\kilo\ohm>, *-] (0,0)
4     to[R, l=2<\kilo\ohm>] (2,0)
5     to[V, v=2<\volt>] (2,2)
6     to[cspst, l=$t_0$] (4,2) -- (4,1.5)
7     to [generic, i=$i_1$, v=$v_1$] (4,-.5) -- (4,-1.5)
8   (0,2) -- (0,-1.5) to[V, v=4<\volt>] (2,-1.5)
9     to [R, l=1<\kilo\ohm>] (4,-1.5);
10
11 \begin{scope}[xshift=6.5cm, yshift=.5cm]
12   \draw [->] (-2,0) -- (2.5,0) node[anchor=west] {$v_1/\text{volt}$};
13   \draw [->] (0,-2) -- (0,2) node[anchor=west] {$i_1/\text{SI}\{\}\text{milli}\text{ampere}\}$} ;
14   \draw (-1,0) node[anchor=north] {-2} (1,0) node[anchor=south] {2}
15         (0,1) node[anchor=west] {4} (0,-1) node[anchor=east] {-4}
16         (2,0) node[anchor=north west] {4}
17         (-1.5,0) node[anchor=south east] {-3};
18   \draw [thick] (-2,-1) -- (-1,1) -- (1,-1) -- (2,0) -- (2.5,.5);
19   \draw [dotted] (-1,1) -- (-1,0) (1,-1) -- (1,0)
20             (-1,1) -- (0,1) (1,-1) -- (0,-1);
21 \end{scope}
22 \end{circuitikz}

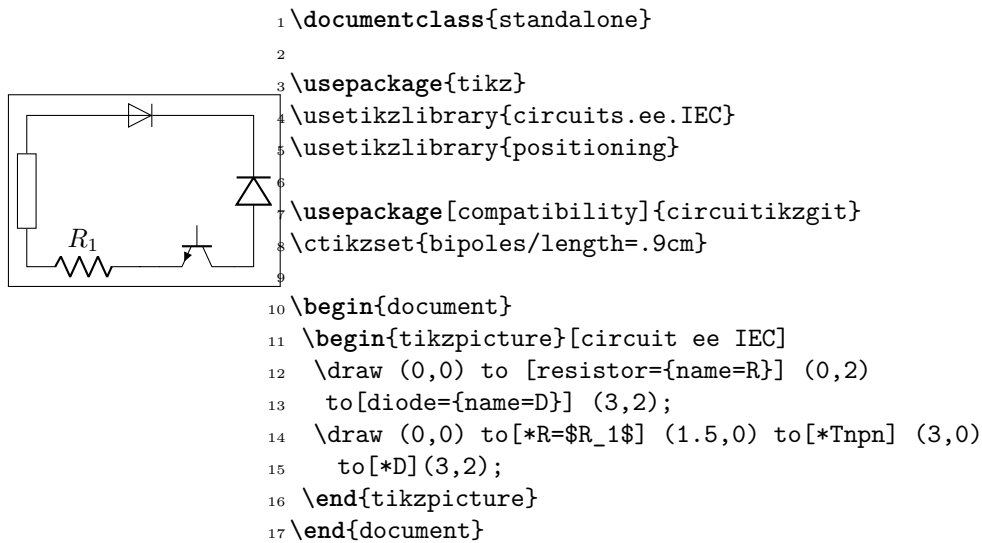
```



```

1  \begin{circuitikz}[scale=1]
2      \ctikzset{bipoles/detector/width=.35}
3      \ctikzset{quadpoles/coupler/width=1}
4      \ctikzset{quadpoles/coupler/height=1}
5      \ctikzset{tripoles/wilkinson/width=1}
6      \ctikzset{tripoles/wilkinson/height=1}
7      %\draw[help lines,red,thin,dotted] (0,-5) grid (5,5);
8      \draw
9      (-2,0) node[wilkinson](w1){}
10     (2,0) node[coupler] (c1) {}
11     (0,2) node[coupler,rotate=90] (c2) {}
12     (0,-2) node[coupler,rotate=90] (c3) {}
13     (w1.out1) .. controls ++(0.8,0) and ++(0,0.8) .. (c3.3)
14     (w1.out2) .. controls ++(0.8,0) and ++(0,-0.8) .. (c2.4)
15     (c1.1) .. controls ++(-0.8,0) and ++(0,0.8) .. (c3.2)
16     (c1.4) .. controls ++(-0.8,0) and ++(0,-0.8) .. (c2.1)
17     (w1.in) to[short,-o] ++(-1,0)
18     (w1.in) node[left=30] {LO}
19     (c1.2) node[match,yscale=1] {}
20     (c1.3) to[short,-o] ++(1,0)
21     (c1.3) node[right=30] {RF}
22     (c2.3) to[detector,-o] ++(0,1.5)
23     (c2.2) to[detector,-o] ++(0,1.5)
24     (c3.1) to[detector,-o] ++(0,-1.5)
25     (c3.4) to[detector,-o] ++(0,-1.5)
26     ;
27     \end{circuitikz}

```



8 Changelog

The major changes among the different circuitikz versions are listed here. See <https://github.com/circuitikz/circuitikz/commits> for a full list of changes.

- Version git (unreleased)
 - Fixed placement of straightlabels within 4th quadrant
 - Fixed straightvoltages at Diodes, varcap and some other components
 - Adjusted ground symbols to better match ISO standard
 - Fixed a bug about straightlabels (thanks to @fotesan)
 - Added Romano as contributor
 - Added a CONTRIBUTING file
 - Added new sources (cute european versions, noise sources)
 - Added new types of amplifiers, and option to flip inputs and outputs
 - Added bidirectional diodes (diac) thanks to Andre Lucas Chinazzo
 - Added L,R,C sensors (with european, american and cute variants)
 - Added stacked labels (thanks to the original work by Claudio Fiandrino)
 - Make the position of voltage symbols adjustable
 - Make the position of arrows in FETs and BJTs adjustable
 - Added the bulb symbol
 - Added options for solving the voltage direction problems.
 - Added chips (DIP, QFP) with a generic number of pins.
 - Added special anchors for transformers (and fixed the wrong center anchor)
 - Changed the logical port implementation to multiple inputs (thanks to John Kormylo)
 - Changed labels spacing so that they are independent on scale factor
 - Fixed the position of text labels in amplifiers
 - Added new switches.
 - Added border anchors for the logic gates

- Added antennas with no wires, loudspeakers and microphone
- Version 0.8.3 (2017-05-28)
 - Removed unwanted lines at to-paths if the starting point is a node without a explicit anchor.
 - Fixed scaling option, now all parts are scaled by bipoles/length
 - Surge arrester appears no more if a to path is used without []-options
 - Fixed current placement now possible with paths at an angle of around 280°
 - Fixed voltage placement now possible with paths at an angle of around 280°
 - Fixed label and annotation placement (at some angles position not changable)
 - Adjustable default distance for straight-voltages: ‘bipoles/voltage/straight label distance’
 - Added Symbol for bandstop filter
 - New annotation type to show flows using f=... like currents, can be used for thermal, power or current flows
- Version 0.8.2 (2017-05-01)
 - Fixes pgfkeys error using alternatively specified mixed colors(see pgfplots manual section “4.7.5 Colors”)
 - Added new switches “ncs” and “nos”
 - Reworked arrows at spst-switches
 - Fixed direction of controlled american voltage source
 - “v<=” and “i<=” do not rotate the sources anymore(see them as “counting direction indication”, this can be different then the shape orientation); Use the option “invert” to change the direction of the source/appearance of the shape.
 - current label “i=” can now be used independent of the regular label “l=” at current sources
 - rewrite of current arrow placement. Current arrows can now also be rotated on zero-length paths
 - New DIN/EN compliant operational amplifier symbol “en amp”
- Version 0.8.1 (2017-03-25)
 - Fixed unwanted line through components if target coordinate is a name of a node
 - Fixed position of labels with subscript letters.
 - Absolute distance calculation in terms of ex at rotated labels
 - Fixed label for transistor paths (no label drawn)
- Version 0.8 (2017-03-08)
 - Allow use of voltage label at a [short]
 - Correct line joins between path components (to[...])
 - New Pole-shape .-. to fill perpendicular joins
 - Fixed direction of controlled american current source
 - Fixed incorrect scaling of magnetron
 - Fixed: Number of american inductor coils not adjustable

- Fixed Battery Symbols and added new battery2 symbol
- Added non-inverting Schmitttrigger
- Version 0.7 (2016-09-08)
 - Added second annotation label, showing, e.g., the value of an component
 - Added new symbol: magnetron
 - Fixed name conflict of diamond shape with tikz.shapes package
 - Fixed varcap symbol at small scalings
 - New packet-option "straightvoltages, to draw straight(no curved) voltage arrows
 - New option "invert" to revert the node direction at paths
 - Fixed american voltage label at special sources and battery
 - Fixed/rotated battery symbol(longer lines by default positive voltage)
 - New symbol Schmitttrigger
- Version 0.6 (2016-06-06)
 - Added Mechanical Symbols (damper,mass,spring)
 - Added new connection style diamond, use (d-d)
 - Added new sources voosource and ioosource (double zero-style)
 - All diode can now drawn in a stroked way, just use globel option "strokediode" or stroke instead of full/empty, or D-. Use this option for compliance with DIN standard EN-60617
 - Improved Shape of Diodes:tunnel diode, Zener diode, schottky diode (bit longer lines at cathode)
 - Reworked igbt: New anchors G,gate and new L-shaped form Lnight, Lpigt
 - Improved shape of all fet-transistors and mirrored p-chan fets as default, as pnp, pmos, pfet are already. This means a backward-incompatibility, but smaller code, because p-channels mosfet are by default in the correct direction(source at top). Just remove the 'yscale=-1' from your p-chan fets at old pictures.
- Version 0.5 (2016-04-24)
 - new option boxed and dashed for hf-symbols
 - new option solderdot to enable/disable solderdot at source port of some fets
 - new parts: photovoltaic source, piezo crystal, electrolytic capacitor, electromechanical device(motor, generator)
 - corrected voltage and current direction(option to use old behaviour)
 - option to show body diode at fet transistors
- Version 0.4
 - minor improvements to documentation
 - comply with TDS
 - merge high frequency symbols by Stefan Erhardt
 - added switch (not opening nor closing)
 - added solder dot in some transistors
 - improved ConTeXt compatibility

- Version 0.3.1
 - different management of color...
 - fixed typo in documentation
 - fixed an error in the angle computation in voltage and current routines
 - fixed problem with label size when scaling a tikz picture
 - added gas filled surge arrester
 - added compatibility option to work with Tikz's own circuit library
 - fixed infinite in arctan computation
- Version 0.3.0
 - fixed gate node for a few transistors
 - added mixer
 - added fully differential op amp (by Kristofer M. Monisit)
 - now general settings for the drawing of voltage can be overridden for specific components
 - made arrows more homogeneous (either the current one, or latex' bt pgf)
 - added the single battery cell
 - added fuse and asymmetric fuse
 - added toggle switch
 - added varistor, photoresistor, thermocouple, push button
 - added thermistor, thermistor ptc, thermistor ptc
 - fixed misalignment of voltage label in vertical bipoles with names
 - added isfet
 - added noiseless, protective, chassis, signal and reference grounds (Luigi «Liverpool»)
- Version 0.2.4
 - added square voltage source (contributed by Alistair Kwan)
 - added buffer and plain amplifier (contributed by Danilo Piazzalunga)
 - added squid and barrier (contributed by Cor Molenaar)
 - added antenna and transmission line symbols contributed by Leonardo Azzinnari
 - added the changeover switch spdt (suggestion of Fabio Maria Antoniali)
 - rename of context.tex and context.pdf (thanks to Karl Berry)
 - updated the email address
 - in documentation, fixed wrong (non-standard) labelling of the axis in an example (thanks to prof. Claudio Beccaria)
 - fixed scaling inconsistencies in quadrupoles
 - fixed division by zero error on certain vertical paths
 - introduced options straightlabels, rotatelabels, smartlabels
- Version 0.2.3
 - fixed compatibility problem with label option from tikz
 - Fixed resizing problem for shape ground
 - Variable capacitor

- polarized capacitor
- ConTeXt support (read the manual!)
- nfet, nigate, nigfetd, pfet, pigfete, pigfetd (contribution of Clemens Helfmeier and Theodor Borsche)
- njfet, pjfet (contribution of Danilo Piazzalunga)
- pigbt, nigbt
- *backward incompatibility* potentiometer is now the standard resistor-with-arrow-in-the-middle; the old potentiometer is now known as variable resistor (or vR), similarly to variable inductor and variable capacitor
- triac, thyristor, memristor
- new property “name” for bipoles
- fixed voltage problem for batteries in american voltage mode
- european logic gates
- *backward incompatibility* new american standard inductor. Old american inductor now called “cute inductor”
- *backward incompatibility* transformer now linked with the chosen type of inductor, and version with core, too. Similarly for variable inductor
- *backward incompatibility* styles for selecting shape variants now end are in the plural to avoid conflict with paths
- new placing option for some tripoles (mostly transistors)
- mirror path style
- Version 0.2.2 - 20090520
 - Added the shape for lamps.
 - Added options `europeanresistor`, `europeaninductor`, `americanresistor` and `americaninductor`, with corresponding styles.
 - FIXED: error in transistor arrow positioning and direction under negative `xscale` and `yscale`.
- Version 0.2.1 - 20090503
 - Op-amps added
 - added options `arrowmos` and `noarrowmos`, to add arrows to pmos and nmos
- Version 0.2 - 20090417 First public release on CTAN
 - *Backward incompatibility*: labels ending with `:angle` are not parsed for positioning anymore.
 - Full use of TikZ keyval features.
 - White background is not filled anymore: now the network can be drawn on a background picture as well.
 - Several new components added (logical ports, transistors, double bipoles, ...).
 - Color support.
 - Integration with `{siunitx}`.
 - `Voltage`, `american style`.
 - Better code, perhaps. General cleanup at the very least.
- Version 0.1 - 2007-10-29 First public release

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