

CircuiTikZ version 1.2.1-797a746 (2020/07/06)

Massimo A. Redaelli (m.redaelli@gmail.com)
Stefan Lindner (stefan.lindner@fau.de)
Stefan Erhardt (stefan.erhardt@fau.de)
Romano Giannetti (romano.giannetti@gmail.com)

July 6, 2020

Contents

1	Intr	oduction 7
	1.1	About
	1.2	License
	1.3	Loading the package
	1.4	Installing a new version of the package
	1.5	Requirements
	1.6	Incompatible packages
	1.7	Known bugs and limitation
	1.8	Scale factors inaccuracies
	1.9	Incompabilities between version
	1.10	Feedback
	1.11	Package options
2	Tuto	orials 14
	2.1	Getting started with Circui $TikZ$: a current shunt
	2.2	A more complex tutorial: circuits, Romano style
	2.3	Tutorial: a logic circuit
3	The	components 25
	3.1	Path-style components
	0.1	3.1.1 Anchors
		3.1.2 Customization
		3.1.2.1 Components size
		3.1.2.2 Thickness of the lines
		3.1.2.3 Shape of the components
		3.1.3 Descriptions
	3.2	Node-style components
	J	3.2.1 Mirroring and flipping
		3.2.2 Anchors
		3.2.3 Descriptions
	3.3	Styling circuits and components
	0.0	3.3.1 Relative size
		3.3.2 Fill color
		3.3.3 Line thickness
		3.3.4 Style files
		3.3.5 Style files: how to write them
	3.4	Grounds and supply voltages
	O.T	3.4.1 Grounds
		3 4 1.1 Grounds anchors

		3.4.1.2 Grounds customization	7
	3.4.2	Power supplies $\dots \dots \dots$	7
		3.4.2.1 Power supply anchors	7
		3.4.2.2 Power supplies customization	7
3.5	Resisti	ve bipoles	8
	3.5.1	Potentiometers: wiper position	0
	3.5.2	Generic sensors anchors	0
	3.5.3	Resistive components customization	1
3.6	Capaci	tors and inductors: dynamical bipoles	1
	3.6.1	Capacitors	1
	3.6.2	Capacitive sensors anchors	2
	3.6.3	Capacitors customizations	2
	3.6.4	Inductors	2
	3.6.5	Inductors customizations	3
	3.6.6	Inductors anchors	4
3.7	Diodes	and such	4
	3.7.1	Tripole-like diodes	6
	3.7.2	Triacs anchors	7
	3.7.3	Diode customizations	7
3.8	Source	s and generators	8
	3.8.1	Batteries	8
	3.8.2	Stationary sources	8
	3.8.3	Sinusoidal sources	9
	3.8.4	Controlled sources	0
	3.8.5	Noise sources	1
	3.8.6	Special sources	2
	3.8.7	DC sources	3
	3.8.8	Sources customizations	3
3.9	Instru	nents	4
	3.9.1	Instruments customizations	5
	3.9.2	Rotation-invariant elements	5
	3.9.3	Instruments as node elements	6
	3.9.4	Measuring voltage and currents, multiple ways	6
3.10	Mecha	nical Analogy	9
	3.10.1	Mechanical elements customizations	9
3.11	Miscell	aneous bipoles	0
	3.11.1	Miscellanous element customization	1
3.12	Multip	le wires (buses)	1
		ngs	2
		5	3
	9 1 / 1	Arrows size	ก

3.15	Termin	nal shapes
	3.15.1	BNC connector/terminal
3.16	Block	diagram components
	3.16.1	Blocks anchors
	3.16.2	Blocks customization
		3.16.2.1 Multi ports
		3.16.2.2 Labels and custom two-port boxes 69
		3.16.2.3 Box option
		3.16.2.4 Dash optional parts
3.17	Transis	stors
	3.17.1	Standard bipolar transistors
	3.17.2	Multi-terminal bipolar transistors
	3.17.3	Field-effect transistors
	3.17.4	Transistor texts (labels)
	3.17.5	Transistors customization
		3.17.5.1 Size
		3.17.5.2 Arrows
		3.17.5.3 Body diodes and similar things
		3.17.5.4 Schottky transistors
		3.17.5.5 IGBT outer base
		3.17.5.6 Base/Gate terminal
		3.17.5.7 Bulk terminals
	3.17.6	Multiple terminal transistors customization
	3.17.7	Transistors anchors
	3.17.8	Transistor paths
3.18	Electro	onic Tubes
	3.18.1	Tubes customization
3.19	RF cor	mponents
	3.19.1	RF elements customization
	3.19.2	Microstrip customization
3.20	Electro	o-Mechanical Devices
	3.20.1	Electro-Mechanical Devices anchors
3.21	Double	e bipoles (transformers)
	3.21.1	Double dipoles anchors
	3.21.2	Double dipoles customization
	3.21.3	Styling transformer's coils independently
3.22	Amplif	iers
	3.22.1	Amplifiers anchors
	3.22.2	Amplifiers customization
		3.22.2.1 European-style amplifier customization $\dots \dots \dots$
	3 22 3	Designing your own amplifier 100

3.23	Switch	es and buttons
	3.23.1	Traditional switches
	3.23.2	Cute switches
		3.23.2.1 Cute switches anchors
		3.23.2.2 Cute switches customization
	3.23.3	Rotary switches
		3.23.3.1 Rotary switch anchors
		3.23.3.2 Rotary switch customization $\dots \dots \dots$
3.24	Logic g	gates
	3.24.1	American Logic gates
	3.24.2	IEEE logic gates
	3.24.3	European Logic gates
	3.24.4	Path-style logic ports
	3.24.5	American ports usage
		3.24.5.1 American logic port customization
		3.24.5.2 American logic port anchors $\dots \dots \dots$
	3.24.6	IEEE logic gates usage
		3.24.6.1 Stacking and aligning IEEE standard gates
		3.24.6.2 IEEE standard ports customization $\dots \dots \dots$
		3.24.6.3 IEEE standard ports anchors
	3.24.7	European logic port usage
		3.24.7.1 European logic port customization
		3.24.7.2 European logic port anchors $\dots \dots \dots$
3.25	Flip-flo	pps
	3.25.1	Custom flip-flops
	3.25.2	Flip-flops anchors
	3.25.3	Flip-flops customization
3.26	Multip	lexer and de-multiplexer
	3.26.1	Mux-Demux: design your own shape
	3.26.2	Mux-Demux customization
	3.26.3	Mux-Demux anchors
3.27	Chips	(integrated circuits)
	3.27.1	DIP and QFP chips customization
	3.27.2	Chips anchors
	3.27.3	Chips rotation
	3.27.4	Chip special usage
3.28	Seven	segment displays
	3.28.1	Seven segments anchors
	3 28 2	Seven segments customization 13

4	Lab	els and similar annotations	131
	4.1	Labels and Annotations	131
	4.2	Currents and voltages	133
		4.2.1 Common properties of voltages and currents	137
	4.3	Currents	138
	4.4	Flows	139
	4.5	Voltages	140
		4.5.1 European style	140
		4.5.2 Straight European style	142
		4.5.3 American style	142
		4.5.4 Raised American style	143
		4.5.5 Voltage position	144
		4.5.6 American voltages customization	145
	4.6	Changing the style of labels and text ornaments	146
	4.7	Accessing labels text nodes	146
	4.8	Advanced voltages, currents and flows	147
		4.8.1 Activating the anchors	148
		4.8.2 Auxiliary information	149
	4.9	Integration with siunitx	150
5	Usi	ng bipoles in circuits	151
	5.1	Nodes (also called poles)	151
		5.1.1 Transparent poles	153
	5.2	Mirroring and Inverting	153
	5.3	Putting them together	154
	5.4	Line joins between Path Components	154
6	Col	ors	155
	6.1	Shape colors	155
	6.2	Fill colors	157
7	FAC	Q	159
8	Def	ining new components	160
	8.1	Suggested setup	160
	8.2	Path-style component	161
	8.3	Node-style component	164
		8.3.1 Finishing your work	164

9	Exa	mples	165
	9.1	A red diode	165
	9.2	Using the (experimental) siunitx syntax	166
	9.3	Photodiodes	167
	9.4	A Sallen-Key cell	167
	9.5	Mixing circuits and graphs	168
	9.6	RF circuit	169
	9.7	A styled low noise input stage	170
	9.8	An example with the compatibility option	171
	9.9	3-phases block schematic	172
10	Cha	angelog and Release Notes	173
In	dex	of the components	182

1 Introduction

Lorenzo and Mirella, 57 years ago, started a trip that eventually lead to a lot of things — among them, CircuiTikZ v1.0. In loving memory — R.G., 2020-02-04

1.1 About

CircuiTikZ was initiated by Massimo Redaelli in 2007, who was working as a research assistant at the Polytechnic University of Milan, Italy, and needed a tool for creating exercises and exams. After he left University in 2010 the development of CircuiTikZ slowed down, since LATEX is mainly established in the academic world. In 2015 Stefan Lindner and Stefan Erhardt, both working as research assistants at the University of Erlangen-Nürnberg, Germany, joined the team and now maintain the project together with the initial author. In 2018 Romano Giannetti, full professor of Electronics at Comillas Pontifical University of Madrid, joined the team.

The use of CircuiTikZ is, of course, not limited to academic teaching. The package gets widely used by engineers for typesetting electronic circuits for articles and publications all over the world.

1.2 License

Copyright © 2007–2020 by Massimo Redaelli, 2013-2020 by Stefan Erhardt, 2015-2020 by Stefan Lindner, and 2018-2020 by Romano Giannetti. This package is author-maintained. Permission is granted to copy, distribute and/or modify this software under the terms of the \LaTeX Project Public License, version 1.3.1, or the GNU Public License. This software is provided 'as is', without warranty of any kind, either expressed or implied, including, but not limited to, the implied warranties of merchantability and fitness for a particular purpose.

1.3 Loading the package

IATEX	ConT _E Xt ¹
\usepackage{circuitikz}	\usemodule[circuitikz]

TikZ will be automatically loaded.

CircuiTikZ commands are just TikZ commands, so a minimum usage example would be:



¹ConT_EXt support was added mostly thanks to Mojca Miklavec and Aditya Mahajan.

1.4 Installing a new version of the package.

The stable version of the package should come with your LATEX distribution. Downloading the files from CTAN and installing them locally is, unfortunately, a distribution-dependent task and sometime not so trivial. If you search for local texmf tree and the name of your distribution on https://tex.stackexchange.com/ you will find a lot of hints.

Anyway, the easiest way of using whichever version of CircuiTikZ is to point to the github page https://circuitikz.github.io/circuitikz/ of the project, and download the version you want. You will download a simple (biggish) file, called circuitikz.sty.

Now you can just put this file in your local texmf tree, if you have one, or simply adding it into the same directory where your main file resides, and then use

```
\usepackage[...options...]{circuitikzgit}
```

instead of circuitikz. This is also advantageous for "future resilience"; the authors try hard not to break backward compatibility with new versions, but sometime things happen.

1.5 Requirements

- tikz, version ≥ 3 ;
- xstring, not older than 2009/03/13;
- siunitx, if using siunitx option.

1.6 Incompatible packages

TikZ's own circuit library, which is based on CircuiTikZ, (re?)defines several styles used by this library. In order to have them work together you can use the compatibility package option, which basically prefixes the names of all CircuiTikZ to[] styles with an asterisk.

So, if loaded with said option, one must write (0,0) to [*R] (2,0) and, for transistors on a path, (0,0) to [*Tnmos] (2,0), and so on (but (0,0) node [nmos] $\{\}$). See example at page 171.

Another thing to take into account is that any TikZ figure (and CircuiTikZ ones qualify) will have problems if you use the babel package with a language that changes active characters (most of them). The solution is normally to add the line \usetikzlibrary{babel} in your preamble, after loading TikZ or CircuiTikZ. This will normally solve the problem; some language also requires using \deactivatequoting or the option shorthands=off for babel. Please check the documentation of TikZ or this question on TEX stackexchange site.

1.7 Known bugs and limitation

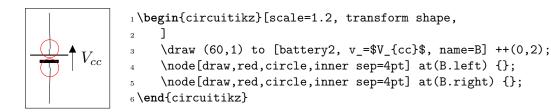
CircuiTikZ will **not work** correctly with global (in the main **circuitikz** environment, or in **scope** environments) negative scale parameters (**scale**, **xscale** or **yscale**), unless **transform shape** is also used, and even in this cases the behavior is not guaranteed. Neither it will work with angle-changing scaling (when **xscale** is different form **yscale**) and with the global **rotate** parameter.

Correcting this will need a big rewrite of the path routines, and although the authors are thinking about solving it, don't hold your breath; it will need changing a lot of interwoven code (labels, voltages, currents and so on). Contributions and help would be highly appreciated.

This same issue create a lot of problem of compatibility between CircuiTikZ and the new pic TikZ feature, so basically don't put components into pics.

1.8 Scale factors inaccuracies

Sometimes, when using fractional scaling factors and big values for the coordinates, the basic layer inaccuracies from T_FX can bite you, producing results like the following one:



A general solution for this problem is difficult to find; probably the best approach is to use a scalebox command to scale the circuit instead of relying on internal scaling.

Nevertheless, Schrödinger's cat found a solution which has been ported to CircuiTikZ: you can use the key use fpu reciprocal which will patch a standard low-level math routine with a more precise one.

The use fpu reciprocal key seems to have no side effects, but given that it is patching an internal interface of TikZ it can break any time, so it is advisable to use it only if and when needed.

1.9 Incompabilities between version

Here, we will provide a list of incompabilitys between different version of circuitikz. We will try to hold this list short, but sometimes it is easier to break with old syntax than including a lot of switches and compatibility layers. You can check the used version at your local installation using the macro \pgfcircversion{}.

• After v1.2.1: **Important:** the routine that implement the to[...] component positioning has been rewritten. That should enhance the line joins in path, and it's safer, but it can potentially change behavior.

One of the changes is that the previous routine did the wrong thing if you used (node) to [...] (you should use an anchor or a coordinate, not a node there — like (node.anchor) to [...]).

The other one was that in the structure ... to[...] node[pos=something] (coord) the value of pos was completely wrong (even if you don't use pos explicitly, remeber it's pos=0.5 by default).

Additionally, the old code disrupted the TikZ path-fill mechanism, so that you could get away with using the fill option on paths and having just the components to be filled, not the path. That was incorrect, although sometime it was handy.

• After v1.2.0: voltage arrows, symbols and label positions are calculated with a rewritten routine. There should be little change, *unless* you touched internal values...

- After v1.1.3: during the 1.1.0 1.1.2 version, the inverted Schmitt buffer in IEEE style ports was called inv schmitt (with an additional space). The correct name is invschmitt port (the same as the legacy american port).
- After v1.1.2: the position of american voltages for the open bipoles (you can revert to old behavior, see section 4.5.5).
- After v0.9.7: the position of the text of transistor nodes has changed; see section 3.17.4.
- After v0.9.4: added the concept of styling of circuits. It should be backward compatible, but it's a big change, so be ready to use the 0.9.3 snapshot (see below for details).
- After v0.9.0: the parameters tripoles/american or port/aaa, ...bbb, ...ccc and ...ddd are no longer used and are silently ignored; the same stands for nor, xor, and xnor ports.
- After v0.9.0: voltage and current directions/sign (plus and minus signs in case of american voltages and arrows in case of european voltages have been rationalized with a couple of new options (see details in section 4.2. The default case is still the same as v0.8.3.
- Since v0.8.2: voltage and current label directions (v<= / i<=) do NOT change the orientation of the drawn source shape anymore. Use the invert option to rotate the shape of the source. Furthermore, from this version on, the current label (i=) at current sources can be used independent of the regular label (1=).
- Since v0.7?: The label behaviour at mirrored bipoles has changes, this fixes the voltage drawing, but perhaps you have to adjust your label positions.
- Since v0.5.1: The parts pfet, pigfete, pigfetebulk and pigfetd are now mirrored by default. Please adjust your yscale-option to correct this.
- Since v0.5: New voltage counting direction, there exists an option to use the old behaviour.

If you have older projects that show compatibility problems, you have two options:

- you can use an older version locally using the git-version and picking the correct commit from the repository (branch gh-pages) or the main GitHub site directly;
- if you are using LATEX, the distribution has embedded several important old versions: 0.4, 0.6, 0.7, 0.8.3, 0.9.3, 0.9.6, 1.0 and 1.1.2. To switch to use them, you simply change your \usepackage invocation like

```
\usepackage[]{circuitikz-0.8.3} % or circuitikz-0.4, 0.6...
```

You have to take care of the options that may have changed between versions;

• if you are using ConT_EXt, only versions 0.8.3, 0.9.3, 0.9.6, 1.0 and 1.1.2 are packaged; if can use it with

\usemodule[circuitikz-0.8.3]

1.10 Feedback

The easiest way to contact the authors is via the official Github repository: https://github.com/circuitikz/circuitikz/issues. For general help question, a lot of nice people is quite active on https://tex.stackexchange.com/questions/tagged/circuitikz — be sure to read the help pages for the site and ask!

1.11 Package options

Circuit people are very opinionated about their symbols. In order to meet the individual gusto you can set a bunch of package options.

There are arguably way too much options in CircuiTikZ, as you can see in the following list. Since version 1.0, it is recommended to just use the basic ones — voltage directions (you **should** specify one of them), siunitx, the global style (american or european) and use styles (see 3.3) for the remaining options.

The standard options are what the authors like, for example you get this:

```
1 \begin{circuitikz}
2 \draw (0,0) to [R=2<\ohm>, i=?, v=84<\volt>] (2,0) --
3 (2,2) to [V<=84<\volt>] (0,2)
4 -- (0,0);
5 \end{circuitikz}
```

Feel free to load the package with your own cultural options:

IAT _E X	ConTEXt
\usepackage[american]{circuitikz}	\usemodule[circuitikz][american]

However, most of the global package options are not available in ConTEXt; in that case you can always use the appropriate \tikzset{} or \ctikzset{} command after loading the package.

```
1 \begin{circuitikz}
2 \draw (0,0) to [R=2<\ohm>, i=?, v=84<\volt>] (2,0) --
3 (2,2) to [V<=84<\volt>] (0,2)
4 -- (0,0);
5 \end{circuitikz}
```

Here is the list of all the options:

- europeanvoltages: uses arrows to define voltages, and uses european-style voltage sources;
- straightvoltages: uses arrows to define voltages, and and uses straight voltage arrows;
- \bullet americanvoltages: uses and + to define voltages, and uses american-style voltage sources;
- europeancurrents: uses european-style current sources;
- americancurrents: uses american-style current sources;
- europeanresistors: uses rectangular empty shape for resistors, as per european standards;
- americanresistors: uses zig-zag shape for resistors, as per american standards;
- europeaninductors: uses rectangular filled shape for inductors, as per european standards;

- americaninductors: uses "4-bumps" shape for inductors, as per american standards;
- cuteinductors: uses my personal favorite, "pig-tailed" shape for inductors;
- americanports: uses triangular logic ports, as per american standards;
- europeanports: uses rectangular logic ports, as per european standards;
- americangfsurgearrester: uses round gas filled surge arresters, as per american standards;
- europeangfsurgearrester: uses rectangular gas filled surge arresters, as per european standards;
- european: equivalent to europeancurrents, europeanvoltages, europeanresistors, europeaninductors, europeanports, europeangfsurgearrester;
- american: equivalent to americancurrents, americanvoltages, americanresistors, americaninductors, americanports, americangf surgearrester;
- siunitx: integrates with SIunitx package. If labels, currents or voltages are of the form #1<#2> then what is shown is actually \SI{#1}{#2};
- nosiunitx: labels are not interpreted as above;
- fulldiode: the various diodes are drawn and filled by default, i.e. when using styles such as diode, D, sD, ...Other diode styles can always be forced with e.g. Do, D-, ...
- strokediode: the various diodes are drawn and stroke by default, i.e. when using styles such as diode, D, sD, ...Other diode styles can always be forced with e.g. Do, D*, ...
- emptydiode: the various diodes are drawn but not filled by default, i.e. when using styles such as D, sD, ...Other diode styles can always be forced with e.g. Do, D-, ...
- arrowmos: pmos and nmos have arrows analogous to those of pnp and npn transistors;
- noarrowmos: pmos and nmos do not have arrows analogous to those of pnp and npn transistors;
- fetbodydiode: draw the body diode of a FET;
- nofetbodydiode: do not draw the body diode of a FET;
- fetsolderdot: draw solderdot at bulk-source junction of some transistors;
- nofetsolderdot: do not draw solderdot at bulk-source junction of some transistors;
- emptypmoscircle: the circle at the gate of a pmos transistor gets not filled;
- lazymos: draws lazy nmos and pmos transistors. Chip designers with huge circuits prefer this notation;
- legacytransistorstext: the text of transistor nodes is typeset near the collector;
- nolegacytransistorstext or centertransistorstext: the text of transistor nodes is typeset near the center of the component;
- straightlabels: labels on bipoles are always printed straight up, i.e. with horizontal baseline:
- rotatelabels: labels on bipoles are always printed aligned along the bipole;
- smartlabels: labels on bipoles are rotated along the bipoles, unless the rotation is very close to multiples of 90°;

- compatibility: makes it possibile to load CircuiTikZ and TikZ circuit library together.
- Voltage directions: until v0.8.3, there was an error in the coherence between american and european voltages styles (see section 4.2) for the batteries. This has been fixed, but to guarantee backward compatibility and to avoid nasty surprises, the fix is available with new options:
 - oldvoltagedirection: Use old way of voltage direction having a difference between european and american direction, with wrong default labelling for batteries;
 - nooldvoltagedirection: The standard from 0.5 onward, utilize the (German?) standard of voltage arrows in the direction of electric fields (without fixing batteries);
 - RPvoltages (meaning Rising Potential voltages): the arrow is in direction of rising potential, like in oldvoltagedirection, but batteries and current sources are fixed to follow the passive/active standard;
 - EFvoltages (meaning Electric Field voltages): the arrow is in direction of the electric field, like in nooldvoltagedirection, but batteries are fixed;

If none of these option are given, the package will default to nooldvoltagedirection, but will give a warning. The behavior is also selectable circuit by circuit with the voltage dir style.

• betterproportions²: nicer proportions of transistors in comparision to resistors;

The old options in the singular (like american voltage) are still available for compatibility, but are discouraged.

Loading the package with no options is equivalent to the following options: [nofetsolderdot, europeancurrents, europeanvoltages, americanports, americanresistors, cuteinductors, europeangfsurgearrester, nosiunitx, noarrowmos, smartlabels, nocompatibility, centertransistorstext].

In ConTEXt the options are similarly specified: current= european|american, voltage= european|american, resistor= american|european, inductor= cute|american|european, logic= american|european, siunitx= true|false, arrowmos= false|true.

²May change in the future!

2 Tutorials

To draw a circuit, you have to load the circuitikz package; this can be done with

1 \usepackage[siunitx, RPvoltages]{circuitikz}

somewhere in your document preamble. It will load automatically the needed packages if not already done before.

2.1 Getting started with CircuiTikZ: a current shunt

Let's say we want to prepare a circuit to teach how a current shunt works; the idea is to draw a current generator, a couple of resistors in parallel, and the indication of currents and voltages for the discussion.

A circuit in CircuiTikZ is drawn into a circuitikz environment (which is really an alias for tikzpicture). In this first example we will use absolute coordinates. The electrical components can be divided in two main categories: the one that are bipoles and are placed along a path (also known as to-style component, for their usage), and components that are nodes and can have any number of poles or connections.

Let's start with the first type of component, and build a basic mesh:

```
1\begin{circuitikz}[]
2 \draw (0,0) to[isource] (0,3) -- (2,3)
3 to[R] (2,0) -- (0,0);
4\end{circuitikz}
```

The symbol for the current source can surprise somebody; this is actually the european-style symbol, and the type of symbol chosen reflects the default options of the package (see section 1.11). Let's change the style for now (the author of the tutorial, Romano, is European - but he has always used American-style circuits, so ...); and while we're at it, let's add the other branch and some labels.

```
In the sint circuitikz is a single from the single form of the single
```

You can use a single path or multiple path when drawing your circuit, it's just a question of style (but be aware that closing path could be non-trivial, see section 5.4), and you can use standard TikZ lines (--, |- or similar) for the wires. Nonetheless, sometime using the CircuiTikZ specific short component for the wires can be useful, because then we can add labels and nodes at it, like for example in the following circuit, where we add a current (with the key i=..., see section 4.3) and a connection dot (with the special shortcut -* which adds a circ node at the end of the connection, see sections 3.15 and 5.1).



```
1 \begin{circuitikz} [american]
2   \draw (0,0) to[isource, l=$I_0$] (0,3)
3   to[short, -*, i=$I_0$] (2,3)
4   to[R=$R_1$, i=$i_1$] (2,0) -- (0,0);
5   \draw (2,3) -- (4,3)
6   to[R=$R_2$, i=$i_2$]
7   (4,0) to[short, -*] (2,0);
8 \end{circuitikz}
```

One of the problems with this circuit is that we would like to have the current in a different position, such as for example on the upper side of the resistors, so that Kirchoff's Current Law at the node is better shown to students. No problem; as you can see in section 4.2 you can use the position specifier <>^_ after the key i:



```
1 \begin{circuitikz} [american]
2    \draw (0,0) to[isource, 1=$I_0$] (0,3)
3    to[short, -*, i=$I_0$] (2,3)
4    to[R=$R_1$, i>_=$i_1$] (2,0) -- (0,0);
5    \draw (2,3) -- (4,3)
6    to[R=$R_2$, i>_=$i_2$]
7    (4,0) to[short, -*] (2,0);
8 \end{circuitikz}
```

Finally, we would like to add voltages indication for carrying out the current formulas; as the default position of the voltage signs seems a bit cramped to me, I am adding the voltage shift parameter to make a bit more space for it...



Et voilá!. Remember that this is still LATEX, which means that you have done a description of your circuit, which is, in a lot of way, independent of the visualization of it. If you ever have to adapt the circuit to, say, a journal that force European style and flows instead of currents, you just change a couple of things and you have what seems a completely different diagram:



And finally, this is still TikZ, so that you can freely mix other graphics element to the circuit.



2.2 A more complex tutorial: circuits, Romano style.

The idea is to draw a two-stage amplifier for a lesson, or exercise, on the different qualities of BJT and MOSFET transistors.

Please Notice that this section uses the "new" position for transistors labels, enabled since version 0.9.7. You should refer to older manuals to see how to do the same with older versios; basically the transistor's names where put with a different node{} command.

Also notice that this is a more "personal" tutorial, showing a way to draw circuits that is, in the author's opinion, highly reusable and easy to do. The idea is using relative coordinates and named nodes as much as possible, so that changes in the circuit are easily done by changing keys numbers of position, and crucially, each block is reusable in other diagrams.

First of all, let's define a handy function to show the position of nodes:

```
1\def\normalcoord(#1){coordinate(#1)}
2\def\showcoord(#1){node[circle, red, draw, inner sep=1pt,
3    pin={[red, overlay, inner sep=0.5pt, font=\tiny, pin distance=0.1cm,
4    pin edge={red, overlay}]45:#1}](#1){}}
5\let\coord=\normalcoord
6\let\coord=\showcoord
```

The idea is that you can use \coord() instead of coordinate() in paths, and that will draw sort of *markers* showing them. For example:

After the circuit is drawn, simply commenting out the second \let command will hide all the markers.

So let's start with the first stage transistor; given that my preferred way of drawing a MOSFET is with arrows, I'll start with the command \ctikzset{tripoles/mos style/arrows}:

```
1\begin{circuitikz} [american,]
2\ctikzset{tripoles/mos style/arrows}
3\def\killdepth#1{{\raisebox{0pt}[\height][0pt]{#1}}}
4\path (0,0) -- (2,0); % bounding box
5\draw (0,0) node[nmos](Q1){\killdepth{Q1}};
6\end{circuitikz}
```

I had to do draw an invisible line to take into account the text for Q1 — the text is not taken into account in calculating the bounding box. This is because the "geographical" anchors (north, north west, ...) are defined for the symbol only. In a complex circuit, this is rarely a problem.

Another thing I like to modify with respect to the standard is the position of the arrows in transistors, which are normally in the middle the symbol. Using the following setting (see section 3.17.5) will move the arrows to the start or end of the corresponding pin.

```
1\ctikzset{transistors/arrow pos=end}
```

The tricky thing about \killdepth{} macro is finicky details. Without the \killdepth macro, the labels of different transistor will be adjusted so that the vertical center of the box is at the center anchor, and as an effect, labels with descenders (like Q) will have a different baseline than labels without. You can see this here (it's really subtle):

```
1\begin{circuitikz} [american,]
2\draw (0,0) node [nmos] (Q1) {q1} ++(2,0)
3 node [nmos] (M1) {m1};
4\draw [red] (Q1.center) ++(0,-0.7ex) -- ++(3,0);
5\draw (0,-2)node [nmos] (Q1) {\killdepth{q1}} ++(2,0)
6 node [nmos] (M1) {\killdepth{m1}};
7\draw [red] (Q1.center) ++(0,-0.7ex) -- ++(3,0);
8\end{circuitikz}
```

We will start connecting the first transistor with the power supply with a couple of resistors. Notice that I am naming the nodes GND, VCC and VEE, so that I can use the coordinates to have all the supply rails at the same vertical position (more on this later).

```
V_{CC} = 10\,\text{V}
R_D
10\,\text{k}\Omega
^1 \text{ begin}\{\text{circuitikz}\}[\text{american},]
^2 \text{ draw } (0,0) \text{ node}[\text{nmos},] (Q1) \{\text{killdepth}\{Q1\}\};
^3 \text{ draw } (Q1.S) \text{ to}[R, 12^-\$R_S\$ \text{ and } \S1\{5\} \{\text{k} \text{ hohm}\}]
^4 ++(0,-3) \text{ node}[\text{vee}] (\text{VEE}) \{\$V_{\text{EE}}\}=\text{SI}\{-10\} \{\text{V}\}\$\};
^5 \text{ draw } (Q1.D) \text{ to}[R, 12_-\$R_D\$ \text{ and } \S1\{10\} \{\text{V}\}\$\};
^6 ++(0,3) \text{ node}[\text{vec}] (\text{VCC}) \{\$V_{\text{CC}}\}=\text{SI}\{10\} \{\text{V}\}\$\};
^7 \text{ draw } (Q1.S) \text{ to}[\text{short}] ++(2,0) \text{ to}[\text{C}=\$\text{C}_1\$]
^8 ++(0,-1.5) \text{ node}[\text{ground}] (\text{GND}) \{\};
^9 \text{ % show the named coordinates!}
^{10} \text{ path } (\text{GND}) \text{ $$coord}(\text{VCC})$}
^{12} \text{ (VEE) $$$coord}(\text{VEE});
^{13} \text{ end}\{\text{circuitikz}\}
```

After that, let's add the input part. I will use a named node here, to refer to it to add the input source. Notice how the ground node is positioned: the coordinate (in \mid - GND) is the point with the horizontal coordinate of (in) and the vertical one of (GND), lining it up with the ground of the capacitor C_1 (you can think it as "the point on the vertical of in and the horizontal of GND").

```
V_{CC} = 10 \text{ V}
R_D
10 \text{ k}\Omega
R_S
1 \text{ M}\Omega
R_S
5 \text{ k}\Omega
V_{EE} = -10 \text{ V}
```

```
1\begin{circuitikz}[american, scale=0.7, transform
      shape]
_2 \cdot (0,0) \text{ node[nmos,](Q1)}{\left(\frac{Q1}{1}\right)};
_3 \det (Q1.S) to[R, 12^=$R_S$ and \SI{5}{k\ohm}]
     ++(0,-3) node[vee](VEE){$V_{EE}=\SI{-10}{V}$};
_5 \det (Q1.D) to[R, 12_=$R_D$ and <math>SI\{10\}\{k \cosh m\}]
     ++(0,3) node[vcc](VCC){V_{CC}=SI_{10}{V}};
7 \draw (Q1.S) to[short] ++(2,0) to[C=$C_1$]
     ++(0,-1.5) node[ground](GND){};
9 \operatorname{Q1.G} to[short] ++(-1,0)
     \coord (in) to [R, 12^=$R_G$ and \SI{1}{M\ohm}]
     (in |- GND) node[ground]{};
11
_{12}\draw (in) to[C, 1_=$C_2$,*-o]
     ++(-1.5,0) node[left](vi1){v_i=v_{i1}};
14 \end{circuitikz}
```

Notice that the only absolute coordinate here is the first one, (0,0); so the elements are connected with relative movements and can be moved by just changing one number (for example, changing the to [C=\$C_1\$] ++(0,-1.5) will move all the grounds down).

This is the final circuit, with the nodes still marked:

```
1% this is for the blue brackets under the circuit
 2\tikzset{blockdef/.style={%
            {Straight Barb[harpoon, reversed, right, length=0.2cm]}-{Straight Barb[harpoon,
                      reversed, left, length=0.2cm]},
 5 }}
 6 \def\killdepth#1{{\raisebox{0pt}[\height][0pt]{#1}}}
 7 \def\coord(#1) {coordinate(#1)}
 s \def\coord(#1) \{node[circle, red, draw, inner sep=1pt,pin={[red, overlay, inner sep=0.5
              pt, font=\tiny, pin distance=0.1cm, pin edge={red, overlay,}]45:#1}](#1){}}
 9 \begin{circuitikz}[american, ]
            \draw (0,0) node[nmos,](Q1){\killdepth{Q1}};
            \label{local_condition} $$\operatorname{Q1.S} \ to[R, 12^=$R_S$ \ and \SI\{5\}_{k\to m}] ++(0,-3) \ node[vee] (VEE)_{$V_{EE}=\SI \ and \SI\{5\}_{k\to m}}$
11
                       {-10}{V}$}; %define VEE level
            \displaystyle (Q1.S) \ to[short] ++(2,0) \ to[C=C_1] ++(0,-1.5) \ node[ground](GND){};
12
             \label{local-condition} $$\operatorname{Q1.G} \ to[\operatorname{short}] ++(-1,0) \ \coord\ (in) \ to[R,\ 12^=$R_G$ and \SI\{1\}_{M\ohm}] \ (in \ |-1,0) \ \coord\ (in) \ to[R,\ 12^-]_{R_G} \ (in) \ \coord\ (in) \coord\ (in) \ \coord\ (in) \coord\ (in) \ \coord\ (in) \coor
13
                         GND) node[ground]{};
            \label{left} $$ \dim (in) \ to[C, l_=$C_2$,*-o] ++(-1.5,0) \ node[left](vi1){$v_i=v_{i1}}$;
14
            \label{local_self_condition} $$ \operatorname{Q1.D} \ to[R, 12_=$R_D$ \ and \SI_{10}_{k}\ ++(0,3) \ node[vcc](VCC)_{$V_{CC}=SI_{CC}$} $$
15
                       {10}{V}$};
            \draw (Q1.D) to[short, -o] ++(1,0) node[right](vo1){$v_{01}};
16
17
             \path (vo1) -- ++(2,0) \coord(bjt);
18
19
            \draw (bjt) node[npn, anchor=B](Q2){\killdepth{Q2}};
20
             \draw (Q2.B) to[short, -o] ++(-0.5,0) node[left](vi2){<math>v_{12}};
21
             \del{condition} \delcar{line} $$ \operatorname{Q2.E} = R_2^= \ \delcar{9.3}_{k\phi}] (Q2.E - VEE) \node[vee]_{};
22
            \draw (Q2.E) to[short, -o] ++(1,0) node[right](vo2){$v_{02}$};
23
            \draw (Q2.C) to[short] (Q2.C |- VCC) node[vcc]{};
24
            \path (vo2) ++(1.5,0) \coord(load);
26
            \label{local_cond} $$ \operatorname{C}_{C_3} ++(1,0) \subset (tmp) \to [R=\$R_L\$] (tmp \mid -GND) \ node[ground] $$
27
                      ]{};
            \draw [densely dashed] (vo2) -- (load);
28
```



You can see that after having found the place where we want to put the BJT transistor (line 18), we use the option anchor=B so that the base anchor will be put at the coordinate bjt.

Finally, if you like a more compact drawing, you can add the options (for example):

```
1\begin{circuitikz} [american, scale=0.8] % this will scale only the coordinates
2 \ctikzset{resistors/scale=0.7, capacitors/scale=0.6}
3 ...
4\end{circuitikz}
```

and you will obtain the following diagram with the exact same code (I just removed the second \coord definition to hide the coordinates markings).



2.3 Tutorial: a logic circuit

Let's suppose we want to reproduce the circuit on the right³, maybe as part of a more complex one.

Looking at the circuit to draw, I see that there is a basic block: the flip-flop with the added three-port circuit to its left, marked with the red dashed rectangle. The main distance to respect here is that we want the two ANDs in line with the flip-flop inputs, so I'll start with the flip-flop and then add the rest of the block.

The shapes are very similar to the IEEE logic gates (see section 3.24.2); after a first check, the standard size of the port is a bit too big with respect to the flip-flop, so I scale them down a bit.

```
1\ctikzset{
2 logic ports=ieee,
3 logic ports/scale=0.7,
4}
```



I want a reusable block, so I will start from a coordinate and then use only relative, defining coordinates along the way.

The first thing is to define a suitable flip-flop. The standard SR (see 3.25) is *almost* what we need, but not exactly the same. So let's define a new one:

```
1\tikzset{sr-ff/.style={flipflop, flipflop def={
2    t1=S, t2=CP, t3=R, t4={\ctikztextnot{Q}},
3    t6=Q, nd=1}},
```

If you look closer, you can notice that the new flip-flop has no lead in the bottom pin; this is due to the fact that there is no label here, and leads are drawn in flip-flops only if there is a label. This can be fixed by adding a blank label (like td=~); otherwise you have to utilize the anchors on the internal "not" circle.





Now we can add the "and" gates. For example, we can add the gates to the right like this:

```
SR-FF
S Q

| 1\begin{circuitikz}[]
| 2 \draw (0,0) node[sr-ff](FF){} (FF.bup)
| 3 node[above]{SR-FF};
| 4 \draw (FF.pin 1) -- ++(-1,0) node[and port,
| 5 anchor=out](AND1){}
| 6 (FF.pin 3) -- ++(-1,0) node[and port,
| 7 anchor=out](AND2){};
| 8 \end{circuitikz}
```

 $^{^3\}mathrm{It}$ seems a quite popular one on tex. stackexchange...

You can notice a pair of things here: first of all, the use of the anchor=out in the port, to tell TikZ that we want the node moved so that the out anchor is the reference one. The second one is that we have repeated the absolute shift (the ++(-1, 0)) twice. This is a bad practice; it is much better to have the "free" parameters of a schematic just stated once, so that we can change them in just one point.

You can of course use a macro, like \newcommand{\andshift}{(-1,0)} but it is much more elegant to do something like this:



```
1 \begin{circuitikz}[]
2    \draw (0,0) node[sr-ff](FF){} (FF.bup)
3    node[above]{SR-FF};
4    \draw (FF.pin 1) -- ++(-1,0) node[and port,
5         anchor=out](AND1){}
6    (FF.pin 3) -- (FF.pin 3 -| AND1.out)
7    node[and port, anchor=out](AND2){};
8 \end{circuitikz}
```

In this snippet, the coordinate (FF.pin 3 -| AND1.out) is the TikZ way to say "the point which is horizontally straight from FF.pin 3 and vertically form AND1.out". That way one can change the number -1 to move both AND ports nearer or farther away.

Now we can add the not port. Since version 1.1.3 you can use a path-style not port, so you can just say: this:



```
1\begin{circuitikz}[scale=0.8, transform shape]
2\draw (0,0) node[sr-ff](FF){} (FF.bup)
3    node[above]{SR-FF} (FF.pin 1) -- ++(-1,0)
4    node[and port, anchor=out](AND1){}
5    (FF.pin 3) -- (FF.pin 3 -| AND1.out)
6    node[and port, anchor=out](AND2){}
7    (AND1.in 1) to[short, -*] ++(-1,0) coordinate(in)
8    to[inline not] (in |- AND2.in 2) -- (AND2.in 2);
9\end{circuitikz}
```

In earlier version, you should have found the center point between the two terminal, position the "not" shape and ten connect it, like for example (this code must stay into the \draw command):

Now we have the basic block; we have to use it twice, so one of the possible way to do it is to prepare a command. We will change the names of the nodes and the coordinates to be different for any "call" of the block (another option is to use a pic; but this is more straightforward).

So now we can draw two of our blocks:

```
1 \draw (0,0) \myblock{A};
2 \draw (0,-4) \myblock{B};
```

Part of the anchors and coordinates that we have accessible are marked in red in the diagram at the side.

Now we have to just connect the relevant parts and add the labels. The names of the inputs are quite easy:





And finally:

```
1  \draw (A-AND1.in 2) to[short, -*] (A-AND2.in 1)
2    to[short, -*] (B-AND1.in 2) to[short, -*] (B-AND2.in 1)
3    -- ++(0, -2) coordinate(down) node[below]{ENABLE};
4  \draw (A-FF.pin 2) to[short, -*] (B-FF.pin 2)
5    -- (B-FF.pin 2 |- down) node[below]{CP};
6  \draw (B-FF.down) to[short, -*] ++(0,-0.3) coordinate(dd);
7  \draw (A-FF.down) -- ++(0,-.5) -- ++(1.5,0) |- (dd)
8    -- (dd |- down) node[below]{RESET};
```

Will create the final diagram:



3 The components

Components in CircuiTikZ come in two forms: path-style, to be used in to path specifications, and node-style, which will be instantiated by a node specification.

3.1 Path-style components

The path-style components are used as shown below:

- begin{circuitikz}
 draw (0,0) to[#1=#2, #options] (2,0);
- 3 \end{circuitikz}

where #1 is the name of the component, #2 is an (optional) label, and options are optional labels, annotations, style specifier that will be explained in the rest of the manual.

Transistors and some other node-style components can also be placed using the syntax for bipoles. See section 3.17.8.

Most path-style components can be used as a node-style components; to access them, you add a shape to the main name of component (for example, diodeshape). Such a "node name" is specified in the description of each component.

3.1.1 Anchors

Normally, path-style components do not need anchors, although they have them just in case you need them. You have the basic "geographical" anchors (bipoles are defined horizontally and then rotated as needed):



In the case of bipoles, also shortened geographical anchors exists. In the description, it will be shown when a bipole has additional anchors. To use the anchors, just give a name to the bipole element.

```
1\begin{circuitikz}
2 \draw (0,0) to[potentiometer, name=P, mirror] ++(0,2);
3 \draw (P.wiper) to[L] ++(2,0);
4\end{circuitikz}
```

Alternatively, that you can use the shape form, and then use the left and right anchors to do your connections.

```
1\begin{circuitikz}
2 \draw (0,0) node[potentiometershape, rotate=-90](P){};
3 \draw (P.wiper) to[L] ++(2,0);
4\end{circuitikz}
```

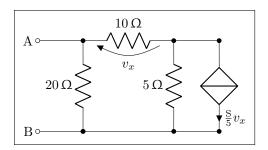
3.1.2 Customization

Pretty much all CircuiTikZ relies heavily on pgfkeys for value handling and configuration. Indeed, at the beginning of circuitikz.sty and in the file pfgcirc.define.tex a series of key definitions can be found that modify all the graphical characteristics of the package.

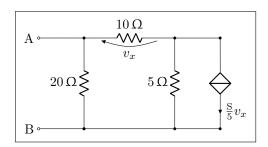
All can be varied using the \ctikzset command, anywhere in the code.

Note that the details of the parameters that are not described in the manual can change in the future, so be ready to use a fixed version of the package (the ones with the specific number, like circuitikz-0.9.3) if you dig into them.

3.1.2.1 Components size Perhaps the most important parameter is bipoles/length (default 1.4 cm), which can be interpreted as the length of a resistor (including reasonable connections): all other lengths are relative to this value. For instance:



```
1\ctikzset{bipoles/length=1.4cm}
2\begin{circuitikz}[scale=1.2]\draw
3  (0,0) node[anchor=east] {B}
4          to[short, o-*] (1,0)
5          to[R=20<\ohm>, *-*] (1,2)
6          to[R=10<\ohm>, v=$v_x$] (3,2) -- (4,2)
7          to[cI=$\frac{\si{\siemens}}{5} v_x$, *-*] (4,0) -- (3,0)
8          to[R=5<\ohm>, *-*] (3,2)
9  (3,0) -- (1,0)
10  (1,2) to[short, -o] (0,2) node[anchor=east]{A}
11;\end{circuitikz}
```



```
1\ctikzset{bipoles/length=.8cm}
2\begin{circuitikz}[scale=1.2]\draw
3  (0,0) node[anchor=east] {B}
4          to[short, o-*] (1,0)
5          to[R=20<\ohm>, *-*] (1,2)
6          to[R=10<\ohm>, v=$v_x$] (3,2) -- (4,2)
7          to[cI=$\frac{\siemens}{5} v_x$, *-*] (4,0) -- (3,0)
8          to[R=5<\ohm>, *-*] (3,2)
9  (3,0) -- (1,0)
10  (1,2) to[short, -o] (0,2) node[anchor=east]{A}
11;\end{circuitikz}
```

The changes on bipoles/length should, however, be globally applied to every path, because they affect every element — including the poles. So you can have artifacts like these:

```
1 \begin{circuitikz}[
2    bigR/.style={R, bipoles/length=3cm}
3    ]
4    \draw (0,3) to [bigR, o-o] ++(4,0);
5    \draw (0,1.5) to [bigR, o-o] ++(4,0);
6    to[R, o-o] ++(2,0); % will fail here
7    \draw (0,0) to [R, o-o] ++(4,0);
8 \end{circuitikz}
```

Several groups of components, on the other hand, have a special scale parameter that can be used safely in this case (starting with 0.9.4 — more groups of components will be added going forward); the key to use will be explained in the specific description of the components. For example, in the case of resistors you have resistors/scale available:

```
1\begin{circuitikz}[
2    bigR/.style={R, resistors/scale=1.8}
3    ]
4    \draw (0,3) to [bigR, o-o] ++(4,0);
5    \draw (0,1.5) to [bigR, o-o] ++(4,0);
6    to[R, o-o] ++(2,0); % ok now
7    \draw (0,0) to [R, o-o] ++(4,0);
8\end{circuitikz}
```

3.1.2.2 Thickness of the lines (globally)

The best way to alter the thickness of components is using styling, see section 3.3.3. Alternatively, you can use "legacy" classes like bipole, tripoles and so on — for example changing the parameter bipoles/thickness (default 2). The number is relative to the thickness of the normal lines leading to the component.

3.1.2.3 Shape of the components (on a per-component-class basis)

The shape of the components are adjustable with a lot of parameters; in this manual we will comment the main ones, but you can look into the source files specified above to find more.

It is recommended to use the styling parameters to change the shapes; they are not so fine grained (for example, you can change the width of resistor, not the height at the moment), but they are more stable and coherent across your circuit.

3.1.3 Descriptions

The typical entry in the component list will be like this:

```
resistor: resistor, american style, type: path-style ,
nodename: resistorshape.Aliases: R, american
resistor. Class: resistors.

pR: potentiometer, american style, type: path-style ,
nodename: potentiometershape.Aliases: pR, american
potentiometer. Class: resistors.
```

where you have all the needed information about the bipole, with also no-standard anchors. If the component can be filled it will be specified in the description. In addition, as an example, the component shown will be filled with the option fill=cyan!30!white:

```
ammeter: Ammeter, type: path-style, fillable, nodename: ammetershape. Class: instruments.
```

The Class of the component (see section 3.3) is printed at the end of the description.

3.2 Node-style components

Node-style components (monopoles, multipoles) can be drawn at a specified point with this syntax, where #1 is the name of the component:

```
1\begin{circuitikz}
2 \draw (0,0) node[#1,#2] (#3) {#4};
3\end{circuitikz}

Explanation of the parameters:
#1: component name<sup>4</sup> (mandatory)
#2: list of comma separated options (optional)
#3: name of an anchor (optional)
#4: text written to the text anchor of the component (optional)
```

Most path-style components can be used as a node-style components; to access them, you add a shape to the main name of component (for example, diodeshape). Such a "node name" is specified in the description of each component.

Notice: Nodes must have curly brackets at the end, even when empty. An optional anchor (#3) can be defined within round brackets to be addressed again later on. And please don't forget the semicolon to terminate the \draw command.

Also notice: If using the \tikzexternalize feature, as of Tikz 2.1 all pictures must end with \end{tikzpicture}. Thus you cannot use the circuitikz environment.

Which is ok: just use the environment tikzpicture: everything will work there just fine.

3.2.1 Mirroring and flipping

Mirroring and flipping of node components is obtained by using the TikZ keys xscale and yscale. Notice that this parameters affect also text labels, so they need to be un-scaled by hand.

To simplify this task, CircuiTikZ when used in IATEX has three helper macros — \ctikzflipx{}, \ctikzflipx{}, and \ctikzflipxy{}, that can be used to "un-rotate" the text of nodes drawn with, respectively, xscale=-1, yscale=-1, and scale=-1 (which is equivalent to xscale=-1, yscale=-1). In other formats they are undefined; contributions to fill the gap are welcome.

⁴For using bipoles as nodes, the name of the node is #1shape.

3.2.2 Anchors

Node components anchors are variable across the various kind of components, so they will described better after each category is presented in the manual.

3.2.3 Descriptions

The typical entry in the component list will be like this:



All the shapes defined by CircuiTikZ. These are all pgf nodes, so they are usable in both pgf and TikZ. If the component can be filled it will be specified in the description. In addition, as an example, the component shown will be filled with the option fill=cyan!30!white:



Sometime, components will expose internal (sub-)shapes that can be accessed with the syntax <node name>-<internal node name> (a dash is separating the node name and the internal node name); that will be shown in the description as a blue "anchor":

```
N-out 1.n

cout 1 out 1

cin o mid

in o mid

center N-out 4.w

Rotary switch, type: node (node[rotaryswitch](N){}).

Class: switches.
```

The Class of the component (see section 3.3) is printed at the end of the description.

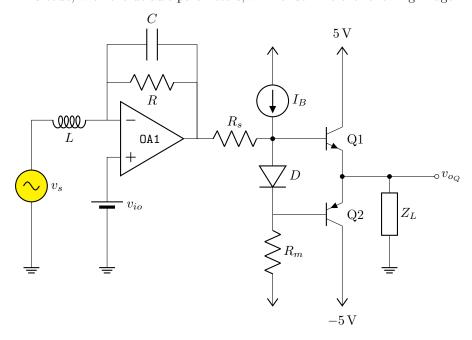
3.3 Styling circuits and components

You can change the visual appearance of a circuit by using a circuit style different from the default. For styling the circuit, the concept of *class* of a component is key: almost every component has a class, and a style change will affect all the components of that class.

Let's see the effect over a simple circuit⁵.

```
1 \def\killdepth#1{{\raisebox{Opt}}{\height}[Opt]{#1}}}
2 \newcommand\bjtname[1]{($(#1.C)!0.5!(#1.E)$) node[anchor=west]{\killdepth{#1}}} }
3 \begin{circuitikz}[american, cute inductors]
4  \node [op amp](A1){\texttt{OA1}};
5  \draw (A1.-) to[short] ++(0,1) coordinate(tmp) to[R, l_=$R$] (tmp -| A1.out) to[short] (A1.out);
6  \draw (tmp) to[short] ++(0,1) coordinate(tmp) to[C=$C$] (tmp -| A1.out) to[short] (A1.out);
7  \draw (A1.+) to [battery2, invert] ++(0,-2.5) node[ground](GND){};
8  \draw (A1.-) to [L=$L$] ++(-2,0) coordinate(tmp) to[sV, l=$v_s$, fill=yellow] (tmp |-GND) node[ground]{};
9  \draw (A1.out) to[R=$R_s$] ++(2,0) coordinate(bb) to[I, l_=$I_B$, invert] ++(0,2) node[vcc](VCC){};
10  \draw (bb) to[D, l=$D$, *-] ++(0,-2) coordinate(bb) to[R=$R_m$] ++(0,-2) node[vec](VEE){};
11  \draw (bb) --++(1,0) node[npn, anchor=B](Q1){} \bjtname{Q1};
12  \draw (bb) --++(1,0) node[npn, anchor=B](Q2){} \bjtname{Q2};
13  \draw (Q1.E) -- (Q2.E) ($(Q1.E)!0.5!(Q2.E)$) to [short, *-o, name=S] ++(2.5,0)
14  node[right]{$v_{0}};
15  \draw (S.s) to[european resistor, l=$Z_L$, *-] (S.s|-GND) node[ground]{};
16  \draw (Q1.C) -- (Q1.C|-VCC) node[vcc]{\SI{5}{V}};
17  \draw (Q2.C) -- (Q2.C|-VEE) node[vce]{\SI{5}{V}};
18  \land{ergentiates}
```

This code, with the default parameters, will render like the following image.

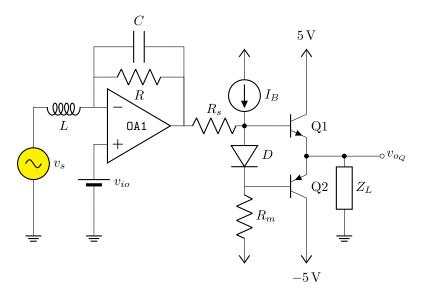


3.3.1 Relative size

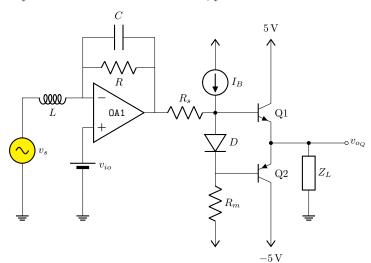
Component size can be changed globally (see section 3.1.2.1), or you can change their relative size by scaling a family of components by setting the key class/scale; for example, you can change the size of all the diodes in your circuit by setting diodes/scale to something different from the default 1.0.

Remember that if you use a global scale (be sure to read section 1.7!) you change the coordinate only, so using scale=0.8 in the environment options you have:

⁵This is a just an example, the circuit is not intended to be functional.



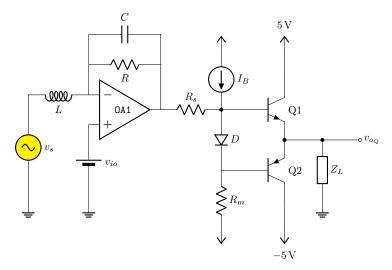
If you want to scale all the circuit, you have to use also transform shape:



Using relative sizes as described in section 3.1.2.1 enables your style for the circuit. For example, setting:

ctikzset{resistors/scale=0.8, % smaller R
capacitors/scale=0.7, % even smaller C
diodes/scale=0.6, % small diodes
transistors/scale=1.3} % bigger BJTs

Will result in a (much more readable in Romano's opinion) circuit:



Warning: relative scaling is meant to work for a reasonable range of stretching and shortening, so try to keep your scale parameter in the 0.5 to 2.0 range (more or less). Bigger or smaller value can result in awkward shapes.

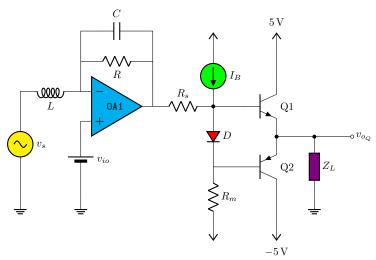
3.3.2 Fill color

You can also set a default fill color for the components. You can use the keys <code>class/fill</code> (which defaults to <code>none</code>, no fill, i.e. transparent component) for all fillable components in the library.

If you add to the previous styles the following commands:

```
1\ctikzset{
2 amplifiers/fill=cyan,
3 sources/fill=green,
4 diodes/fill=red,
5 resistors/fill=violet,
6}
```

you will have the following circuit (note that the first generator is *explicitly* set to be yellow, so if will not be colored green!):



Please use this option with caution. Although two-color circuits can be nice, using more than that can become rapidly unbearable. Old textbooks used the two-color style quite extensively, filling

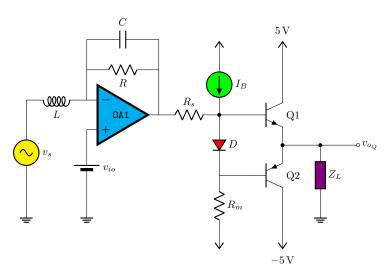
with a kind of light blue like $\verb|blue!30!|$ white "closed" components, but that was largely to hinder black-and-white photocopying...

3.3.3 Line thickness

You can change the line thickness for any class of component in an independent way. The default standard thickness of components is defined on a loose "legacy" category (like bipoles, tripoles and so on, see section 3.1.2.2); to override that you set the key class/thickness to any number. The default is none, which means that the old way of selecting thickness is used.

For example, *amplifiers* have the legacy class of tripoles, as well as transistors and tubes. By default they are drawn with thickness 2 (relative to the base linewidth). To change them to be thicker, you can for example add to the previous style

\ctikzset{amplifier/thickness=4}



Caveat: not every component has a "class", so you have to play with the available ones (it's specified in the component description) and with the absolute values to have the circuit following your taste. A bit of experimentation will create a kind of *style options* that you could use in all your documents.

3.3.4 Style files

When using styles, it is possible to use *style files* (see section 3.3.5), that then you can load with the command \ctikzloadstyle. For example, in the distribution you have a number of style files: legacy, romano, example. When you load a style name name, you will have available a style called name circuit style that you can apply to your circuits. The last style loaded is not enacted — you have to explicitly do it if you want the style used by default, by putting for example in the preamble:

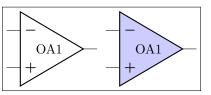
```
\ctikzloadstyle{romano}
\tikzset{romano circuit style}
```

Please notice that the style is at TikZ level, not CircuiTikZ—that let's you use it in the top option of the circuit, like:

```
\begin{circuitikz}[legacy circuit style,
..., ]
...
\end{circuitikz}
```

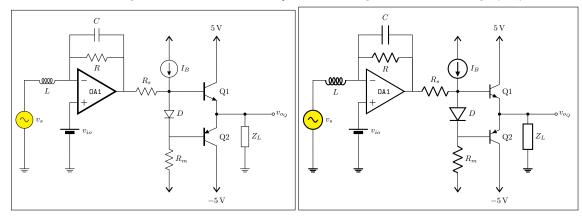
If you just want to use one style, you can load and activate it in one command with \ctikzsetstyle{romano}

The example style file will simply make the amplifiers filled with light blue:



```
1\begin{circuitikz}
2 \draw (0,0) node[op amp]{OA1};
3\end{circuitikz}
4\ctikzloadstyle{example}
5\begin{circuitikz}[example circuit style]
6 \draw (0,0) node[op amp]{OA1};
7\end{circuitikz}
```

The styles legacy is a style that set (most) of the style parameters to the default, and romano is a style used by one of the authors; you can use these styles as is or you can use them to learn to how to write new file style following the instructions in section 3.3.5. In the next diagrams, the left hand one is using the romano circuit style and the right hand one the legacy style.



3.3.5 Style files: how to write them

The best option is to start from ctikzstyle-legacy.tex and edit your style file from it. Then you just put it in your input path and that's all. If you want, you can contribute your style file to the project.

Basically, to write the style example, you edit a file named ctikzstyle-romano.tex with will define and enact TikZ style with name example circuit style; basically it has to be something along this:

```
1% example style for circuits
2% Do not use LaTeX commands if you want it to be compatible with ConTeXt
3% Do not add spurious spaces
4\tikzset{example circuit style/.style={%}
5 \circuitikzbasekey/.cd,%
6 amplifiers/fill=blue!20!white,
7},% end .style
8}% end \tikzset
9%
10 \endinput
```

This kind of style will add to the existing style. If you want to have a style that substitute the current style, you should do like this:

```
1\ctikzloadstyle{legacy}% start from a know state
2\tikzset{romano circuit style/.style={%
3legacy circuit style, % load the legacy style
4\circuitikzbasekey/.cd,%
5% Resistors
6resistors/scale=0.8,
7[...]
```

3.4 Grounds and supply voltages

3.4.1 Grounds

For the grounds, the center anchor is put on the connecting point of the symbol, so that you can use them directly in a path specification.

center =	Ground, type: node (node[ground] {}). Class: grounds.
e-center	Tailless ground, type: node (node[tlground]{}). Class: grounds.
上	Reference ground, type: node (node[rground]{}). Class: grounds.
\bigvee	Signal ground, type: node, fillable (node[sground]{}). Class: grounds.
_	Thicker tailless reference ground, type: node (node[tground]{}). Class: grounds.
	Noiseless ground, type: node (node[nground]{}). Class: grounds.
	Protective ground, type: node, fillable (node[pground]{}). Class: grounds.
	Chassis ground ⁶ , type: node (node[cground]{}). Class: grounds.
1111	European style ground, type: node (node[eground]{}). Class: grounds.
111	European style ground, version 2 ⁷ , type: node (node[eground2]{}). Class: grounds.

 $^{^6\}mathrm{These}$ last three were contributed by Luigi «Liverpool»

⁷These last two were contributed by **@fotesan**

3.4.1.1 Grounds anchors Anchors for grounds are a bit strange, given that they have the center spot at the same location than north and all the ground will develop "going down":



3.4.1.2 Grounds customization You can change the scale of these components (all the ground symbols together) by setting the key grounds/scale (default 1.0).

3.4.2 Power supplies

\uparrow	VCC/VDD, type: node (node[vcc]{}). Class: power supplies.
\downarrow	VEE/VSS, type: node (node[vee]{}). Class: power supplies.

The power supplies are normally drawn with the arrows shown in the list above.

3.4.2.1 Power supply anchors They are similar to grounds anchors, and the geographical anchors are correct only for the default arrow.

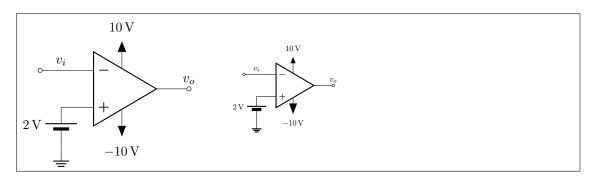


3.4.2.2 Power supplies customization You can change the scale of the power supplies by setting the key power supplies/scale (default 1.0).

Given that the power supply symbols are basically arrows, you can change them using all the options of the arrows.meta package (see the TikZ manual for details) by changing the keys monopoles/vcc/arrow and monopoles/vee/arrow (the default for both is legacy, which will use the old code for drawing them). Note that the anchors are at the start of the connecting lines, and that geographical anchors are just approximation if you change the arrow symbol!

However, arrows in TikZ are in the same class with the line thickness, so they do not scale with neither the class power supplies scale nor the global scale parameter (you should use transform canvas={scale...} for this).

If you want that the arrows behave like the legacy symbols (which are shapes), only in the arrow definitions, you can use the special length parameter \scaledwidth⁸ in the arrow definition, which correspond to the width of the legacy vcc or vee. Compare the effects on the following circuit.



```
1\ctikzset{%
                   monopoles/vcc/arrow={Triangle[width=0.8*\scaledwidth, length=\scaledwidth]},
                   monopoles/vee/arrow={Triangle[width=6pt, length=8pt]},
  4 }
  5 \begin{circuitikz}[baseline=(vo.center)]
                    \node [ocirc](TW) at (0,0) {};
                    \draw (TW.east) -- ++(1,0) node[midway, above]{\$v_i\$} node[op amp, anchor=-](A1){};
                    \frac{A1.up}{--} ++(0, 0.3) \text{ node}[vcc]{SI}{+10}{V};
                    \draw (A1.down) -- ++(0,-0.3) node[vee]{\SI{-10}{V}};
                    \frac{A1.+}{-0.5,0} to [battery2, invert, 1_=\SI{2}{V}] ++(0,-1) node [ground \draw (A1.+) -- ++(-0.5,0) to [battery2, invert, 1_=\SI\{2}\{V\}] ++(0,-1)
                                   ]{};
                    \draw (A1.out) to[short, -o] ++(0.5,0) node[above](vo){$v_o$};
12 \end{circuitikz} \qquad
13 \begin{circuitikz}[baseline=(vo.center), scale=0.6, transform shape]
                    \node [ocirc](TW) at (0,0) {};
14
                    \label{lem:condition} $$ \operatorname{TW.east} -- ++(1,0) \ \operatorname{node[midway, above]} {\v_i$} \ \operatorname{node[op amp, anchor=-](A1)};
15
                    \frac{A1.up}{--} ++(0, 0.3) \text{ node}[vcc]{SI}{+10}{V};
16
                    \draw (A1.down) -- ++(0,-0.3) node[vee]{\SI{-10}{V}};
 17
                    \frac{1}{-1} = \frac{1}{-1} + (-0.5,0) + \frac{1}{-1} = \frac{1}{-1} \frac{1}{-1} 
                                   ]{};
                    \draw (A1.out) to[short, -o] ++(0.5,0) node[above](vo){$v_o$};
20 \end{circuitikz}
```

3.5 Resistive bipoles

```
short: Short circuit, type: path-style , nodename: shortshape. Class: default.

open: Open circuit, type: path-style , nodename: openshape. Class: default.

generic: Generic (symmetric) bipole, type: path-style, fillable , nodename: genericshape. Class: resistors.
```

 $^{^8{\}rm Thanks}$ to @Schrödinger's cat on TEX stack exchange site

	<pre>xgeneric: Crossed generic (symmetric) bipole, type: path-style, fillable , nodename: xgenericshape. Class: resistors.</pre>
	tgeneric: Tunable generic bipole, type: path-style, fillable, nodename: tgenericshape. Class: resistors.
	ageneric: Generic asymmetric bipole, type: path-style, fillable, nodename: agenericshape. Class: resistors.
<u> </u>	memristor: Memristor, type: path-style, fillable, nodename: memristorshape. Aliases: Mr. Class: resistors.

If americanresistors option is active (or the style [american resistors] is used; this is the default for the package), the resistors are displayed as follows:

	R: Resistor, type: path-style , nodename: resistorshape. Aliases: american resistor. Class: resistors.
_ \ _	vR: Variable resistor, type: path-style , nodename: vresistorshape. Aliases: variable american resistor. Class: resistors.
wiper — wiper	pR: Potentiometer, type: path-style , nodename: potentiometershape. Aliases: american potentiometer. Class: resistors.
——————————————————————————————————————	sR: Resisitive sensor, type: path-style , nodename: resistivesensshape. Aliases: american resisitive sensor. Class: resistors.

If instead europeanresistors option is active (or the style [european resistors] is used), the resistors, variable resistors and potentiometers are displayed as follows:

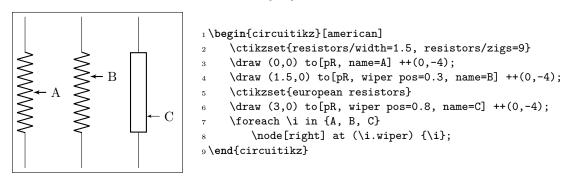
	R: Resistor, type: path-style, fillable, nodename: genericshape. Aliases: european resistor. Class: resistors.
	vR: Variable resistor, type: path-style, fillable, nodename: tgenericshape. Aliases: variable european resistor. Class: resistors.
ç—wiper ————	pR: Potentiometer, type: path-style, fillable, nodename: genericpotentiometershape. Aliases: european potentiometer. Class: resistors.
label	sR: Resistive sensor, type: path-style, fillable, nodename: thermistorshape. Aliases: european resistive sensor. Class: resistors.

Other miscellaneous resistor-like devices:

U	varistor: Varistor, type: path-style, fillable, nodename: varistorshape. Class: resistors.
	phR: Photoresistor, type: path-style, fillable, nodename: photoresistorshape. Aliases: photoresistor. Class: resistors.
	thR: Thermistor, type: path-style, fillable, nodename: thermistorshape. Aliases: thermistor. Class: resistors.
₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹ ₹	thRp: PTC thermistor, type: path-style, fillable, nodename: thermistorptcshape. Aliases: thermistorptc. Class: resistors.
- DAY	thRn: NTC thermistor, type: path-style, fillable, nodename: thermistorntcshape. Aliases: thermistorntc. Class: resistors.

3.5.1 Potentiometers: wiper position

Since version 0.9.5, you can control the position of the wiper in potentiometers using the key wiper pos, which is a number in the range [0,1]. The default middle position is wiper pos=0.5.



3.5.2 Generic sensors anchors

Generic sensors have an extra anchor named label to help position the type of dependence, if needed:

The anchor is positioned just on the corner of the segmented line crossing the component.

3.5.3 Resistive components customization

You can change the scale of these components (all the resistive bipoles together) by setting the key resistors/scale (default 1.0). Similarly, you can change the widths by setting resistors/width (default 0.8).

You can change the width of these components (all the resistive bipoles together) by setting the key resistors/width to something different from the default 0.8.

For the american style resistors, you can change the number of "zig-zags" by setting the key resistors/zigs (default value 3).

```
| The second control of the control
```

3.6 Capacitors and inductors: dynamical bipoles

3.6.1 Capacitors

	capacitor: Capacitor, type: path-style , nodename: capacitorshape.Aliases: C. Class: capacitors.
	curved capacitor: Curved (polarized) capacitor, type: path-style , nodename: ccapacitorshape.Aliases: cC. Class: capacitors.
+	ecapacitor: Electrolytic capacitor, type: path-style, fillable, nodename: ecapacitorshape. Aliases: eC,elko. Class: capacitors.
	variable capacitor: Variable capacitor, type: path-style , nodename: vcapacitorshape.Aliases: vC. Class: capacitors.
label	capacitive sensor: Capacitive sensor, type: path-style , nodename: capacitivesensshape.Aliases: sC. Class: capacitors.
	piezoelectric: Piezoelectric Element, type: path-style, fillable, nodename: piezoelectricshape. Aliases: PZ. Class: capacitors.

There is also the (deprecated — its polarity is not coherent with the rest of the components) polar capacitor:

⁹Thanks to Anshul Singhv for noticing.

		polar capacitor: Polar capacitor, type: path-style, nodename: pcapacitorshape. Aliases: pC. Class: capacitors.
--	--	--

3.6.2 Capacitive sensors anchors

For capacitive sensors, see section 3.5.2.

3.6.3 Capacitors customizations

You can change the scale of the capacitors by setting the key capacitors/scale to something different from the default 1.0.

3.6.4 Inductors

If the cuteinductors option is active (default behaviour), or the style [cute inductors] is used, the inductors are displayed as follows:

midtap	L: Inductor, type: path-style , nodename: cuteinductorshape. Aliases: cute inductor. Class: inductors.
	cute choke: Choke, type: path-style , nodename: cutechokeshape. Class: inductors.
	vL: Variable inductor, type: path-style , nodename: vcuteinductorshape. Aliases: variable cute inductor. Class: inductors.
——————————————————————————————————————	<pre>sL: Inductive sensor, type: path-style , nodename: scuteinductorshape. Aliases: cute inductive sensor. Class: inductors.</pre>

If the americaninductors option is active (or the style [american inductors] is used), the inductors are displayed as follows:

midtap 	L: Inductor, type: path-style , nodename: americaninductorshape. Aliases: american inductor. Class: inductors.
	vL: Variable inductor, type: path-style , nodename: vamericaninductorshape. Aliases: variable american inductor. Class: inductors.
label	sL: Inductive sensor, type: path-style , nodename: samericaninductorshape. Aliases: american inductive sensor. Class: inductors.

Finally, if the europeaninductors option is active (or the style [european inductors] is used), the inductors are displayed as follows:

midtap	L: Inductor, type: path-style , nodename: fullgenericshape. Aliases: european inductor. Class: inductors.
	vL: Variable inductor, type: path-style , nodename: tfullgenericshape.Aliases: variable european inductor. Class: inductors.
——label	sL: Inductive sensor, type: path-style , nodename: sfullgenericshape. Aliases: european inductive sensor. Class: inductors.

3.6.5 Inductors customizations

You can change the scale of the inductors by setting the key inductors/scale to something different from the default 1.0.

You can change the width of these components (all the inductors together, unless you use style or scoping) by setting the key inductors/width to something different from the default, which is 0.8 for american and european inductors, and 0.6 for cute inductors.

Moreover, you can change the number of "coils" drawn by setting the key inductors/coils (default value 5 for cute inductors and 4 for american ones). Notice that the minimum number of coils is 1 for american inductors, and 2 for cute ones.

Chokes (which comes only in the cute style) can have single and double lines, and can have the line thickness adjust (the value is relative to the thickness of the inductor).

```
1 \begin{circuitikz} [american]
2   \draw (0,0) to [cute choke] ++(3,0);
3   \draw (0,-1) to [cute choke, twolineschoke] ++(3,0);
4
5   \ctikzset{bipoles/cutechoke/cthick=2, twolineschoke}}
6
7   \draw (0,-2) to [cute choke] ++(3,0);
8   \draw (0,-3) to [cute choke, onelinechoke] ++(3,0);
9 \end{circuitikz}
```

3.6.6 Inductors anchors

For inductive sensors, see section 3.5.2.

Inductors have an additional anchor, called midtap, that connects to the center of the coil "wire". Notice that this anchor could be on one side or the other of the component, depending on the number of loops of the element; if you need a fixed position, you can use the geographical anchors.

1 \begin{circuitikz}[

11 \end{circuitikz}

```
loops/.style={circuitikz/inductors/coils=#1}]

3 \ctikzset{cute inductors}
4 \draw (0,2) to [L, loops=5, name=A] ++(2,0)
5 to [L, loops=6, name=B] ++(2,0);
6 \ctikzset{american inductors}
7 \draw (0,0) to [L, loops=5, name=C] ++(2,0);
8 to [L, loops=6, name=D] ++(2,0);
9 \foreach \i in {A, B, C, D}
10 \node[circle, fill=red, inner sep=1pt] at (\i.midtap){};
```

3.7 Diodes and such

There are three basic styles for diodes: empty (fillable in color), full (completely filled with the draw color) and stroke (empty, but with a line across them).

You can switch between the styles setting the key diode (for example \ctikzset{diode=full} or empty or stroke, or with the styles full diodes, empty diodes and stroke diodes.

To use the default element, simply use the name shown for the empty diodes without the final "o"—that is D, sD, and so on. The names shown in the following tables will draw the specified diode independently on the style chosen (that is, leD* is always a full LED diode).

The package options fulldiode, strokediode, and emptydiode (and the styles [full diodes], [stroke diodes], and [empty diodes]) define which shape will be used by abbreviated commands such that D, sD, zD, zzD, tD, pD, leD, VC, Ty,Tr (no stroke symbol available!).

<pre>empty diode: Empty diode, type: path-style, fillable , nodename: emptydiodeshape.Aliases: Do. Class: diodes.</pre>
empty Schottky diode: Empty Schottky diode, type: path-style, fillable, nodename: emptysdiodeshape.Aliases: sDo. Class: diodes.
empty Zener diode: Empty Zener diode, type: path-style, fillable, nodename: emptyzdiodeshape. Aliases: zDo. Class: diodes.
empty ZZener diode: Empty ZZener diode, type: path-style, fillable, nodename: emptyzzdiodeshape.Aliases: zzDo. Class: diodes.

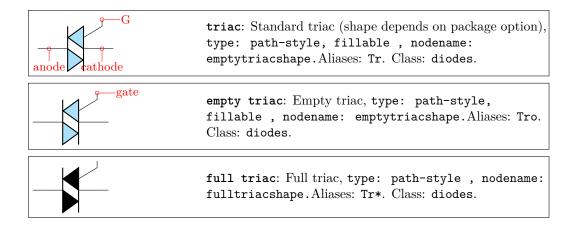
	empty tunnel diode: Empty tunnel diode, type: path-style, fillable, nodename: emptytdiodeshape. Aliases: tDo. Class: diodes.
	empty photodiode: Empty photodiode, type: path-style, fillable, nodename: emptypdiodeshape. Aliases: pDo. Class: diodes.
	<pre>empty led: Empty led, type: path-style, fillable , nodename: emptylediodeshape.Aliases: leDo. Class: diodes.</pre>
	<pre>empty varcap: Empty varcap, type: path-style, fillable , nodename: emptyvarcapshape.Aliases: VCo. Class: diodes.</pre>
	empty bidirectionaldiode: Empty bidirectionaldiode, type: path-style, fillable, nodename: emptybidirectionaldiodeshape. Aliases: biDo. Class: diodes.
	full diode: Full diode, type: path-style , nodename: fulldiodeshape.Aliases: D*. Class: diodes.
	full Schottky diode: Full Schottky diode, type: path-style , nodename: fullsdiodeshape.Aliases: sD*. Class: diodes.
	full Zener diode: Full Zener diode, type: path-style, nodename: fullzdiodeshape. Aliases: zD*. Class: diodes.
	full ZZener diode: Full ZZener diode, type: path-style , nodename: fullzzdiodeshape.Aliases: zzD*. Class: diodes.
-	<pre>full tunnel diode: Full tunnel diode, type: path-style , nodename: fulltdiodeshape.Aliases: tD*. Class: diodes.</pre>
	<pre>full photodiode: Full photodiode, type: path-style , nodename: fullpdiodeshape.Aliases: pD*. Class: diodes.</pre>
	full led: Full led, type: path-style , nodename: fulllediodeshape.Aliases: leD*. Class: diodes.
	full varcap: Full varcap, type: path-style , nodename: fullvarcapshape.Aliases: VC*. Class: diodes.
	full bidirectionaldiode: Full bidirectionaldiode, type: path-style , nodename: fullbidirectionaldiodeshape. Aliases: biD*. Class: diodes.

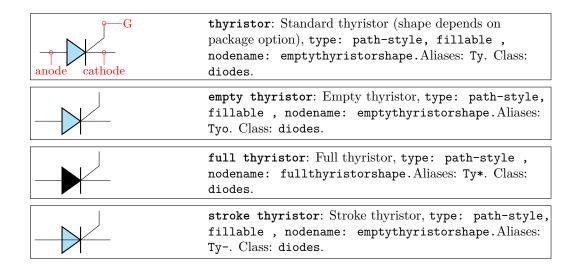
These shapes have no exact node-style counterpart, because the stroke line is built upon the empty variants:

stroke diode: Stroke diode, type: path-style, fillable, nodename: emptydiodeshape. Aliases: D Class: diodes.
stroke Schottky diode: Stroke Schottky diode, type: path-style, fillable, nodename: emptysdiodeshape. Aliases: sD Class: diodes.
stroke Zener diode: Stroke Zener diode, type: path-style, fillable, nodename: emptyzdiodeshape. Aliases: zD Class: diodes.
stroke ZZener diode: Stroke ZZener diode, type: path-style, fillable, nodename: emptyzzdiodeshape.Aliases: zzD Class: diodes.
stroke tunnel diode: Stroke tunnel diode, type: path-style, fillable, nodename: emptytdiodeshape.Aliases: tD Class: diodes.
stroke photodiode: Stroke photodiode, type: path-style, fillable, nodename: emptypdiodeshape.Aliases: pD Class: diodes.
stroke led: Stroke led, type: path-style, fillable, nodename: emptylediodeshape. Aliases: leD Class: diodes.
stroke varcap: Stroke varcap, type: path-style, fillable, nodename: emptyvarcapshape. Aliases: VC Class: diodes.

3.7.1 Tripole-like diodes

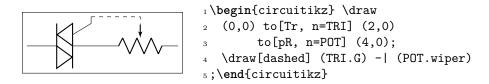
The following tripoles are entered with the usual command, of the form to [Tr, ...].





3.7.2 Triacs anchors

When inserting a thrystor, a triac or a potentiometer, one needs to refer to the third node-gate (gate or G) for the former two; wiper (wiper or W) for the latter one. This is done by giving a name to the bipole:



3.7.3 Diode customizations

You can change the scale of the diodes by setting the key diodes/scale to something different from the default 1.0. In Romano's opinion, diodes are somewhat big with the default style of the package, so a setting like \ctikzset{diode/scale=0.6} is recommended.

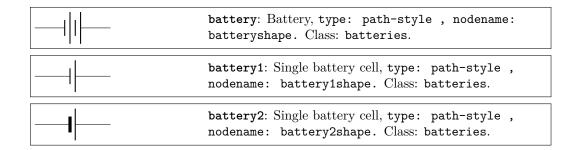
You can change the direction of the LEDs and photodiodes' arrows by using the binary keys led arrows from cathode and pd arrows to cathode (the default are led arrows from anode and pd arrows to anode), as you can see in the following example.

```
\begin{circuitikz}
        \ctikzset{led arrows from anode} % default
        \ctikzset{pd arrows to anode} % default
        \ctikzset{full diodes}
        \frac{0,0}{to[leD]} ++(1.5,0) to[pD] ++(1.5,0);
        \ctikzset{stroke diodes}
        \det (0,-1) \ to[leD] ++(1.5,0) \ to[pD] ++(1.5,0);
        \ctikzset{empty diodes}
        draw (0,-2) to[leD] ++(1.5,0) to[pD] ++(1.5,0);
10
        \ctikzset{led arrows from cathode}
11
        \ctikzset{pd arrows to cathode}
        \ctikzset{full diodes}
13
        draw (0,-4) to[leD] ++(1.5,0) to[pD] ++(1.5,0);
14
        \ctikzset{stroke diodes}
15
        draw (0,-5) to[leD] ++(1.5,0) to[pD] ++(1.5,0);
        \ctikzset{empty diodes}
17
        draw (0,-6) to[leD] ++(1.5,0) to[pD] ++(1.5,0);
     \end{circuitikz}
```

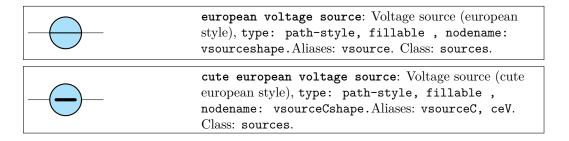
3.8 Sources and generators

Notice that source and generators are divided in three classes that can be styled independently: traditional battery symbols (class batteries), independent generators (class sources) and dependent generators (class csources). This is because they are often treated differently, and so you can choose to, for example, fill the dependent sources but not the independent ones.

3.8.1 Batteries



3.8.2 Stationary sources



	american voltage source: Voltage source (american style), type: path-style, fillable, nodename: vsourceAMshape.Aliases: vsourceAM. Class: sources.
	european current source: Current source (european style), type: path-style, fillable, nodename: isourceshape. Aliases: isource. Class: sources.
<u> </u>	cute european current source: Current source (cute european style), type: path-style, fillable, nodename: isourceCshape.Aliases: isourceC, ceI. Class: sources.
	american current source: Current source (american style), type: path-style, fillable, nodename: isourceAMshape.Aliases: isourceAM. Class: sources.

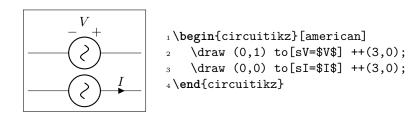
If (default behaviour) europeancurrents option is active (or the style [european currents] is used), the shorthands current source, isource, and I are equivalent to european current source. Otherwise, if americancurrents option is active (or the style [american currents] is used) they are equivalent to american current source.

Similarly, if (default behaviour) europeanvoltages option is active (or the style [european voltages] is used), the shorthands voltage source, vsource, and V are equivalent to european voltage source. Otherwise, if americanvoltages option is active (or the style [american voltages] is used) they are equivalent to american voltage source.

3.8.3 Sinusoidal sources

These two are basically the same symbol; to distinguish among them, you have to add a label, which will be a voltage or a current.

_ <u></u>	sinusoidal voltage source: Sinusoidal voltage source, type: path-style, fillable, nodename: vsourcesinshape. Aliases: vsourcesin, sV. Class: sources.
	sinusoidal current source: Sinusoidal current source, type: path-style, fillable, nodename: isourcesinshape. Aliases: isourcesin, sI. Class: sources.



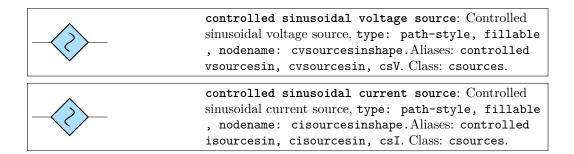
3.8.4 Controlled sources

	european controlled voltage source: Controlled voltage source (european style), type: path-style, fillable, nodename: cvsourceshape. Aliases: cvsource. Class: csources.
	cute european controlled voltage source: Voltage source (cute european style), type: path-style, fillable, nodename: cvsourceCshape.Aliases: cvsourceC, cceV. Class: csources.
	american controlled voltage source: Controlled voltage source (american style), type: path-style, fillable, nodename: cvsourceAMshape.Aliases: cvsourceAM. Class: csources.
	european controlled current source: Controlled current source (european style), type: path-style, fillable, nodename: cisourceshape. Aliases: cisource. Class: csources.
<u></u>	cute european controlled current source: Current source (cute european style), type: path-style, fillable, nodename: cisourceCshape.Aliases: cisourceC, cceI. Class: csources.
	american controlled current source: Controlled current source (american style), type: path-style, fillable, nodename: cisourceAMshape.Aliases: cisourceAM. Class: csources.
<u> </u>	empty controlled source: Empty controlled source, type: path-style, fillable, nodename: ecsourceshape. Aliases: ecsource. Class: csources.

If (default behaviour) europeancurrents option is active (or the style [european currents] is used), the shorthands controlled current source, cisource, and cI are equivalent to european controlled current source. Otherwise, if americancurrents option is active (or the style [american currents] is used) they are equivalent to american controlled current source.

Similarly, if (default behaviour) europeanvoltages option is active (or the style [european voltages] is used), the shorthands controlled voltage source, cvsource, and cV are equivalent to european controlled voltage source. Otherwise, if americanvoltages option is active (or the style [american voltages] is used) they are equivalent to american controlled voltage source.

The following two behave like the corresponding independent sources, see section 3.8.3.

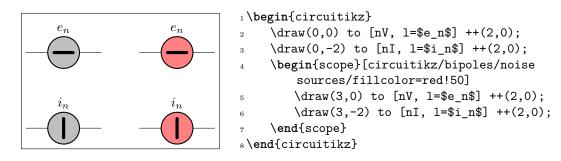


3.8.5 Noise sources

In this case, the "direction" of the source is undefined. Noise sources are filled in gray by default, but if you choose the dashed style, they become fillable.



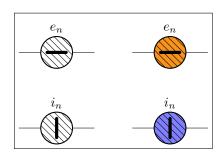
You can change the fill color with the key circuitikz/bipoles/noise sources/fillcolor:



If you prefer a patterned noise generator (similar to the one you draw by hand) you can use the fake color dashed:



Notice that if you choose the dashed style, the noise sources are fillable:



3.8.6 Special sources

	square voltage source: Square voltage source, type: path-style, fillable, nodename: vsourcesquareshape. Aliases: vsourcesquare, sqV. Class: sources.
_ <u>\</u>	vsourcetri: Triangle voltage source, type: path-style, fillable, nodename: vsourcetrishape. Aliases: tV. Class: sources.
	esource: Empty voltage source, type: path-style, fillable, nodename: esourceshape. Class: sources.
	<pre>pvsource: Photovoltaic-voltage source, type: path-style, fillable , nodename: pvsourceshape. Class: sources.</pre>
	ioosource: Double Zero style current source, type: path-style, fillable, nodename: oosourceshape. Class: sources.
<u> </u>	voosource: Double Zero style voltage source, type: path-style, fillable, nodename: oosourceshape. Class: sources.
<u> </u>	oosourcetrans: transformer source, type: path-style, fillable, nodename: oosourcetransshape. Class: sources.
prim1 sec3 sec2 sec1 right prim2 tert3 tert2	ooosource: transformer with three windings, type: path-style, fillable, nodename: ooosourceshape. Class: sources.

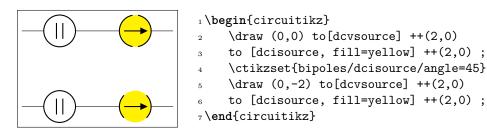
The transformershapes vector group options can be specified for the primary (prim=< value >), the secondary (sec=< value >) and tertiary (tert=< value >) three-phase vector groups: **delta**, **wye** and **zig**.

```
1\begin{circuitikz}
2   \draw (0,0) to[oosourcetrans,prim=zig,sec=delta,o-] ++(2,0)
3   to[oosourcetrans, prim=delta, sec=wye,-o] ++(0,-2)
4   to[oosource, prim=wye,sec=zig,tert=delta] (0,0);
5\end{circuitikz}
```

3.8.7 DC sources

<u>—————————————————————————————————————</u>	dcvsource: DC voltage source, type: path-style, fillable, nodename: dcvsourceshape. Class: sources.
_ 	dcisource: DC current source, type: path-style, fillable, nodename: dcisourceshape. Class: sources.

The size of the broken part of the DC current source is configurable by changing the value of bipoles/dcisource/angle (default 80); values must be between 0 (no circle at all, probably not useful) and 90 (full circle, again not useful).



3.8.8 Sources customizations

You can change the scale of the batteries by setting the key batteries/scale, for the controlled (dependent) sources with csources/scale, and for all the other independent sources and generators with sources/scale, to something different from the default 1.0.

The symbols drawn into the american voltage source¹⁰ can be changed by using the \ctikzset keys bipoles/vsourceam/inner plus and bipoles/vsourceam/inner minus (by default they are \$+\$ and \$-\$ respectively, in the current font), and move them nearer of farther away by twiddling bipoles/vsourceam/margin (default 0.7, less means nearer).

Moreover, you can move the two symbols nearer of farther away by twiddling bipoles/vsourceam/margin (default 0.7, less means nearer).

You can do the same with the american controlled voltage sources, substituting cvsourceam to vsourceam (notice the initial "c").

 $^{^{10}}$ Since version 1.1.0, thanks to the suggestions and discussion in this TeX.SX question.

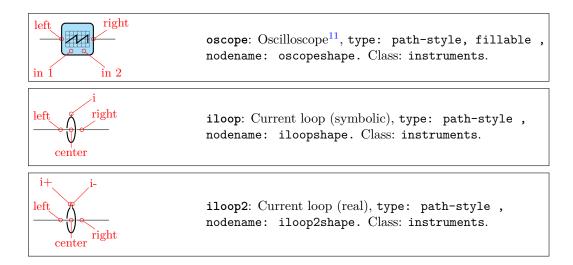


1 \begin{circuitikz}[american]

- ctikzset{bipoles/vsourceam/inner plus={\tiny \$+\$}}
- ctikzset{bipoles/vsourceam/inner minus={\tiny \$-\$}}
- $_{4}$ \draw (0,0) to[V, 1_=\$V\$] ++(0,3)
- 5 to[R=\SI{5}{\ohm}] ++(3,0)
- 6 to[V, invert,
- pipoles/vsourceam/inner plus={\color{red}\tiny \$\oplus\$},
- 9 bipoles/vsourceam/margin=0.5]
- 10 ++(0,-3) to[short, -*] (0,0) node[ground]{};
- $_{11} \setminus \texttt{end} \{ \texttt{circuitikz} \}$

3.9 Instruments

	ammeter: Ammeter, type: path-style, fillable, nodename: ammetershape. Class: instruments.
	<pre>voltmeter: Voltmeter, type: path-style, fillable , nodename: voltmetershape. Class: instruments.</pre>
<u></u>	ohmmeter: Ohmmeter, type: path-style, fillable, nodename: ohmmetershape. Class: instruments.
left right center	<pre>rmeter: Round meter (use t= for the symbol), type: path-style, fillable , nodename: rmetershape. Class: instruments.</pre>
left right center	<pre>rmeterwa: Round meter with arrow (use t= for the symbol), type: path-style, fillable, nodename: rmeterwashape. Class: instruments.</pre>
left right in 2	<pre>smeter: Square meter (use t= for the symbol), type: path-style, fillable , nodename: smetershape. Class: instruments.</pre>
left right	qiprobe: QUCS-style current probe, type: path-style, fillable, nodename: qiprobeshape. Class: instruments.
left right center	<pre>qvprobe: QUCS-style voltage probe, type: path-style, fillable , nodename: qvprobeshape. Class: instruments.</pre>
left right v+ v-	<pre>qpprobe: QUCS-style power probe, type: path-style, fillable , nodename: qpprobeshape. Class: instruments.</pre>



3.9.1 Instruments customizations

You can change the scale of all the instruments (including the current loops) by setting the key instruments/scale to something different from the default 1.0.

3.9.2 Rotation-invariant elements

The oscope element will not rotate the "graph" shown with the component:



The rmeter, rmaterwa, and smeter have the same behavior.

However, if you prefer that the oscope, rmeter, smeter and rmeterwa instruments rotate the text or the diagram, you can use the key or style rotated instruments (the default style is straight instruments).

¹¹Suggested by @nobrl on GitHub

```
\begin{circuitikz}[scale=0.8, transform shape]
     \verb|\ctikzset{rotated instruments}| \% \textit{ new default}|
2
     \draw (0,0) to[oscope] ++(0:3);
3
     \draw (0,0) to[oscope] ++(60:3);
4
     \draw (0,0) to[rmeter, t=A] ++(120:3);
5
     % local override
6
     \draw (0,0) to[rmeterwa, t=A, straight instruments]
           ++(180:3);
     \ctikzset{straight instruments} % back to default
     \draw (0,0) to[rmeterwa, t=A] ++(240:3);
10
     % local override
     \draw (0,0) to[smeter, t=A, rotated instruments]
11
         ++(300:3);
12 \end{circuitikz}
```

3.9.3 Instruments as node elements

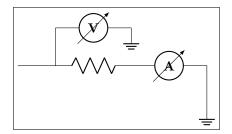
The node-style usage of the oscope is also interesting, using the additional in 1 and in 2 anchors; notice that in this case you can use the text content of the node to put labels above it. Moreover, you can change the size of the oscilloscope by changing bipoles/oscope/width and bipoles/oscope/height keys (which both default to 0.6).

3.9.4 Measuring voltage and currents, multiple ways

This is the classical (legacy) option, with the voltmeter and ammeter. The problem is that elements are intrinsically horizontal, so they look funny if put in vertically.

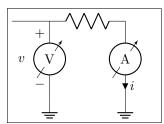
```
1\begin{circuitikz}
2 \draw (0,0) -- ++(1,0) to[R] ++(2,0)
3 to [ammeter] ++(0,-2) node[ground]{};
4 \draw (1,0) to[voltmeter] ++(0,-2)
5 node[ground]{};
6\end{circuitikz}
```

So the solution is often changing the structure to keep the meters in horizontal position.



```
1\begin{circuitikz}
2   \draw (0,0) -- ++(1,0) to[R] ++(2,0)
3   to [ammeter] ++(2,0) --
4   ++(0,-1) node[ground]{};
5   \draw (1,0) -- (1,1) to[voltmeter]
6   ++(2,0) node[ground]{};
7\end{circuitikz}
```

Since version 0.9.0 you have more options for the measuring instruments. You can use the generic rmeterwa (round meter with arrow), to which you can specify the internal symbol with the option t=... (and is fillable).



```
begin{circuitikz}[american]
draw (0,0) -- ++(1,0) to [R] ++(2,0)
to [rmeterwa, t=A, i=$i$] ++(0,-2) node [ground] {};
draw (1,0) to [rmeterwa, t=V, v=$v$] ++(0,-2)
node [ground] {};
e\end{circuitikz}
```

This kind of component will keep the symbol horizontal, whatever the orientation:



```
begin{circuitikz}[american]
draw (0,0) -- ++(1,0) to[R] ++(2,0)

to [rmeterwa, t=A, i=$i$] ++(2,0) --
++(0,-1) node[ground]{};
draw (1,0) -- (1,1) to[rmeterwa, t=V, v^=$v $]
++(2,0) node[ground]{};
\[ \text{end}{circuitikz} \]
```

The plain rmeter is the same, without the measuring arrow:



```
1 \begin{circuitikz} [american]
2    \draw (0,0) -- ++(1,0) to [R] ++(2,0)
3    to [rmeter, t=A, i=$i$] ++(0,-2) node [ground] {};
4    \draw (1,0) to [rmeter, t=V, v=$v$] ++(0,-2)
5    node [ground] {};
6 \end{circuitikz}
```

If you prefer it, you have the option to use square meters, in order to have more visual difference from generators:

```
1\begin{circuitikz}[american]
2   \draw (0,0) -- ++(1,0) to[R] ++(2,0)
3   to [smeter, t=A, i=$i$] ++(0,-2) node[ground]{};
4   \draw (1,0) to[smeter, t=V, v=$v$] ++(0,-2)
5   node[ground]{};
6\end{circuitikz}
```

Another possibility is to use QUCS¹²-style probes, which have the nice property of explictly showing the type of connection (in series or parallel) of the meter:



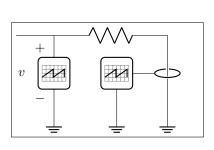
```
1 \begin{circuitikz} [american]
2     \draw (0,0) -- ++(1,0) to [R] ++(2,0)
3     to [qiprobe, l=$i$] ++(0,-2) node [ground] {};
4     \draw (1,0) to [qvprobe, l=$v$] ++(0,-2)
5     node [ground] {};
6 \end{circuitikz}
```

If you want to explicitly show a power measurement, you can use the power probe qpprobe and using the additional anchors v+ and v-:



```
1 \begin{circuitikz} [american]
2    \draw (0,0) to[short,-*] ++(1,0) coordinate(b)
3    to[R] ++(2,0) to [qpprobe, l=$i$, a=$v$, name=P]
4    ++(0,-2.5) node[ground] (GND){};
5    \draw (P.v-) -| ++(-0.5,-1) coordinate(a)
6    to [short, -*] (a-|GND);
7    \draw (P.v+) -| (b);
8 \end{circuitikz}
```

The final possibility is to use oscilloscopes. For example:



Or, if you want a more physical structure for the measurement setup:

¹²QUCS is an open source circuit simulator: http://qucs.sourceforge.net/

```
1\begin{circuitikz} [american]
2    \draw (0,0) -- ++(1,0) to [R] ++(3,0) to [iloop2, name=I] ++(0,-2)
3    node[ground] (GND){};
4    \ctikzset{bipoles/oscope/width=1.6}\ctikzset{bipoles/oscope/height=1.2}
5    \node [oscopeshape, fill=green!10](0) at (6,2){};
6    \node [bnc, xscale=-1, anchor=zero](bnc1) at (0.in 1){};
7    \node [bnc, , anchor=zero, rotate=-90](bnc2) at (0.in 2){};
8    \draw [-latexslim] (bnc1.hot) -| (1,0);
9    \draw (bnc2.hot) |- (I.i+);
10    \draw (I.i-) node[ground, scale=0.5]{};
11\end{circuitikz}
```



3.10 Mechanical Analogy

	damper: Mechanical Damping, type: path-style, fillable, nodename: dampershape. Class:
	mechanicals.
	<pre>inerter: Mechanical Inerter, type: path-style, fillable , nodename: inertershape. Class: mechanicals.</pre>
	<pre>spring: Mechanical Stiffness, type: path-style , nodename: springshape. Class: mechanicals.</pre>
left right center	<pre>viscoe: Mechanical viscoelastic element¹³, type: path-style, fillable , nodename: viscoeshape. Class: mechanicals.</pre>
	mass: Mechanical Mass, type: path-style, fillable, nodename: massshape. Class: mechanicals.

3.10.1 Mechanical elements customizations

You can change the scale of all the mechanical elements by setting the key mechanicals/scale to something different from the default 1.0.

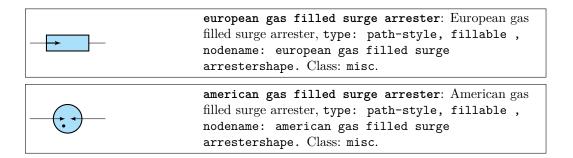
¹³Suggested by @Alex in https://tex.stackexchange.com/q/484268/38080

3.11 Miscellaneous bipoles

Here you'll find bipoles that are not easily grouped in the categories above.

	thermocouple: Thermocouple, type: path-style , nodename: thermocoupleshape. Class: misc.
	<pre>fuse: Fuse, type: path-style, fillable , nodename: fuseshape. Class: misc.</pre>
	afuse: Asymmetric fuse, type: path-style, fillable, nodename: afuseshape. Aliases: asymmetric fuse. Class: misc.
_ _	squid: Squid, type: path-style , nodename: squidshape. Class: misc.
_ -	barrier: Barrier, type: path-style , nodename: barriershape. Class: misc.
X	openbarrier: Open barrier, type: path-style , nodename: openbarriershape. Class: misc.

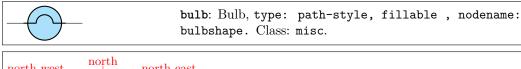
You can tune how big is the gap in the openbarrier component by setting the key bipoles/openbarrier/gap (default value 0.5; 0 means no gap and 1 full gap).



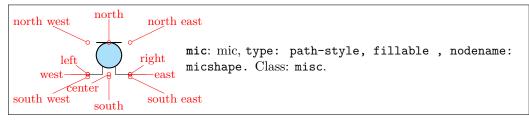
If (default behaviour) europeangfsurgearrester option is active (or the style [european gas filled surge arrester] is used), the shorthands gas filled surge arrester and gf surge arrester are equivalent to the european version of the component.

If otherwise americangfsurgearrester option is active (or the style [american gas filled surge arrester] is used), the shorthands the shorthands gas filled surge arrester and gf surge arrester are equivalent to the american version of the component.

lamp: Lamp, type: path-style, fillable, nodename: lampshape. Class: misc.	
---	--







You can use microphones and loudspeakers with waves (see section 3.19) too:

```
| \begin{circuitikz}
| \draw (0,0) to [mic, name=M] ++(0,2) |
| to [amp, t=$A$] ++(2,0) |
| to [loudspeaker, name=L] ++(0,-2) |
| to [short, -*] (0,0) node [ground] {}; |
| \node [waves, scale=0.7, left=5pt] |
| at(M.north) {}; |
| s \ \node [waves, scale=0.7, right] |
| at(L.north) {}; |
| \end{circuitikz}
```

3.11.1 Miscellanous element customization

You can change the scale of all the miscellaneous elements by setting the key misc/scale to something different from the default 1.0.

3.12 Multiple wires (buses)

This are simple drawings to indicate multiple wires.

 multiwire: Single line multiple wires, type: path-style , nodename: multiwireshape. Aliases: multiwire. Class: default.
 bmultiwire: Double line multiple wires, type: path-style , nodename: bmultiwireshape.Aliases: bmultiwire. Class: default.
 tmultiwire: Triple line multiple wires 14, type: path-style, nodename: tmultiwireshape. Aliases: tmultiwire. Class: default.

¹⁴added by olfline

3.13 Crossings

Path style:

```
crossing: Jumper style non-contact crossing, type:
path-style , nodename: crossingshape.Aliases:
xing. Class: default.
```

Node style:

```
Jumper-style crossing node, type: node (node[jump crossing] {}). No class.

+ Plain style crossing node, type: node (node[plain crossing] {}). No class.
```

All circuit-drawing standards agree that to show a crossing without electric contact, a simple crossing of the wires suffices; the electrical contact must be explicitly marked with a filled dot.

```
1\begin{circuitikz}[]
2\draw(1,-1) to[short] (1,1)
3 (0,0) to[short] (2,0);
4\draw(4,-1) to[short] (4,1)
5 (3,0) to[short] (5,0)
6 (4,0) node[circ]{};
7\end{circuitikz}
```

However, sometime it is advisable to mark the non-contact situation more explicitly. To this end, you can use a path-style component called **crossing**:

```
1 \begin{circuitikz}[]
2 \draw(1,-1) to[short] (1,1) (0,0) to[crossing]
(2,0);
3 \draw(4,-1) to[short] (4,1) (3,0) to[short]
(5,0)
4 (4,0) node[circ]{};
5 \end{circuitikz}
```

That should suffice most of the time; the only problem is that the crossing jumper will be put in the center of the subpath where the to[crossing] is issued, so sometime a bit of trial and error is needed to position it.

For a more powerful (and elegant) way you can use the crossing nodes:



Notice that the plain crossing and the jump crossing have a small gap in the straight wire, to enhance the effect of crossing (as a kind of shadow).

The size of the crossing elements can be changed with the key bipoles/crossing/size (default 0.2).

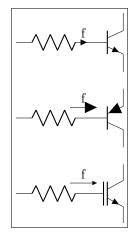
3.14 Arrows

These are pseudo-arrows used in lot of places in the packages (for transistors, flows, currents, and so on). The first three arrows are magnified by a factor 3 in the boxes below; for the trarrow, the anchor tip is exactly on the tip and btip is slightly receded.

-center	Arrow for current and voltage, type: node (node[currarrow]{}). No class.
-center	Arrow that is anchored at its tip, useful for block diagrams., type: node (node[inputarrow]{}). No class.
center tip btip	Arrow the same size of currarrow but only filled., type: node (node[trarrow]{}). No class.
$\begin{array}{c} I_p \text{ text} \\ \text{west} \longrightarrow -\text{east} \\ \text{center} \end{array}$	Arrow used for the flows, with a text anchor, type: node (node[flowarrow]{\$I_p\$}). No class.

3.14.1 Arrows size

You can use the parameter current arrow scale to change the size of the arrows in various components and indicators; the normal value is 16, higher numbers give smaller arrows and so on. You need to use circuitikz/current arrow scale if you use it into a node.



Moreover, you have the arrow tip latexslim which is an arrow similar to the old (in deprecated arrows library) latex' element:

```
1 \begin{circuitikz}[american,]
2 \draw [latexslim-latexslim] (0,0) -- (1,0);
3 \end{circuitikz}
```

3.15 Terminal shapes

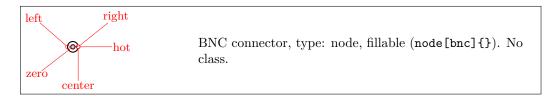
These are the so-called "bipole nodes" shapes, or poles (see section 5.1). These nodes are always filled; the "open" versions (starting with an o) are by default filled white, but you can override it with the fill parameter.

•	Connected terminal, type: node (node[circ]{}). No class.
0	Unconnected terminal, type: node (node[ocirc]{}). No class.
•	Diamond-square terminal, type: node (node[diamondpole]{}). No class.
\Diamond	Open diamond-square terminal, type: node (node[odiamondpole]{}). No class.
•	Square-shape terminal, type: node (node[squarepole]{}). No class.
	Open square-shape terminal, type: node (node[osquarepole]{}). No class.

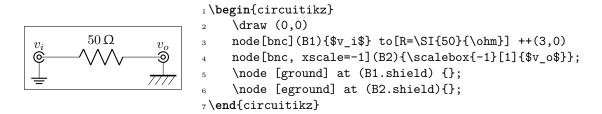
Since version 0.9.0, "bipole nodes" shapes have all the standard geographical anchors, so you can do things like these:

The size of the poles is controlled by the key nodes width (default 0.04, relative to the basic length). Be sure to see section 5.1 for more usage and configurability.

3.15.1 BNC connector/terminal



The BNC connector is defined so that you can easily connect it as input or output (but remember that you need to flip the text if you flip the component):

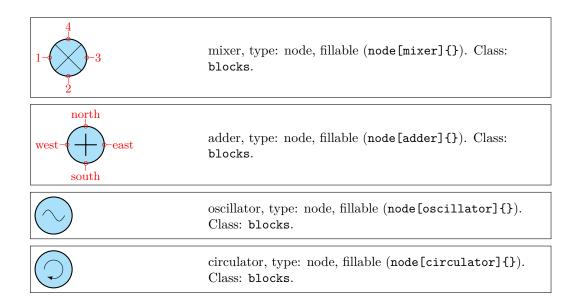


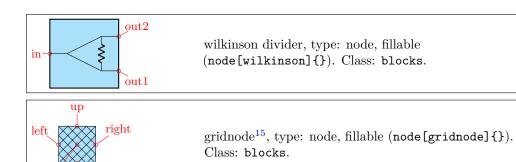
It also has a zero anchor if you need to rotate it about its real center.



3.16 Block diagram components

Contributed by Stefan Erhardt.





	<pre>twoport: generic two port¹⁶, type: path-style, fillable , nodename: twoportshape. Class: blocks.</pre>
	vco: vco, type: path-style, fillable, nodename: vcoshape. Class: blocks.
	bandpass: bandpass, type: path-style, fillable, nodename: bandpassshape. Class: blocks.
	bandstop: bandstop, type: path-style, fillable, nodename: bandstopshape. Class: blocks.
	highpass: highpass, type: path-style, fillable, nodename: highpassshape. Class: blocks.
	lowpass: lowpass, type: path-style, fillable, nodename: lowpassshape. Class: blocks.
A D	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$
D _A	$\begin{tabular}{llll} $\tt dac: D/A \ converter, \ type: \ path-style, \ fillable \ , \\ nodename: \ dacshape. \ Class: \ blocks. \end{tabular}$
— DSP —	<pre>dsp: DSP, type: path-style, fillable , nodename: dspshape. Class: blocks.</pre>
— FFT —	fft: FFT, type: path-style, fillable, nodename: fftshape. Class: blocks.
	amp: amplifier, type: path-style, fillable, nodename: ampshape. Class: blocks.

 $^{^{15}{}m added}$ by olfline

center

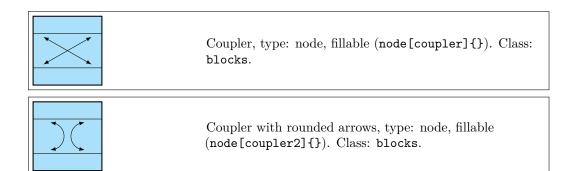
down

 $^{^{16}}$ To specify text to be put in the component: twoport[t=text]):

	$\mbox{\tt vamp}\colon VGA, \mbox{\tt type:} \mbox{\tt path-style, fillable ,} \\ \mbox{\tt nodename:} \mbox{\tt vampshape.} \mbox{\tt Class:} \mbox{\tt blocks.}$
	piattenuator: π attenuator, type: path-style, fillable, nodename: piattenuatorshape. Class: blocks.
	vpiattenuator: var. π attenuator, type: path-style, fillable , nodename: vpiattenuatorshape. Class: blocks.
	tattenuator: T attenuator, type: path-style, fillable, nodename: tattenuatorshape. Class: blocks.
	vtattenuator: var. T attenuator, type: path-style, fillable, nodename: vtattenuatorshape. Class: blocks.
$\boxed{ - \boxed{\varphi} }$	<pre>phaseshifter: phase shifter, type: path-style, fillable , nodename: phaseshiftershape. Class: blocks.</pre>
	<pre>vphaseshifter: var. phase shifter, type: path-style, fillable , nodename: vphaseshiftershape. Class: blocks.</pre>
	<pre>detector: detector, type: path-style, fillable , nodename: detectorshape. Class: blocks.</pre>
	<pre>sacdc: sacdc, type: path-style, fillable , nodename: sacdcshape. Class: blocks.</pre>
	<pre>sdcac: sdcac, type: path-style, fillable , nodename: sdcacshape. Class: blocks.</pre>
	tacdc: tacdc, type: path-style, fillable, nodename: tacdcshape. Class: blocks.
dc1 ac1 right ac2 center ac3	tdcac: $tdcac^{17}$, type: path-style, fillable, nodename: tdcacshape. Class: blocks.

Generic fourport, type: node, fillable (node[fourport]{}). Class: blocks.

 $^{^{17}}$ the 4 converter blocks added by olfline



3.16.1 Blocks anchors

The ports of the mixer and adder can be addressed with numbers or west/south/east/north:

```
1\begin{circuitikz} \draw
2 (0,0) node[mixer] (mix) {}
3 (mix.1) node[left] {1}
4 (mix.2) node[below] {2}
5 (mix.3) node[right] {3}
6 (mix.4) node[above] {4}
7;\end{circuitikz}
```

The Wilkinson divider has:

```
1 \begin{circuitikz} \draw
2 (0,0) node[wilkinson] (w) {\SI{3}{dB}}
3 (w.in) to[short,-o] ++(-0.5,0)
4 (w.out1) to[short,-o] ++(0.5,0)
5 (w.out2) to[short,-o] ++(0.5,0)
6 (w.in) node[below left] {\texttt{in}}
7 (w.out1) node[below right] {\texttt{out1}}}
8 (w.out2) node[above right] {\texttt{out2}}
9 ;
10 \end{circuitikz}
```

The couplers have:

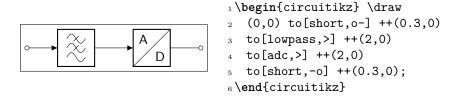
```
1\begin{circuitikz} \draw
2 (0,0) node[coupler] (c) {\SI{10}{dB}}
3 (c.1) to[short,-o] ++(-0.5,0)
4 (c.2) to[short,-o] ++(0.5,0)
5 (c.3) to[short,-o] ++(0.5,0)
6 (c.4) to[short,-o] ++(-0.5,0)
7 (c.1) node[below left] {\texttt{1}}
8 (c.2) node[below right] {\texttt{2}}
9 (c.3) node[above right] {\texttt{3}}
10 (c.4) node[above left] {\texttt{4}}
11 ;
12 \end{circuitikz}
```

```
1\begin{circuitikz} \draw
2 (0,0) node[coupler2] (c) {\SI{3}{dB}}
3 (c.1) to[short,-o] ++(-0.5,0)
4 (c.2) to[short,-o] ++(0.5,0)
5 (c.3) to[short,-o] ++(0.5,0)
6 (c.4) to[short,-o] ++(-0.5,0)
7 (c.1) node[below left] {\texttt{1}}
8 (c.2) node[below right] {\texttt{2}}
9 (c.3) node[above right] {\texttt{3}}
10 (c.4) node[above left] {\texttt{4}}
11 ;
12\end{circuitikz}
```

3.16.2 Blocks customization

You can change the scale of all the block elements by setting the key blocks/scale to something different from the default 1.0.

With the option > you can draw an arrow to the input of the block diagram symbols.



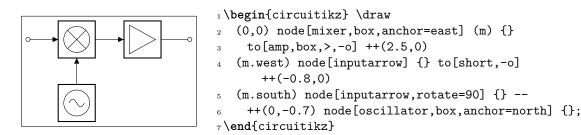
3.16.2.1 Multi ports Since inputs and outputs can vary, input arrows can be placed as nodes. Note that you have to rotate the arrow on your own:

```
1 \begin{circuitikz} \draw
2 (0,0) node[mixer] (m) {}
3 (m.1) to[short,-o] ++(-1,0)
4 (m.2) to[short,-o] ++(0,-1)
5 (m.3) to[short,-o] ++(1,0)
6 (m.1) node[inputarrow] {}
7 (m.2) node[inputarrow,rotate=90] {};
8 \end{circuitikz}
```

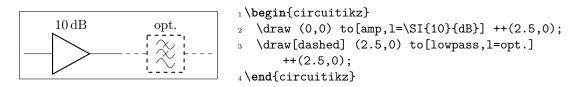
3.16.2.2 Labels and custom two-port boxes Some two-ports have the option to place a normal label (1=) and a inner label (t=).

```
 \begin{array}{c|c} & & & \\ & & LNA \\ \hline \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & &
```

3.16.2.3 Box option Some devices have the possibility to add a box around them. The inner symbol scales down to fit inside the box.

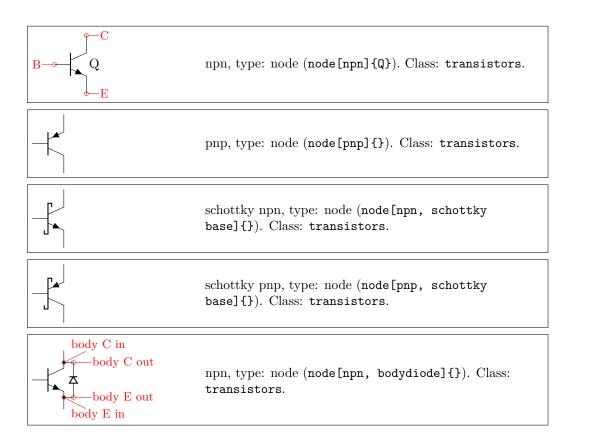


3.16.2.4 Dash optional parts To show that a device is optional, you can dash it. The inner symbol will be kept with solid lines.



3.17 Transistors

3.17.1 Standard bipolar transistors

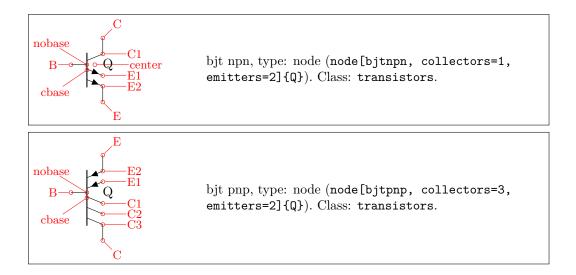


nobase	photo npn, type: node (node[npn,photo]{}). Class: transistors.
	photo pnp, type: node (node[pnp,photo]{}). Class: transistors.
Q	night, type: node (node[night] {Q}). Class: transistors.
	pigbt, type: node (node[pigbt]{}). Class: transistors.
Q	Lnigbt, type: node (node[Lnigbt]{Q}). Class: transistors.
	Lpigbt, type: node (node[Lpigbt]{}). Class: transistors.
body E in body E out body C out body C in	Lpigbt, type: node (node[Lpigbt, bodydiode]{Q}). Class: transistors.

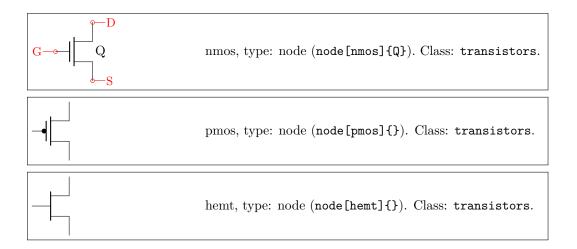
3.17.2 Multi-terminal bipolar transistors

In addition to the standard BJTs transistors, since version 0.9.6 the bjtnpn and bjtpnp are also available; these are devices where you can have more collectors and emitters (on the other hand, they have no photo nor bodydiode options — they are silently ignored).

Basically they are the same as the normal npn and pnp, and they (by default) have similar sizes; the options collectors and emitters will change the number of the relative terminals. The base terminal is connected midway from the collector and the emitter, not on the center of the base; a cbase anchor is available if you prefer to use it. The label of the component (the text) is set on the right side, vertically centered around the base terminal. They will accept the schottky base key.



3.17.3 Field-effect transistors



NFETS and PFETS have been incorporated based on code provided by Clemens Helfmeier and Theodor Borsche. Use the package options fetsolderdot/nofetsolderdot to enable/disable solderdot at some fet-transistors. Additionally, the solderdot option can be enabled/disabled for single transistors with the option solderdot and nosolderdot, respectively.

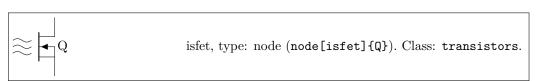




NJFET and PJFET have been incorporated based on code provided by Danilo Piazzalunga:



ISFET



3.17.4 Transistor texts (labels)

In versions before 0.9.7, transistors text (the node text) was positioned near the collector terminal; since version 0.9.7 the default has been changed to a more natural position near the center of the device, similar to the multi-teminal transistors. You can revert to the old behavior locally with the key legacy transistors text, or globally by setting the package option legacytransistorstext.

Notice the use of the utility functions $\text{ctikzflip}\{x,y,xy\}$ as explained in section 3.2.1.

```
1 \begin{circuitikz} [scale=0.8, transform shape]
2 \draw (0,0) node [npn] {T1}
3 ++(1.2,0) node [npn, xscale=-1] {\ctikzflipx{T1}}}
4 ++(2,0) node [npn, yscale=-1] {\ctikzflipxy{T1}};
5 ++(1.2,0) node [npn, scale=-1] {\ctikzflipxy{T1}};
6 \ctikzset{legacy transistors text}}
7 \draw (0,-2) node [npn] {T1}
8 ++(1.2,0) node [npn, xscale=-1] {\ctikzflipx{T1}}};
9 ++(2,0) node [npn, yscale=-1] {\ctikzflipxy{T1}};
10 ++(1.2,0) node [npn, scale=-1] {\ctikzflipxy{T1}};
11 \end{circuitikz}
```

3.17.5 Transistors customization

3.17.5.1 Size. You can change the scale of all the transistors by setting the key transistors/scale (default 1.0). The size of the arrows (if any) is controlled by the same parameters as currarrow (see section 3.14.1) and the dots on P-type transistors (if any) are the same as the nodes/poles (see section 5.1).

3.17.5.2 Arrows. The default position of the arrows in transistors is somewhat in the middle of the terminal; if you prefer you can move them to the end with the style key transistors/arrow pos=end (the default value is legacy).

```
1\begin{circuitikz}
2 \ctikzset{tripoles/mos style=arrows}
3 \ctikzset{transistors/arrow pos=end}
4 \draw (0,0) node[npn, ](npn){};
5 \draw (2,0) node[pnp, ](npn){};
6 \draw (0,-2) node[nmos, ](npn){};
7 \draw (2,-2) node[pmos, ](npn){};
8 \end{circuitikz}
```

If the option arrowmos is used (or after the command \ctikzset{tripoles/mos style/arrows} is given), this is the output:

```
nmos, type: node (node[nmos]{}). Class: transistors.

pmos, type: node (node[pmos]{}). Class: transistors.
```

You can go back to the no-arrows mos with noarrowmos locally or with \ctikzset{tripoles/mos style/no arrows}.

3.17.5.3 Body diodes and similar things. For all transistors (minus bjtnpn and bjtpnp) a body diode (or freewheeling or flyback diode) can automatically be drawn. Just use the global option bodydiode, or for single transistors, the tikz-option bodydiode. As you can see in the next example, the text for the diode is moved if a bodydiode is present (but beware, if you change a lot the relative dimension of components, it may become misplaced):

```
| \begin{circuitikz}
| \draw (0,0) node[npn,bodydiode] (npn) \{1\}
| \draw (0,0) node[npn,bodydiode] (npn) \{\};
| \draw (0,-2) node[nigbt,bodydiode] (npn) \{2\}
| \draw (0,-2) node[pigbt,bodydiode] (npn) \{\};
| \draw (0,-4) node[nfet,bodydiode] (npn) \{\};
| \draw (0,-4) node[pfet,bodydiode] (npn) \{\};
| \draw (0,
```

You can use the body ... anchors to add more or different things to the transistors in addition (or instead) of the flyback diode.

```
1\def\snubb#1#2{% add a snubber to a transistor
     \draw (#1.body C #2) to[short, *-, nodes width=0.02]
     ++(0.3,0) coordinate(tmp) to [R, resistors/scale=0.3]
     % 2/3 space for R, 1/3 for C
     ($(tmp)!0.66!(tmp|-#1.body E #2)$)
     to [C, capacitors/scale=0.3] (tmp|-#1.body E #2)
     to [short, -*, nodes width=0.02] (#1.body E #2);
8}
9 \begin{circuitikz}
     \node[npn](Q1) at(0,0) {};
10
11
     \node[pnp](Q2) at(2,0) {};
     \node[pnp, bodydiode](Q3) at(0,-3) {};
12
     \node[npn, bodydiode](Q4) at(2,-3) {};
13
     \sl \{Q1\} \{in\} \sl \{Q2\} \{in\}
     \square{Q3}{out} \square{Q4}{out}
16 \end{circuitikz}
```

3.17.5.4 Schottky transistors. The Schottky transistors are generated by adding the schottky base key (there is also a no schottky base key that can be used if you use the other one as a default). You can change the size of the Schottky "hook" changing the parameter tripoles/schottky base size with \ctikzset{} (default 0.07; the unit is the standard resistor length, scaled if needed.)

```
begin{circuitikz}

draw (0,4) node[npn]{}

++(2,0) node[npn, schottky base]{};

draw (1,2) node[bjtnpn, collectors=2, emitters=3,

schottky base, rotate=90]{};

tikzset{schottky base}

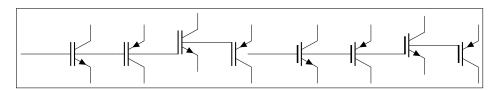
tikzset{tripoles/schottky base size=0.1}

draw (0,0) node[pnp]{}

++(2,0) node[npn, no schottky base]{};

end{circuitikz}
```

3.17.5.5 IGBT outer base Normally, in bipolar IGBTs the outer base is the same size (height) of the inner one, and of the same thickness (which will depend on the class thickness value). You can change this by setting (via \ctikzset`) the keys tripoles/igbt/outer base height (default 0.4, the same as base height), and tripoles/igbt/outer base thickness (default 1.0), which will be relative to the class thickness.



```
1 \begin{circuitikz}
     \text{draw} (0,0)
     -- ++(1,0) node[nigbt, anchor=B](B){} (B.nobase)
     -- ++(1,0) node[pigbt, anchor=B](B){} (B.nobase)
     -- ++(1,0) node[Lnigbt, anchor=B](B){} (B.nobase)
     -- ++(1,0) node[Lpigbt, anchor=B](B){} (B.nobase)
     \ctikzset{tripoles/igbt/outer base height=0.3}
     \ctikzset{tripoles/igbt/outer base thickness=1.5}
10
     \draw (6,0)
11
     -- ++(1,0) node[nigbt, anchor=B](B){} (B.nobase)
     -- ++(1,0) node[pigbt, anchor=B](B){} (B.nobase)
     -- ++(1,0) node[Lnigbt, anchor=B](B){} (B.nobase)
     -- ++(1,0) node[Lpigbt, anchor=B](B){} (B.nobase)
16 \end{circuitikz}
```

3.17.5.6 Base/Gate terminal. The Base/Gate connection of all transistors can be disabled by the options *nogate* or *nobase*, respectively. The Base/Gate anchors are floating, but there is an additional anchor nogate/nobase, which can be used to point to the unconnected base:

```
C
| 1 \begin{circuitikz}
| 2 \draw (2,0) node[npn,nobase](npn){};
| 3 \draw (npn.E) node[below]{E};
| 4 \draw (npn.C) node[above]{C};
| 5 \draw (npn.B) node[circ]{} node[left]{B};
| 6 \draw[dashed,red,-latex] (1,0.5)--(npn.nobase);
| 7 \end{circuitikz}
```

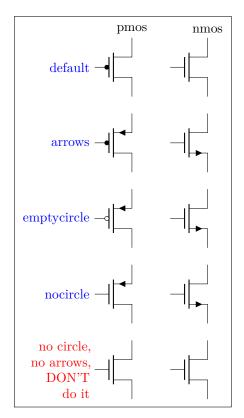
To draw the PMOS circle non-solid, use the option emptycircle or the command \ctikzset{tripoles/pmos style/emptycircle}. To remove the dot completely (only useful if you have arrowmos enabled, otherwise there will be no difference between P-MOS and N-MOS), you can use the option nocircle or \ctikzset{tripoles/pmos style/nocircle}.



3.17.5.7 Bulk terminals. You can add a bulk terminal to nmos and pmos using the key bulk in the node (and nobulk if you set the bulk terminal by default); additional anchors bulk and nobulk are added (in the next example, tripoles/mos style/arrows is enacted, too):



¹⁸Thanks to Burak Kelleci <kellecib@hotmail.com>.



```
1\begin{circuitikz}[
     info/.style={left=1cm, blue, text width=5
         em, align=right},]
     \draw (0,1) node{pmos} (2,1) node{nmos};
     \draw (0,0) node[info]{default} node[pmos
        ]{} (2,0) node[nmos]{};
     \ctikzset{tripoles/mos style/arrows}
     \draw (0,-2) node[info]{arrows} node[pmos
        ]{} (2,-2) node[nmos]{};
     \ctikzset{tripoles/pmos style/emptycircle}
     \draw (0,-4) node[info]{emptycircle} node[
        pmos]{} (2,-4) node[nmos]{};
     \ctikzset{tripoles/pmos style/nocircle}
     \draw (0,-6) node[info]{nocircle} node[
10
         pmos]{} (2,-6) node[nmos]{};
     \ctikzset{tripoles/mos style/no arrows}
11
     \draw (0,-8) node[info, red]{no circle, no
          arrows, DON'T do it}
          node[pmos]{} (2,-8) node[nmos]{};
14 \end{circuitikz}
```

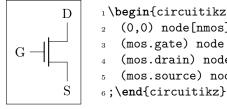
3.17.6 Multiple terminal transistors customization

You can create completely "bare" transistors (without the connection leads to the B, C y E terminals), by changing the parameter tripoles/bjt/pins width (default 0.3; it is expressed as a fraction of the basic (scaled) length) or using the style bjt pins width; and you can change the distance between multiple collectors/emitters setting with \ctikzset{} the parameter tripoles/bjt/multi height (default 0.5) or the style bjt multi height.



3.17.7 Transistors anchors

For NMOS, PMOS, NFET, NIGFETE, NIGFETD, PFET, PIGFETE, and PIGFETD transistors one has base, gate, source and drain anchors (which can be abbreviated with B, G, S and D):



```
1\begin{circuitikz} \draw
2 (0,0) node[nmos] (mos) {}
3 (mos.gate) node[anchor=east] {G}
4 (mos.drain) node[anchor=south] {D}
```

(mos.source) node[anchor=north] {S}

Similarly NJFET and PJFET have gate, source and drain anchors (which can be abbreviated with G, S and D):

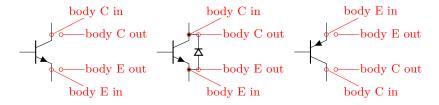
```
G | \begin{circuitikz} \draw \\ 2 \ (0,0) \ \text{node[pjfet] (pjfet) } \\ \\ 3 \ \ (\text{pjfet.G}) \ \text{node[anchor=east] } \\ \\ \\ 4 \ \ (\text{pjfet.D}) \ \text{node[anchor=north] } \\ \\ \\ 5 \ \ (\text{pjfet.S}) \ \text{node[anchor=south] } \\ \\ \\ \\ 6 \ \end{circuitikz}
```

For NPN, PNP, NIGBT and PIGBT transistors, the anchors are base, emitter and collector anchors (which can be abbreviated with B, E and C):

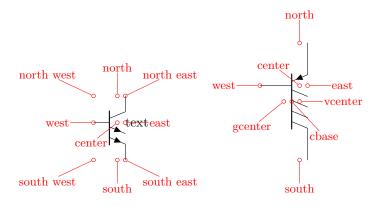
```
C
| 1 \begin{circuitikz} \draw | 2 (0,0) node[npn] (npn) {} | 3 (npn.base) node[anchor=east] {B} | 4 (npn.collector) node[anchor=south] {C} | 5 (npn.emitter) node[anchor=north] {E} | 6 ;\end{circuitikz} | draw | 2 (0,0) node[pigbt] (pigbt) {} | 3 (pigbt.B) node[anchor=east] {B} | 4 (pigbt.C) node[anchor=north] {C} | 5 (pigbt.E) node[anchor=south] {E} | 6 ;\end{circuitikz}
```

Finally, all transistors, except the multi-terminal bjtnpn and bjtpnp, (since 0.9.6) have internal nodes on the terminal corners, called inner up and inner down; you do not normally need them, but they are here for special applications:

Additionally, you can access the position for the flyback diodes and possibly snubbers as shown in 3.17.5.3.



The multi-terminal transistors have all the geographical anchors; note though that the center anchor is not the geometrical center of the component, but the logical one (at the same height than the base). The additional anchors vcenter (vertical geometric center of the collector—emitter zone) and gcenter (graphical center) are provided, as shown in the following picture. They have no bodydiode anchors nor inner up/down ones.



A complete example of multiple terminal transistor application is the following PNP double current mirror circuit.

```
1 \begin{circuitikz}
     \ctikzset{transistors/arrow pos=end}
     \draw (0,0) node[bjtpnp, xscale=-1](Q1){%
         \scalebox{-1}[1]{Q1}};
     \draw (Q1.B) node[bjtpnp, anchor=B, collectors=2]
         (Q2){Q2} (Q1.B) node[circ]{};
     \draw (Q1.E) node[circ]{} node[vcc]{} (Q2.E)
        node[vcc]{} (Q1.E) -| (Q1.B);
     \draw (Q1.C) to [R, 1_=$R_0$, f=$I_0$] ++(0,-3.5)
        node[ground](GND){};
10
     \draw (Q2.C) -- ++(0,-0.5) coordinate(a);
11
     \draw (Q2.C1) -- ++(1,0) coordinate(b) -- (b|-a);
     \del{draw} (a) ++(0,-0.1) node[flowarrow, rotate=-90,
13
        anchor=west]{\rotatebox{90}{$I_0$}};
14
     \draw (b|-a) ++(0,-0.1) node[flowarrow, rotate=-90,
15
         anchor=west]{\rotatebox{90}{$I_0$}};
16
     \path (b) ++(0.5,0); % bounding box adjust
18 \end{circuitikz}
```

Here is one composite example (please notice that the xscale=-1 style would also reflect the label of the transistors, so here a new node is added and its text is used, instead of that of pnp1):

Notice that the text labels of transistors are somewhat buggy. It is better to se explicit anchors to set transistor's names.

Similarly, transistors like other components can be reflected vertically:

3.17.8 Transistor paths

For syntactical convenience standard transistors (not multi-terminal ones) can be placed using the normal path notation used for bipoles. The transitor type can be specified by simply adding a "T" (for transistor) in front of the node name of the transistor. It will be placed with the base/gate orthogonal to the direction of the path:

```
1\begin{circuitikz} \draw
2 (0,0) node[njfet] {1}
3 (-1,2) to[Tnjfet=2] (1,2)
4 to[Tnjfet=3, mirror] (3,2);
5;\end{circuitikz}
```

Access to the gate and/or base nodes can be gained by naming the transistors with the n or name path style:

Transistor paths have the possibility to use the poles syntax (see section 5.1) but they have **no** voltage, current, flow, annotation options. Also, the positioning of the labels is very simple and is not foolproof for all rotations; if you need to control them more please name the node and position them by hand, or use the more natural node style for transistors.

The name property is available also for bipoles; this is useful mostly for triac, potentiometer and thyristor (see 3.7.1).

3.18 Electronic Tubes

Electronic tubes, also known as vacuum tubes, control current flow between electrodes. They come in many different flavours. Contributed by J. op den Brouw (J.E.J.opdenBrouw@hhs.nl).

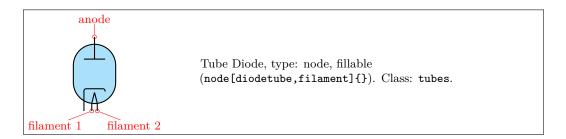




Some pentodes have the suppressor grid internally connected to the control grid, which saves a pin on the tube's housing.



Note that the diodetube is used as component name to avoid clashes with the semiconductor diode. Normally, the filament is not drawn. If you want a filament, put the filament option in the node description:



Sometimes, you don't want the cathode to be drawn (but you do want the filament). Use the nocathode option in the node description:



If you want a full cathode to be drawn, use the fullcathode option in the node description. You can then use the anchors cathode 1 and cathode 2.



3.18.1 Tubes customization

The tubes can be scaled using the key tubes/scale, default 1.0. In addition, they are fully configurable, and the attributes are described below:

Key	Default value	Description
tubes/scale	1	scale factor
tubes/width	1	relative width
tubes/height	1.4	relative height
tubes/tube radius	0.40	radius of tube circle
tubes/anode distance	0.40	distance from center
tubes/anode width	0.40	width of an anode/plate
tubes/grid protrusion	0.25	distance from center
tubes/grid dashes	5	number of grid dashes
tubes/grid separation	0.2	separation between grids
tubes/grid shift	0.0	y shift of grids from center
tubes/cathode distance	0.40	distance from grid
tubes/cathode width	0.40	width of a cathode
tubes/cathode corners	0.06	corners of the cathode wire
tubes/cathode right extend	0.075	extension at the right side
tubes/filament distance	0.1	distance from cathode
tubes/filament angle	15	angle from the centerpoint

Conventionally, the model of the tube is indicated at the east anchor:



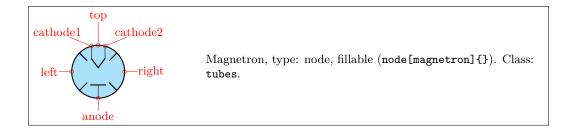
Example triode amplifier:

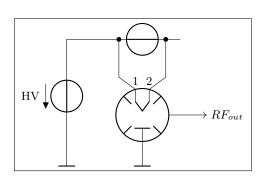
```
to [short] ++(3.5,0) node(Vatop) {}
to [V<=$V_a$] (Vatop |- start)
to [short] (start)

13 (Tri.anode) ++(0,0.2) to [C=$C_o$,*-o] ++(2,0)
14 (Tri.cathode) ++(0,-0.2) to [short,*-] ++(1.5,0) node(Cctop) {}
to [C=$C_c$,-*] (start -| Cctop)
16;
17 \draw[red,thin,dashed] (Tri.north west) rectangle (Tri.south east);
18 \draw (Tri.east) node[right] {12AX7};
19 \end{circuitikz}</pre>
```



The magnetron shape will also scale with tubes/scale.



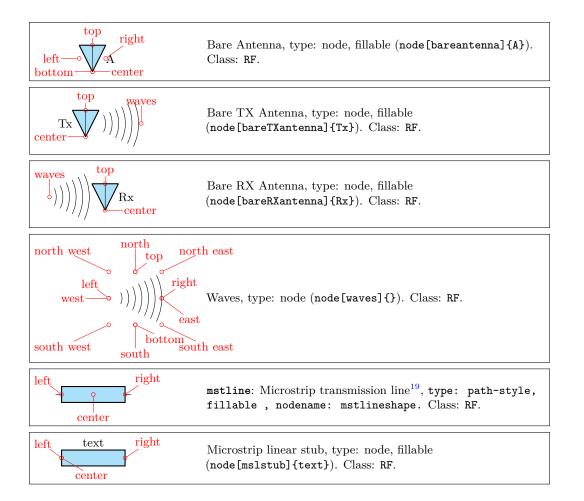


```
1\begin{circuitikz}
2\draw (0,-2)node[rground](gnd){} to[
     voltage source, v \le \{HV\}\} + (0,3) - - + (1,0)
     to [V, n=DC] ++(2,0);
3 \draw (2,-1) node [magnetron, scale=1] (magn)
4 \draw (DC.left)++(-0.2,0)to [short,*-]
     ++(0,-1) to [short] (magn.cathode1);
5 \draw (DC.right)++(0.2,0)to [short,*-]
     ++(0,-1) to [short] (magn.cathode2);
6 \draw (magn.anode) to [short] (magn.anode|-
     gnd) node[rground]{};
7 \draw (magn.cathode1)node[above]{$1$};
s\draw (magn.cathode2)node[above]{$2$};
9 \draw[->] (magn.east) --++(1,0)node[right
     ]{$RF_{out}$};
10 \end{circuitikz}
```

3.19 RF components

For the RF components, similarly to the grounds and supply rails, the center anchor is put on the connecting point of the symbol, so that you can use them directly in a path specification.

Notes that in the transmission and receiving antennas, the "waves" are outside the geographical anchors.



left right center	Microstrip port, type: node, fillable (node[msport]{T}). Class: RF.
len ght	Microstrip radial stub, type: node, fillable (node[msrstub]{}). Class: RF.
-center	Legacy antenna (with tails), type: node (node[antenna]{}). Class: RF.
	Legacy receiving antenna (with tails), type: node (node[rxantenna]{}). Class: RF.
	Legacy transmitting antenna (with tails), type: node (node[txantenna]{}). Class: RF.
	Transmission line stub, type: node, fillable (node[tlinestub]{}). Class: RF.
<u> </u>	TL: Transmission line, type: path-style, fillable, nodename: tlineshape. Aliases: transmission line, tline. Class: RF.
	match, type: node (node[match] {}). Class: RF.

3.19.1 RF elements customization

The RF elements can be scaled using the key RF/scale, default 1.0.

3.19.2 Microstrip customization

The microstrip linear components' (mstline, mslstub, msport) heights can be changed by setting the parameter bipoles/mstline/height (for the three of them, default 0.3). The widths are specified in bipoles/mstline/width for the first two and by monopoles/msport/width for the port (defaults: 1.2, 0.5).

For the length parameter of the transmission line there is a shortcut in the form of the direct parameter mstlinelen.

 $^{^{19}}$ This four components were suggested by @tcpluess on GitHub



```
1\begin{circuitikz}
2  \draw (0,0) node[msport, right, xscale=-1]{}
3  to[mstline, -o] ++(3,0) coordinate(there)
4  to[mstline, mstlinelen=2, l=longer, o-*] ++(4,0)
5  coordinate(here) -- ++(0.5,0) node[mslstub, fill=yellow]{stub}
6  (here) -- ++(0,0.5) node[mslstub, rotate=90, mstlinelen=0.5]{short};
7  \draw (there) to[short, o-] ++(0, 0.5) node[msrstub]{};
8  \draw (here) -- ++(0, -0.5) node[msrstub, yscale=-1]{};
9 \end{circuitikz}
```

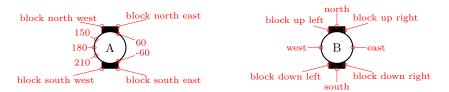
3.20 Electro-Mechanical Devices

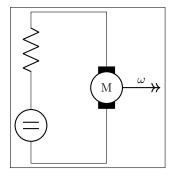
The internal part of the motor and generator are, by default, filled white (to avoid compatibility problems with older versions of the package).



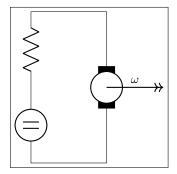
3.20.1 Electro-Mechanical Devices anchors

Apart from the standard geographical anchors, elmech has the border anchors (situated on the inner circle) and the following anchors on the "block":



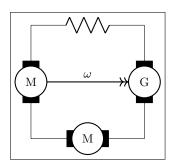


```
1 \begin{circuitikz}
2 \draw (2,0) node[elmech] (motor) {M};
3 \draw (motor.north) |-(0,2) to [R] ++(0,-2) to[
    dcvsource] ++(0,-2) -| (motor.bottom);
4 \draw[thick,->>] (motor.right)--++(1,0) node[midway,
    above] {$\omega$};
5 \end{circuitikz}
```



```
1\begin{circuitikz}
2\draw (2,0) node[elmech](motor){};
3\draw (motor.north) |-(0,2) to [R] ++(0,-2) to[
    dcvsource]++(0,-2) -| (motor.bottom);
4\draw[thick,->>](motor.center)--++(1.5,0)node[midway,
    above]{$\omega$};
5\end{circuitikz}
```

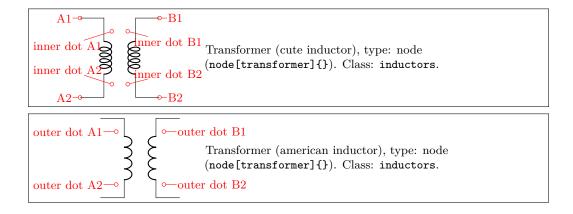
The symbols can also be used along a path, using the transistor-path-syntax(T in front of the shape name, see section 3.17.8). Don't forget to use parameter n to name the node and get access to the anchors:



```
1 \begin{circuitikz}
2 \draw (0,0) to [Telmech=M,n=motor] ++(0,-3) to [
    Telmech=M] ++(3,0) to [Telmech=G,n=generator]
    ++(0,3) to [R] (0,0);
3 \draw[thick,->>] (motor.left)--(generator.left)node[
    midway,above]{$\omega$};
4 \end{circuitikz}
```

3.21 Double bipoles (transformers)

Transformers automatically use the inductor shape currently selected. These are the three possibilities:





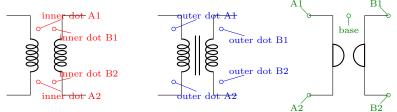
Transformers with core are also available:



3.21.1 Double dipoles anchors

All the double bipoles/quadrupoles have the four anchors, two for each port. The first port, to the left, is port A, having the anchors A1 (up) and A2 (down); same for port B.

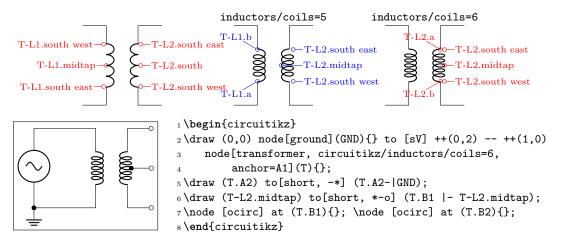
They also expose the base anchor, for labelling, and anchors for setting dots or signs to specify polarity. The set of anchors, to which the standard "geographical" north, north east, etc. is here:



Also, the standard "geographical" north, north east, etc. are defined. A couple of examples follow:



Moreover, you can access the two internal coils (inductances); if your transformer node is called T, they are named T-L1 and T-L2. Notice that the two inductors are rotated (by -90 degrees the first, +90 degrees the second) so you have to be careful with the anchors. Also, the midtap anchor of the inductors can be on the external or internal side depending on the numbers of coils. Finally, the anchors L1.a and L1.b are marking the start and end of the coils.



3.21.2 Double dipoles customization

Transformers are in the inductors class (also the gyrator...), so they scale with the key inductors/scale. You can change the aspect of a quadpole using the corresponding parameters quadpoles/*/width and quadpoles/*/heigth (substitute the star for transformer, transformer core or gyrator; default value is 1.5 for all). You have to be careful to not choose value that overlaps the components!

```
1\begin{circuitikz}
2\ctikzset{quadpoles/transformer/width=1,
3 quadpoles/transformer/height=2}
4 \draw (0,0) node[transformer] (T) {}
5 (T.base) node{K}
6 (T.inner dot A1) node[circ]{}
7 (T.inner dot B2) node[circ]{};
8\end{circuitikz}
```

Transformers also inherits the inductors/scale (see 3.6.5) and similar parameters. It's your responsibility to set the aforementioned parameters if you change the scale or width of inductors.

Transformers core line distance is specified by the parameter quadpoles/transformer core/core width (default 0.05) and the thickness of the lines follows the choke one; in other words, you can set it changing bipoles/cutechoke/cthick.

Another very useful parameter is quadpoles/*/inner (default 0.4) that determine which part of the component is the "vertical" one. So, setting that parameter to 1 will eliminate the horizontal part of the component (obviously, to maintain the general aspect ratio you need to change the width also):

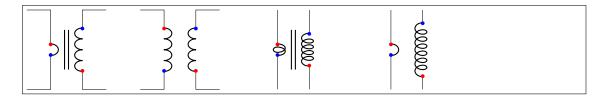
```
1 \begin{circuitikz}
A1
        K
               B1
                     2\draw (0,0) node[transformer] (T) {}
                        (T.A1) node[anchor=east] {A1}
                        (T.A2) node[anchor=east] {A2}
                        (T.B1) node[anchor=west] {B1}
                        (T.B2) node[anchor=west] {B2}
A2
               B2
                        (T.base) node{K};
                     &\ctikzset{quadpoles/transformer/inner=1, quadpoles/
                          transformer/width=0.6}
                     9 \draw (0,-3) node[transformer] (P) {}
                        (P.base) node{T}
                        (P.inner dot A2) node[ocirc]{}
                        (P.inner dot B2) node[ocirc]{};
                     13 \end{circuitikz}
```

This can be useful if you want to put seamlessly something in series with either side of the component; for simplicity, you have a style setting quadpoles style to toggle between the standard shape of double bipoles (called inward, default) and the one without horizontal leads (called inline):

```
1\begin{circuitikz}
2\ctikzset{inductor=cute, quadpoles style=inline}
3\draw
4 (0,0) to[R] ++(0,-2)
5 node[transformer, anchor=A1](T){}
6 (T.A2) node[ground](GND){}
7 (T.inner dot A1) node[font=\small\boldmath]{$\oplus$}
8 (T.inner dot B2) node[]{$+$}
9 (T.B1) node[above, ocirc]{}
10 (T.B2) -- (GND);
11\end{circuitikz}
```

3.21.3 Styling transformer's coils independently

Since 0.9.6, you can tweak the style of each of the coils of the transformers by changing the value of the two styles transformer L1 and transformer L2; the default for both are {}, that means inherit the inductors style in force.



```
1 \begin{circuitikz}[american]
     \begin{scope}
         \ctikzset{transformer L1/.style={inductors/coils=1, inductors/width=0.2}}
         \draw (0,0) node[transformer core](T1){};
     \end{scope}
     \draw (3,0) node[transformer](T2){};
     \ctikzset{cute inductors, quadpoles style=inline}
     \ctikzset{transformer L1/.style={inductors/coils=2, inductors/width=0.2}}
     \draw (6,0) node[transformer core](T3){};
     \ctikzset{transformer L1/.style={american inductors, inductors/coils=1, inductors/
10
         width=0.2}}
     \ctikzset{transformer L2/.style={inductors/coils=7, inductors/width=1.0}}
11
12
     \draw (9,0) node[transformer ](T4){};
     foreach \ t in \{T1, T2, T3, T4\} 
         foreach \l in \{L1, L2\} 
            \foreach \a/\c in {a/blue, b/red}
15
            \node [circle, fill=\c, inner sep=1pt] at (\t-\l.\a) \{\};
16
         }
17
     }
18
19 \end{circuitikz}
```

Caveat: the size of the transformer is independent from the styles for L1 and L2, so they follow whatever the parameters for the inductances were before applying them. In other words, the size of the transformer could result too small if you are not careful.

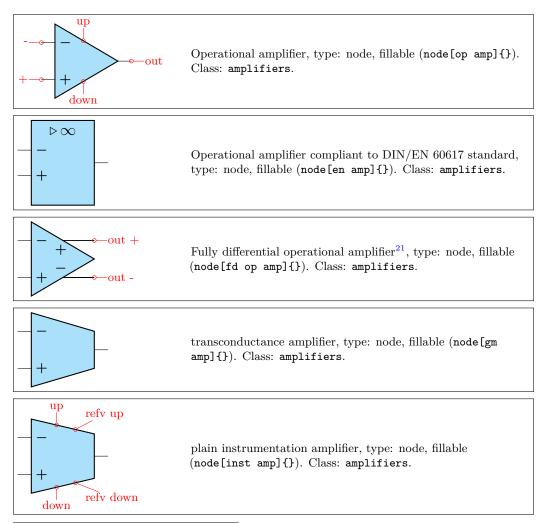


You can obviously define a style for a "non-standard" transformer. For example, you can have a current transformer 20 defined like this:

 $^{^{20}\}mathrm{Suggested}$ by Alex Pacini on GitHub

Remember that the default pgfkeys directory is /tikz for nodes and for the options of the environment, so you have to use the full path (with circuitikz/) there.

3.22 Amplifiers



 $^{^{21}\}mathrm{Contributed}$ by Kristofer M. Monisit.



3.22.1 Amplifiers anchors

The op amp defines the inverting input (-), the non-inverting input (+) and the output (out) anchors:



There are also two more anchors defined, up and down, for the power supplies:

```
1\begin{circuitikz} \draw
2  (0,0) node[op amp] (opamp) {}
3  (opamp.+) node[left] {$v_+$}
4  (opamp.-) node[left] {$v_-$}
5  (opamp.out) node[right] {$v_0$}
6  (opamp.down) node[ground] {}
7  (opamp.up) ++ (0,.5) node[above] {\SI{12}{\volt}}
8     -- (opamp.up)
9 ;\end{circuitikz}
```

The fully differential op amp defines two outputs:



```
1 \begin{circuitikz} \draw
2 (0,0) node[fd op amp] (opamp) {}
3 (opamp.+) node[left] {$v_+$}
4 (opamp.-) node[left] {$v_-$}
5 (opamp.out +) node[right] {out +}
6 (opamp.out -) node[right] {out -}
7 (opamp.down) node[ground] {}
8 ;\end{circuitikz}
```

The instrumentation amplifier inst amp defines also references (normally you use the down, unless you are flipping the component):



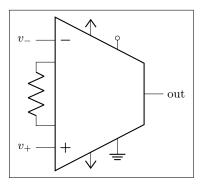
```
1 \begin{circuitikz} \draw
2 (0,0) node[inst amp] (opamp) {}
3 (opamp.+) node[left] {$v_+$}
4 (opamp.-) node[left] {$v_-$}
5 (opamp.out) node[right] {out}
6 (opamp.up) node[vcc]{}
7 (opamp.down) node[vee] {}
8 (opamp.refv down) node[ground]{}
9 (opamp.refv up) to[short, -o] ++(0,0.3)
10; \end{circuitikz}
```

The fully diffential instrumentation amplifier inst amp defines two outputs:



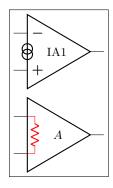
```
1 \begin{circuitikz} \draw
2  (0,0) node[fd inst amp] (opamp) {}
3  (opamp.+) node[left] {$v_+$}
4  (opamp.-) node[left] {$v_-$}
5  (opamp.out +) node[right] {out +}
6  (opamp.out -) node[right] {out -}
7  (opamp.up) node[vcc]{}
8  (opamp.down) node[vee] {}
9  (opamp.refv down) node[ground]{}
10  (opamp.refv up) to[short, -o] ++(0,0.3)
11 ;\end{circuitikz}
```

The instrumentation amplifier with resistance terminals (inst amp ra) defines also terminals to add an amplification resistor:



```
1 \begin{circuitikz} \draw
2  (0,0) node[inst amp ra] (opamp) {}
3  (opamp.+) node[left] {$v_+$}
4  (opamp.-) node[left] {$v_-$}
5  (opamp.out) node[right] {out}
6  (opamp.up) node[vcc]{}
7  (opamp.down) node[vee] {}
8  (opamp.refv down) node[ground]{}
9  (opamp.refv up) to[short, -o] ++(0,0.3)
10  (opamp.ra-) to[R] (opamp.ra+)
11 ;\end{circuitikz}
```

Amplifiers have also "border" anchors (just add b, without space, to the anchor, like b+ or bin up and so on). These can be useful to add "internal components" or to modify the component. Also the leftedge anchors (on the border midway between input) is available.

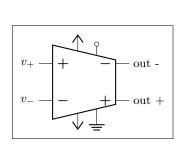


```
1 \begin{circuitikz}[]
2    \draw (0,2.2) node[op amp](OA){IA1};
3    \node[oosourceshape, rotate=90, scale=0.5]
4    at (OA.leftedge) {};
5    \draw (0,0) node[plain amp](A){$A$};
6    \draw [color=red] (A.bin up) -- ++(0.2,0)
7    coordinate (tmp)
8    to[R, resistors/scale=0.5]
9    (tmp|-A.bin down) -- (A.bin down);
10 \end{circuitikz}
```

3.22.2 Amplifiers customization

You can scale the amplifiers using the key amplifiers/scale and setting it to something different from 1.0. The font used for symbols will not scale, so it's your responsibility to change it if the need arises.

All these amplifier have the possibility to flip input and output (if needed) polarity. You can change polarity of the input with the noinv input down (default) or noinv input up key; and the output with noinv output up (default) or noinv output down key:



```
1 \begin{circuitikz} \draw
2 (0,0) node[fd inst amp,
3     noinv input up,
4     noinv output down] (opamp) {}
5     (opamp.+) node[left] {$v_+$}
6     (opamp.-) node[left] {$v_-$}
7     (opamp.out +) node[right] {out +}
8     (opamp.out -) node[right] {out -}
9     (opamp.up) node[vcc]{}
10     (opamp.down) node[vee] {}
11     (opamp.refv down) node[ground]{}
12     (opamp.refv up) to[short, -o] ++(0,0.3)
13 ;\end{circuitikz}
```

When you use the noinv input/output ... keys the anchors (+, -, out +, out -) will change with the effective position of the terminals. You have also the anchors in up, in down, out up, out down that will not change with the positive or negative sign.

You can change the symbols "+" or "-" appearing in the amplifiers if you want, both globally and on component-by-component basis. The plus and minus symbols can be changed with \ctikzset of the keys

amplifiers/plus and amplifiers/minus (which defaults to the math mode plus or minus cited before), or using the styles amp plus and amp minus.

The font used is set in several keys, but you can change it globally with \tikzset{amp symbol font}, which has a default of 10-point (in LATEX, and the corresponding one in ConTEXt). You can change it for example with

- 1 \tikzset{amp symbol font={%
- 2 \color{blue}\fontsize{12}{12}\selectfont\boldmath}}

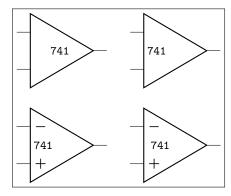
to have plus and minus symbols that are bigger and blue.



If you want different symbols for input and output you can use a null symbol and put them manually using the border anchors.

```
1 \begin{circuitikz}[]
2 \ctikzset{amplifiers/plus={}}
3 \ctikzset{amplifiers/minus={}}
4 \draw (0,0) node[fd op amp](A){};
5 \node [font=\small\bfseries, right] at(A.bin up) {1};
6 \node [font=\small\bfseries, right] at(A.bin down) {2};
7 \node [font=\small\bfseries, below] at(A.bout up) {3};
8 \node [font=\small\bfseries, above] at(A.bout down) {4};
9 \end{circuitikz}
```

The amplifier label (given as the text of the node) is normally more or less centered in the shape (in the case of the triangular shape, it is shifted a bit to the left to *seem* visually centered); since version 1.1.0 you can move it at the left side plus a fixed offset setting the key component text or the style with the same name to left; by default the key is center. You can change the offset with the key left text distance (default 0.3em; you must use a length here). These parameters are shared with IEEE-style logic ports.



These keys are also used for the positioning of the labels in the label positioning of IEEE logic gates (see 3.24.2).

3.22.2.1 European-style amplifier customization Thanks to the suggestions from David Rouvel (david.rouvel@iphc.cnrs.fr) there are several possible customization for the European-style amplifiers. Since 0.9.0, the default appearance of the symbol has changed to be more in line with the standard;

Since 0.9.0, the default appearance of the symbol has changed to be more in line with the standard; notice that to have a bigger triangle by default we should require more packages, and I fear ConTEXt compatibility; but see later on how to change it. Notice that the font used for the symbol is defined in tripoles/en amp/font2 and that the font used for the + and - symbols is tripoles/en amp/font.

You can change the distances of the inputs, using tripoles/en amp/input height (default 0.3):

and of course the key noinv input up is fully functional:

```
v_{\rm in+} - + \\ v_{\rm in-} - v_{\rm out} \\ v_{\rm in-} - v_{\rm in-} \\ v_{\rm in-} -
```

To flip the amplifier in the horizontal direction, you can use xscale=-1 as usual:

```
v_{\rm out} = v_{\rm in+} - v_{\rm in+} - v_{\rm in+} - v_{\rm in-} - v_{\rm
```

Notice that the label is fully mirrored, so check below for the generic way to change this.

You can use the new key en amp text A to change the infinity symbol with an A:

```
v_{\text{in-}} = \begin{array}{|c|c|c|c|c|}\hline & & & & & & \\ & v_{\text{in-}} & & & & \\ & & & & \\ & & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ &
```

And if you want, you can completely change the text using the key en amp text=, which by default is \$\mathstrut{\triangleright}\,{\infty}\$:

```
v_{\mathrm{in-}} \stackrel{\triangleright}{-} v_{\mathrm{out}} v_{\mathrm{in+}} \stackrel{\triangleright}{-} v_{\mathrm{out}}
```

Notice two things here: the first, that \triangleright is enclosed in braces to remove the default spacing it has as a binary operator, and that en amp text A is simply a shortcut for

```
en amp text={$\mathstrut{\triangleright}\,\mathrm{A}$}
```

To combine flipping with a generic label you just do:

```
v_{\rm out} = \begin{bmatrix} A \lhd & \\ - & v_{\rm in-} \\ + & v_{\rm in+} \\ \end{bmatrix} \begin{circuitikz} \\ 2 & \text{draw } (0,0) \text{node} [\text{en amp, xscale=-1, en amp text A}] (\\ E) \{\} \\ 3 & \text{(E.out) node} [\text{left}] \ \{\$v_{\text{mathrm } \{\text{in}\}-}\} \} \\ 4 & \text{(E.-) node} [\text{right}] \ \{\$v_{\text{mathrm } \{\text{in}\}-}\} \} \} \\ 5 & \text{(E.+) node} [\text{right}] \ \{\$v_{\text{mathrm } \{\text{in}\}+}\} \} \ ; \\ 6 & \text{end} \{\text{circuitikz}\} \end{circuitikz} \end{circuitikz} \end{circuitikz}
```

But notice that the "A" is also flipped by the xscale parameter. So the solution in this case is to use scalebox, like this:

3.22.3 Designing your own amplifier

If you need a different kind of amplifier, you can use the muxdemux (see section 3.26) shape for defining one that suits your needs (you need version 1.0.0 for this to work).

3.23 Switches and buttons

Switches and button come in to-style (the simple ones and the pushbuttons), and as nodes.

The switches can be scaled with the key switches/scale (default 1.0). Notice that scaling the switches will not scale the poles, which are controlled with their own parameters (see section 3.15).

3.23.1 Traditional switches

These are all of the to-style type:

X	<pre>switch: Switch, type: path-style , nodename: cspstshape.Aliases: spst. Class: switches.</pre>
	closing switch: Closing switch, type: path-style , nodename: cspstshape. Aliases: cspst. Class: switches.
	opening switch: Opening switch, type: path-style , nodename: ospstshape. Aliases: ospst. Class: switches.
	normal open switch: Normally open switch, type: path-style , nodename: nosshape.Aliases: nos. Class: switches.
	normal closed switch: Normally closed switch, type: path-style , nodename: ncsshape. Aliases: ncs. Class: switches.
	<pre>push button: Normally open push button, type: path-style , nodename: pushbuttonshape.Aliases: normally open push button, nopb. Class: switches.</pre>
	normally closed push button: Normally closed push button, type: path-style , nodename: ncpushbuttonshape.Aliases: ncpb. Class: switches.
	toggle switch: Toggle switch, type: path-style , nodename: toggleswitchshape. Class: default.
	reed: Reed switch, type: path-style, fillable, nodename: reedshape. Class: switches.

while this is a node-style component:

```
spdt, type: node (node[spdt]{}). Class: switches.
```

```
in \longrightarrow out 1 \bigcirc out 2
```

```
2 (0,0) node[spdt] (Sw) {}
3 (Sw.in) node[left] {in}
4 (Sw.out 1) node[right] {out 1}
5 (Sw.out 2) node[right] {out 2}
6;\end{circuitikz}
```

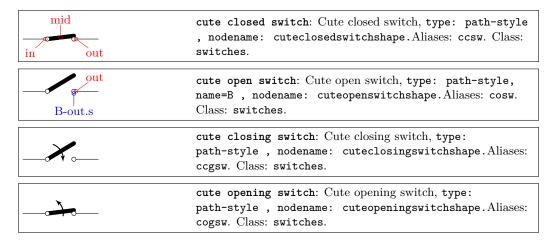
1\begin{circuitikz} \draw

```
1\begin{circuitikz} \draw
2 (0,0) to[C] (1,0) to[toggle switch , n=Sw] (2.5,0)
3 -- (2.5,-1) to[battery1] (1.5,-1) to[R] (0,-1) -| (0,0)
4 (Sw.out 2) -| (2.5, 1) to[R] (0,1) -- (0,0)
5;\end{circuitikz}
```

3.23.2 Cute switches

These switches have been introduced after version 0.9.0, and they come in also in to-style and in node-style, but they are size-matched so that they can be used together in a seamless way.

The path element (to-style) are:



while the node-style components are the single-pole, double-throw (\mathtt{spdt}) ones:

in—w—out 1 o—out 2	Cute spdt up, type: node (node[cute spdt up]{}). Class: switches.
•	Cute spdt mid, type: node (node[cute spdt mid]{}). Class: switches.
° mid	Cute spdt down, type: node (node[cute spdt down]{}). Class: switches.
%	Cute spdt up with arrow, type: node (node[cute spdt up arrow]{}). Class: switches.
${}$	Cute spdt mid with arrow, type: node (node[cute spdt mid arrow]{}). Class: switches.
\sim	Cute spdt down with arrow, type: node (node[cute spdt down arrow]{}). Class: switches.

3.23.2.1 Cute switches anchors The nodes-style switches have the following anchors:



Please notice the position of the normal anchors at the border of the ocirc shape for the cute switches; they are thought to be compatible with an horizontal wire going out. Additionally, you have the cin, cout 1 y cout 2 which are anchors on the center of the contacts.

For more complex situations, the contact nodes are available 22 using the syntax name of the node-in, ...-out 1 and ...-out 2, with all their anchors.

The mid anchor in the cute switches (both path- and node-style) can be used to combine switches to get more complex configurations:

```
1\begin{circuitikz}
2 \draw (0,1.4) node[cute spdt up](S1){};
3 \draw (0,0) node[cute spdt up](S2){};
4 \draw (0,-1) node[cuteclosedswitchshape, yscale=-1](S3){};
5 \draw [densely dashed] (S1.mid)--(S2.mid)--(S3.mid);
6 \end{circuitikz}
```

3.23.2.2 Cute switches customization You can use the key bipoles/cuteswitch/thickness to decide the thickness of the switch lever. The units are the diameter of the ocirc connector, and the default is 1.

```
1\begin{circuitikz}
2 \ctikzset{bipoles/cuteswitch/thickness=0.5}
3 \draw (0,1.4) node[cute spdt up](S1){};
4 \draw (0,0) node[cute spdt up](S2){};
5 \draw (0,-1) node[cuteclosedswitchshape, yscale=-1](S3){};
6 \draw [densely dashed] (S1.mid)--(S2.mid)--(S3.mid);
7\end{circuitikz}
```

Finally, the switches are normally drawn using the ocirc shape, but you can change it, as in the following example, with the key bipoles/cuteswitch/shape. Be careful that the shape is used with its defaults (which can lead to strange results), and that the standard anchors will be correct only for circ and ocirc shapes, so you have to use the internal node syntax to connect it.

 $^{^{22}{\}rm Thanks}$ to ${\tt Cmarmot}$ on tex.stackexchange.com.

```
1\begin{circuitikz}
     \begin{scope}
        \ctikzset{bipoles/cuteswitch/thickness=0.5,
        bipoles/cuteswitch/shape=circ}
        \draw (0,2) node[cute spdt up](S1){};
        \ctikzset{bipoles/cuteswitch/thickness=0.25,
        bipoles/cuteswitch/shape=emptyshape}
        \draw (0,0) node[cute spdt up](S2){};
        \draw (S2.cin) node[draw, inner sep=2pt]{};
        \draw (S2.cout 1) node[draw, inner sep=2pt]{};
10
        \draw (S2.cout 2) node[draw=red, inner sep=2pt]{};
11
     \end{scope}
12
     \draw (0,-2) node[cuteclosedswitchshape, yscale=-1](S3){};
13
     \draw [densely dashed] (S1.mid)--(S2.mid)--(S3.mid);
15 \end{circuitikz}
```

3.23.3 Rotary switches

Rotary switches are a kind of generic multipole switches; they are implemented as a strongly customizable element (and a couple of styles to simplify its usage). The basic element is the following one, and it has the same basic anchors of the cute switches, included the access to internal nodes (shown in blue here).

```
N-out 1.n

cout 1 — out 1

cin o mid

in o mid

center N-out 4.w

Rotary switch, type: node (node[rotaryswitch](N){}). Class: switches.
```

Notice that the name of the shape is rotaryswitch, no spaces. The default rotary switch component has 5 channels (this is set in the parameter multipoles/rotary/channels), spanning form -60° to 60° (parameter multipoles/rotary/angle) and with the wiper at 20° (parameter multipoles/rotary/wiper).

Moreover, there are by default no arrows on the wiper; if needed, you can change this default setting the parameter multipoles/rotary/arrow which can assume the values none, cw (clockwise), ccw (counterclockwise) or both.

To simplify the usage of the component, a series of styles are defined: rotary switch=<channels> in <angle> wiper <wiper angle> (notice the space in the name of the style!). Using rotary switch without parameters will generate a default switch.

To add arrows, you can use the styles rotary switch - (no arrow, whatever the default), rotary switch <- (counterclockwise arrow), rotary switch -> (clockwise) and rotary switch <-> (both).

Notice that the defaults of the styles are the same as the default values of the parameters, but that if you change globally the defaults using the keys mentioned above, you only change the defaults for the "bare" component rotaryswitch, not for the styles.



```
1 \begin{circuitikz}
2 \ctikzset{multipoles/rotary/arrow=both}
3 \draw (0,0) -- ++(1,0) node[rotary switch <-=8 in 120 wiper 40, anchor=in](A){};
4 \draw (3,0) -- ++(1,0) node[rotary switch, anchor=in](B){}; % default values
5 \draw[red] (A.out 4) -| (3,0);
6 \draw[blue] (A-out 2.n) -- ++(0,0.5) -| (B-out 1.n);
7 \draw (B.out 3) -- ++(1,0) node[rotary switch -=5 in 90 wiper 15, anchor=in](C){};
8 \draw (C.out 3) -- ++(1,0) node[rotary switch ->, xscale=-1, anchor=out 3](D){};
9 \draw[green, dashed] (B.mid) -- ++(-.5,-1) -| (C.mid);
10 \end{circuitikz}
```

3.23.3.1 Rotary switch anchors Rotary switches anchors are basically the same as the cute switches, including access (with the <node name>-<anchor name> notation) to the internal connection nodes. The geographical anchors work as expected, marking the limits of the component.



In addition to the anchors they have in common with the cute switches, the rotary switch has the so called "angled" anchors and the "external square anchors". Angled anchors, called aout 1, aout 2 and so forth, are anchors placed on the output poles at the same angle as the imaginary lines coming from the input pole; square anchors, called squat 1..., are located on an imaginary square surrounding the rotary switch on the same line.



The code for the diagram at the left, above, without the markings for the anchors, is:

```
1 \begin{circuitikz}
2   \draw (8,0) node[rotary switch -=31 in 150 wiper 10](D){};
3   \foreach \i in {1,...,31} \draw (D.sqout \i) -- (D.aout \i);
4   \draw[blue, densely dotted] (D.ext north west) rectangle (D.ext south east);
5 \end{circuitikz}
```

One possible application for the angled and the "on square" anchors is that you can use them to move radially from the output poles, for example for adding numbers:

```
1\begin{circuitikz}
2\draw (0,0) node[rotary switch=13 in 120 wiper 0](S){};
2\draw (0,0) node[rotary switch=13 in 120 wiper 0](S){};
3\foreach \i in {1,...,13} % requires "calc"
4\path ($(S.aout \i)!1ex!(S.sqout \i)$)
5\node[font=\tiny\color{red}]{\i};
6\end{circuitikz}
```

Finally, notice that the value of width for the rotary switches is taken from the one for the "cute switches" which in turn is taken from the width of traditional **spdt** switch, so that they match (notice that the "center" anchor is better centered in the rotary switch, so you have to explicitly align them).

```
1\begin{circuitikz}
2  \draw (0,0) node[color=blue, rotary switch=2 in 35 wiper 30,
3     anchor=in](R){};
4  \draw (0,-1) node[cute spdt up, anchor=in](C){};
5  \draw (0,-2) node[color=blue, rotary switch=3 in 35 wiper 30,
6     anchor=in](R){};
7\end{circuitikz}
```

3.23.3.2 Rotary switch customization Apart from the basic customization seen above (number of channels, etc.) you can change, as in the cute switches, the shape used by the connection points with the parameter multipoles/rotary/shape, and the thickness of the wiper with multipoles/rotary/thickness. The optional arrow has thickness equal to the standard bipole thickness bipoles/thickness (default 2).

```
1\begin{circuitikz}
2 \ctikzset{multipoles/rotary/thickness=0.5}
3 \draw (0,1.6) node[rotary switch ->, color=blue](S1){};
4 \ctikzset{multipoles/rotary/shape=circ}
5 \draw (0,0) node[rotary switch ->](S2){};
6 \ctikzset{bipoles/thickness=0.5}
7 \draw (0,-1.6) node[rotary switch ->, color=red](S3){};
8 \end{circuitikz}
```

Finally, the size can be changed using the parameter tripoles/spdt/width (default 0.85).

```
1\begin{circuitikz}
2 \draw (0,2) node[rotary switch ->, color=blue](S1){};
3 \ctikzset{tripoles/spdt/width=1.6, fill=cyan,}
4 multipoles/rotary/shape=osquarepole}
5 \draw (0,0) node[rotary switch ->](S2){};
6\end{circuitikz}
```

3.24 Logic gates

Logic gates, with two or more input, are supported. Albeit in principle these components are multipoles, the are considered tripoles here, for historical reasons (when they just had two inputs).

3.24.1 American Logic gates

in 1—o—out	American AND port, type: node, fillable (node[american and port]{}). Class: logic ports.
bin 1 bin 2	American OR port, type: node, fillable (node[american or port]{}). Class: logic ports.
	American NAND port, type: node, fillable (node[american nand port]{}). Class: logic ports.
<u> </u>	American NOR port, type: node, fillable (node[american nor port]{}). Class: logic ports.
	American XOR port, type: node, fillable (node[american xor port]{}). Class: logic ports.
<u> </u>	American XNOR port, type: node, fillable (node[american xnor port]{}). Class: logic ports.
	American BUFFER port, type: node, fillable (node[american buffer port]{}). Class: logic ports.
	American NOT port, type: node, fillable (node[american not port]{}). Class: logic ports.

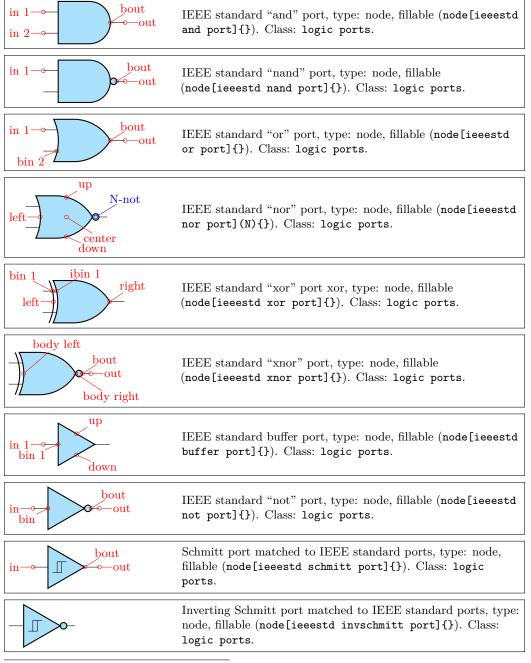
There is no "european" version of the following symbols; for now they are used both in american and european styles, but iy may change in the future.

Non-Inverting Schmitt trigger, type: node, fillable (node[schmitt]{}). Class: logic ports.
Inverting Schmitt trigger, type: node, fillable (node[invschmitt]{}). Class: logic ports.

3.24.2 IEEE logic gates

In addition to the legacy ports, since release 1.1.0, logic ports following the recommended geometry of distinctive-shape symbols in IEEE Std 91a-1991 Annex A (Recommended symbol proportions) are also available 23 .

These ports are completely independent from the legacy set (either american or european); they are not earbled by default because the relative size of the ports is very different from the legacy ones, and that will disrupt every schematic (especially if drawn with absolute coordinate). If you want to use them as default, you can use the command \ctikzset{logic ports=ieee} and by default the shapes and port, or port and so on will be the IEEE standard ones.



 $^{^{23}}$ Thanks to Jason for proposing it and digging out the info, see this GitHub issue.

west- © -east	Inverting dot for IEEE ports, type: node, fillable (node[notcirc]{}). Class: logic ports.
north west south east	Schmitt symbol to add to input pins if needed, type: node, fillable (node[schmitt symbol]{}). Class: logic ports.

3.24.3 European Logic gates



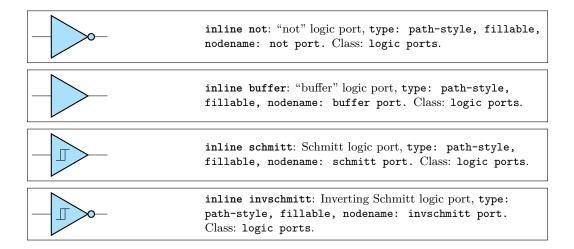
If (default behaviour) americanports option is active (or the style [american ports] is used), the shorthands and port, or port, buffer port, nand port, nor port, not port, xor port, xnor port, schmitt port and invschmitt port are equivalent to the american version of the respective logic port.

If otherwise europeanports option is active (or the style [european ports] is used), the shorthands and port, or port, buffer port, nand port, nor port, not port, xor port, xnor port are equivalent to the european version of the respective logic port; schmitt port and invschmitt port are the same as in american ports style.

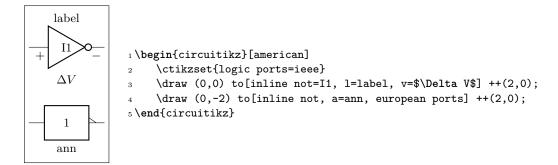
Finally, for version 1.1.0 and up, you can use the style ieee ports to set the shorthands to the set of ieeestd ports. (There is no global option for this).

3.24.4 Path-style logic ports

The one-input, one-output ports have a handy path-style equivalent; they are the following:



Those ports follows the current selected style, although you can change it on the fly (even if it has not a lot of sense); you can apply labels, annotations and (again, not a lot of sense) voltages to them. The assigned value is typeset as if it were the main text of the node.



3.24.5 American ports usage

Since version 1.0.0, the default shape of the family of american "or" ports has changed to a more "pointy" one, for better distinguish them from the "and"-type ports. You can still going back to the previous aspect with the key american or shape that can be set to pointy or roundy. The legacy style will enact the old, roundy style also.

```
1\begin{circuitikz}[
     american]
     % legacy shapes
     \ctikzset{american or shape=roundy}
     \ctikzset{logic ports/fill=yellow}
     \node [or port](01) at (0,0) {};
     \node [nor port](02) at (0,-1.5) {};
     \node [xor port](03) at (0,-3) {};
     \node [xnor port](04) at (0,-4.5) {};
     \begin{scope} [xshift=3cm]
10
     % new shapes
11
         \ctikzset{american or shape=pointy}
12
13
         \node [or port](01) at (0,0) {};
         \node [nor port](02) at (0,-1.5) {};
14
         \node [xor port](03) at (0,-3) {};
15
         \node [xnor port](04) at (0,-4.5) {};
     \end{scope}
17
18 \end{circuitikz}
```

3.24.5.1 American logic port customization Logic port class is called logic ports, so you can scale them all with logic ports/scale (default 1.0).

As for most components, you can change the width and height of the ports; the thickness is given by the parameter tripoles/thickness (default 2).

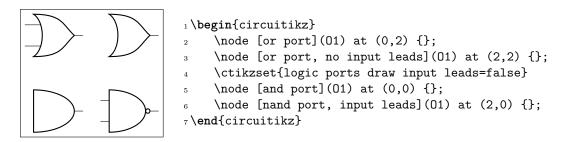
It is possible to change height and width of the logic ports using the parameters tripoles/american type port/ plus width or height:

```
1\tikz \draw (0,0) node[nand port] {}; \par
2\ctikzset{tripoles/american nand port/input height=.2}
3\ctikzset{tripoles/american nand port/port width=.4}
4\ctikzset{tripoles/thickness=4}
5\tikz \draw (0,0) node[nand port] {};
```

This is especially useful if you have ports with more than two inputs, which are instantiated with the parameter number inputs:

```
| begin{circuitikz}
| 2 \draw (0,3) node[american and port] (A) {P1};
| 3 \begin{scope}
| 4 \ctikzset{tripoles/american or port/height=1.6}
| 5 \draw (A.out) -- ++(0.5,0) |
| 6 node[american or port, |
| 7 number inputs=5, |
| 8 anchor=in 1] (B) {P2};
| 9 \end{scope}
| 10 \draw (0,1.5) node[american or port] (C) {P3};
| 1 \draw (C.out) |- (B.in 2);
| 12 \end{circuitikz}
```

You can suppress the drawing of the logic ports input leads by using the boolean key logic ports draw input leads (default true) or, locally, with the style no inputs leads (that can be reverted with input leads), like in the following example. The anchors do not change and you have to take responsibility do do the connection to the "border"-anchors.



This is useful if you need to draw a generic port, like the one following here:

```
1\begin{circuitikz}
2 \ctikzset{tripoles/american nand port/height=1.6}
3 \draw (0,0)
4 node[american nand port,
5 circuitikz/tripoles/american nand port/height=1.1,
6 number inputs=5, no input leads,
7 ] (B) {Pn};
8 \draw (B.in 1) -- (B.bin 1) (B.in 5) -- (B.bin 5);
9 \node[rotate=90] at (B.in 3) {\dots};
10 \end{circuitikz}
```

In an analogous manner, there is a setting logic ports draw output leads (and a corresponding style no output leads) that suppresses the drawing of the output lead. A shortcut boolean key logic ports draw leads will suppress or enable all leads (the corresponding styles are no leads and all leads).

You can tweak the appearance of american "or" family (or, nor, xor and xnor) ports, too, with the parameters inner (how much the base circle go "into" the shape, default 0.3) and angle (the angle at which the base starts, default 70).

```
1\tikz \draw (0,0) node[xnor port] {};
2\ctikzset{tripoles/american xnor port/inner=.7};
3\ctikzset{tripoles/american xnor port/angle=40};
4\tikz \draw (0,0) node[xnor port] {};
```

3.24.5.2 American logic port anchors These are the anchors for logic ports:



You have also "border pin anchors":



These anchors are especially useful if you want to negate inputs:

```
P1 P2 P2
```

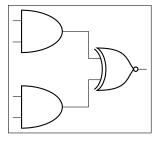
```
1 \begin{circuitikz}
2 \draw (0,3) node[american and port] (A) {P1};
3 \node at (A.bin 1) [ocirc, left]{};
4 \begin{scope}
5  \ctikzset{tripoles/american or port/height=1.6}
6  \draw (A.out) -- ++(0.5,0) node[american or port,
7  number inputs=5, anchor=in 1] (B) {P2};
8  \node at (B.bin 3) [ocirc, left]{};
9 \end{scope}
10 \draw (0,1.5) node[american or port] (C) {P3};
11 \node at (C.bin 2) [ocirc, left]{};
12 \draw (C.out) |- (B.in 2);
13 \end{circuitikz}
```

As you can see, the center anchor is (for historic reasons) not in the center at all. You can fix this with the command \ctikzset{logic ports origin=center}:

```
\begin{array}{c|c}
1 & - & \\
2 & - & \\
\end{array}

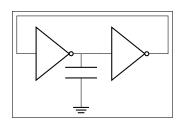
center
```

```
1\begin{circuitikz}
2\ctikzset{logic ports origin=center}
3\draw (0,0) node[and port] (myand) {}
4  (myand.in 1) node[anchor=east] {1}
5  (myand.in 2) node[anchor=east] {2}
6  (myand.out) node[anchor=west] {3};
7\draw[<-] (myand.center) -- ++(1,-1)
8  node{center};
9\end{circuitikz}</pre>
```



```
1\begin{circuitikz} \draw
2 (0,2) node[and port] (myand1) {}
3 (0,0) node[and port] (myand2) {}
4 (2,1) node[xnor port] (myxnor) {}
5 (myand1.out) -| (myxnor.in 1)
6 (myand2.out) -| (myxnor.in 2)
7;\end{circuitikz}
```

In the case of NOT, there are only in and out (although for compatibility reasons in 1 is still defined and equal to in):



```
1 \begin{circuitikz} \draw
2   (1,0) node[not port] (not1) {}
3   (3,0) node[not port] (not2) {}
4   (0,0) -- (not1.in)
5   (not2.in) -- (not1.out)
6   ++(0,-1) node[ground] {} to[C] (not1.out)
7   (not2.out) -| (4,1) -| (0,0)
8   ;\end{circuitikz}
```

This last circuit could be drawn also (and probably in a more natural manner) using the path-style components:

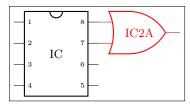
```
1 \begin{circu}
2 \draw (0,
3 coordinat
4 to[inline
5 -| ++(-5,
6 to[inline
7 \end{circuit
```

```
1 \begin{circuitikz} [american]
2     \draw (0,0) node [ground] {} to [C] ++(0,1.5)
3     coordinate(c)
4     to [inline not] ++(2.5,0) -- ++(0,1)
5     -| ++(-5,-1)
6     to [inline not] (c);
7 \end{circuitikz}
```

3.24.6 IEEE logic gates usage.

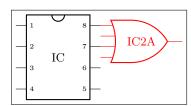
The rest of this section will assume you have issued the command \ctikzset{logic ports=ieee}, so that the short form of the names is used.

IEEE standard logic gates have a basic difference with the legacy ones: the proportions of their shapes does not change when you change the size, so you can't have a "tall" port or a "squatty" ones. The two-inputs gates, by default, have their default size designed so that they match the chips component (see 3.27).



```
1 \begin{circuitikz}
2    \draw (0,0) node[dipchip](C){IC} (C.pin 8)
3     node[or port, anchor=in 1,
4     color=red](A){IC2A};
5 \end{circuitikz}
```

If you need, say, a 4-inputs port, the port will look like this:



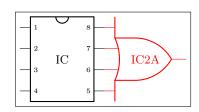
```
1 \begin{circuitikz}
2    \draw (0,0) node[dipchip](C){IC} (C.pin 8)
3         node[or port, anchor=in 1, number inputs=4,
4         color=red](A){IC2A};
5 \end{circuitikz}
```

...and in this case it is clear that it does not match. With standard ports, there are two possibilities. The first one is to scale the port; if you set the port height so that it has the same size (see "IEEE logic gates customization" below for details) as the number of ports, they will match again.



```
1 \begin{circuitikz}
2    \draw (0,0) node[dipchip](C){IC} (C.pin 8)
3     node[or port, anchor=in 1,
4     number inputs=4,
5     circuitikz/ieeestd ports/height=4,
6     color=red](A){IC2A};
7 \end{circuitikz}
```

But then the size of the port is quite "unusual". The solution is technical literature is to use what we can call a "rack" for the inputs; basically, only a certain number of pins are kept on the port, and the other are put on an extended input line.



```
1 \begin{circuitikz}
2    \draw (0,0) node[dipchip](C){IC} (C.pin 8)
3     node[or port, anchor=in 1,
4     number inputs=4,
5     inner inputs=2,
6     color=red](A){IC2A};
7 \end{circuitikz}
```

When using the inner inputs key, keep in mind the rule of thumbs:

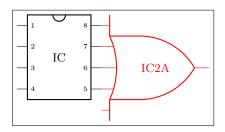
- the distance between the pins is matched with the chip ones when the inner inputs match the /ieeestd ports/height key;
- when the number of pins in the rack is odd, the result is often quite ugly, so try to avoid it.

For example, look at the following example; given that we are asking an odd number of pins on the rack, some of the inputs are drawn on the port's border, resulting in a less-than-ideal diagram.

```
1 8 2 7 7 3 IC 4 5 IC2A
```

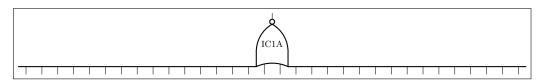
```
1 \begin{circuitikz}
2     \draw (0,0) node[dipchip](C){IC} (C.pin 8)
3         node[or port, anchor=in 1,
4         number inputs=5,
5         inner inputs=2,
6         color=red](A){IC2A};
7 \end{circuitikz}
```

In this case, if you don't like the solution, the better approach is to let the gate grow a bit.

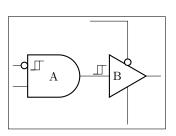


```
1 \begin{circuitikz}
2    \draw (0,0) node[dipchip](C){IC} (C.pin 8)
3     node[or port, anchor=in 1,
4     number inputs=5,
5     inner inputs=3,
6     circuitikz/ieeestd ports/height=3,
7     color=red](A){IC2A};
8 \end{circuitikz}
```

The good thing about the rack mechanism is that you can have quite big ports without problems.



You can use the additional elements (the notcirc and the schmitt symbol to obtain circuits like the following ones (well, a bit of a mix of conventions, but...):



```
1\begin{circuitikz}
2  \draw (0,0) node[and port](A){A} (A.out)
3  node[buffer port, anchor=in,
4     component text=left](B){B} (B.bin)
5  node[schmitt symbol, above left]{}
6  (A.bin 1) node[schmitt symbol, right]{};
7  \node [notcirc, left] at (A.bin 1) {};
8  \node [notcirc, above](C) at (B.up) {};
9  \draw (C.north) |- ++(-1,1) (B.down) --++(0,-1);
10 \end{circuitikz}
```

Notice the key component text=left that moves the label near to the left border of the component. There is also a \ctikzset{component text=left} if you prefer to have it as a default for all the IEEE ports.²⁴

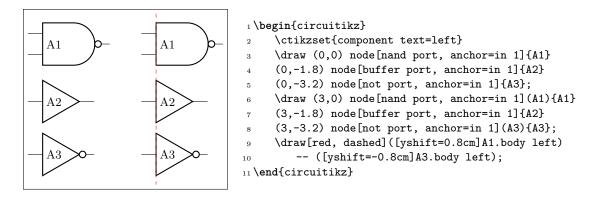
 $^{^{24}\}mathrm{You}$ can use the same key with amplifiers, too.

3.24.6.1 Stacking and aligning IEEE standard gates. The standard gates are designed so that they stacks up nicely when positioned using the external leads as anchors. Notice that the ports **do** have different sizes, but the leads lengths are designed to counter the differences.



The length of the external leads can be changed by the user, but notice that if you use a too small value you can jeopardize that property.

The single input ports (not port, buffer port and their Schmitt equivalent) are smaller that the six standard ports, so they are not kept aligned by default; the just have the same distance at the input side. For the not ports, the left position of the text results often in a better look (the centered text in the triangle seems to be much more at the right).



3.24.6.2 IEEE standard ports customization There are several parameters that can be used to customize the IEEE standard ports, although less than the ones in the legacy american ones — the basic shape is set to follow the IEEE recommendation. The basic parameters are shown in the following table, and they can be set via \ctikzset{ieeestd ports/...}

key	default	description
baselen	0.4	the basic length for every dimension, as a fraction of the (scaled) resistor length
height	2	the height of the port, in term of baselen. Pin distance is given by this parameter divided by the inner pins.
pin length	0.7	length of the external pin leads that are drawn with the port. This length is always calculated starting from the inner body of the shape.
not radius	0.154	radius of the "not circle" added to the negated-output ports. The default value is the IEEE recommended one.
xor bar distance	0.192	distance of the detached input shape in xor and xnor ports. The default value is the IEEE recommended one.
xor leads in	1	If set to 0, there will be no leads drawn between the detached input line and the body in the xor and xnor ports. IEEE recommends 1 here.
schmitt symbol size	0.3	Size of the small Schmitt symbol to use near input leads.

For example, using a **not radius** of **0.1** will give a "not ball" of the same size of a connecting pole, as it is in the legacy ports.

```
1 \begin{circuitikz}
2  \draw (0,2) node[xnor port](P){}
3     (P.out) to[short, -o] ++(1,0);
4  \ctikzset{ieeestd ports/.cd, not radius=0.1,
5     xor bar distance=0.3, xor leads in=0}
6  \draw (0,0) node[xnor port](P){}
7     (P.out) to[short, -o] ++(1,0);
8 \end{circuitikz}
```

In addition to the specific parameters, you can also apply to these ports the boolean style no input leads as in legacy ones (this simply does not draw the input leads, but the anchors stays where they should):

```
1\begin{circuitikz}
2\draw (0,0) node[nand port,
3 number inputs=5, no input leads,](B){Pn};
4\draw (B.in 1) -- (B.bin 1) (B.in 5) -- (B.bin 5);
5\node[rotate=90] at (B.in 3) {\dots};
6\end{circuitikz}
```

Changing the leads length must be done with a bit of care, because if the length is shorter than the port left or right extrusions strange things can happen (yes, a 4-inputs xnor gates is not so well defined...but it's a nice example to show):



```
1 \begin{circuitikz}
2   \ctikzset{ieeestd ports/pin length=0.2}
3   \draw (0,0) node[xnor port,
4     number inputs=4, inner inputs=2](B){};
5 \end{circuitikz}
```

3.24.6.3 IEEE standard ports anchors Geographical anchors define the rectangular space that the port is using, included the leads if presents.



Most of the anchors can be seen in the following diagram:



The inputs anchor are in *number* (on the tip of the lead) and bin *number* (border inputs) on the component's border (useful if you draw the ports with no inut leads). Additionally, you have ibin *number* (inner border inputs) for the x-type ports. The anchor named left is where a central border input would be.

In one-input ports (not port, the buffer, and Schmitt-type ports) you can use plain in or in 1 indifferently. On the output, out is on the tip of the lead, and bout on the rightmost border (so, if there is a negation circle, it is on it); right is the same as bout.

The main body of the port is marked with body left and body right anchors (as seen in the middle port in the diagram above); you have also an up and down anchors centered on the body (you can use them as enable signals or similar things).

Finally, the internal notcirc node used for the output negation is accessible with the name nodename-not, where nodename is the name given to the logic port node.

3.24.7 European logic port usage

European logic port are the same class as american and IEEE-style ones, and they obey the same class modifier. Moreover, you can use the no inputs pin as in the other logic ports to suppress input pins.

3.24.7.1 European logic port customization Normally the European-style logic port with inverted output are marked with a small triangle; if you want you can change it with the key tripoles/european not symbol; its default is triangle but you can set it to circle like in the following example. As you can see, the circle size is the same as the circuit poles; if you prefer the size used in the IEEE standard ports, you can use set it to ieee circle.

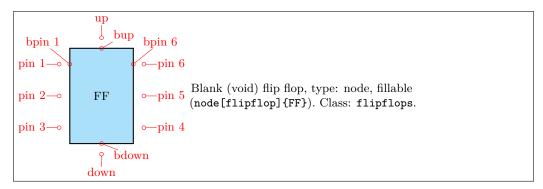
3.24.7.2 European logic port anchors The anchors are basically the same as in the american-style ports.



3.25 Flip-flops

Flip-flops (available since version 1.0.0) are an hybrid between the logic ports and the chips. They have a class by themselves (flipflops) but the default parameters are set at the same values as the logic gates one.

The default flip flop is empty: it is just a rectangular box like a blank dipchip with 6 pins.



As you can see, in a void flip flop no external pins are drawn: you have to define the meaning of each of them to see them. To define a specific flip-flop, you have to set a series of keys under the \ctikzset directory multipoles/flipflop/, corresponding to pins 1...6, u for "up" and d for "down":

- a text value t0, t1, ...t6, and tu and td (the last ones for up and down) which will set a label on the pin;
- a clock wedge flag (c0, ...c6, cu, cd), with value 0 or 1, which will draw a triangle shape on the border of the correspondig pin;
- a negation flag (n0, ...n6, nu, nd), with value 0 or 1, which will put and ocirc shape on the outer border of the corresponding pin.

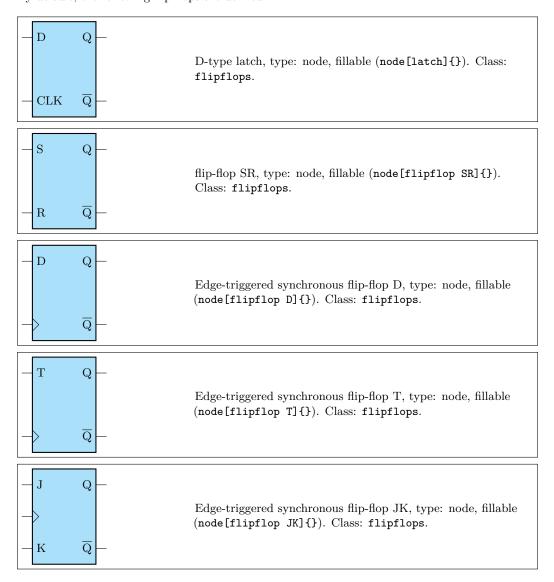
To set all this keys, an auxiliary style flipflop def is defined, so that you can do the following thing:

```
1\tikzset{flipflop AB/.style={flipflop,
2 flipflop def={t1=A, t3=B, t6=Q, t4={\ctikztextnot{Q}}},
3 td=rst, nd=1, c2=1, n2=1, t2={\texttt{CLK}}},
4}}
```

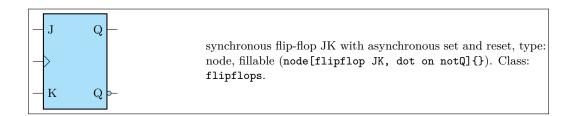
to obtain:



RST (created by RST). By default, the following flip-flops are defined:



If you prefer that the negated output is labelled Q and a dot indicating negation is shown, you can add the dot on not Q key:



You can also add "vertical" asynchronous set and reset (active low) adding the style add async SR to all of them:



3.25.1 Custom flip-flops

If you like different pin distributions, you can easily define different flip-flops to your taste. For example, somebody likes the clock pin on the bottom pin:

```
1\tikzset{flipflop myJK/.style={flipflop,
2 flipflop def={t1=J, t2=K, t6=Q, t4={\ctikztextnot{Q}}}, c3=1}}
3}
```



The standard definition of the default flip-flops are the following (in the file pgfcircmultipoles.tex):

```
1 \tikzset{
     % async
     latch/.style=\{flipflop, flipflop def=\{t1=D, t6=Q, t3=CLK, t4=\ctikztextnot\{Q\}\}\},
     flipflop SR/.style={flipflop, flipflop def={t1=S, t3=R, t6=Q, t4=\ctikztextnot{Q}}},
     flipflop D/.style={flipflop, flipflop def={t1=D, t6=Q, c3=1, t4=\ctikztextnot{Q}}},
     flipflop T/.style={flipflop, flipflop def={t1=T, t6=Q, c3=1, t4=\ctikztextnot{Q}}},
     flipflop JK/.style={flipflop,
                flipflop def=\{t1=J, t3=K, c2=1, t6=Q, t4=\ctikztextnot{Q}\}\},
     \% additional features
     add async SR/.style={flipflop def={%
11
                tu={\ctikztextnot{SET}}, td={\ctikztextnot{RST}}}},
12
     dot on notQ/.style={flipflop def={t4={Q}, n4=1}},
13
14 }
```

3.25.2 Flip-flops anchors

Flip-flops have all the standard geometrical anchors, although it should be noticed that the external pin are *outside* them. The pins are accessed by the number 1 to 6 for the lateral ones (like in DIP chips), and with the up and down anchors for the top and bottom one. All the pins have the "border" variant (add a b in front of them, no spaces).



If you have negated pins, you can access the ocirc shapes with the name as <nodename>-N<pin number>, and all the respective anchors (for example — myFFnode-N4.west).

3.25.3 Flip-flops customization

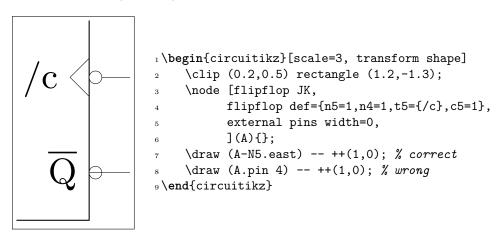
Flip-flop's size is controlled by the class parameters (like flipflops/scale) and the specific \ctikzset keys multipoles/flipflop/width and multipoles/flipflop/pin spacing. Class parameters are also used for line thickness and fill color. The default values are matched with the logic ports ones.

The fonts used for the pins 1...6 is set by the key multipoles/flipflop/font (by default \small in IFTEX and the equivalent in other formats) and the font used for pins u and d is multipoles/flipflop/fontud (\tiny by default). You can change it globally or specifically for each flip flop.

As in chips, you can change the length of the external pin with the key external pins width; you can for example have a pinless flip-flop like this:



Notice however that negated pins when the pins width is zero has to be handled with care. As explained in the poles sections, the ocirc shape is drawn at the end of the shape to cancel out the wires below; so if you use a pinless flipflop when you do the connection you should take care of connecting the symbol correctly. To this end, the shapes of the negation circles are made available as <nodename>-N<pin number>, as you can see in the next (contrived) example.



Normally the symbols on the flip-flop are un-rotated when you rotate the symbol, but as in case of chips, you can avoid it.



1 \begin{tikzpicture}

- draw (0,0) node[flipflop JK, add async SR]{};
- draw (3,0) node[flipflop JK, add async SR, rotate=90]{};
- \draw (7,0) node[flipflop JK, add async SR, rotate=90, rotated numbers]{}; $\frac{1}{100}$

3.26 Multiplexer and de-multiplexer

The shape used for muxes and de-muxes is probably the most configurable shape of the package; it has been added by Romano in v1.0.0. The basic shape is a multiplexer with 8 input pin, one output pin, and three control pins ($2^3 \rightarrow 1$ multiplexer). The pins are not named as input or output pins (see below for a full description for anchors) for reasons that will be clear later.



You can define a custom shape for the muxdemuxes using an interface similar to the one used in flip-flops; for example:

1\tikzset{demux/.style={muxdemux, muxdemux def={Lh=4, Rh=8, NL=1, NB=3, NR=8}}} will generate the following shape (the definition above is already defined in the package):



The shape can be also defined with an inset. For example it can be used like this to define a 1-bit adder (also already available):

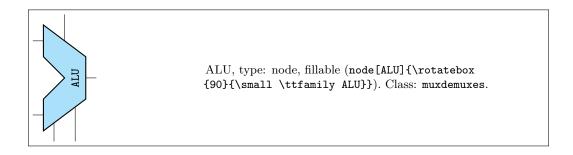
```
1\tikzset{one bit adder/.style={muxdemux,
2 muxdemux def={Lh=4, NL=2, Rh=2, NR=1, NB=1, w=1.5,
3 inset w=0.5, inset Lh=2, inset Rh=1.5}}}
```



One-bit adder, type: node, fillable (node[one bit adder]{\Large \$\oplus \$}). Class: muxdemuxes.

Or a Arithmetic Logic Unit (again, already defined by default):

```
1\tikzset{ALU/.style={muxdemux,
2 muxdemux def={Lh=5, NL=2, Rh=2, NR=1, NB=2, NT=1, w=2,
3 inset w=1, inset Lh=2, inset Rh=0, square pins=1}}}
```



3.26.1 Mux-Demux: design your own shape

In designing the shape there are several parameters to be taken into account. In the diagram on the right they are shown in a (hopefully) practical way. The parameter can be set in a node or in a style using the muxdemux def key as shown above, or set with \ctikzset as multipoles/muxdemux/Lh keys and so on.

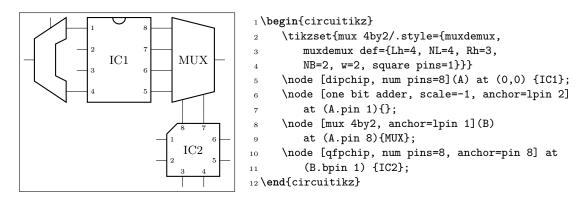


The default values are Lh = 8, Rh = 6, w = 3 and no inset: inset Lh = inset Rh = inset w = 0. In addition, you can set the following parameters:

NL, NR, NB, NT: number of pins relatively on the left, right, bottom and top side (default 8, 1, 3, 0). When an inset is active (in other words, when $\mathtt{Lh} > 0$) the pins are positioned on the top and bottom part, not in the inset; the exception is when the number of left pins is odd, in which case you have one pin set on the center of the inset. If you do not want a pin in one side, use 0 as number of pins.

square pins: set to 0 (default) if you want the square pins to stick out following the slope of the bottom or top side, 1 if you want them to stick out in a square way (see the example above for the ALU).

All the distances are multiple of multipoles/muxdemux/base len (default 0.4, to be set with \ctikzset), which is relative to the basic length. That value has been chosen so that, if you have a numbers of pins which is equal to the effective distance where they are spread (which is Lh without inset, Lh - (inset Lh) with an inset), then the distance is the same as the default pin distance in chips, as shown in the next circuit. In the same drawing you can see the effect of square pins parameters (without it, the rightmost bottom lead of the mux 4by2 shape will not connect with the below one).



3.26.2 Mux-Demux customization

Mux-demuxes have the normal parameters of their class (muxdemuxes): you can scale them with the \ctikzset key muxdemuxes/scale, control the border thickness with muxdemuxes/thickness and the default fill color with muxdemuxes/fill — they are set, by default, at the same values than logic ports.

External pins' length is controlled by the key multipoles/external pins width (default 0.2) or by the style external pins width. The parameter multipoles/external pins thickness is also respected. like in chips. In addition, like in logic ports, you can suppress the drawing of the leads by using the boolean key logic ports draw input leads (default true) or, locally, with the style no inputs leads (that can be reverted with input leads).

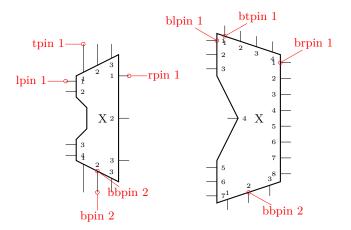
The main difference between setting external pins width to 0 or using no inputs lead is that in the first case the normal pin anchors and the border anchors will coincide, and in the second case they will not move and stay where they should have been if the leads were drawn.

3.26.3 Mux-Demux anchors

Mux-demuxes have a plethora of anchors. As in the case of chips, the geographic anchors mark the rectangle occupied by the component, without taking into account the pin leads.



The pins anchors are named lpin, rpin, bpin and tpin for the left, right, bottom and top pin respectively, and points to the "external" pin. The border pins are named the same, with a b added in front: blpin, brpin, bbpin and btpin. The following graph will show the numbering and position of the pin anchors.



The code that implemented the printing of the numbers (which in muxdemuxes, differently from chips, are never printed automatically) in the last graph is the following one.

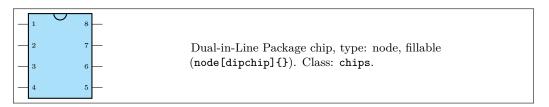
```
1 \begin{circuitikz}
2 \node [muxdemux, muxdemux def={NL=4, NR=3, NT=3, NB=3, w=2, inset w=0.5,
3    Lh=4, inset Lh=2.0, inset Rh=1.0, square pins=1}](C) at (0,0) {X};
4 \node [muxdemux, muxdemux def={NL=7, NR=8, NT=4, inset w=1.0,
5    inset Lh=4.0, inset Rh=0.0}](D) at (4,0) {X};
6 \foreach \myn/\NL/\NR/\NB/\NT in {C/4/3/3/3,D/7/8/3/4} {
7    \foreach \myp in {1,...,\NL} \node[right, font=\tiny] at (\myn.blpin \myp){\myp};
8    \foreach \myp in {1,...,\NR} \node[left, font=\tiny] at (\myn.brpin \myp) {\myp};
9    \foreach \myp in {1,...,\NB} \node[above, font=\tiny] at (\myn.btpin \myp){\myp};
10    \foreach \myp in {1,...,\NT} \node[below, font=\tiny] at (\myn.btpin \myp){\myp};
11}
```

You can use these shapes to draw a lot of symbols that are unavailable; using a bit of \LaTeX command trickery you can use them quite naturally too...

```
1 \def\tgate#1{
2     node[simple triangle, anchor=left, no input leads](#1-LR){}
3     (#1-LR.right) node[simple triangle, xscale=-1,
4     anchor=left](#1-RL){}
5     ([yshift=.5ex]#1-RL.btpin 1) node[ocirc]{}}
6 \begin{circuitikz}[
7     simple triangle/.style={muxdemux, muxdemux def={
8          NL=1, NR=1, NB=1, NT=1, w=2, Lh=2, Rh=0,
9     }}]
10     \draw (0,0) \tgate{A} (0,-2) \tgate{B};
11     \draw (A-RL.bpin 1) -- (B-RL.tpin 1);
12 \end{circuitikz}
```

3.27 Chips (integrated circuits)

 $\label{eq:circuitikZ} \mbox{ circuitikZ supports two types of variable-pin chips: DIP (Dual-in-Line Package) and QFP (Quad-Flat Package).}$





3.27.1 DIP and QFP chips customization

You can scale chips with the key chips/scale. As ever, that will **not** scale text size of the labels, when they are printed.

You can customize the DIP chip with the key multipoles/dipchip/width (with a default of 1.2) and the key multipoles/dipchip/pin spacing (default 0.4) that are expressed in fraction of basic lengths (see section 3.1.2). The height of the chip will be equal to half the numbers of pins multiplied by the spacing, plus one spacing for the borders.

For the QFP chips, you can only chose the pin spacing with multipoles/qfpchip/pin spacing key.

The pins of the chip can be "hidden" (that is, just a spot in the border, optionally marked with a number) or "stick out" with a thin lead by setting multipoles/external pins width greater than 0 (default value is 0.2, so you'll have leads as shown above). Moreover, you can transform the thin lead into a pad by setting the key multipoles/external pad fraction to something different form 0 (default is 0); the value expresses the fraction of the pin spacing space that the pad will use on both sides of the pin.

The number of pins is settable with the key num pins. Please notice that the number of pins must be even for dipchips and multiple of 4 for qfpchips, otherwise havoc will ensue.

You can, if you want, avoid printing the numbers of the pin with hide numbers (default show numbers) if you prefer positioning them yourself (see the next section for the anchors you can use). The font used for the pins is adjustable with the key multipoles/font (default \tiny) For special use you can suppress the orientation mark with the key no topmark (default topmark).

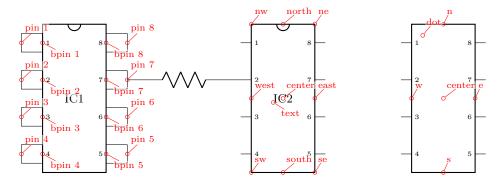
The line thickness of the main shape is controlled by multipoles/thickness (default 2) and the one of the external pins/pads with multipoles/external pins thickness (default 1).

```
\begin{circuitikz}
                       \ctikzset{multipoles/thickness=4}
                       \ctikzset{multipoles/external pins thickness=2}
                       \draw (0,0) node[dipchip,
                        num pins=12,
                        hide numbers,
  IC1
                        external pins width=0.3,
                        external pad fraction=4 ](C){IC1};
                       \draw (C.pin 1) -- ++(-0.5,0) to[R]
              9
                        ++(0,-3) node[ground]{};
              10
                        \node [right, font=\tiny]
                        at (C.bpin 1) {RST};
              12
                   \end{circuitikz}
              \begin{circuitikz}
                  \draw (0,0) node[dipchip,
         2
                    num pins=8,
         3
IC1
                    external pins width=0.0](C){IC1};
                  \det (C.pin 1) -- ++(-0.5,0) to[R]
         5
                    ++(0,-1.5) node[ground]{};
          6
              \end{circuitikz}
```

3.27.2 Chips anchors

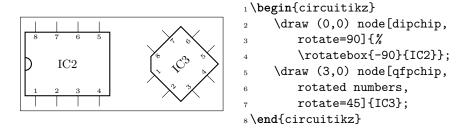
Chips have anchors on pins and global anchors for the main shape. The pin anchors to be used to connect wires to the chip are called pin 1, pin 2, ..., with just one space between pin and the number. Border pin anchors (bpin 1...) are always on the box border, and can be used to add numbers or whatever markings are needed. Obviously, in case of multipoles/external pins width equal to zero, border and normal pin anchors will coincide.

Additionally, you have geometrical anchors on the chip "box", see the following figure. The nodes are available with the full name (like north) and with the short abbreviations n, nw, w.... The dot anchor is useful to add a personalized marker if you use the no topmark key.



3.27.3 Chips rotation

You can rotate chips, and normally the pin numbers are kept straight (option straight numbers, which is the default), but you can rotate them if you like with rotated numbers. Notice that the main label has to be (counter-) rotated manually in this case.

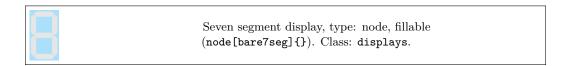


3.27.4 Chip special usage

You can use chips to have special, personalized blocks. Look at the following example, which is easily put into a macro.

```
1 \begin{circuitikz}
                          \ctikzset{multipoles/thickness=3}
                          \ctikzset{multipoles/dipchip/width=2}
                          \draw (0,0) node[dipchip,
                              num pins=10, hide numbers, no topmark,
                              external pins width=0](C){Block};
                          \node [right, font=\tiny] at (C.bpin 1) {RST};
RST
                          \node [right, font=\tiny] at (C.bpin 2) {IN1};
IN1
                          \node [right, font=\tiny] at (C.bpin 4) {/IN2};
                          \node [left, font=\tiny] at (C.bpin 8) {OUT};
    Block
           OUT
                     10
                          \draw (C.bpin 2) -- ++(-0.5,0) coordinate(extpin);
                     11
/IN2
                          \node [ocirc, anchor=0](notin2) at (C.bpin 4) {};
CLK
                          \draw (notin2.180) -- (C.bpin 4 -| extpin);
                          \draw (C.bpin 8) to[short,-o] ++(0.5,0);
                     14
                          \draw (C.bpin 5) ++(0,0.1) -- ++(0.1,-0.1)
                     15
                              node[right, font=\tiny]{CLK} -- ++(-0.1,-0.1);
                          \draw (C.n) -- ++(0,1) node[vcc]{};
                     17
                          \draw (C.s) -- ++(0,-1) node[ground]{};
                     19 \end{circuitikz}
```

3.28 Seven segment displays



The seven segment display lets you show values as if they were displayed in a classical seven segment display. 25

The main "bare" component is the one shown above, but for simplicity a couple of style interfaces are defined:

```
1 \begin{circuitikz}
2      \draw (0,0) node[seven segment val=A dot off box on]{};
3      \draw (1,0) node[seven segment val=- dot none box on]{};
4      \draw (0,-2) node[seven segment bits=1001001 dot empty box on]{};
5      \draw (1,-2) node[seven segment bits=0011101 dot none box off]{};
6 \end{circuitikz}
```

There are two main configuration methods. The first one is **seven segment val**, which will take an hexadecimal number or value and display it: the possible values are 0,...,15, plus A, B, C, D, E, F (or lowercase) and the symbol - (minus).

The other interface is **seven segment bits**, where you specify seven bits saying which segment must be on (please never specify a different number of bits, it will throw a very obscure error); you can see in the anchors the name of each segment.

 $^{^{25}\}mathrm{This}$ component has been loosely inspired by the package SevenSeg by Germain Gondor, 2009, see TeXexample.net.

The option dot specifies if you want a decimal dot or not. The key none will remove the dot and the space it would take; empty will not show the dot at all but reserve the space, and on or off will show the dot in the corresponding state.

The option box (can be on or off) simply toggles the drawing of the external box. You can separate it from the display with the key seven seg/box sep (default 1pt), and it will use the thickness specified in multipoles/thickness (The same as the chips).

You can use these option with the "bare" object bare7seg and the keys seven seg/bits (default 0000000), seven seg/dot (default none) and seven seg/box (default off); there is no option equivalent to the val interface.

3.28.1 Seven segments anchors

These are the anchors for the seven segment displays; notice that when the dot parameter is not none, the cell is a bit wider at the right side.



3.28.2 Seven segments customization

You can scale the seven segment display with the key displays/scale. This will scale the size of the digit, but not the absolute sizes shown below — if you want them to scale, yo have to do it manually.

You can change several parameters to adjust the displays:

```
1\ctikzset{seven seg/width/.initial=0.4}% relative to \pgf@circ@Rlen (scalable)
2\ctikzset{seven seg/thickness/.initial=4pt}% segment thickness (not scaled)
3\ctikzset{seven seg/segment sep/.initial=0.2pt}% gap between segments (not scaled)
4\ctikzset{seven seg/box sep/.initial=1pt}% external box gap (not scaled)
5\ctikzset{seven seg/color on/.initial=red}% color for segment "on"
6\ctikzset{seven seg/color off/.initial=gray!20!white} % ...and "off"
```

A couple of examples are shown below.

```
1 \begin{circuitikz} [scale=0.5]
2 \ctikzset{seven seg/width=0.2, seven seg/thickness=2pt}
3 \foreach \i in {0,...,15} \path (\i,0)
4    node[seven segment val=\i dot on box off]{};
5 \ctikzset{seven seg/color on=black}
6 \foreach \i in {0,...,15} \path (\i,-1.5)
7    node[seven segment val=\i dot off box off, fill=gray!30!white]{};
8 \ctikzset{seven seg/color on=green, seven seg/color off=yellow!30}
9 \foreach \i in {0,...,15} \path[color=red] (\i,-3)
10    node[seven segment val=\i dot none box on, xslant=0.2]{};
11 \end{circuitikz}
```



4 Labels and similar annotations

```
1 \begin{circuitikz}
R_1
          2 \draw (0,0) to[R, l=$R_1$] (2,0);
          3 \end{circuitikz}
          1 \begin{circuitikz}
R_1
              draw (0,0) to [R=$R_1$] (2,0);
          3\end{circuitikz}
          1 \begin{circuitikz}
              draw (0,0) to [R, i=$i_1$] (2,0);
          3 \end{circuitikz}
          1 \begin{circuitikz}
              \draw (0,0) to[R, v=$v_1$] (2,0);
          3\end{circuitikz}
R_1
          1\begin{circuitikz}
              draw (0,0) to [R=$R_1$, i=$i_1$, v=$v_1$] (2,0);
          3 \end{circuitikz}
v_1
R_1
          1\begin{circuitikz}
              draw (0,0) to [R=$R_1$, i=$i_1$, v=$v_1$] (2,0);
          3 \end{circuitikz}
```

Long names/styles for the bipoles can be used:



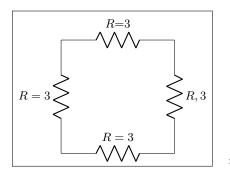
4.1 Labels and Annotations

Since Version 0.7, beside the original label (1) option, there is a new option to place a second label, called annotation (a) at each bipole.

The position of annotations and labels can be adjusted with $_$ and $\widehat{\ }.$

```
 \begin{array}{c} R_1 \\ \hline \\ 1 \, \mathrm{k}\Omega \\ \hline \\ 2 \, \mathrm{k}\Omega \\ \hline \\ 2 \, \mathrm{k}\Omega \\ \hline \\ 3 \, \mathrm{k}\Omega \\ \ 3 \, \mathrm{k}\Omega
```

Caveat: notice that the way in which circuitikz processes the options, there will be problems if the label (or annotation, voltage, or current) contains one of the characters = (equal) or , (comma), giving unexpected errors and wrong output. These two characters must be protected from the option parser using an \mbox{mbox} command, or redefining the characters with a $T_FX \mbox{def}$:

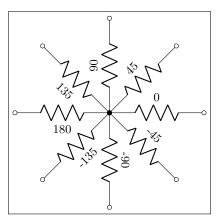


```
\def\eq{=}
\begin{circuitikz}
    % the following will fail:
    % \draw (0,0) to[R, l={$R=3}] (3,0);
    \draw (0,0) to[R, l=\mbox{$R=3$}] (3,0);
    \draw (0,0) to[R, l=$R\eq3$] (0,3);
    \draw (3,3) to[R, l=\mbox{$R,3$}] (3,0);
    % this works, but it has wrong spacing
    \draw (0,3) to[R, l=$R{=}3$] (3,3);
\end{circuitikz}
```

The default orientation of labels is controlled by the options smartlabels, rotatelabels and straightlabels (or the corresponding label/align keys). Here are examples to see the differences:



```
1\begin{circuitikz}
2\ctikzset{label/align = straight}
3\def\DIR{0,45,90,135,180,-90,-45,-135}
4\foreach \i in \DIR {
5 \draw (0,0) to[R=\i, *-o] (\i:2.5);
6}
7\end{circuitikz}
```



```
1 \begin{circuitikz}
2 \ctikzset{label/align = rotate}
3 \def\DIR{0,45,90,135,180,-90,-45,-135}
4 \foreach \i in \DIR {
5 \draw (0,0) to[R=\i, *-o] (\i:2.5);
6}
7 \end{circuitikz}
```

```
1\begin{circuitikz}
2\ctikzset{label/align = smart}
3\def\DIR{0,45,90,135,180,-90,-45,-135}
4\foreach \i in \DIR {
5\draw (0,0) to [R=\i, *-o] (\i:2.5);
6}
7\end{circuitikz}
```

You also can use stacked (two lines) labels. The example should be self-explanatory: the two lines are specified as 12=line1 and line2. You can use the keys 12 halign to control horizontal position (left, center, right) and 12 valign to control the vertical one (bottom, center, top).

```
\begin{circuitikz}[ american, ]
                                       %
                                       % default is 12 halign=1, 12 valign=c
             R_{CC}
R_{CC}
                                       \draw (0,0) to [R, 12_=$R_{CC}$ and <math>SI{4.7}{k}
             4.7 \,\mathrm{k}\Omega
4.7 \,\mathrm{k}\Omega
                                                       , 12 valign=t] (2,0);
                                       \label{lem:condition} $$\operatorname{CC}$ and $\operatorname{SI}\{4.7\}_k$$
                                             ohm},
                                                                      ] (0,2);
              R_{CC}
   R_{CC}
                                       \draw (0,0) to [R, 12_=$R_{CC}$ and <math>SI{4.7}{k}
              4.7\,\mathrm{k}\Omega
 4.7\,\mathrm{k}\Omega
                                             ohm}, 12 halign=c, 12 valign=b] (-2,0);
                                       \draw (0,0) to [R, 12_=$R_{CC}$ and <math>SI{4.7}{k}
                                             ohm}, 12 halign=r, 12 valign=c] (0, -2);
                                   \end{circuitikz}
                                   \begin{circuitikz}[ american, ]
  R_{CC}
                                       draw (0,0) to [R, 12^=$R_{CC}$ and <math>SI{4.7}{k}
               R_{CC}
 4.7\,\mathrm{k}\Omega
                                             ohm}, 12 halign=c, 12 valign=b] (2,0);
              4.7\,\mathrm{k}\Omega
                                        \draw (0,0) to [R, 12^=$R_{CC}$ and <math>SI{4.7}{k}
                                             ohm}, 12 halign=c, ] (0,2);
R_{CC}
                                        draw (0,0) to[R, 12^=$R_{CC}$ and <math>SI\{4.7\}\{k\}
4.7 \,\mathrm{k}\Omega
                                                     , 12 valign=t] (-2,0);
                                             ohm},
              R_{CC}
                                        \label{lem:condition} $$\operatorname{CC}$ and $\operatorname{SI}_{4.7}_{k}$$
             4.7\,\mathrm{k}\Omega
                                             ohm}, 12 halign=c, 12 valign=t](0, -3);
                                   \end{circuitikz}
```

4.2 Currents and voltages

The default direction/sign for currents and voltages in the components is, unfortunately, not standard, and can change across country and sometime across different authors. This unfortunate situation created a bit of confusion in circuitikz across the versions, with several incompatible changes starting from version 0.5. From version 0.9.0 onward, the maintainers agreed a new policy for the directions of bipoles' voltages and currents, depending on 4 different possible options:

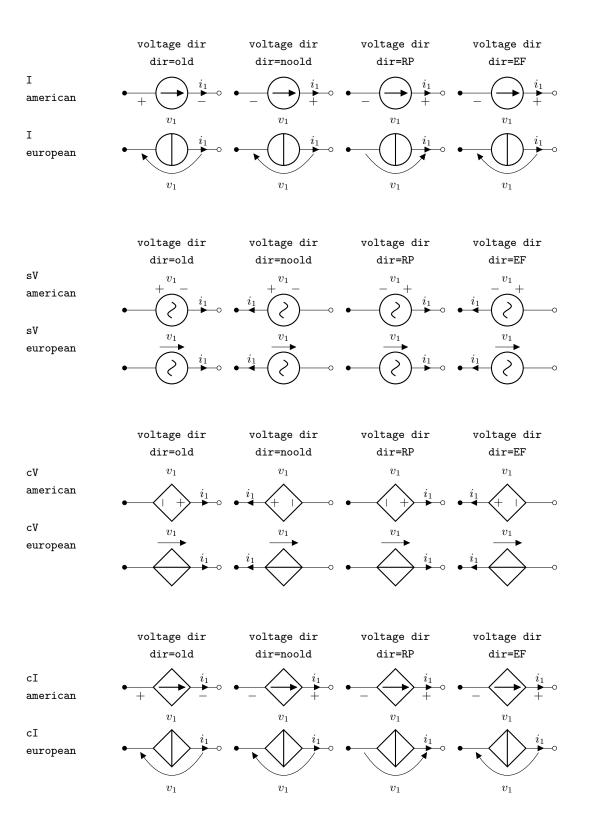
- oldvoltagedirection, or the key style voltage dir=old: Use old way of voltage direction having a difference between european and american direction, with wrong default labelling for batteries (it was the default before version 0.5);
- nooldvoltagedirection, or the key style voltage dir=noold: The standard from version 0.5 onward, utilize the (German?) standard of voltage arrows in the direction of electric fields (without fixing batteries);
- RPvoltages (meaning Rising Potential voltages), or the key style voltage dir=RP: the arrow is in direction of rising potential, like in oldvoltagedirection, but batteries and current sources are fixed so that they follow the passive/active standard: the default direction of v and i are chosen so that, when both values are positive:
 - in passive component, the element is *dissipating power*;
 - in active components (generators), the element is *generating power*.
- EFvoltages (meaning Electric Field voltages), or the key style voltage dir=EF: the arrow is in direction of the electric field, like in nooldvoltagedirection, but batteries are fixed;

Notice that the four styles are designed to be used at the environment level: that is, you should use them at the start of your environment as in \begin{circuitikz}[voltage dir=old] ... and not as a key for single components, in which case the behaviour is not guaranteed.

The standard direction of currents, flows and voltages are changed by these options; notice that the default drops in case of passive and active elements is normally different. Take care that in the case of noold and EFvoltages also the currents can switch directions. It is much easier to understand the several behaviors by looking at the following examples, that have been generated by the code:

```
1\foreach\element in {R, C, D, battery2, V, I, sV, cV, cI}{%
     \noindent\ttfamily
     \begin{tabular}{p{2cm}}
3
         \element \\ american \\[15pt]
4
        \element \\ european \\
5
     \end{tabular}
6
     \foreach\mode in {old, noold, RP, EF} {
7
         \begin{tabular}{0{}10{}}
            \multicolumn{1}{c}{voltage dir} \\
9
            \multicolumn{1}{c}{dir=\mode} \\[4pt]
10
            \begin{tikzpicture}[
11
                american, voltage dir=\mode,
12
13
                draw (0,0) to[\ell, *-o, v=$v 1$, i=$i 1$, ] (2.5,0);
14
            \end{tikzpicture}\\
15
            \begin{tikzpicture}[
                european, voltage dir=\mode,
17
18
                \draw (0,0) to[\element, *-o, v=$v_1$, i=$i_1$, ] (2.5,0);
19
            \end{tikzpicture}
        \end{tabular}
21
         \medskip
22
     }
23
     \par
24
25 }
```



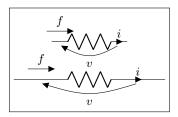


Obviously, you normally use just one between current and flows, but anyway you can change direction of the voltages, currents and flows using the complete keys $i_>$, $i^<$, $i^>$, as shown in the following examples.

This manual has been typeset with the option RPvoltages.

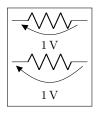
4.2.1 Common properties of voltages and currents

Currents, voltages and flows (see later) are positioned along, or across, the part of the wires that connect the inner component to the rest of the circuit. So, changing the length of the connection (the coordinates that embrace the to[...] command) will change the position of the components.



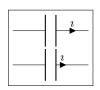
```
1\begin{circuitikz}
2 \draw (-1,1) to[R, v=$v$, i=$i$, f>^=$f$] (1,1);
3 \draw (-2,0) to[R, v=$v$, i=$i$, f>^=$f$] (2,0);
4\end{circuitikz}
```

However, you can override the properties voltage/distance from node (default 0.5: how distant from the initial and final points of the path the arrow starts and ends or the plus and minus symbols are drawn) and voltage/bump b (how high the bump of the arrow is — how curved it is, default 1.5), and also voltage/european label distance (how distant from the normal position the voltage label will be, default 1.4) on a per-component basis, in order to fine-tune the voltages:



```
1 \tikz \draw (0,0) to[R, v=1<\volt>] (2,0); \par
2 \ctikzset{voltage/distance from node=.1}
3 \ctikzset{voltage/bump b=2.5}
4 \tikz \draw (0,0) to[R, v=1<\volt>] (2,0);
```

The same concept as distance from node applies to the key current/distance for the position of the current's arrow:



```
1\tikz \draw (0,0) to[C, i=$\imath$] (2,0); \par
2\ctikzset{current/distance = .2}
3\tikz \draw (0,0) to[C, i=$\imath$] (2,0);
```

You can change globally these parameters by defining a component-specific key; you have to use the internal name of the component (in the component list, is the nodename without the terminal "shape" part):

```
1 V 2 V 1 V 2 V
```

```
1\tikz \draw (0,0) to[R, v=1<\volt>] (1.5,0)
2     to[C, v=2<\volt>] (3,0); \par
3\ctikzset{bipoles/capacitor/voltage/distance from node/.
        initial=.7}
4\tikz \draw (0,0) to[R, v=1<\volt>] (1.5,0)
5     to[C, v=2<\volt>] (3,0); \par
```

Note the .initial; you have to create such key the first time you use it. These kind of adjustments are not guaranteed to work in future upgrades, though; if you have to create a key you are somehow touching the internal structure of the package; it's much safer to create a style.

One common request is to change the style of the arrows (both head and line) of these elements. Voltages, currents and flows are part of the same path of the component, so this is not possible in simple way; you have to drawn your own with TikZ commands using the facilities explained in section 4.8.

4.3 Currents

Inline (along the wire) currents are selected with $i_>$, $i^<$, $i^>$, $i^>$, and various combination; the default position and direction is obtained with the simple key i=...

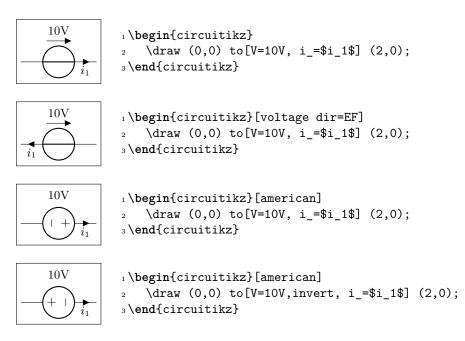
Basically, $\hat{}$ and $\hat{}$ control if the label is above or below the line (above and below **do** depend on the direction of the component path), and < and > the direction of the arrow; swapping them (from for example from $i^>$ to $i^>$) will switch the side of the component where the symbol is drawn. See the following examples:

```
1\begin{circuitikz}
   draw (0,0) to [R, i^=$i_1$] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
_{2} \draw (0,0) to [R, i >=\$i 1\$] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
   draw (0,0) to[R, i^{=}i_1^{1}] (2,0);
3\end{circuitikz}
1 \begin{circuitikz}
   draw (0,0) to[R, i_<=$i_1$] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
   draw (0,0) to[R, i>^=$i_1$] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
   draw (0,0) to[R, i>=$i_1$] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
   draw (0,0) to[R, i<^=$i_1$] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
   \draw (0,0) to[R, i<=$i_1$] (2,0);
3 \end{circuitikz}
```

Also notice that the direction of the path is important:

Default directions can change if the component is active or passive, ²⁶ following the chosen global voltage direction strategy (see section 4.2).

 $^{^{26}}$ This, in hindsight, has been a bad feature — and I'm partly responsible for it. But removing it would create too small variations in circuits, so it stays.



Current generators with the direct label (the one obtained by, for example, $\mathtt{I} = \mathtt{something}$) will treat it as a current:

```
1\begin{circuitikz}
2 \draw (0,0) to[I=$a_1$] (2,0);
3\end{circuitikz}
```

If you use the option americancurrent or using the style [american currents] you can changle the style of current generators.

```
1\begin{circuitikz}[american currents]
2 \draw (0,0) to[I=$a_1$] (2,0);
3\end{circuitikz}
```

4.4 Flows

As an alternative for the current arrows, you can also use the following "flows". They can also be used to indicate thermal or power flows. The syntax is pretty the same as for currents.

```
| 1 | begin{circuitikz}
| 2 | \draw (0,0) to[R, f=$i_1$] (3,0);
| 3 | \end{circuitikz}
| 1 | begin{circuitikz}
| 2 | \draw (0,0) to[R, f<=$i_1$] (3,0);
| 3 | \end{circuitikz}
| 1 | begin{circuitikz}
| 2 | \draw (0,0) to[R, f_=$i_1$] (3,0);
| 3 | \end{circuitikz}
| 1 | \text{begin{circuitikz}
| 2 | \draw (0,0) to[R, f_=$i_1$] (3,0);
| 3 | \end{circuitikz}
| 3 | \draw (0,0) to[R, f_=$i_1$] (3,0);
| 3 | \end{circuitikz}
| 1 | \text{begin{circuitikz}
| 3 | \draw (0,0) to[R, f_=$i_1$] (3,0);
| 3 | \end{circuitikz}
| 4 | \draw (0,0) to[R, f_=$i_1$] (3,0);
| 4 | \draw (0,0) to[R, f_=$i_1$] (3,0);
| 5 | \draw (0,0) to[R, f_=$i_1$] (3,0);
| 6 | \draw (0,0) to[R, f_=$i_1$] (3,0);
| 7 | \text{begin{circuitikz}} \text{circuitikz} \text{circuitikz
```

```
1\begin{circuitikz}
2 \draw (0,0) to[R, f<^=$i_1$] (3,0);
3\end{circuitikz}

1\begin{circuitikz}
2 \draw (0,0) to[R, f<_=$i_1$] (3,0);
3\end{circuitikz}

1\begin{circuitikz}
2 \draw (0,0) to[R, f<_=$i_1$] (3,0);
3\end{circuitikz}

1\begin{circuitikz}
3 \draw (0,0) to[R, f>_=$i_1$] (3,0);
3\end{circuitikz}
```

4.5 Voltages

See the introduction at Currents and Voltages (section 4.2, page 133) for the default direction of the voltage and currents.

Voltages come in four different styles: European (with curved or straight arrows) and American (with signs that can stay near the wire or raised at the label level).

Direction and position of the symbols are controlled in the same way as for the currents (see section 4.3) with the $_^<>$ symbols.

4.5.1 European style

The default, with curved arrows. Use option europeanvoltage or style [european voltages], or setting (even locally) voltage=european.

The default direction for active elements can change, depending on the global voltage dir setting, so be careful.



```
1 \begin{circuitikz}
2 \draw (0,0) to[I<=1A, v_=$u_1$] (2,0);
3 \end{circuitikz}</pre>
```



```
1\begin{circuitikz}
2 \draw (0,0) to[I=$~$,l=1A, v_=$u_1$] (2,0);
3\end{circuitikz}
```



```
1\begin{circuitikz}
2 \draw (0,0) to[I,l=1A, v_=$u_1$] (2,0);
3\end{circuitikz}
```

Moreover, for historical reasons, voltage generators have differently looking arrows (they are straight even in curved European style).

```
\begin{array}{c|c} u_1 \\ \hline \\ 1V \end{array}
```

```
1\begin{circuitikz}
2 \draw (0,0) to[battery,l_=1V, v=$u_1$, i=$i_1$] (2,0);
3\end{circuitikz}
```



```
1 \begin{circuitikz}
2     \draw (0,0) to[V=10V, i_=$i_1$] (2,0);
3 \end{circuitikz}
```

You can change this last thing by forcing "off" the status of "voltage generator" of the component; but now the normal (passive) rule will apply, so, again, be careful.



As for the currents, the direct label of voltage sources is passed as a voltage:



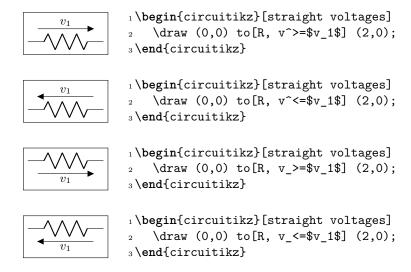
```
1\begin{circuitikz}
2 \draw (0,0) to[cV=$k\cdot a_1$] (2,0);
3\end{circuitikz}
```

The following results from using the option americanvoltage or the style [american voltages].

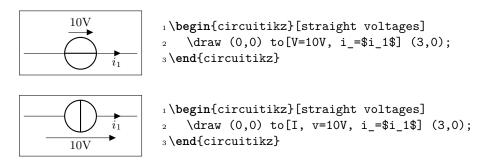
```
1\begin{circuitikz}[american voltages]
2 \draw (0,0) to[V=$a_1$] (2,0);
3\end{circuitikz}
```

4.5.2 Straight European style

Using straight arrows. Use option straightvoltages or style [straight voltages], or setting (even locally) voltage=straight.



Again, voltage generators are treated differently:



And you can override that with bipole/is voltage keeping into account that the default direction will be the one of passive components:

4.5.3 American style

Use option americanvoltage or set [american voltages] or use the option voltage=american.

```
 \begin{array}{c|c} v_1 & & \text{$^1$ begin{circuitikz}[american voltages]}\\ & & \text{$^2$ draw (0,0) to[R, v^*]$} \end{array} \\ & & \text{$^2$ draw (0,0) to[R, v^*]$} \end{array} \\ & & \text{$^1$ begin{circuitikz}[american voltages]}\\ & & \text{$^2$ draw (0,0) to[R, v^*]$} \end{array} \\ & & \text{$^3$ end{circuitikz}$} \end{aligned}
```

```
| \lambda \rightarrow \rightar
```

4.5.4 Raised American style

Since version 1.2.1, "raised" American voltages are available; to use them, set the style [raised voltages] or use the option voltage=raised. This is a version of the American-style voltage where the signs are raised to the level of the label. The label is centered between the two signs, and the position of the signs is calculated supposing that the label itself will be pretty simple; if you have very big labels you will need to adjust the position with voltage shift and/or the voltage/distance from node properties (see section 4.2.1).

```
1\begin{circuitikz}[raised voltages]
              \draw (0,0) to [R, v^>=$v_1$] (2,0);
          3 \end{circuitikz}
          1 \begin{circuitikz}[raised voltages]
          2 \draw (0,0) to[R, v^<=$v_1$] (2,0);</pre>
          3 \end{circuitikz}
          1 \begin{circuitikz}[raised voltages]
              draw (0,0) to[R, v_>=$v_1$] (2,0);
v_1
          3 \end{circuitikz}
          1 \begin{circuitikz}[raised voltages]
             draw (0,0) to[R, v_<=$v_1$] (2,0);
v_1
          3 \end{circuitikz}
          1\begin{circuitikz}[american]
              \ctikzset{voltage=raised}
              draw (0,0) to [I=1A, v_=$u_1$] (2,0);
u_1
          4 \end{circuitikz}
          1 \begin{circuitikz}[raised voltages]
              draw (0,0) to [I <= 1A, v_= i_1 ] (2,0);
          3 \end{circuitikz}
```

4.5.5 Voltage position

It is possible to move the arrows and the plus or minus signs away form the component with the key voltages shift (default value is 0, which gives the standard position):



```
1 \begin{circuitikz}[]
2   \draw (0,0) to[R, v=$v_1$, i=$i_1$] (2,0);
3   \draw (0,-1) to[R, v=$v_1$, i=$i_1$,
4     voltage shift=0.5] (2,-1);
5   \draw (0,-2) to[R, v=$v_1$, i=$i_1$,
6     voltage shift=1.0] (2,-2);
7 \end{circuitikz}
```



```
1 \begin{circuitikz}[american voltages, voltage shift=0.5]
2 \draw (0,0) to[R, v=$v_1$, i=$i_1$] (2,0);
3 \end{circuitikz}
```

Negative values do work as expected:

```
\begin{array}{c|c} + v_1 & - \\ \hline - & \downarrow \\ + v_1 & - \\ \hline - & \downarrow \\ \hline \end{array}
```

```
1\begin{circuitikz}[raised voltages]
2   \draw (0,1.5) to[R, v^=$v_1$, i=$i_1$] ++(2,0);
3   \draw (0,0) to[R, v^=$v_1$, i=$i_1$,
4      voltage shift=-1.0] ++(2,0);
5 \end{circuitikz}
```

You can fine-tune the position of the + and - symbols and the label in independent way using voltage/shift (default 0.0 for the former and voltage/american label distance (the distance of the label form the lines of the symbols, default 1.4) for the latter.



```
1 \begin{circuitikz} [american voltages]
2    \draw (0,1) to [R, v=$v_1$, i=$i_1$] ++(2,0);
3    % normally 1.4, make it tighter
4    \ctikzset{voltage/american label distance=0.5}
5    \draw (0,0) to [R, v=$v_1$, i=$i_1$] ++(2,0);
6 \end{circuitikz}
```

Notes that american voltage also affects batteries.



```
begin{circuitikz}[voltage shift=0.5]
draw (0,0) to[battery,1_=1V, v=$u_1$, i=$i_1$] (2,0);
hend{circuitikz}
```

```
\begin{array}{c|c}
 & u_1 \\
 & + \\
 & & \downarrow \downarrow \downarrow i_1 \\
 & 1V
\end{array}
```

When using american or straight voltage style, the open component is treated differently, and the voltage is placed in the middle of the open space²⁷:

If you want or need to maintain the old behavior for open voltage, you can set the key american open voltage to legacy (the default is the new behavior, which correspond to the value center).

4.5.6 American voltages customization

Since 0.9.0, you can change the font²⁸ used by the american voltages style, by setting to something different from nothing the key voltage/american font (default: nothing, using the current font) style:

Also, if you want to change the symbols (sometime just the + sign is drawn, for example, or for highlighting something), using the keys voltage/american plus and voltage/american minus (default \$+\$ and \$-\$).

This could be especially useful if you define a style, to use like this:

 $^{^{27}\}mathrm{Since}\ \mathtt{v1.1.2},$ thank to an issue opened by user $\mathtt{rhandley}$ on GitHub.

²⁸There was a bug before, noticed by the user dzereb on tex.stackexchange.com which made the symbols using different fonts in a basically random way. In the same page, user campa found the problem. Thanks!

4.6 Changing the style of labels and text ornaments

Since version 0.9.5, it is possible to change the style of bipole text ornaments (labels, annotations, voltages etc) by using the appropriate styles or keys. The basic style applied to the text are defined in the <code>/tikz/circuitikz</code> key directory and applied to every node that contains the text; you can also change them locally by using the tikz direct keys in local scopes.

For example, you can make all annotations small by using:

```
\ctikzset{bipole annotation style/.style={font=\small}}
```

And/or change (override) the setting in one specific bipole using:

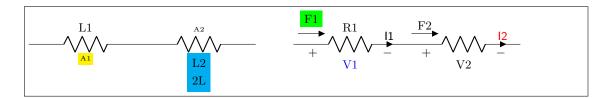
```
...to[bipole annotation style={color=red}, R, a={Red note}]...
```

where the annotation will be in normal font (it has been reset!) and red, or append to the style:

```
...to[bipole annotation append style={color=red}, R, a={Red small note}]...
```

Caveat: you have to put the style changing key at the start of the to arguments to have any effect²⁹.

The available styles and commands are bipole label style, bipole annotation style, bipole voltage style, bipole current style, and bipole flow style. The following example shows a bit of everything.



```
1 \begin{circuitikz}[american]
```

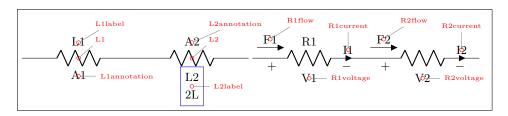
```
ctikzset{bipole annotation style/.style={font=\tiny}}
ctikzset{bipole current style/.style={font=\tiny}}
ctikzset{bipole current style/.style={font=\small\sffamily}}
draw (0,0) to [bipole annotation append style={fill=yellow}, R=L1, a=A1] ++(3,0)
for [bipole label style={fill=cyan}, R, 12_=L2 and 2L, a^=A2] ++(3,0);
draw (7,0) to [bipole voltage style={color=blue},
bipole flow style={fill=green, outer sep=5pt},
R=R1, v=V1, i=I1, f>^=F1] ++(3,0)
for [bipole current append style={color=red}, R, v<=V2, i^=I2, f>^=F2] ++(3,0);
lower [color=thin]
```

4.7 Accessing labels text nodes

Since 0.9.5, you can access all the labels nodes³⁰ using special node names. So, if you use name to give a name to the bipole node, you can access also the following nodes: namelabel (notice: no space nor any other symbol between name and label!), nameannotation, namevoltage, namecurrent and nameflow. Notice that the node names are available only if the bipole has an anchor or an annotation, of course.

²⁹No, I do not know why. Hints and fixes are welcome.

 $^{^{30}}$ The access to labels and annotations was present before, but not documented.

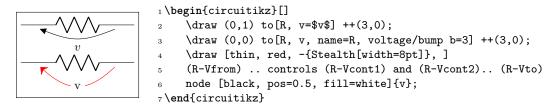


```
_1 \neq 0 \newcommand \marknode \[ [2] [45] \{ \% \newcommand \[ \]
     \node[circle, draw, red, inner sep=1pt,
     pin={[red, font=\tiny]#1:#2}] at (#2.center) {};
4 }
5 \begin{circuitikz}[ american]
     draw (0,0) to [R=L1, a=A1, name=L1] ++(3,0)
     to [R, 12\_=L2 \text{ and } 2L, a^=A2, name=L2] ++(3,0);
     \marknode{L1} \marknode{L1label} \marknode[0]{L1annotation}
     \label{local_local_local} $$\max_{L2} \end{[0]} L2label} \end{[L2annotation}
     \draw[blue] (L2label.south west) rectangle (L2label.north east);
10
     \frac{6.1,0}{to} [R=R1, v=V1, i=I1, f>^=F1, name=R1] ++(3,0)
11
     to [R, v \le V2, i^=I2, f \le F2, name=R2] ++(3,0);
12
     \marknode[0]{R1voltage} \marknode[0]{R2voltage} \marknode[90]{R1current}
     \marknode[90]{R2current} \marknode{R1flow} \marknode{R2flow}
15 \end{circuitikz}
```

4.8 Advanced voltages, currents and flows

Since version 1.2.1, it is possible to access the anchors of the "ornaments" — voltage, current and flows, together with some additional information that makes it possible to personalize them. Normally, voltages and flow and currents are drawn into the path of the bipoles, so that it is not possible, for example, to change the line type or color of the arrows, or the type of arrows³¹. Access to the anchors allows to do all this things, and more.

For example, you can do something like this:



Or, for example, to have a different voltage style; normally you would define a macro:

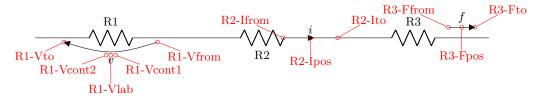
 $^{^{31}}$ in regular voltages, the arrows are not real TikZ arrows, but the auxiliary arrow shapes of CircuiTikZ

4.8.1 Activating the anchors

You will have access to the anchors for voltages, currents and flows when, in the bipole, you have both a v, i, f specification (one or more of them) and a name key, to give the bipole a name. Otherwise, the anchors and the associated functions are not defined. To suppress the normal output of the v, i, f keys, you can use such keys without any argument, like in the previous example; notice that the $\underline{\ }$ and $\widehat{\ }$ modifiers work as expected.

The following line of resistors has been drawn with the following commands; it is used to show the name of the available anchors.

```
\draw (0,0) to [R=R1, v=\sv\s, name=R1] ++(4,0) to [R, 1_=R2, i=\si\s\, name=R2] ++(4,0) to [R=R3, f=\s\f\s\, name=R3] ++(4,0);
```

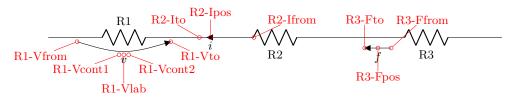


The meaning of the anchors is the following:

- Vfrom and Vto are the main points where the voltage information is given: start and end point of the arrow, or position of the + or sign. This is the same for the Ffrom or Fto anchors for flows; for inline currents, the corresponding Ifrom and Ito mark the wire segment where the arrowhead is positioned (at the specified current/distance fraction. The direction of the arrow is available using the auxiliary macro \ctikzgetdirection (see below).
- Vcont1 and Vcont2 are the control points for the curved arrow (see the examples above); in the case of straight arrows or american-style voltages, they are set at the midpoint between Vfrom and Vto.
- Vlab is where the text label for the voltage is normally positioned. The anchor used for such label is available using the auxiliary macro \ctikzgetanchor (see below)
- Ipos and Fpos are the position for the arrowhead or the small flow arrow (which is a currarrow or flowarrow node normally) is positioned, respectively. The label is then added to the correct side of it using the anchor available via \ctikzgetanchor. 32

Changing the options of the elements, will change the anchors accordingly:

```
1  \ctikzset{current/distance=0.2}
2  \draw (0,0) to [R=R1, v>=$v$, name=R1] ++(4,0)
3  to [R, 1_=R2, i<_=$i$, name=R2] ++(4,0)
4  to [R, 1_=R3, f<_=$f$, name=R3] ++(4,0);</pre>
```



Obviously, the anchors follow the voltage style you choose:

```
\draw (0,0) to [R=R1, v=\sv\state, name=R1, voltage=straight] ++(4,0)
to [R=R2, v=\sv\state, name=R2, voltage=american] ++(4,0)
to [R=R3, v=\sv\state, name=R3, voltage=raised] ++(4,0);
```

³²In this case, the exact position of the label is not available if you do not position the element, for this there is no Flab or Ilab coordinate; you have to use the Fpos and Ipos coordinate with the corresponding Ilab and Flab anchors.



Notice the postion of the control points, as well as the fact that the anchor available with \ctikzgetanchor is applied to Vfrom and Vto symbols, too.

Finally, as ever, generators are treated differently, but you have all your anchors too.

```
\tag{G1-Vfrom} \( \text{ctikzset{american}} \\ \text{ctikzset{american}} \\ \text{draw (0,0) to [V=$v$, name=G1, voltage=european] ++(4,0)} \\ \text{to [V=$v$, v=$v$, name=G2, voltage=american] ++(4,0)} \\ \text{to [battery2, v=$v$, name=G3, voltage=raised] ++(4,0);} \\ \text{G1-Vlab} \quad \text{G2-Vlab} \quad \text{G3-Vlab} \\ \text{G3-Vlab} \quad \text{G3-Vto} \\ \text{G3-Vto} \quad \quad \text{G3-Vto} \\ \text{G3-Vto} \quad \text{G3-Vto} \\ \text{
```

4.8.2 Auxiliary information

When the anchors are activated, there are additional macros that you can use:

- \ctikzgetanchor{<name>}{<anchor>}: name is the name of the bipole, and anchor can be Vlab, Fpos or Ipos. This macro expands to the normal anchor position (something like north, south west). Notice that if you have not activated the corresponding anchor, the content of this macro is not specified. It could be equivalent to \relax (basically, empty) or contains the anchor of a bipole with the same name from another drawing it's a global macro like the coordinates.
- \ctikzgetdirection{<name>}: a number which is the direction of the named bipole.

For example, you could like the voltage label oriented with the bipole:

```
1\begin{circuitikz}[]
2 \def\myvv#1#2{%
3 \draw [thin, blue, ->,]
4 (#1-Vfrom) .. controls (#1-Vcont1) and (#1-Vcont2).. (#1-Vto)
5 node [pos=0.5, below,
6 rotate=\ctikzgetdirection{#1}] at (#1-Vlab) {#2}; }
7 \draw (0,0) to[R, v, name=A] ++(3,0);
8 \draw (0,0) to[R, v, name=B] ++(3,3);
9 \myvv{A}{$v_A$}\myvv{B}{$v_B$}
10 \end{circuitikz}
```

Or you could use the anchor to substitute the flow with a fancy one and still position automatically the label; suppose you have the following definition in your preamble (see TikZ manual, "Path decorations"):

```
1% requires \usetikzlibrary{decorations, decorations.pathmorphing}
2\tikzset{%
3lray/.style={decorate, decoration={
4     snake, amplitude=2pt,pre length=1pt,post length=2pt, segment length=5pt,},
5   -Triangle,
6}}
```

You can then define a kind of "power flow" style:

```
\begin{array}{c|c} & P_A \\ & & \\ \hline & & \\ \hline & & \\ \hline & & \\ P_B \end{array}
```

4.9 Integration with siunitx

If the option siunitx is active (and not in ConTEXt), then the following are equivalent:

```
1 \begin{circuitikz}
1 \, \mathrm{k} \Omega
            2 \draw (0,0) to[R, l=1<\kilo\ohm>] (2,0);
            3 \end{circuitikz}
            1 \begin{circuitikz}
1 \, \mathrm{k} \Omega
                \draw (0,0) to [R, 1=$SI{1}{\kappa ilo \ohm}$] (2,0);
            3\end{circuitikz}
             1 \begin{circuitikz}
   \sqrt{1 \, \text{mA}}
             2 \draw (0,0) to[R, i=1<\milli\ampere>] (2,0);
             3\end{circuitikz}
             1 \begin{circuitikz}
                 \draw (0,0) to[R, i=$SI{1}{\min[ampere]}] (2,0);
             3 \end{circuitikz}
            1\begin{circuitikz}
            2 \draw (0,0) to[R, v=1<\volt>] (2,0);
            3 \end{circuitikz}
            1\begin{circuitikz}
               draw (0,0) to[R, v=$SI{1}{volt}$] (2,0);
            3 \end{circuitikz}
```

5 Using bipoles in circuits

5.1 Nodes (also called poles)

You can add nodes to the bipoles, positioned at the coordinates surrounding the component. The general style to use is bipole nodes={start}{stop}, where start and stop are the nodes — to be chosen between none, circ, ocirc, squarepole, osquarepole, diamondpole, odiamondpole and rectfill³³ (see section 3.15).



These bipole nodes are added after the path is drawn, as every node in TikZ — this is the reason why they are always filled (with the main color the normal nodes, with white the open ones), in order to "hide" the wire below. You can override the fill color if you want; but notice that if you draw things in two different paths, you will have "strange" results; notice that in the second line of resistors the second wire is starting from the center of the white ocirc of the previous path.



```
1 \begin{circuitikz}
2     \draw (0,0) to[R, *-o] ++(2,0) to[R, -d] ++(2,0)
3     to[R, bipole nodes={diamondpole}{odiamondpole, fill=red}] ++(2,0);
4     \draw (0,-1) to[R, *-o] ++(2,0);
5     \draw (2,-1) to[R, -d] ++(2,0) to[R, bipole nodes={none}{squarepole}] ++(2,0);
6 \end{circuitikz}
```

You can define shortcuts for the bipole nodes you use most; for example if you want a shortcut for a bipole with open square node in red in the right side you can:

```
1 \begin{circuitikz}
2 \ctikzset{-s/.style = {bipole nodes={none}{osquarepole, fill=red}}}
3 \draw (0,0) to[R, -s] ++(2,0);
4 \end{circuitikz}
```

There are several predefined shorthand as the above; in the following pages you can see all of them.

 $^{^{33}}$ You can use other shapes too, but at your own risk...Moreover, notice that none is not really a node, just a special word used to say "do not put any node here".

```
1\begin{circuitikz}
2 \draw (0,0) to[R, -o] (2,0);
3\end{circuitikz}
1\begin{circuitikz}
2 \draw (0,0) to[R, o-] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
   draw (0,0) to[R, *-*] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
2 \draw (0,0) to[R, -*] (2,0);
3\end{circuitikz}
1 \begin{circuitikz}
  \text{draw } (0,0) \text{ to } [R, *-] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
2 \draw (0,0) to[R, d-d] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
2 \draw (0,0) to[R, -d] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
   \draw (0,0) to[R, d-] (2,0);
3 \end{circuitikz}
1 \begin{circuitikz}
2 \draw (0,0) to[R, o-*] (2,0);
3\end{circuitikz}
1 \begin{circuitikz}
2 \draw (0,0) to[R, *-o] (2,0);
3\end{circuitikz}
1\begin{circuitikz}
   draw (0,0) to [R, o-d] (2,0);
3\end{circuitikz}
1\begin{circuitikz}
2 \draw (0,0) to[R, d-o] (2,0);
3 \end{circuitikz}
1\begin{circuitikz}
   draw (0,0) to[R, *-d] (2,0);
3\end{circuitikz}
1\begin{circuitikz}
   draw (0,0) to[R, d-*] (2,0);
3 \end{circuitikz}
```

5.1.1 Transparent poles

"Open-poles" terminals (ocirc, odiamondpole, and osquarepole) are normally filled with the background color at full opacity. This is because, for simplicity of operation, the nodes are placed *after* the wires are drawn and have to "white-out" the underlying lines.

Anyway, if you know what you are doing, you can change it with the key poles/open fill opacity (with \ctikzset) or the style open poles opacity. Notice that you will have artifacts if you don't use the border anchors of the poles to connect wires, and you need to do that by hand.



You also have the similar keys for the "full" poles (albeit they are probably not useful at all).

5.2 Mirroring and Inverting

Bipole paths can also mirrored and inverted (or reverted) to change the drawing direction.

```
1\begin{circuitikz}
2 \draw (0,0) to[pD] (2,0);
3\end{circuitikz}

1\begin{circuitikz}
2 \draw (0,0) to[pD, mirror] (2,0);
3\end{circuitikz}

1\begin{circuitikz}
2 \draw (0,0) to[pD, invert] (2,0);
3\end{circuitikz}
```

Placing labels, currents and voltages works also, please note, that mirroring and inverting does not influence the positioning of labels and voltages. Labels are by default above/right of the bipole and voltages below/left, respectively.

5.3 Putting them together

```
1 kΩ
2 \draw (0,0) to [R=1<\kilo\ohm>,
3 i>_=1<\milli\ampere>, o-*] (3,0);
4 \end{circuitikz}

1 \begin{circuitikz}

1 \begin{circuitikz}
2 \draw (0,0) to [D*, v=$v_D$,
3 i=1<\milli\ampere>, o-*] (3,0);
4 \end{circuitikz}
```

5.4 Line joins between Path Components

Line joins should be calculated correctly - if they are on the same path, and the path is not closed. For example, the following path is not closed correctly (-cycle does not work here!):

To correct the line ending, there are support shapes to fill the missing rectangle. They can be used like the support shapes (*,o,d) using a dot (.) on one or both ends of a component (have a look at the last resistor in this example:

6 Colors

Color support in CircuiTikZ is quite limited. You will have no problem if:

- 1. You stick to use styles (see 3.3.2) for filling your components, or using a direct fill=... option directly;
- 2. when coloring whole circuits, use the option color=... in your global picture options or in the \draw command (not just the color name as a shorthand);
- 3. forget about transparency.

Nevertheless, if you really need to do strange things with colors you can read on; you can do almost everything but there are several glitches to take into account.

6.1 Shape colors

The color of the components is stored in the key \circuitikzbasekey/color. CircuiTikZ tries to follow the color set in TikZ, although sometimes it fails. If you change color in the picture, please do not use just the color name as a style, like [red], but rather assign the style [color=red].

Compare for instance



```
1 \begin{circuitikz} \draw[red]
2  (0,2) node[and port] (myand1) {}
3  (0,0) node[and port] (myand2) {}
4  (2,1) node[xnor port] (myxnor) {}
5  (myand1.out) -| (myxnor.in 1)
6  (myand2.out) -| (myxnor.in 2)
7 ;\end{circuitikz}
```

and



```
1\begin{circuitikz} \draw[color=red]
2  (0,2) node[and port] (myand1) {}
3  (0,0) node[and port] (myand2) {}
4  (2,1) node[xnor port] (myxnor) {}
5  (myand1.out) -| (myxnor.in 1)
6  (myand2.out) -| (myxnor.in 2)
7;\end{circuitikz}
```

One can of course change the color in medias res:

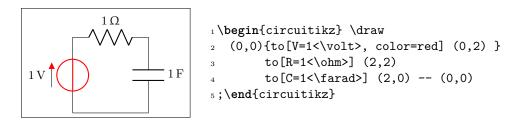


```
1 \begin{circuitikz} \draw
2  (0,0) node[pnp, color=blue] (pnp2) {}
3  (pnp2.B) node[pnp, xscale=-1, anchor=B, color=brown] (pnp1) {}
4  (pnp1.C) node[npn, anchor=C, color=green] (npn1) {}
5  (pnp2.C) node[npn, xscale=-1, anchor=C, color=magenta] (npn2) {}
6  (pnp1.E) -- (pnp2.E) (npn1.E) -- (npn2.E)
7  (pnp1.B) node[circ] {} |- (pnp2.C) node[circ] {}
8  ;\end{circuitikz}
```

The all-in-one stream of bipoles poses some challanges, as only the actual body of the bipole, and not the connecting lines, will be rendered in the specified color. Also, please notice the curly braces around the to:

```
1 \( \text{login{circuitikz} \draw \\ 2 \( 0,0 \) \to [V=1<\volt>] \( (0,2) \) \( \tag{to [R=1<\ohm>, color=red] \( (2,2) \) \\ 4 \\ \to [C=1<\farad>] \( (2,0) \) -- \( (0,0) \) \( \tag{circuitikz} \)
```

Which, for some bipoles, can be frustrating:



The only way out is to specify different paths:

```
1 \( \text{login{circuitikz} \draw[color=red]} \\ 2 \ (0,0) \to[V=1<\volt>, \color=red] \( (0,2) \); \\ \draw \( (0,2) \to[R=1<\ohm>] \( (2,2) \\ \draw \to[C=1<\farad>] \( (2,0) \) -- \( (0,0) \) \\ \sigma \; \end{circuitikz} \}
```

And yes: this is a bug and not a feature...

6.2 Fill colors

Since version 0.9.0, you can also fill most shapes with a color (the manual specifies which ones are fillable or not). The syntax is quite intuitive:



```
1 \begin{circuitikz} \draw
2    (0,2) node[and port, fill=yellow] (myand1) {}
3    (0,0) node[and port, fill=cyan] (myand2) {}
4    (2,1) node[xnor port,fill=red!30!white] (myxnor) {}
5    (myand1.out) -| (myxnor.in 1)
6    (myand2.out) -| (myxnor.in 2)
7 ;\end{circuitikz}
```

This fill color will override any color defined by the style (see section 3.3.2). If you want to override a style fill color with no-fill for a specific component, you need to override the style — it's a bit unfortunate but it should be an exceptional thing anyway:

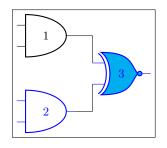


You can combine shape colors with fill colors, too, but you should use the draw color option style for this:

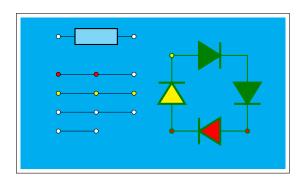


```
1 \begin{circuitikz} \draw[color=red]
2    (0,2) node[and port, fill=yellow] (myand1) {1}
3    (0,0) node[and port, fill=cyan] (myand2) {2}
4    (2,1) node[xnor port,fill=red!30!white] (myxnor) {3}
5    (myand1.out) -| (myxnor.in 1)
6    (myand2.out) -| (myxnor.in 2)
7 ;\end{circuitikz}
```

This is because, as you can see from the following example in port 2, you can't specify both a fill and a color in the node (yes, it's a bug too, but it's quite complex to solve given the current circuitTikZ architecture). A workaround is shown in port 3:



Notice also that the connection point are always filled, although the color tries to follow the color of the filling of the component (but look at section 5.1.1). Moreover, if you want to pass fill transparency down to path-style components, you have to put it into the options of the \draw command.



As you can see, the "black" components (as D*) follow the color of the line, not the fill.

7 FAQ

Q: When using \tikzexternalize I get the following error:

! Emergency stop.

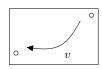
A: The TikZ manual states:

Furthermore, the library assumes that all \LaTeX pictures are ended with $\end{tikzpicture}$.

Just substitute every occurrence of the environment circuitikz with tikzpicture. They are actually pretty much the same.

Q: How do I draw the voltage between two nodes?

A: Between any two nodes there is an open circuit!



```
1\begin{circuitikz} \draw
2 node[ocirc] (A) at (0,0) {}
3 node[ocirc] (B) at (2,1) {}
4 (A) to[open, v=$v$] (B)
5;\end{circuitikz}
```

Q: I cannot write to [R = \$R_1=12V\$] nor to [ospst = open, 3s]: I get errors.

A: It is a limitation of the parser.

Use $\def{eq}=\ to[R = R_1\eq 12V]$ and to[ospst = open(,) 3s] instead; see caveat in section 4.1.

Q: I tried to change the direction of the y axis with yscale=-1, but the circuit is completely messed up.

A: Yes, it's a known bug (or misfeature, or limitation). See section 1.7. Don't do that.

Q: I tried to put a diode in a pic, but it's coming out badly rotated.

A: Yes, it's a known bug (or misfeature, or limitation). See section 1.7. Circui $\mathrm{Ti}k\mathrm{Z}$ is not compatible with pics at this point.

8 Defining new components

```
Per me si va ne la città dolente,
per me si va ne l'etterno dolore,
per me si va tra la perduta gente.
...
Lasciate ogne speranza, voi ch'intrate.<sup>34</sup>
```

Big fat warning: this material is reserved to T_EX -hackers; do not delve into this if you have no familiarity with (at least) a bit of core T_EX programming and to the basic T_EX layer. You have been warned.

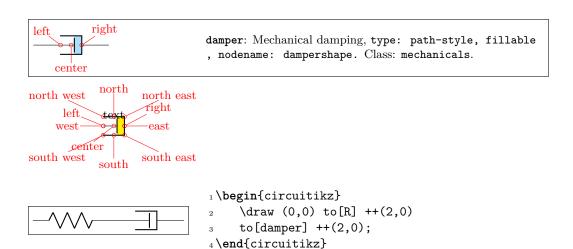
8.1 Suggested setup

The suggested way to start working on a new component is to use the utilities of the CircuiTikZ manual for checking and testing your device. Basically, find (or download) the source code of the last version of CircuiTikZ and find the file ctikzmanutils.sty; copy it in your directory and prepare a file like this:

```
1\documentclass[a4paper, titlepage]{article}
2\usepackage{a4wide} %smaller borders
3 \usepackage[utf8]{inputenc}
4 \usepackage [T1] {fontenc}
5 \parindent=0pt
6\parskip=4pt plus 6pt minus 2pt
7 \usepackage[siunitx, RPvoltages]{circuitikzgit}
8 \usepackage{ctikzmanutils}
9 \makeatletter
10 %% Test things here
11 % defines
13 % components
15% paths
16 \makeatother
18 \begin{document}
20 \circuitdescbip*{damper}{Mechanical damping}{}(left/135/0.2, right/45/0.2,
     center/-90/0.3)
21
22 \geolrcoord{dampershape, fill=yellow}
24 \begin{LTXexample} [varwidth]
25 \begin{circuitikz}
     draw (0,0) to [R] ++(2,0)
     to [damper] ++(2,0);
28 \end{circuitikz}
29 \end{LTXexample}
30 \end{document}
```

This will compile to something like this (in this case, we are using a couple of existing components to check everything is ok):

 $^{^{34} \}texttt{https://classicsincontext.wordpress.com/2010/02/28/canto-iii-per-me-si-va-ne-la-citta-dolente/linearity.}$



The command circuitdescbip* is used to show the component description (you can check the definition and the usage looking at ctikzmanutils.sty file, and the \geolrcoord is used to show the main anchors (geographical plus left and right) of the component.

From now on, you can add the new commands for the component between the \makeatletter and \makeatother commands and, modifying the example, check the results.

8.2 Path-style component

Let's define for example a path style component, like the one suggested by the user @alex on TEX stack-exchange site. The component will be a mix of the damper and the spring components already present.

The first step is to check if we can use the definition already existing for similar elements (for coherence of size) or if we need to define new ones; for this you have to check the file pgfcirc.defines.tex: we find

```
1 \ctikzset{bipoles/spring/height/.initial=.5}
2 \ctikzset{bipoles/spring/width/.initial=.5}
3 \ctikzset{bipoles/damper/height/.initial=.35}
4 \ctikzset{bipoles/damper/length/.initial=.3}
5 \ctikzset{bipoles/damper/width/.initial=.4}
```

We will use them; at this stage you can decide to add other parameters if you need them. (Notice, however, than although flexibility is good, these parameters should be described in the manual, otherwise they're as good as a fixed number in the code).

To define the new component we will look into pgfcircbipoles.tex and we will copy, for example, the definition of the damper into our code, just changing the name:

```
1 %% mechanical resistor - damper
2\pgfcircdeclarebipolescaled{mechanicals}
                                 % extra anchors
4 {\ctikzvalof{bipoles/damper/height}} % depth (under the path line)
                                 % name
6 {\ctikzvalof{bipoles/damper/height}} % height (above the path line)
7 {\ctikzvalof{bipoles/damper/width}} % width
8 {
     \pgfpathrectanglecorners{\pgfpoint{\ctikzvalof{bipoles/damper/length}\
        pgf@circ@res@right}{\pgf@circ@res@down}}{\pgf@circ@res@right
        }{\pgf@circ@res@up}}
     \pgf@circ@maybefill
10
11
     % line into the damper
12
```

```
\pgfpathmoveto{\pgfpoint{\pgf@circ@res@left}{\pgf@circ@res@zero}}
13
     \pgfpathlineto{\pgfpoint{\ctikzvalof{bipoles/damper/length}\
14
         pgf@circ@res@right}
         {\pgf@circ@res@zero}}
15
     \pgfusepath{stroke}
16
17
     % damper box
18
     \pgf@circ@setlinewidth{bipoles}{\pgfstartlinewidth}
19
     \pgfpathmoveto{\pgfpoint{\pgf@circ@res@left}{\pgf@circ@res@down}}
20
     \pgfpathlineto{\pgfpoint{\pgf@circ@res@right}{\pgf@circ@res@down}}
     \pgfpathlineto{\pgfpoint{\pgf@circ@res@right}{\pgf@circ@res@up}}
     \pgfpathlineto{\pgfpoint{\pgf@circ@res@left}{\pgf@circ@res@up}}
23
24
     \pgfsetrectcap
     \pgfsetmiterjoin
26
     \pgfusepath{stroke}
27
     % damper vertical element
     \pgfpathmoveto{\pgfpoint{\ctikzvalof{bipoles/damper/length}\
30
         pgf@circ@res@right}
         {.8\pgf@circ@res@down}}
31
     \pgfpathlineto{\pgfpoint{\ctikzvalof{bipoles/damper/length}\
32
         pgf@circ@res@right}
         {.8\pgf@circ@res@up}}
33
     \pgfsetbuttcap
34
     \pgfusepath{stroke}
36
37 }
```

This command will define a shape that is named viscoeshape, with all the correct geographical anchors based on the depth, height and width defined in the parameters of \pgfcircdeclarebipolescaled. Moreover, the element is assigned to the class mechanicals for styling.

To be coherent with the styling, you should use (when needed) the length \pgf@circ@scaled@Rlen as the "basic" length for drawing, using the fill functions defined at the start of pgfcirc.defines.tex to fill and stroke — so that the operation will follow the style parameters and, finally, use the macro \pgf@circ@setlinewidth to set the line thickness /the first argument is the "legacy" class, if you do not want to assign one you can use the pseudo-legacy class none.

The anchors for the bipole (which then set the lengths \pgf@circ@res@left) are already scaled for your use. You can use these lengths (which defines, normally, the geographical anchors of the element) to draw your shapes.

This is not sufficient for using the element in a to[] path command; you need to "activate" it with (this commands are normally in pgfcircpath.tex):

```
6 \begin{circuitikz}
7   \draw (0,0) to[spring] ++(2,0)
8   to[viscoe] ++(2,0);
9 \end{circuitikz}
10 \end{LTXexample}
```

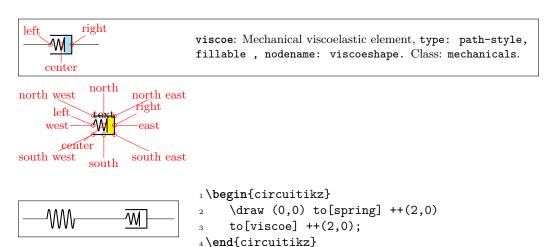
Obviously, at first you you just have a component that is the same as the one you copied with another name. It is now just a matter of modifying it so that it has the desired shape; in the example above you can already see the new symbol after the changes.

When doing the drawing, the \protect

Looking at the implementation of the spring element, a possible implementation is changing the lines between lines 12 and 16 with:

```
% spring into the damper
2
     \pgfscope
        \pgfpathmoveto{\pgfpoint{\pgf@circ@res@left}{\pgf@circ@res@zero}}
        \pgf@circ@setlinewidth{bipoles}{\pgfstartlinewidth}
        \pgfsetcornersarced{\pgfpoint{.25\pgf@circ@res@up}{.25\pgf@circ@res@up}}
5
        \pgfpathlineto{\pgfpoint{.75\pgf@circ@res@left}{.75\pgf@circ@res@up}}
6
        \pgfpathlineto{\pgfpoint{.5\pgf@circ@res@left}{-.75\pgf@circ@res@up}}
        \pgfpathlineto{\pgfpoint{.25\pgf@circ@res@left}{.75\pgf@circ@res@up}}
        \pgfpathlineto{\pgfpoint{0pt}{-.75\pgf@circ@res@up}}
        \pgfpathlineto{\pgfpoint{\ctikzvalof{bipoles/damper/length}\
10
            pgf@circ@res@right}{.75\pgf@circ@res@up}}
        \pgfusepath{stroke}
11
12
     \endpgfscope
```

which leads to:



Now you can check if the voltage labels are correct for your new component:

```
1\begin{circuitikz}[]
2 \draw (0,0) to[spring] ++(2,0)
3 to[viscoe, v=V] ++(2,0);
4\end{circuitikz}
```

If you think they are too tight or too loose you can use a (developer-only) key to adjust the distance:



Notice that by default the key bipoles/mybipole/voltage/additional shift is not defined, so if you want to use it you must create it before (this is the meaning of the .initial here).

As a final note, notice that the viscoe element is already added to the standard package.

8.3 Node-style component

Adding a node-style component is much more straightforward. Just define it by following examples in, for example, pgfcirctripoles.tex or the other files; be careful that you should define all the geographical anchors of the shape if you want that the TikZ positioning options (like left, above, etc.) behave correctly with your component.

To have a scalable component, for example in the transistors class, you should use something like

```
\savedmacro{\ctikzclass}{\edef\ctikzclass{transistors}}
\saveddimen{\scaledRlen}{\pgfmathsetlength{\pgf@x}{\ctikzvalof{\ctikzclass/scale}\pgf@circ@Rlen}}
```

at the start of anchors and macros definition, and use (for example, the exact code will change greatly depending on your component):

in all the savedanchors.

Then, in the drawing part, you should start with:

pgf@circ@scaled@Rlen=\scaledRlen

and then use $\protect\operatorname{\tt Npgf@circ@scaled@Rlen}$ (or the anchors) as default lengths while you draw it.

8.3.1 Finishing your work

Once you have a satisfactory element, you should

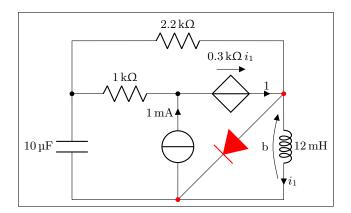
- Clean up your code;
- write a piece of documentation explaining its use, with an example;
- Propose the element for inclusion in the GitHub page of the project (you will have to license this as explained in that page, of course).

The best way of contributing is forking the project, adding your component in the correct files, modifying the manual and creating a pull request for the developers to merge. Anyway, if this is a problem, just open an issue and someone (when they have time...) will answer.

9 Examples

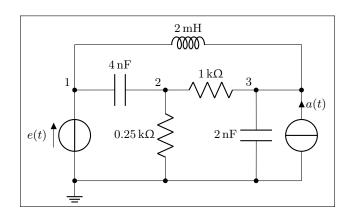
Here a series of example, contributed by several people, is shown with their code.

9.1 A red diode



```
1\begin{circuitikz}[scale=1.4]\draw
2  (0,0) to[C, l=10<\micro\farad>] (0,2) -- (0,3)
3         to[R, l=2.2<\kilo\ohm>] (4,3) -- (4,2)
4         to[L, l=12<\milli\henry>, i=$i_1$,v=b] (4,0) -- (0,0)
5  (4,2) { to[D*, *-*, color=red] (2,0) }
6  (0,2) to[R, l=1<\kilo\ohm>, *-] (2,2)
7         to[cV, i=1,v=$\SI{.3}{\kilo\ohm}\, i_1$] (4,2)
8  (2,0) to[I, i=1<\milli\ampere>, -*] (2,2)
9 ;\end{circuitikz}
```

9.2 Using the (experimental) siunitx syntax



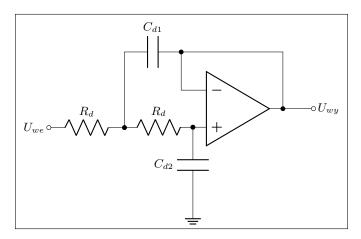


9.3 Photodiodes



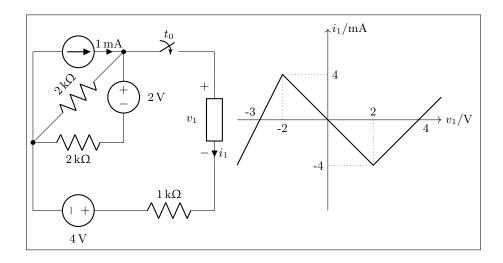
```
1\begin{circuitikz}[scale=1]\draw
2  (0,0) node[transformer] (T) {}
3   (T.B2) to[pD] ($(T.B2)+(2,0)$) -| (3.5, -1)
4   (T.B1) to[pD] ($(T.B1)+(2,0)$) -| (3.5, -1)
5;\end{circuitikz}
```

9.4 A Sallen-Key cell



```
1\begin{circuitikz}[scale=1]\draw
2    (5,.5) node [op amp] (opamp) {}
3    (0,0) node [left] {$U_{we}$} to [R, l=$R_d$, o-*] (2,0)
4    to [R, l=$R_d$, *-*] (opamp.+)
5    to [C, l_=$C_{d2}$, *-] ($(opamp.+)+(0,-2)$) node [ground] {}
6    (opamp.out) |- (3.5,2) to [C, l_=$C_{d1}$, *-] (2,2) to [short] (2,0)
7    (opamp.-) -| (3.5,2)
8    (opamp.out) to [short, *-o] (7,.5) node [right] {$U_{wy}$}
9;\end{circuitikz}
```

9.5 Mixing circuits and graphs



```
1 \begin{circuitikz}[scale=1.2, american] \draw
   (0,2) to [I=1<\milli\ampere>] (2,2)
        to [R, 1_=2<\kilo\ohm>, *-*] (0,0)
        to [R, 1_=2<\kappa 0) (2,0)
        to [V, v_=2<\volt>] (2,2)
5
        to[cspst, l=$t_0$] (4,2) -- (4,1.5)
        to [generic, i=$i_1$, v=$v_1$] (4,-.5) -- (4,-1.5)
   (0,2) -- (0,-1.5) to[V, v_=4<\volt>] (2,-1.5)
        to [R, l=1<\kappa]o(4,-1.5);
9
10
    \begin{scope}[xshift=6.5cm, yshift=.5cm]
11
    \label{lem:continuous} $$ \dim [->] (-2,0) -- (2.5,0) \ node[anchor=west] {$v_1/\volt$};
12
     13
     \draw (-1,0) node[anchor=north] {-2} (1,0) node[anchor=south] {2}
14
         (0,1) node[anchor=west] {4} (0,-1) node[anchor=east] {-4}
          (2,0) node[anchor=north west] {4}
16
         (-1.5,0) node[anchor=south east] {-3};
17
     \draw [thick] (-2,-1) -- (-1,1) -- (1,-1) -- (2,0) -- (2.5,.5);
18
     \draw [dotted] (-1,1) -- (-1,0) (1,-1) -- (1,0)
19
          (-1,1) -- (0,1) (1,-1) -- (0,-1);
20
    \end{scope}
22 \end{circuitikz}
```

9.6 RF circuit



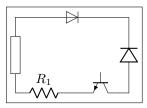
```
\begin{circuitikz}[scale=1]
         \ctikzset{bipoles/detector/width=.35}
        \ctikzset{quadpoles/coupler/width=1}
        \ctikzset{quadpoles/coupler/height=1}
        \ctikzset{tripoles/wilkinson/width=1}
        \ctikzset{tripoles/wilkinson/height=1}
        %\draw[help lines, red, thin, dotted] (0,-5) grid (5,5);
        \draw
         (-2,0) node[wilkinson](w1){}
         (2,0) node[coupler] (c1) {}
10
         (0,2) node[coupler,rotate=90] (c2) {}
11
         (0,-2) node[coupler,rotate=90] (c3) {}
12
         (w1.out1) ... controls ++(0.8,0) and ++(0,0.8) ... (c3.3)
         (w1.out2) .. controls ++(0.8,0) and ++(0,-0.8) .. (c2.4)
14
         (c1.1) .. controls ++(-0.8,0) and ++(0,0.8) .. (c3.2)
15
         (c1.4) .. controls ++(-0.8,0) and ++(0,-0.8) .. (c2.1)
16
         (w1.in) to [short, -o] ++(-1,0)
         (w1.in) node[left=30] {LO}
18
         (c1.2) node[match,yscale=1] {}
19
         (c1.3) to [short, -o] ++(1,0)
         (c1.3) node[right=30] {RF}
21
         (c2.3) to [detector, -0] ++(0,1.5)
22
         (c2.2) to [detector, -0] ++(0,1.5)
23
         (c3.1) to [detector, -0] ++(0, -1.5)
24
         (c3.4) to [detector, -o] ++(0,-1.5)
26
     \end{circuitikz}
27
```

9.7 A styled low noise input stage



```
\node [anchor=center, align=center] at ($(top.south)!0.5!(bot.north)$) {electrodes}
     \node [rectangle, draw, dashed, fit=(egnd) (rgnd) (egndlabel) (rgndlabel)](gnd){};
36
     \node [below, align=center] at (gnd.south) {ground\\ electrode} ;
37
     \node [rectangle, draw, dashed, fit=(s1) (s4label), inner ysep=8pt](switches){};
     % ADC and micro
     \draw (LNA.out) -- ++(0.5,0) node[msport,circuitikz/RF/scale=2](ADC){ADC};
     \draw (ADC.right) -- ++(0.5,0) node[twoportshape, anchor=left, t=$\upmu$C](uC){};
     \draw (uC.south) -- (uC.south |- switches.east) -- ++(-4,0)
     node[align=left, anchor=east](DCS){\small digitally\\ controlled\\ switches};
     \draw[-Stealth] (DCS.west) -- (switches.east);
44
     % components
45
     \node [anchor=north west] at ([xshift=-10pt, yshift=-5pt]switches.south east) {ADG
         1414};
     \node [anchor=north west] at ([yshift=-5pt]LNA.refv down) {AD8429};
48 \end{circuitikz}
49} % scalebox
```

9.8 An example with the compatibility option



```
1 \documentclass{standalone}
2
3 \usepackage{tikz}
4 \usetikzlibrary{circuits.ee.IEC}
5 \usetikzlibrary{positioning}
6
7 \usepackage[compatibility]{circuitikzgit}
8 \ctikzset{bipoles/length=.9cm}
9
10 \begin{document}
11 \begin{tikzpicture}[circuit ee IEC]
12 \draw (0,0) to [resistor={name=R}] (0,2)
13 to[diode={name=D}] (3,2);
14 \draw (0,0) to[*R=$R_1$] (1.5,0) to[*Tnpn] (3,0)
15 to[*D](3,2);
16 \end{tikzpicture}
17 \end{document}
```

9.9 3-phases block schematic



```
\begin{circuitikz}[smallR/.style={european resistor, resistors/scale=0.5}]
     \dot (0,0) \ node[tacdcshape, anchor=ac2](acdc){} to[smallR] ++(-2,0) --
        node[circ](point){} ++(-.5,0);
     \draw (acdc.ac1) to [nos, invert, mirror, name=switch,color=red] ++(-2,0) --
         (point);
    \draw (acdc.ac3) to[smallR] ++(-2,0)
        -- (point)
5
        to[oosourcetrans,prim=wye,sec=delta,l=transformer] ++(-1.5,0)
        to[tmultiwire] ++(-.5,0)
        node[gridnode, anchor=right]{};
    \node[above=.3cm,color=red] at (switch) {fault};
     \draw (acdc.dc1) to[smallR,l=HVDC line] ++(2,0) node[tdcacshape, anchor=dc
10
        1](dcac){};
     \draw (acdc.dc2) -- (dcac.dc2);
11
     \draw (dcac.right) to[ooosource,prim=delta,sec=delta,tert=wye,invert]
12
        ++(1.5,0)
                      to[tmultiwire] ++(.5,0) node[gridnode,anchor=left]{};
14 \end{circuitikz}
```

10 Changelog and Release Notes

The major changes among the different circuitikz versions are listed here. See https://github.com/circuitikz/circuitikz/commits for a full list of changes.

• Version 1.2.1 (2020-07-06)

Several changes, both internal and user-visible. These are quite risky, although they *should* be backeard-compatible.

From the user point of view:

- there is now a new style of voltages ("raised American")
- a powerful mechanism for customize voltages, current and flows has been added.

The internal changes are basically the re-implementation of the macros that draw the path elements (to[...]), which have been completely rewritten. Please be sure to read the possible incompatibilities in the manual (section 1.9).

- Added access to voltages, currents and flows anchors
- Added "raised american" voltage style
- Rewrite of the path generation macros
- Several small bugs fixed (no one ever used some "f^>" options...)
- Version 1.2.0 (2020-06-21)

In this release, the big change is the rewriting of the voltages output routine. Now all voltage options (american, european, and straight) take into account the shape (square border) of the component. The adjusting parameters are now (at least for passive elements) acting in similar way for all the options, too.

- Bumped version number to 1.2 (potentially incompatible changes!)
- Added 1.1.2 checkpoint
- New path-style not, buffer, and Schmitt logic ports
- New tutorial (using the "inline not" component)
- Voltage output routine rewrite; now it takes into account the shape of the component also for "american" and "straight" voltages
- Several fixes in the logic ports: fixed IEEE invschmitt name, added symmetry to the threestyle shorthands for the ports, and so on
- Fixed a gross bug in square poles anchor borders
- Fixed size of not circles in flip-flops (based on logic ports style)
- Fixed the order of initial options, to avoid "european" overwriting single options
- \bullet Version 1.1.2 (2020-05-17)
 - Blocks and component for three-phase networks (3-lines wire, AC/DC and DC/AC converters blocks and grid node block) added by user @olfline on GitHub
 - added transformer sources with optional vector groups for three-phase networks by @olfline on Github
 - added subsections to the examples
 - fixed position of american voltages on open circuits (suggested by user @rhandley on GitHub)
- Version 1.1.1 (2020-04-24)

One-line bugfix release for the IEEE ports "not" circle thickness

• Version 1.1.0 (2020-04-19)

Version bumped to 1.1 because the new logic ports are quite a big addition: now there is a new style for logic ports, conforming to IEEE recommendations.

Several minor additions all over the map too.

- added IEEE standard logic ports suggested by user Jason-s on GitHub
- added configurability to european logic port "not" output symbol, suggested by j-hap on GitHub
- added inerter component by user Tadashi on GitHub
- added variable outer base height for IGBT, suggested by user RA-EE on GitHub
- added configurable "+" and "-" signs on american-style voltage generators
- text on amplifiers can be positioned to the left or centered
- Version 1.0.2 (2020-03-22)
 - added Schottky transistors (thanks to a suggestion by Jérôme Monclard on GitHub)
 - fixed formatting of CHANGELOG.md
- Version 1.0.1 (2020-02-22)

Minor fixes and addition to 1.0, in time to catch the freeze for TL2020.

- add v1.0 version snapshots
- added crossed generic impedance (suggested by Radványi Patrik Tamás)
- added open barrier bipole (suggested by Radványi Patrik Tamás)
- added two flags to flip the direction of light's arrows on LED and photodiode (suggested by karlkappe on GitHub)
- added a special key to help with precision loss in case of fractional scaling (thanks to AndreaDiPietro92 on GitHub for noticing and reporting, and to Schrödinger's cat for finding a fix)
- fixed a nasty bug for the flat file generation for ConTeXt
- Version 1.0 (2020-02-04)

And finally... version 1.0 (2020-02-04) of circuitikz is released.

The main updates since version 0.8.3, which was the last release before Romano started comaintaining the project, are the following — part coded by Romano, part by several collaborators around the internet:

- The manual has been reorganized and extended, with the addition of a tutorial part; tens of examples have been added all over the map.
- Around 74 new shapes where added. Notably, now there are chips, mux-demuxes, multiterminal transistors, several types of switches, flip-flops, vacuum tubes, 7-segment displays, more amplifiers, and so on.
- Several existing shapes have been enhanced; for example, logic gates have a variable number
 of inputs, transistors are more configurable, resistors can be shaped more, and more.
- You can style your circuit, changing relative sizes, default thickness and fill color, and more details of how you like your circuit to look; the same you can do with labels (voltages, currents, names of components and so on).
- A lot of bugs have been squashed; especially the (very complex) voltage direction conundrum
 has been clarified and you can choose your preferred style here too.

A detailed list of changes can be seen below.

- Version 1.0.0-pre3 (not released)
 - Added a Reed switch
 - Put the copyright and license notices on all files and update them
 - Fixed the loading of style; we should not guard against reload
- Version 1.0.0-pre2 (2020-01-23)

Really last additions toward the 1.0.0 version. The most important change is the addition of multiplexer and de-multiplexers; also added the multi-wires (bus) markers.

- Added mux-demux shapes
- Added the possibility to suppress the input leads in logic gates
- Added multiple wires markers
- Added a style to switch off the automatic rotation of instruments
- Changed the shape of the or-type american logic ports (reversible with a flag)

• Version 1.0.0-pre1 (2019-12-22)

Last additions before the long promised 1.0! In this pre-release we feature a flip-flop library, a revamped configurability of amplifiers (and a new amplifier as a bonus) and some bug fix around the clock.

- Added a flip-flop library
- Added a single-input generic amplifier with the same dimension as "plain amp"
- Added border anchors to amplifiers
- Added the possibility (expert only!) to add transparency to poles (after a suggestion from user @matthuszagh on GitHub)
- Make plus and minus symbol on amplifiers configurable
- Adjusted the position of text in triangular amplifiers
- Fixed "plain amp" not respecting "noinv input up"
- Fixed minor incompatibility with ConTeXt and Plain TeX

• Version 0.9.7 (2019-12-01)

The important thing in this release is the new position of transistor's labels; see the manual for details.

- Fix the position of transistor's text. There is an option to revert to the old behavior.
- Added anchors for adding circuits (like snubbers) to the flyback diodes in transistors (after a suggestion from @EdAlvesSilva on GitHub).

• Version 0.9.6 (2019-11-09)

The highlights of this release are the new multiple terminals BJTs and several stylistic addition and fixes; if you like to pixel-peep, you will like the fixed transistors arrows. Additionally, the transformers are much more configurable now, the "pmos" and "nmos" elements have grown an optional bulk connection, and you can use the "flow" arrows outside of a path.

Several small and less small bugs have been fixed.

- Added multi-collectors and multi-emitter bipolar transistors
- Added the possibility to style each one of the two coils in a transformer independently
- Added bulk connection to normal MOSFETs and the respective anchors
- Added "text" anchor to the flow arrows, to use them alone in a consistent way
- Fixed flow, voltage, and current arrow positioning when "auto" is active on the path
- Fixed transistors arrows overshooting the connection point, added a couple of anchors
- Fixed a spelling error on op-amp key "noinv input down"
- Fixed a problem with "quadpoles style=inner" and "transformer core" having the core lines running too near

• Version 0.9.5 (2019-10-12)

This release basically add features to better control labels, voltages and similar text "ornaments" on bipoles, plus some other minor things.

On the bug fixes side, a big incompatibility with ConTeXt has been fixed, thanks to help from @TheTeXnician and @hmenke on github.com.

- Added a "midtap" anchor for coils and exposed the inner coils shapes in the transformers

- Added a "curved capacitor" with polarity coherent with "ecapacitor"
- Added the possibility to apply style and access the nodes of bipole's text ornaments (labels, annotations, voltages, currents and flows)
- Added the possibility to move the wiper in resistive potentiometers
- Added a command to load and set a style in one go
- Fixed internal font changing commands for compatibility with ConTeXt
- Fixed hardcoded black color in "elko" and "elmech"

• Version 0.9.4 (2019-08-30)

This release introduces two changes: a big one, which is the styling of the components (please look at the manual for details) and a change to how voltage labels and arrows are positioned. This one should be backward compatible *unless* you used voltage shift introduced in 0.9.0, which was broken when using the global scale parameter.

The styling additions are quite big, and, although in principle they are backward compatible, you can find corner cases where they are not, especially if you used to change parameters for pgfcirc.defines.tex; so a snapshot for the 0.9.3 version is available.

- Fixed a bug with "inline" gyrators, now the circle will not overlap
- Fixed a bug in input anchors of european not ports
- Fixed "tlinestub" so that it has the same default size than "tline" (TL)
- Fixed the "transistor arrows at end" feature, added to styling
- Changed the behavior of "voltage shift" and voltage label positioning to be more robust
- Added several new anchors for "elmech" element
- Several minor fixes in some component drawings to allow fill and thickness styles
- Add 0.9.3 version snapshots.
- Added styling of relative size of components (at a global or local level)
- Added styling for fill color and thickeness
- Added style files
- Version 0.9.3 (2019-07-13)
 - Added the option to have "dotless" P-MOS (to use with arrowmos option)
 - Fixed a (puzzling) problem with coupler2
 - Fixed a compatibility problem with newer PGF (>3.0.1a)
- Version 0.9.2 (2019-06-21)
 - (hopefully) fixed ConTeXt compatibility. Most new functionality is not tested; testers and developers for the ConTeXt side are needed.
 - Added old ConTeXt version for 0.8.3
 - Added tailless ground
- Version 0.9.1 (2019-06-16)
 - Added old LaTeX versions for 0.8.3, 0.7, 0.6 and 0.4 $\,$
 - Added the option to have inline transformers and gyrators
 - Added rotary switches
 - Added more configurable bipole nodes (connectors) and more shapes
 - Added 7-segment displays
 - Added vacuum tubes by J. op den Brouw
 - Made the open shape of dcisources configurable
 - Made the arrows on vcc and vee configurable

- Fixed anchors of diamondpole nodes
- Fixed a bug (#205) about unstable anchors in the chip components
- Fixed a regression in label placement for some values of scaling
- Fixed problems with cute switches anchors

• Version 0.9.0 (2019-05-10)

- Added Romano Giannetti as contributor
- Added a CONTRIBUTING file
- Added options for solving the voltage direction problems.
- Adjusted ground symbols to better match ISO standard, added new symbols
- Added new sources (cute european versions, noise sources)
- Added new types of amplifiers, and option to flip inputs and outputs
- Added bidirectional diodes (diac) thanks to Andre Lucas Chinazzo
- Added L,R,C sensors (with european, american and cute variants)
- Added stacked labels (thanks to the original work by Claudio Fiandrino)
- Make the position of voltage symbols adjustable
- Make the position of arrows in FETs and BJTs adjustable
- Added chips (DIP, QFP) with a generic number of pins
- Added special anchors for transformers (and fixed the wrong center anchor)
- Changed the logical port implementation to multiple inputs (thanks to John Kormylo) with border anchors.
- Added several symbols: bulb, new switches, new antennas, loudspeaker, microphone, coaxial connector, viscoelastic element
- Make most components fillable
- Added the oscilloscope component and several new instruments
- Added viscoelastic element
- Added a manual section on how to define new components
- Fixed american voltage symbols and allow to customize them
- Fixed placement of straightlabels in several cases
- Fixed a bug about straightlabels (thanks to @fotesan)
- Fixed labels spacing so that they are independent on scale factor
- Fixed the position of text labels in amplifiers

• Version 0.8.3 (2017-05-28)

- Removed unwanted lines at to-paths if the starting point is a node without a explicit anchor.
- Fixed scaling option, now all parts are scaled by bipoles/length
- Surge arrester appears no more if a to path is used without []-options
- Fixed current placement now possible with paths at an angle of around 280°
- Fixed voltage placement now possible with paths at an angle of around 280°
- Fixed label and annotation placement (at some angles position not changable)
- Adjustable default distance for straight-voltages: 'bipoles/voltage/straight label distance'
- Added Symbol for bandstop filter
- New annotation type to show flows using f=... like currents, can be used for thermal, power or current flows

• Version 0.8.2 (2017-05-01)

- Fixes pgfkeys error using alternatively specified mixed colors(see pgfplots manual section "4.7.5 Colors")
- Added new switches "ncs" and "nos"
- Reworked arrows at spst-switches
- Fixed direction of controlled american voltage source
- "v<=" and "i<=" do not rotate the sources anymore(see them as "counting direction indication", this can be different then the shape orientation); Use the option "invert" to change the direction of the source/apperance of the shape.</p>
- current label "i=" can now be used independent of the regular label "l=" at current sources
- rewrite of current arrow placement. Current arrows can now also be rotated on zero-length paths
- New DIN/EN compliant operational amplifier symbol "en amp"

• Version 0.8.1 (2017-03-25)

- Fixed unwanted line through components if target coordinate is a name of a node
- Fixed position of labels with subscript letters.
- $-\,$ Absolute distance calculation in terms of ex at rotated labels
- Fixed label for transistor paths (no label drawn)

• Version 0.8 (2017-03-08)

- Allow use of voltage label at a [short]
- Correct line joins between path components (to[...])
- New Pole-shape .-. to fill perpendicular joins
- Fixed direction of controlled american current source
- Fixed incorrect scaling of magnetron
- Fixed: Number of american inductor coils not adjustable
- Fixed Battery Symbols and added new battery2 symbol
- Added non-inverting Schmitttrigger

• Version 0.7 (2016-09-08)

- Added second annotation label, showing, e.g., the value of an component
- Added new symbol: magnetron
- Fixed name conflict of diamond shape with tikz.shapes package
- Fixed varcap symbol at small scalings
- $-\,$ New packet-option "straight voltages, to draw straight(no curved) voltage arrows
- New option "invert" to revert the node direction at paths
- Fixed american voltage label at special sources and battery
- Fixed/rotated battery symbol(longer lines by default positive voltage)
- New symbol Schmitttrigger

• Version 0.6 (2016-06-06)

- Added Mechanical Symbols (damper, mass, spring)
- Added new connection style diamond, use (d-d)
- Added new sources voosource and ioosource (double zero-style)
- All diode can now drawn in a stroked way, just use globel option "strokediode" or stroke instead of full/empty, or D-. Use this option for compliance with DIN standard EN-60617

- Improved Shape of Diodes:tunnel diode, Zener diode, schottky diode (bit longer lines at cathode)
- Reworked igbt: New anchors G,gate and new L-shaped form Lnight, Lpight
- Improved shape of all fet-transistors and mirrored p-chan fets as default, as pnp, pmos, pfet are already. This means a backward-incompatibility, but smaller code, because p-channels mosfet are by default in the correct direction(source at top). Just remove the 'yscale=-1' from your p-chan fets at old pictures.

• Version 0.5 (2016-04-24)

- new option boxed and dashed for hf-symbols
- new option solderdot to enable/disable solderdot at source port of some fets
- new parts: photovoltaic source, piezo crystal, electrolytic capacitor, electromechanical device(motor, generator)
- corrected voltage and current direction(option to use old behaviour)
- option to show body diode at fet transistors

• Version 0.4

- minor improvements to documentation
- comply with TDS
- merge high frequency symbols by Stefan Erhardt
- added switch (not opening nor closing)
- added solder dot in some transistors
- improved ConTeXt compatibility

• Version 0.3.1

- different management of color...
- fixed typo in documentation
- fixed an error in the angle computation in voltage and current routines
- fixed problem with label size when scaling a tikz picture
- added gas filled surge arrester
- added compatibility option to work with Tikz's own circuit library
- fixed infinite in arctan computation

• Version 0.3.0

- fixed gate node for a few transistors
- added mixer
- added fully differential op amp (by Kristofer M. Monisit)
- now general settings for the drawing of voltage can be overridden for specific components
- made arrows more homogeneous (either the current one, or latex' bt pgf)
- added the single battery cell
- added fuse and asymmetric fuse
- added toggle switch
- added varistor, photoresistor, thermocouple, push button
- added thermistor, thermistor ptc, thermistor ptc
- fixed misalignment of voltage label in vertical bipoles with names
- added isfet
- added noiseless, protective, chassis, signal and reference grounds (Luigi «Liverpool»)

• Version 0.2.4

- added square voltage source (contributed by Alistair Kwan)
- added buffer and plain amplifier (contributed by Danilo Piazzalunga)
- added squid and barrier (contributed by Cor Molenaar)
- added antenna and transmission line symbols contributed by Leonardo Azzinnari
- added the changeover switch spdt (suggestion of Fabio Maria Antoniali)
- rename of context.tex and context.pdf (thanks to Karl Berry)
- updated the email address
- in documentation, fixed wrong (non-standard) labelling of the axis in an example (thanks to prof. Claudio Beccaria)
- fixed scaling inconsistencies in quadrupoles
- fixed division by zero error on certain vertical paths
- introduced options straighlabels, rotatelabels, smartlabels

• Version 0.2.3

- fixed compatibility problem with label option from tikz
- Fixed resizing problem for shape ground
- Variable capacitor
- polarized capacitor
- ConTeXt support (read the manual!)
- nfet, nigfete, nigfete, pigfete, pigfete (contribution of Clemens Helfmeier and Theodor Borsche)
- njfet, pjfet (contribution of Danilo Piazzalunga)
- pigbt, nigbt
- backward incompatibility potentiometer is now the standard resistor-with-arrow-in-the-middle;
 the old potentiometer is now known as variable resistor (or vR), similarly to variable inductor
 and variable capacitor
- triac, thyristor, memristor
- new property "name" for bipoles
- fixed voltage problem for batteries in american voltage mode
- european logic gates
- $-\ backward\ incompatibility$ new american standard inductor. Old american inductor now called "cute inductor"
- backward incompatibility transformer now linked with the chosen type of inductor, and version with core, too. Similarly for variable inductor
- backward incompatibility styles for selecting shape variants now end are in the plural to avoid conflict with paths
- new placing option for some tripoles (mostly transistors)
- mirror path style

\bullet Version 0.2.2 - 20090520

- Added the shape for lamps.
- Added options europeanresistor, europeaninductor, americanresistor and americaninductor, with corresponding styles.
- FIXED: error in transistor arrow positioning and direction under negative xscale and yscale.

\bullet Version 0.2.1 - 20090503

- Op-amps added
- added options arrowmos and noarrowmos, to add arrows to pmos and nmos
- - $-\ Backward\ incompatibility$: labels ending with : angle are not parsed for positioning anymore.
 - Full use of TikZ keyval features.
 - White background is not filled anymore: now the network can be drawn on a background picture as well.
 - Several new components added (logical ports, transistors, double bipoles, ...).
 - Color support.
 - Integration with {siunitx}.
 - Voltage, american style.
 - Better code, perhaps. General cleanup at the very least.
- Version 0.1 2007-10-29 First public release

Index of the components

adde, 66 afuse, 60 ancerican controlled current source, 50 american and port, 107 american buffer port, 107 american inductive sensor, see sL american nand port, 107 american nor port, 107 american or port, 107 american voltage source, 48 american wor port, 107 american voltage source, 48 american wor port, 107 american and port, 107 american port, 107 american self-to-attentian see afuse bandpass, 66 bandstop, 66 bandstop, 66 bandstop, 66 bandstop, 66 barderXantenna, 86 barderXantenna, 86 barderXantenna, 86 barderXantenna, 86 barderXantenna, 86 barderXantenna, 86 barderSy, 199 bareantenna, 86 bartery, 48 battery, 49 battery, 48 battery, 49 batt		
ageneric, 39 ALU, 124 american buffer port, 107 american controlled current source, 50 american controlled current source, 50 american controlled voltage source, 50 american inductive sensor, see sL american inductive sensor, see sL american not port, 107 american or port, 107 american resistive sensor, see R american resistive, see resistor, see R american resistive, see resistor, see R american sor port, 107 american sor port, 107 american sor port, 107 american port, 107 american sor port, 107 american sor port, 107 american sor port, 107 american sor port, 107 american solvage source, 48 american resistive, see afuse american solvage source, 48 american solvage source, 50 controlled sinusoidal current source controlled sinusoidal voltage source, 50 controlled sinusoidal voltage source, 50 controlled sinusoidal voltage source, 50 controlled sinusoidal voltage source covel, see cute open switch controlled sinusoidal voltage source covel see see tue open switch controlled sinusoidal voltage source covel see cute open switch controlled sinusoidal voltage source covel see cute open switch controlled sinusoidal voltage source covel see cute open switch controlled sinusoidal current source covel see cute open switch controlled sinusoidal voltage source covel see cute open switch controlled sinusoidal current source covel see cute open switch controlled sinusoidal voltage source covel see se cute open switch controlled sinusoidal current source covel see se cute open switch controlled sinusoidal voltage source covel see cute open controlled voltage source covel see se cute open switch controlled sinusoidal current source covel see se cute open switch controlled sinusoidal voltage source covel see se cute open controlled voltage covered spacitor, 41 covered spacitor, 41 covered spacitor, 42 covered spacitor, 43 covered spacitor, 44 covered sp	adc, 66	ccgsw, see cute closing switch
ageneric, 39 ALU, 124 american and port, 107 american controlled current source, 50 american controlled current source, 49 american gas filled surge arrester, 60 american miductor, see L american mand port, 107 american nor port, 107 american or port, 107 american sor port, 107 american or port, 107 american or port, 107 american or port, 107 american sor port, 107 american potentiometer, see pR, see pR american mother of port, 107 american potentiometer, see pR, see pR american mother of port, 107 american potentiometer, see pR, see pR american mother of port, 107 american potentiometer, see pR, see pR american mother of port, 107 american or port, 107 am	adder, 65	ccsw, see cute closed switch
ALU, 124 american and port, 107 american outrolled voltage source, 50 american controlled voltage source, 50 american controlled voltage source, 50 american gas filled surge arrester, 60 american inductive sensor, see s.L american and port, 107 american or port, 107 american not port, 107 american or port, 107 american not port, 107 american potentiometer, see pR, see pR american resistive sensor, see sR american voltage source, 48 american xor port, 107 american xor port, 107 american xor port, 107 american xor port, 107 american see se use bandpass, 66 barder, 28, 54 amp, 66 antenna, 87 asymmetric fuse, see afuse bandstop, 66 barder, 195 barder, 195 barder, 195 bartery, 48 batteryl, 60 buffer, 95 buffer, 96 coved, see cute european controlled voltage covource, 50 cisource, see european controlled current source cisource, Ms, see american controlled current source cisource, Ms, see actue opening switch, 101 controlled isourcesin, see controlled sinusoidal current source controlled sinusoidal voltage source controlled sinusoidal voltage source coupler, 68 coupler, 68 coupler, 68 coupler, 68 coupler, 68 coupler, 69 coupler, 68 coupler, 69 coupler, 69 court closing switch, 102 cute closing switch, 102 cute closing switch controlled sinusoidal voltage source coupler, 68 coupler, 68 coupler, 69 coupler, 69 cute closing switch controlled sinusoidal voltage source coupler, 68 coupler, 68 coupler, 68 coupler, 68 coupler, 68 coupler, 69 cute clo	afuse, 60	ceI, see cute european current source
ALU, 124 american and port, 107 american buffer port, 107 american controlled current source, 50 american controlled voltage source, 50 american controlled voltage source, 50 american inductive sensor, see sL american inductive sensor, see sL american not port, 107 american not port, 107 american or port, 107 american or port, 107 american or port, 107 american or port, 107 american resisitor, see resistor, see pR american resisitor, see resistor, see R american rosistor, see resistor, see R american xor port, 107 american not port, 107 american not port, 107 american potentiometer, see pR, see pR american rosistor, see resistor, see R american voltage source, 48 american xor port, 107 american sor port, 107 american so, 66 antenna, 87 asymmetric fuse, see afuse bandpass, 66 barderXantenna, 86 barderXantenna, 86 barderXantenna, 86 barderXantenna, 86 barderYantenna, 86 barderYantenna, 86 barderYantenna, 86 bartery, 48 batteryl, 48 batteryl, 48 batteryl, 48 batteryl, 48 batteryl, 60 buttery, 48 butteryl, 60 buttery, 48 butteryl, 60 buttery, 61, see bmultiwire bino, see empty bidirectionaldiode bijtnpn, collectors=1, emitters=2, 72 biptnpn, collectors=2, emitters=2, 72 biptnpn, collectors=3, emitters=2, 72 biptnpn, collectors=4, emitters=2, 72 biptnpn, collectors=3, emitters=2, 72 biptnpn, collectors=3, emitters=2, 72 biptnpn, collectors=4, emitters=2, 72 biptnpn, collectors=3, emitters=2, 72 biptnpn, collectors=4, emitters=2, 72 biptnpn, collectors=3, emitters=2, 72 biptnpn, collectors=4, emitters=2, 72 biptnpn, collectors=6, controlled voltage source courted, vecanical controlled voltage source courted, vecanic	ageneric, 39	ceV, see cute european voltage source
american buffer port, 107 american controlled current source, 50 american controlled current source, 49 american gas filled surge arrester, 60 american inductive sensor, see sL american inductive sensor, see sL american nad port, 107 american or port, 107 american or port, 107 american or port, 107 american or port, 107 american potentiometer, see pR, see pR american resistive sensor, see sR american resistive sensor, see sR american xnor port, 107 american	ALU, 124	
american buffer port, 107 american controlled current source, 50 american controlled current source, 50 american controlled voltage source, 50 american controlled voltage source, 50 american inductive sensor, see sL american inductive sensor, see sL american not port, 107 american ont port, 107 american ont port, 107 american ont port, 107 american or port, 107 american optentiometer, see pR, see pR american resistor, see resistor, see R american resistor, see resistor, see R american voltage source, 48 american xor port, 107 american xor port,	american and port, 107	9
american controlled current source, 50 american controlled voltage source, 50 american current source, 49 american gas filled surge arrester, 60 american inductive sensor, see sL american nor port, 107 american nor port, 107 american nor port, 107 american potentiometer, see pR american resistor, see R american vor port, 107 american or port, 107 american or port, 107 american potentiometer, see pR american voltage source, 48 american voltage source, 48 american voltage source, 48 american voltage source, 48 american von port, 107 ammeter, 28, 54 amp, 66 antenna, 87 controlled sinusoidal voltage source, 50 controlled sinusoidal voltage source voltage source voltage source sistemans, 86 coupler, 68 coupler		
american controlled voltage source, 50 american current source, 49 american gas filled surge arrester, 60 american inductive sensor, see sL american inductive sensor, see sL american not port, 107 american pot port, 107 american potentiometer, see pR, see pR american resistive sensor, see sR american resistive sensor, see sR american resistor, see resistor, see R american potentiometer, see pR, see pR american potentiometer, see pR, see pR american potentiometer, see pR, see pR american resistor, see resistor, see R american ottage source, 48 american xor port, 107 am	- · · · · · · · · · · · · · · · · · · ·	,
american current source, 49 american gas filled surge arrester, 60 american inductor, see st. american inductor, see L american and port, 107 american or port, 107 american or port, 107 american potentiometer, see pR, see pR american resistive sensor, see sR american resistive sensor, see sR american resistive sensor, see R american voltage source, 48 american xor port, 107 american xor port, 108 american xor port,		
american gas filled surge arrester, 60 american inductor, see sL american inductor, see L american nand port, 107 american nor port, 107 american nor port, 107 american or port, 107 american potentiometer, see pR, see pR american resistive sensor, see sR american resistor, see resistor, see R american voltage source, 48 american voltage source, 48 american voltage source, 48 american vor port, 107 american or port, 107 american or port, 107 american resistor, see resistor, see R american voltage source, 48 american voltage source, 48 american sour port, 107 american post, 107 american voltage source, 48 american voltage source, 51 controlled sinusoidal voltage source, 50 controlled visusoidal voltage source, 50 controlled visusoidal voltage source cosw, see cute open switch coupler, 68 coupler, 68 coupler, 68 crossing, 62 csl, see controlled sinusoidal current source csyst, see closing switch coupler, 68 coupler, 68 crossing, 62 csl, see controlled sinusoidal voltage source csyst, see closing switch coupler, 63 current source, 49 current source, 49 cute closed switch, 102 cute closed switch, 102 cute current source, 50 cute european controlled current source, 50 cute european controlled current source, 49 cute european controlled current source, 50 cute european controlled voltage source, 50 cute spdt down, 102 cute spdt down, 102 cute spdt down, 102 cute spdt down arrow, 30, 102 cute spdt upin arrow,		
american inductor, see L american inductor, see L american nand port, 107 american nor port, 107 american nor port, 107 american not port, 107 american pot port, 107 american potentiometer, see pR, see pR american resistive sensor, see sR american resistive sensor, see sR american resistive sensor, see R american voltage source, 48 american xor port, 107 american voltage source, 50 coupler, 68 antenna, 86 antenna, 86 bardays, 66 cutrarrow, 63 cutrarrow, 63 cutre capacitor, 41 cute closing switch, 102 cute closing switch, 102 cute european controlled current source, 50 cute european controlled current source, 50 cute european voltage source, 48 cute inductive sensor, see sL cute inductive, see sL cute inductive, see sL cute inductive, see sL cute inductive, see sc, see sL cute inductive, see sor, see sL cute inductive, see sc, see sL cute inductive, see sor, see sL cute inductive, see sor, see sL cute inductive, sensor, see sL cute inductive, sensor, see sL cute inductive, sen		
american inductor, see L american nor port, 107 american nor port, 107 american nor port, 107 american nor port, 107 american potentiometer, see pR, see pR american potentiometer, see pR, see pR american rosistive sensor, see sR american rosistive sensor, see sR american vor port, 107 american xor port, 107 ameter, 28, 54 amp, 66 antenna, 86 coupler, 68 coupler, 68 coupler, 68 coupler, 68 crossing, 62 crssing, 62 crossing, 62 crurarrow, 63 curred capacitor, 41 cute cloke, 42 cute closed switch, 102 cute closed switch, 102 cute closing switch, 102 cute closed switch, 102 cute closing switch, 102 cute closed switch, 102 cute closed switch, 102 cute closed switch, 102 cute cute uropean controlled current source, 50 cute inductor, see L cute spdt down, 102 cute spdt down, 102 cute spdt down, 102 cute spdt mid, 102 cute spdt		
american nand port, 107 american not port, 107 american not port, 107 american not port, 107 american potentiometer, see pR, see pR american resistive sensor, see sR american resistor, see resistor, see R american xor port, 107 american voltage source, 48 american xor port, 107 american xor port,		
american nor port, 107 american nor port, 107 american potentiometer, see pR, see pR american resistive sensor, see sR american resistive sensor, see R american voltage source, 48 american xor port, 107 cosw, see cute open switch coupler, 68 co		
american not port, 107 american or port, 107 american or potnetimeter, see pR, see pR american resistive sensor, see sR american resistive sensor, see sR american resistor, see resistor, see R american rosistor, see resistor, see R american rositage source, 48 american xor port, 107 american xor p		
american or port, 107 american potentiometer, see pR, see pR american resistive sensor, see sR american resistive, see resistor, see R american voltage source, 48 american voltage source, 48 american voltage source, 48 american voltage source, 48 american xor port, 107 american xor port, 107 ammeter, 28, 54 amp, 66 antenna, 87 asymmetric fuse, see afuse bandpass, 66 bardpass, 66 bardfxantenna, 86 barefXantenna, 86 barefXantenna, 86 barefXantenna, 86 barefys, 48 battery1, 48 battery2, 48 battery1, 48 battery2, 48 battery3, as emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins width=0, bjt multi height=0.8, 78 bjtppn, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 C, see capacitor capacitive sensor, 41 capacitor, 41 capacitor, 41 cc, see cute european controlled voltage source cevV, see cute european controlled voltage source cevuresein, see controlled sinusoidal voltage source, 50 controlled isourcesin, see controlled sinusoidal voltage source, 50 controlled voltage source, 50 controlled voltage source, 50 controlled voltage source, 50 controlled voltage source cosw, see cute open switch coupler, 68 coupler, 68 coupler, 68 coupler, 68 coupler, 68 coupler2, 68 court-olled sinusoidal voltage source cosw, see cute open switch coupler, 68 coupler2, 68 coupler2, 68 court-olled sinusoidal voltage source cosw, see cute open switch coupler, 68 coupler, 69 cute open switch coupler, 68 coupler2, 68 coupler2, 68 court-olled sinusoidal voltage source cosw, see cute open switch coupler, 68 coupler, 68 coupler, 68 coupler, 68 cute open switch coupler, 68 cute open switch coupler, 68 cute open switch coupler, 69 cut closed switch, 102 cut elosed switch, 102 cut elosed switch, 102 cut elosed switch, 102 cut european controlled voltage s		,
american potentiometer, see pR, see pR american resisitive sensor, see sR american resistor, see resistor, see R american rosistor, see resistor, see R american voltage source, 48 american xor port, 107 american xor port, 107 ammeter, 28, 54 amp, 66 antenna, 87 asymmetric fuse, see afuse bandpass, 66 bardpass, 66 bardfase, 129 bareantenna, 86 bareantenna, 86 bareantenna, 86 bareantenna, 86 bareantenna, 86 baretrantenna, 86 bartery, 48 battery, 48 cute closing switch, 102 cute european controlled voltage source, 50 cute european controlled voltage source, 50 cute european voltage source, 48 cute inductor, see L cute	- · · · · · · · · · · · · · · · · · · ·	
american resisitive sensor, see sR american resisitor, see resistor, see R american voltage source, 48 american xor port, 107 american xor port, 108 controlled sinusoidal voltage source csyl, see controlled sinusoidal voltage source csyl, see controlled sinusoidal voltage source cosw, see cute open switch coupler, 68 cute cohon, 62 cute cohon, 42 cute cohon, 42 cute elosed switch, 102 cute european voltage source, 48 cute indu		
american resistor, see resistor, see R american voltage source, 48 american xor port, 107 ammeter, 28, 54 amp, 66 antenna, 87 asymmetric fuse, see afuse bandpass, 66 bandstop, 66 bareartenna, 86 bareartenna, 86 bareartxantenna, 86 bareartxantenna, 86 baretxantenna, 86 baretxy, 48 battery, 48 battery, 48 battery, 48 battery, 48 battery, 48 battery, 48 biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode biDo, see shiften, 61, see bmultiwire bright, 61, see bmultiwire bright, 65 buffer, 95 bulb, 60 cute cute cute cute, see shiften, 102 cute spdt down arrow, 30, 102 cute spdt mid, 102 cute spdt mid, 102 cute spdt mid, 102 cute spdt mid arrow, 102 cute spdt wp, 102 cute spdt wp, 102 cute spdt wp, 102 cute spdt wp, 102 cute spdt up, 102 cute spdt up, 102 cute spdt up, 102 cute spdt up arrow, 102 cute spdt up, 102 cute spd		
american voltage source, 48 american xoro port, 107 ammeter, 28, 54 amp, 66 antenna, 87 asymmetric fuse, see afuse bandpass, 66 bardstop, 66 bareTxantenna, 86 bareTXantenna, 86 bareTXantenna, 86 bartery, 48 battery1, 48 battery2, 48 biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode biDo, see empty bidirectionaldiode biptinpn, collectors=1, emitters=2, 72 bijtinpn, collectors=2, emitters=2, 1pt pins width=0, bjt multi height=0.8, 78 biptipp, collectors=3, emitters=2, 72 buffer, 95 bulb, 60 C, see capacitor capacitive sensor, 41 capacitor, 41 cC, see curved capacitor ccel, see cute european controlled voltage cceV, see cute european controlled voltage controlled voourcesin, see controlled sinusoidal voltage source covsw, see cute open switch coupler, 68 coupler, 68 coupler, 68 coupler2, 68 coupler2, 68 corosing, 62 corssing, 62 coupler2, 68 coupler2 coupler2, 68 cut ecol	,	
american xnor port, 107 american xor port, 107 american xor port, 107 ammeter, 28, 54 amp, 66 antenna, 87 asymmetric fuse, see afuse bandpass, 66 bardstop, 66 bardstop, 66 bareTxantenna, 86 bareRXantenna, 86 bareRXantenna, 86 barety, 48 battery, 48 battery, 48 biDo, see empty bidirectionaldiode bijtopn, collectors=2, emitters=2, 72 bjtpnp, collectors=2, emitters=2, 72 bytpnp, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 C, see capacitor capacitor, 41 capacitor, 41 cute spdt down arrow, 30, 102 cute spdt mid arrow, 102 cu		
american xor port, 107 ammeter, 28, 54 amp, 66 antenna, 87 asymmetric fuse, see afuse bandpass, 66 bandstop, 66 barerseg, 129 bareantenna, 86 barerXantenna, 86 barerXantenna, 86 baretYxantenna, 86 bartiv, 48 battery1, 48 battery2, 48 biD*, see full bidirectionaldiode bijto, see empty bidirectionaldiode bijto, see empty bidirectionaldiode bijto, see empty bidirectionaldiode bijtopn, collectors=2, emitters=2, 72 bijtpnp, collectors=3, emitters=2, bijt pins width=0, bjt multi height=0.8, 78 bijtpnp, collectors=3, emitters=2, 72 bultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 cosw, see cute open switch coupler, 68 cut choke, 42 cute closed switch, 102 cute european controlled current source, 49 cute european controlled voltage source, 50 cute inductor, see L cute inductor, see		
ammeter, 28, 54 amp, 66 antenna, 87 asymmetric fuse, see afuse bandpass, 66 bandstop, 66 barerSseg, 129 bareantenna, 86 bareRXantenna, 86 bareRXantenna, 86 baretry, 48 battery, 48 battery, 48 battery, 48 battery, 48 biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode biDo, see empty bidirectionaldiode biDn, see empty bidirectionaldiode biDn, csee empty bidirectionaldiode bipn, collectors=1, emitters=2, 72 bijtnpn, collectors=2, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 coupler, 68 coupler2, 68 crossing, 62 csl, see controlled sinusoidal current source csypt, see controlled sinusoidal voltage source covervate capacitor cute closed switch, 102 cute closed switch, 102 cute european controlled current source, 50 cute european controlled voltage source, 50 cute european voltage source, 49 cute inductive sensor, see sL cute inductive sensor, see sL cute inductive, see L cute inductor, see L cute open switch, 102 cute european controlled voltage source cute spdt montant of the controlled voltage source coverved capacitor cute european controlled voltage cute spdt mid, 102 cute spdt mid, 102 cute spdt mid, 102 cute spdt mid arrow, 102 cute spdt up arrow, 102 cute spdt up arrow, 102 cvsource, see curopean controlled voltage source cvsourceAM, see american controlled voltage cvsourceC, see cute european controlled voltage cvsourceS, see cute european controlled voltage cvsourceC, see cute european controlled voltage		<u> </u>
amp, 66 antenna, 87 asymmetric fuse, see afuse bandpass, 66 bandstop, 66 bandstop, 66 bare7seg, 129 bareantenna, 86 bareRXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 bareTix antenna, 86 bare Tix antenna, 86 cut closed switch, 102 cut european controlled current source, 50 cut european controlled current source, 50 cut european controlled voltage source, 49 cut european current source, 49 cut european voltage source, 48 cut inductor, see L cut inductor, see L cut open switch, 102 cut eopen switch, 102 cut eopen switch, 102 cut espdt down, 102 cut espdt down arrow, 30, 102 cut espdt mid, 102 cut espdt mid, 102 cut espdt mid arrow, 102 cut espdt up arrow, 102 cvsource, see european controlled voltage source cevi, see cut european controlled voltage covsource, see cut european controlled voltage covsources, see controlled sinusoidal voltage covsources, see cout european controlled voltage covsources, see cout european controlled volta	1 /	
antenna, 87 asymmetric fuse, see afuse bandpass, 66 bandstop, 66 bare7seg, 129 bareantenna, 86 bareRXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 barety, 48 bartery, 48 battery, 48 battery, 48 biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode biDo, see empty bidirectionaldiode bipnpn, collectors=1, emitters=2, 72 bjtpnpn, collectors=2, emitters=2, bjt pins width=0, bjt multi height=0.8, 78 bjtpnp, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 corossing, 62 csI, see controlled sinusoidal current source ceV, see curved capacitor curved capacitor, 41 cute choke, 42 cute closed switch, 102 cute closing switch, 102 cute closed switch, 102 cute european controlled current source, 50 cute european controlled voltage source, 50 cute inductive sensor, see sL cute inductive, see L cute inductor, see L cute inductor, see L cute inductor, see L cute open switch, 102 cute open switch, 102 cute open switch, 102 cute spdt down, 102 cute spdt down arrow, 30, 102 cute spdt mid arrow, 102 cute spdt mid arrow, 102 cute spdt up arrow, 102 cvsource, see european controlled voltage source cvsourceAM, see american controlled voltage covsourceC, see cute european controlled voltage covsource, see cute european controlled voltage covsources, see controlled sinusoidal voltage		
asymmetric fuse, see afuse bandpass, 66 bandstop, 66 bare7seg, 129 bareantenna, 86 bareRXantenna, 86 bareTXantenna, 86 bartery, 48 battery1, 48 battery2, 48 biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode biptnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins width=0, bjt multi height=0.8, 78 biptnp, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 cutous expert ind, 102 cute spdt mid, 102 cute spdt mid, 102 cute spdt mid, 102 cute spdt mid arrow, 102 cu	2 /	
bandpass, 66 bandstop, 66 bare7seg, 129 bareantenna, 86 bareRXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 baretty, 48 battery, 48 battery, 48 battery2, 48 biD*, see mpty bidirectionaldiode biJDo, see empty bidirectionaldiode biJpnp, collectors=1, emitters=2, 72 bijtnpn, collectors=2, emitters=2, bij pins width=0, bjt multi height=0.8, 78 biptpn, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 Custe, see cute european controlled voltage source ceeV, see cute european controlled voltage cute, see cute european controlled voltage cute spdt mid, 102 cute spdt mid arrow, 102 cute spdt mid arrow, 102 cute spdt up arrow, 102 cvsource, see european controlled voltage cvsource cvsourceAM, see american controlled voltage cvsource cceV, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage source cvsourcesin, see controlled sinusoidal voltage source cvsource apacitor csyst, see closing switch csV, see curred capacitor cute closed switch, 102 cute european controlled voltage source, 49 cute european controlled voltage source, 49 cute inductor, see L cute inductor, see L cute open switch, 102 cute spdt down, 102 cute spdt down, 102 cute spdt down, 102 cute spdt mid, 102 cute spdt mid arrow, 102 cute spdt mid arrow, 102 cute spdt up arrow, 102 cvsource, see european controlled voltage cvsourceAM, see american controlled voltage source cvsourceC, see cute european controlled voltage cvsourceSin, see controlled sinusoidal voltage	•	- ·
bandpass, 66 bandstop, 66 bare7seg, 129 bareantenna, 86 bareTXantenna, 86 bareTXantenna, 86 baretYantenna, 86 barety, 48 battery1, 48 battery2, 48 biD*, see full bidirectionaldiode bjtnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins width=0, bjt multi height=0.8, 78 bjtpnp, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bn. 65 buffer, 95 bulb, 60 CsV, see controlled sinusoidal voltage source cveV, see cute european controlled sinusoidal voltage source ceV, see cuterorrow, 63 currer capacitor, 41 cute choke, 42 cute closing switch, 102 cute european controlled current source, 50 cute european controlled voltage source, 50 cute european current source, 49 cute inductor, see sL cute inductor, see L cute open switch, 102 cute open switch, 102 cute spdt down, 102 cute spdt down arrow, 30, 102 cute spdt mid arrow, 102 cute spdt mid arrow, 102 cute spdt up arrow, 102 cute spdt up arrow, 102 cvsource, see european controlled voltage source ceV, see cute european controlled voltage cvsourceC, see cute european controlled voltage cvsourceC, see cute european controlled voltage cvsourceSin, see controlled sinusoidal voltage currer capacitor, 41 cute choke, 42 cute choke, 42 cute closing switch, 102 cute european controlled voltage source, 50 cute european controlled voltage source, see uropean controlled voltage cvsourceC, see cute european controlled voltage cvsourceC, see cute european controlled voltage	asymmetric ruse, see aruse	
bandstop, 66 bare7seg, 129 bareantenna, 86 bareRXantenna, 86 bareTXantenna, 86 cute closed switch, 102 cute closing switch, 102 cute european controlled current source, 50 cute european controlled voltage source, 50 cute european current source, 49 biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode biDo, see empty bidirectionaldiode biDon, see empty bidirectionaldiode biptnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins width=0, bjt multi height=0.8, 78 bjtpnp, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 buffer, 95 bulb, 60 C, see capacitor capacitive sensor, 41 capacitor, 41 cC, see capacitor capacitive sensor, 41 capacitor, 41 cC, see curved capacitor cel, see cute european controlled voltage ccel, see cute european controlled voltage ccel, see cute european controlled voltage cvsourceC, see cute european controlled voltage cvsourceC, see cute european controlled voltage cvsourceSin, see controlled sinusoidal voltage	bandpass, 66	<i>1</i> /
bare7seg, 129 bareantenna, 86 bareRXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 baretry, 48 battery1, 48 battery2, 48 biD*, see empty bidirectionaldiode bipn, collectors=1, emitters=2, 72 bjtpnp, collectors=2, emitters=2, bjt pins width=0, bjt multi height=0.8, 78 bjtpnp, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 c, see capacitor capacitive sensor, 41 capacitor, 41 cy, see cute european controlled voltage source ceV, see cute european controlled current source ceV, see cute european controlled current source cevsources, see cute european controlled current source cevsourcesin, see controlled sinusoidal voltage cutve closed switch, 102 cute closed switch, 102 cute european controlled current source, 50 cute european controlled current source, 49 cute european controlled current source, 48 cute uropean voltage source, 48 cute inductor, see L cute inductor, see L cute inductor, see L cute open switch, 102 cute open switch, 102 cute spdt down, 102 cute spdt down, 102 cute spdt down arrow, 30, 102 cute spdt mid arrow, 102 cute spdt mid arrow, 102 cute spdt up arrow, 102 cute spdt up arrow, 102 cvsource, see european controlled voltage source cvsourceC, see cute european controlled voltage cvsourceS, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage		·
bareantenna, 86 bareTXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 cute closed switch, 102 cute closing switch, 102 cute european controlled current source, 50 battery, 48 battery1, 48 biD*, see full bidirectionaldiode biJo, see empty bidirectionaldiode biJnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins	- ·	•
bareRXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 bareTXantenna, 86 barrier, 60 cute closed switch, 102 cute closing switch, 102 cute curopean controlled current source, 50 cute european current source, 49 biDo, see empty bidirectionaldiode biDo, see empty bidirectionaldiode biJtnpn, collectors=1, emitters=2, 72 bijtnpn, collectors=2, emitters=2, bjt pins width=0, bjt multi height=0.8, 78 bijtnpn, collectors=3, emitters=2, 72 bimultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 cute spdt down arrow, 30, 102 cute spdt down arrow, 30, 102 cute spdt mid arrow, 102 cute spdt mid arrow, 102 cute spdt up arrow, 102 cute spdt up arrow, 102 cute spdt up arrow, 102 cvsource see european controlled voltage cvsource see curopean controlled voltage cvsource see cute european controlled voltage cvsource cvsource see cute european controlled voltage	=:	
bareTXantenna, 86 barrier, 60 battery, 48 battery1, 48 battery2, 48 biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode biptnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins width=0, bjt multi height=0.8, 78 bjtpnp, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 Cute curopean controlled voltage source, 50 cute european current source, 49 cute inductive sensor, see sL cute inductor, see L cute inductor, see L cute open switch, 102 cute open switch, 102 cute spdt down, 102 cute spdt down arrow, 30, 102 cute spdt mid, 102 cute spdt mid, 102 cute spdt mid arrow, 102 cute spdt up, row, 102 cute spdt up, row, 102 cute spdt up, see american controlled voltage source cvsource, see curopean controlled voltage cvsourceAM, see american controlled voltage cvsourceC, see cute european controlled voltage cvsourceC, see cute european controlled voltage cvsourceC, see cute european controlled voltage cvsourceSin, see controlled sinusoidal voltage		cute choke, 42
barrier, 60 battery, 48 battery1, 48 battery2, 48 biD**, see full bidirectionaldiode biDo, see empty bidirectionaldiode biptnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins width=0, bjt multi height=0.8, 78 biptnpn, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 C, see capacitor capacitive sensor, 41 capacitor, 41 cC, see curved capacitor cceI, see cute european controlled voltage cute european controlled current source, 50 cute european current source, 49 cute european voltage source, 48 cute inductor, see L cute inductor, see L cute inductor, see L cute open switch, 102 cute spdt down, 102 cute spdt down arrow, 30, 102 cute spdt mid arrow, 30, 102 cute spdt mid arrow, 102 cute spdt up arrow, 102 cute spdt up arrow, 102 cvsource, see european controlled voltage source cvsourceAM, see american controlled voltage cvsourceC, see cute european controlled voltage cvsourceC, see cute european controlled voltage cvsourceSin, see controlled sinusoidal voltage	,	cute closed switch, 102
battery, 48 battery1, 48 battery2, 48 biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode biptnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins		cute closing switch, 102
battery1, 48 battery2, 48 biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode biptnpn, collectors=1, emitters=2, 72 bijtnpn, collectors=2, emitters=2, bjt pins	•	cute european controlled current source, 50
battery2, 48 biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode biJtnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins	- ·	cute european controlled voltage source, 50
biD*, see full bidirectionaldiode biDo, see empty bidirectionaldiode bjtnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins		cute european current source, 49
biDo, see empty bidirectionaldiode bjtnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins		cute european voltage source, 48
bjtnpn, collectors=1, emitters=2, 72 bjtnpn, collectors=2, emitters=2, bjt pins	,	cute inductive sensor, see sL
bjtnpn, collectors=2, emitters=2, bjt pins width=0, bjt multi height=0.8, 78 bjtpnp, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 C, see capacitor capacitive sensor, 41 capacitor, 41 cC, see curved capacitor cceI, see cute european controlled current source cceV, see cute european controlled voltage cute open switch, 102 cute spdt down, 102 cute spdt mid, 102 cute spdt mid arrow, 102 cute spdt up, 102 cute spdt up arrow, 102 cvsource, see european controlled voltage source cvsourceAM, see american controlled voltage cvsourceC, see cute european controlled voltage cvsourceC, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage		cute inductor, see L
width=0, bjt multi height=0.8, 78 bjtpnp, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 C, see capacitor capacitive sensor, 41 capacitor, 41 capacitor, 41 cC, see curved capacitor cC, see curved capacitor cceI, see cute european controlled current source cceV, see cute european controlled voltage cute opening switch, 102 cute spdt down, 102 cute spdt mid, 102 cute spdt mid arrow, 102 cute spdt up, 102 cute spdt up arrow, 102 cvsource, see european controlled voltage source cvsourceAM, see american controlled voltage cvsourceC, see cute european controlled voltage cvsourceC, see cute european controlled voltage cvsourceSin, see controlled sinusoidal voltage		cute open switch, 102
bjtpnp, collectors=3, emitters=2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 C, see capacitor capacitive sensor, 41 capacitor, 41 capacitor, 41 capacitor, 41 capacitor, see curved capacitor cC, see curved capacitor cC, see curved capacitor ccel, see cute european controlled current source cceV, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage cvsourcesin, see controlled sinusoidal voltage		
bighth, contectors—s, clinters—2, 72 bmultiwire, 61, see bmultiwire bnc, 65 buffer, 95 bulb, 60 cute spdt mid, 102 cute spdt mid arrow, 102 cute spdt up, 102 cute spdt up arrow, 102 cvsource, see european controlled voltage source cvsourceAM, see american controlled voltage cvsourceC, see cute european controlled current source cvsourceC, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage		
bnc, 65 buffer, 95 bulb, 60 cute spdt mid arrow, 102 cute spdt up, 102 cute spdt up arrow, 102 cvsource, see european controlled voltage source capacitive sensor, 41 capacitor, 41 source cC, see curved capacitor cceI, see cute european controlled current source cceV, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage	0 1 1	
buffer, 95 bulb, 60 cute spdt mid arrow, 102 cute spdt up, 102 cute spdt up arrow, 102 C, see capacitor capacitive sensor, 41 capacitor, 41 capacitor, 41 covsource, see european controlled voltage coccel, see curved capacitor ccel, see cute european controlled current source cceV, see cute european controlled voltage cvsourceS, see cute european controlled voltage cvsourceS, see cute european controlled voltage cvsourceS, see controlled sinusoidal voltage		
bulb, 60 Cute spdt up, 102 cute spdt up arrow, 102 C, see capacitor capacitive sensor, 41 capacitor, 41 capacitor, 41 covsource, see european controlled voltage source covsource, see american controlled voltage covsource, see curved american controlled voltage covsource, see european controlled voltage covsource, see coute european controlled voltage covsource, see european controlled voltage		
cute spdt up arrow, 102 C, see capacitor capacitive sensor, 41 capacitor, 41 capacitor, 41 cc, see curved capacitor cceI, see cute european controlled current source cceV, see cute european controlled voltage		
C, see capacitor cvsource, see european controlled voltage source capacitive sensor, 41 cvsourceAM, see american controlled voltage capacitor, 41 source cC, see curved capacitor cvsourceC, see cute european controlled current source cceV, see cute european controlled voltage cvsourceS, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage	build, 60	
capacitive sensor, 41 cvsourceAM, see american controlled voltage capacitor, 41 source CC, see curved capacitor cvsourceC, see cute european controlled current source cceV, see cute european controlled voltage cvsourceS, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage	C. see canacitor	
capacitor, 41 source cC, see curved capacitor cvsourceC, see cute european controlled voltage cceI, see cute european controlled current source cceV, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage	· · · · · · · · · · · · · · · · · · ·	, <u>.</u>
cC, see curved capacitor cvsourceC, see cute european controlled voltage cceI, see cute european controlled current source cceV, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage		
cceI, see cute european controlled current source source cceV, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage	1 ,	
cceV, see cute european controlled voltage cvsourcesin, see controlled sinusoidal voltage		
Source Source		
	boulou	504100

D*, see full diode fft, 66 flipflop, 119 D-, see stroke diode dac, 66 flipflop AB, 119 damper, 59, 160 flipflop D, 120 dcisource, 53 flipflop JK, 120 dcvsource, 53 flipflop JK, add async SR, 121 demux, 123 flipflop JK, add async SR, external pins detector, 67 width=0, 122diamondpole, 64 flipflop JK, dot on notQ, 120 diodetube, 82 flipflop myJK, 121 diodetube, filament, 83 flipflop SR, 120 diodetube, filament, nocathode, 83 flipflop T, 120 diodetube, full cathode, 84 flowarrow, 63 dipchip, 126 fourport, 67 Do, see empty diode full bidirectionaldiode, 45 dsp, 66 full diode, 45 full led, 45 eC, see ecapacitor full photodiode, 45 ecapacitor, 41 full Schottky diode, 45 ecsource, see empty controlled source full thyristor, 47 eground, 36 full triac, 46 eground2, 36 full tunnel diode, 45 elko, see ecapacitor full varcap, 45 elmech, 88 full Zener diode, 45 empty bidirectionaldiode, 45 full ZZener diode, 45 empty controlled source, 50 fuse, 60 empty diode, 44 empty led, 45 generic, 38 empty photodiode, 45 gm amp, 94 gridnode, 66 empty Schottky diode, 44 empty thyristor, 47 ground, 36 empty triac, 46 gyrator, 90 empty tunnel diode, 44 hemt, 72 empty varcap, 45 empty Zener diode, 44 highpass, 66 empty ZZener diode, 44 ieeestd and port, 108 en amp, 94 ieeestd buffer port, 108 esource, 52 ieeestd invschmitt port, 108 european and port, 109 ieeestd nand port, 108 european buffer port, 109 ieeestd nor port, 108 european controlled current source, 50 ieeestd not port, 108 european controlled voltage source, 50 ieeestd or port, 108 european current source, 49 ieeestd schmitt port, 108 european gas filled surge arrester, 60 ieeestd xnor port, 108 european inductive sensor, see sL ieeestd xor port, 108 european inductor, see L iloop, 55 european nand port, 109 iloop2, 55 european nor port, 109 inerter, 59 european not port, 109 inline buffer, 110 european or port, 109 inline invschmitt, 110 european potentiometer, see pR inline not, 110 european resistive sensor, see sR inline schmitt, 110 european resistor, see R inputarrow, 63 european voltage source, 48 inst amp, 94 european xnor port, 109 inst amp ra, 95 european xor port, 109 invschmitt, 107 fd inst amp, 95 ioosource, 52 fd op amp, 94 isfet, 73

isource, see european current source npn, photo, 71 isourceAM, see american current source nV, see noise voltage source isourceC, see cute european current source isourceN, see noise current source ocirc, 64 odiamondpole, 64 isourcesin, see sinusoidal current source ohmmeter, 54 one bit adder, 124 jump crossing, 62 ooosource, 52 L, 42 oosourcetrans, 52 lamp, 60 op amp, 94 latch, 120 open, 38 leD*, see full led openbarrier, 60 leD-, see stroke led opening switch, 101 leDo, see empty led oscillator, 65 Lnight, 71 oscope, 54 loudspeaker, 61 ospst, see opening switch lowpass, 66 osquarepole, 64 Lpigbt, 71 Lpigbt, bodydiode, 71 pC, see polar capacitor pD*, see full photodiode magnetron, 85 pD-, see stroke photodiode mass, 59 pDo, see empty photodiode match, 87 pentode, 83 memristor, 39 pentode suppressor to cathode, 83 mic, 61 pfet, 73 mixer, 65 pground, 36 Mr, see memristor phaseshifter, 67 mslstub, 86 photoresistor, see phR msport, 87 phR, 40 msrstub, 87 piattenuator, 67 mstline, 86 piezoelectric, 41 multiwire, 61, see multiwire pigbt, 71 muxdemux, 123 pigfetd, 73 pigfete, 73 ncpb, see normally closed push button pigfetebulk, 73 ncs, see normal closed switch pifet, 73 nfet, 72 plain amp, 30, 95 nground, 36 plain crossing, 62 nI, see noise current source plain mono amp, 95 night, 71 pmos, 72, 74 nigfetd, 73 pmos, bulk, 77 nigfete, 72 pmos, emptycircle, 77 nigfete, solderdot, 73 pmos, nocircle, arrowmos, 77 nigfetebulk, 73 pnp, 70 njfet, 73 pnp, schottky base, 70 nmos, 72, 74 pnp,photo, 71 nmos, bulk, 77 polar capacitor, 42 noise current source, 51 pR, 28, see pR, 39noise voltage source, 51 push button, 101 nopb, see push button pvsource, 52 normal closed switch, 101 PZ, see piezoelectric normal open switch, 101 normally closed push button, 101 qfpchip, 127 normally open push button, see push button qiprobe, 54 nos, see normal open switch approbe, 54 notcirc, 109 gyprobe, 54 npn, 30, 70 npn, bodydiode, 70 R, see resistor, 39

reed, 101

npn, schottky base, 70

resistor, 28	TL, 87
rground, 36	tlground, 36
rmeter, 54	tline, $see TL$
rmeterwa, 54	tlinestub, 87
rotaryswitch, 30, 104	tmultiwire, 61, see tmultiwire
rxantenna, 87	toggle switch, 101
,	Tr, see triac
sacde, 67	Tr*, see full triac
sC, see capacitive sensor	transformer, 89, 90
schmitt, 107	transformer core, 90
schmitt symbol, 109	transmission line, see TL
sD*, see full Schottky diode	trarrow, 63
sD-, see stroke Schottky diode	triac, 46
sdcac, 67	triode, 82
sDo, see empty Schottky diode	Tro, see empty triac
sground, 36	tV, see vsourcetri
short, 38	twoport, 66
sI, see sinusoidal current source	txantenna, 87
sinusoidal current source, 49	Ty, see thyristor
sinusoidal voltage source, 49	
sL, 42, 43	Ty*, see full thyristor
smeter, 54	Ty-, see stroke thyristor
	Tyo, see empty thyristor
spdt, 101 spring, 59	ALONDON CC
1 0/	vamp, 66
spst, see switch	variable american inductor, see vL
square voltage source, 52	variable american resistor, see vR
squarepole, 64	variable capacitor, 41
squid, 60	variable cute inductor, see vL
sqV, see square voltage source	variable european inductor, see vL
sR, 39	variable european resistor, $see \ vR$
stroke diode, 46	varistor, 39
stroke led, 46	vC, see variable capacitor
stroke photodiode, 46	VC*, see full varcap
stroke Schottky diode, 46	VC-, see stroke varcap
stroke thyristor, 47	vcc, 37
stroke tunnel diode, 46	VCo, see empty varcap
stroke varcap, 46	vco, 66
stroke Zener diode, 46	vee, 37
stroke ZZener diode, 46	viscoe, 59, 163
sV, see sinusoidal voltage source	vL, 42, 43
switch, 101	voltmeter, 54
	voosource, 52
tacdc, 67	vphaseshifter, 67
tattenuator, 67	vpiattenuator, 67
tD*, see full tunnel diode	vR, 39
tD-, see stroke tunnel diode	vsource, see european voltage source
tdcac, 67	vsourceAM, see american voltage source
tDo, see empty tunnel diode	vsourceC, see cute european voltage source
tetrode, 82	vsourceN, see noise voltage source
tgeneric, 39	vsourcesin, see sinusoidal voltage source
tground, 36	vsourcesquare, see square voltage source
thermistor, see thR	vsourcetri, 52
thermistor ntc, see thRn	vtattenuator, 67
thermistor ptc, see thRp	rowoodiawood, or
thermocouple, 60	waves, 86
thR, 40	waves, 60 wilkinson, 66
thRn, 40	winingon, oo
thRp, 40	xgeneric, 38
thyristor 46	xing see crossing

zD*, see full Zener diode zD-, see stroke Zener diode zDo, see empty Zener diode zzD*, see full ZZener diode zzD-, see stroke ZZener diode zzDo, see empty ZZener diode