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**MIT WORLD PEACE
UNIVERSITY** PUNE

TECHNOLOGY, RESEARCH, SOCIAL INNOVATION & PARTNERSHIPS

School of Computer science and Engineering

Department of Computer Engineering and Technology

Final year BTech (CSE) (Academic Year 2024-25)

Mid Term Exam - Semester VII

Course Name: -SSCD

Course Code:-CET3011B

Maximum Marks: 30

Time: 1 Hr 30 Minutes

Date:

Instructions:-

1. Attempt any 3 questions from Q. 1 to Q. 4 AND Attempt any 3 questions from Q. 5 to Q. 8
2. Figure to the right indicates full marks.
3. Use of cell phone is prohibited in the examination hall.
4. Neat diagrams must be drawn wherever necessary.
5. Assume suitable data, if necessary and clearly state.
6. Use of scientific calculator is allowed

Attempt any 3 questions from Q. 1 to Q. 4		
Q.1	START 200 MOVER AREG = '5' MOVER AREG A LOOP MOVEM AREG A MOVER CREG B ADD CREG B LTORG NEXT1 SUB AREG = '1' ORIGIN LOOP+8 MUL CREG B A DS 2 B DC 3 NEXT2 EQU LOOP END Design intermediate code and symbol table for the above program.	[5 Marks]
Q.2	Explain debugger and text editor system software.	[5 Marks]
Q.3	Design LEX program for counting number of words, lines and character from an input file.	[5 Marks]
Q.4	List and explain various phases of compiler with an example.	[5 Marks]
Attempt any 3 questions from Q. 5 to Q. 8		
Q.5	Determine the contents of ESD and RLD cards for the following program.	[5 Marks]

	Ref no	Relative address	Program	
	1	0	P1	START
	2			ENTRY P1E1, P1E2
	3			EXTRN P2E2, P2
	4	20	P1E1	
	5	30	P1E2	
	6	40		DC A (P1E1)
	7	44		DC A (P1E2+15)
	8	48		DC A (P1E2-P1E1-3)
	9	52		DC A (P2)
	10	56		DC A (P2E1+P2-P1E1+4)
	11	60		END
Q.6	Explain the concept of relocation with an example. "Relocation is performed by the linker as well as the loader". Is this statement true or false? Justify your answer.			[5 Marks]
Q.7	List the different types of Loader schemes with one advantage of each.			[5 Marks]
Q.8	Estimate the output generated at the end of Pass I of a 2 pass macroprocessor, for the given assembly language program below. START MACRO INCR &ARG MOVER CREG, LABEL1 ADD AREG, &ARG ADD AREG, DATA1 ADD BREG, &ARG ADD BREG, DATA1 MEND SUB CREG, LABEL1 MOVER CREG, LABEL1 INCR DATA1 SUB CREG, LABEL1 END			[5 Marks]