Type-safe higher-order channels with channel locality¹

SUNGWOO PARK and HYEONSEUNG IM

Department of Computer Science and Engineering,

Pohang University of Science and Technology, Republic of Korea

(e-mail: {gla,genilhs}@postech.ac.kr)

Abstract

As a means of transmitting not only data but also code encapsulated within functions, higher-order channels provide an advanced form of task parallelism in parallel computations. In the presence of mutable references, however, they pose a safety problem because references may be transmitted to remote threads where they are no longer valid. This paper presents an ML-like parallel language with *type-safe* higher-order channels. By type safety, we mean that no value written to a channel contains references, or equivalently, that no reference escapes via a channel from the thread where it is created. The type system uses a typing judgment that is capable of deciding whether the value to which a term evaluates contains references or not. The use of such a typing judgment also makes it easy to achieve another desirable feature of channels, *channel locality*, that associates every channel with a unique thread for serving all values addressed to it. Our type system permits mutable references in sequential computations and also ensures that mutable references never interfere with parallel computations. Thus, it provides both flexibility in sequential programming and ease of implementing parallel computations.

1 Introduction

The advent of multicore processors and the impending demise of free lunch (Sutter, 2005) have changed the conventional wisdom on computer hardware (Asanovic *et al.*, 2006). There is little room for increasing clock frequency while increasing hardware parallelism is now the only viable way of improving processor performance. Such a radical change in the trend of computer hardware has revitalized research on language support for parallel programming, as evidenced by recent parallel languages such as X10 (Charles *et al.*, 2005), Fortress (Allan *et al.*, 2007), Chapel (Cray Inc., 2005), Data Parallel Haskell (Chakravarty *et al.*, 2007), and Manticore (Fluet *et al.*, 2007).

Depending on the granularity of parallel computations, parallel programming models are divided into *data parallelism* or *task parallelism*. Data parallelism applies an independent operation to each element of a collection of homogeneous data and

¹ This is an extended version of the paper that appeared in The 12th ACM SIGPLAN International Conference on Functional Programming (Park, 2007).

often results in massive parallel computations. A simple case of data parallelism is flat data parallelism in which the independent operation is sequential. A more sophisticated model called nested data parallelism (Blelloch, 1996) allows the independent operation itself to be a parallel computation. Task parallelism executes cooperative threads in parallel which communicate via shared memory or channels in order to perform synchronization. The use of shared memory simplifies programming tasks, especially if a high-level abstraction such as software transactional memory (Shavit & Touitou, 1995) is provided, but it entails the memory consistency problem. The use of channels empirically requires more effort than programming with shared memory (Hochstein et al., 2005), but it eliminates the memory consistency problem.

This paper is primarily concerned with an extension of an ML-like language, i.e., a call-by-value functional language with mutable references, that offers task parallelism with *higher-order channels* (but without logically shared memory). Higher-order channels transmit not only data, such as integers and channel names, but also pieces of code encapsulated within functions. The capability to transmit code between threads opens a new range of communication constructs such as futures, remote evaluation, code on demand, and hot code replacement.

Because of the similarity between task parallelism in parallel computations and process parallelism in distributed computations, our target language can also be thought of as a distributed language in which processes share no global memory and communicate via higher-order channels only. Thus, although our work primarily aims at designing higher-order channels for parallel languages, it can be equally applied to distributed languages.

1.1 Type-safe higher-order channels

The main problem with higher-order channels is that in the presence of mutable references, code containing references may travel between threads, but in the absence of logically shared memory, a reference created by a thread cannot be dereferenced at another thread. Previous parallel or distributed languages with a similar programming model avoid this problem by dispensing with mutable references altogether, as in Manticore (Fluet *et al.*, 2007), by designing the runtime system so as to create copies of heap cells whenever their references are transmitted, as in Facile (Knabe, 1995) and JoCAML (Fournet *et al.*, 2003), or by raising runtime exceptions when references are transmitted, as in Alice (Rossberg *et al.*, 2005).

This paper takes a different approach by developing type-safe higher-order channels. The type system ensures that no reference escapes from the thread where it is created so that all values written to channels remain valid even after being transmitted to remote threads. To be specific, suppose that a thread is executing a channel write a!M where a is a channel for a certain type A and M is a term of the same type A. Evaluating M yields a value V which is then transmitted to another thread executing a channel read a?. Type safety of channels means that V contains no references and that V is valid at both threads regardless of its type A.

Conventional type systems, however, are inadequate to guarantee type safety of channels. The crux of the problem is that a typical typing judgment M:A does not

indicate whether the result of evaluating M contains references or not. For example, a term

$$(\lambda v : \text{int. } \lambda x : \text{int. } x + v) \ 0$$

of type int \rightarrow int evaluates to a value containing no reference, but another term

$$(\lambda r : \text{ref int. } \lambda x : \text{int. } x + !r) \text{ (ref 0)}$$

of the same type evaluates to a value containing a reference. Therefore, in order to guarantee type safety of channels, we need at least two kinds of typing judgments: an ordinary typing judgment asserting that a given term evaluates to a value that may contain references, or a *local value*, and another stronger typing judgment asserting that a given term evaluates to a value containing no references, or a *global value*.

The present work is based on the modal type system in our previous work (Park, 2006) which uses two typing judgments and a new modality \Box to distinguish between local values and global values in the context of distributed computations. We simplify the type system by combining the two typing judgments into a single typing judgment $M:A_{@L}$ where L is a locality indicating whether the value to which M evaluates is local (L=L) or global (L=G). The connection between two localities L and G is established by a new construct box M and a modal type $\Box A$ such that $M:A_{@G}$ implies box $M: \Box A_{@L}$, i.e., if M evaluates to a global value of type A, then box M has type $\Box A$. In order to explicitly bind variables to global values, we use another construct letbox x=M in N such that letbox x=box M' in N evaluates N after binding variable x to the global value to which M' evaluates to.

As our modal type system enables us to express that the result of evaluating a term is a global value and contains no references, it is straightforward to guarantee type safety of channels: we simply require that a channel write a!M typecheck only if $M:A_{@G}$ holds. As a result, a channel read a? always returns a global value and thus a? : $A_{@G}$ automatically holds.

1.2 Channel locality

The type system developed for higher-order channels makes it easy to achieve an important feature of task parallelism, *channel locality*, which states that every channel is associated with a unique thread for reading off all values sent to it. Channel locality obviates the need for a sophisticated mechanism in the runtime system for dynamically determining the destination of each value written to a channel.

Most of the previous approaches to enforcing channel locality (Fournet *et al.*, 1996; Amadio, 1997; Yoshida & Hennessy, 1999; Schmitt & Stefani, 2003) have been developed for calculi for concurrent processes based on the pi-calculus. We find that these approaches are difficult to apply to our setting which uses as a base language the simply typed lambda-calculus with mutable references instead of the pi-calculus or its variant.

The main idea for achieving channel locality in our work is to split channels into two kinds, *read channels* and *write channels*, and treat read channels as local values

but write channels as global values. $new_{\langle A \rangle}$, a construct for creating channels for type A, evaluates to a pair of read channel a^r and write channel a^w such that a^r accepts only those values written to a^w . Since a^r is a local value, channel reads from a^r is allowed only at the thread where a^r is created. However, channel writes to a^w is allowed at any thread because a^w is a global value. Thus a pair of a^r and a^w can open unidirectional communications from any thread to the thread to which a^r belongs.

It is easy to classify read channels as local values and write channels as global values. First we assign different types, namely *read channel types* and *write channel types*, to read channels and write channels, respectively. Then we treat read channel types like reference types so that read channels cannot be a part of a global value, but define write channel types as primitive types whose values are all inherently global (like integers). For this reason, our decision to distinguish between read channels and write channels has a different motivation from previous work in which read channels and write channels are also distinguished, but channel locality is not enforced (Odersky, 1995) or enforced only syntactically (Zhang & Potter, 2002).

1.3 Contributions

We develop an ML-like parallel language λ_{\square}^{PC} which features type-safe higher-order channels with channel locality. Its type system inherits from the type system of Park (2006) the ability to distinguish between local values and global values. Its operational semantics models parallel computations where multiple threads communicate via higher-order channels. Channel locality is a direct consequence of assigning different types to read channels and write channels.

Although, λ_{\square}^{PC} deals primarily with type safety for task parallelism, providing type safety for data parallelism is also straightforward by virtue of its ability to distinguish between local values and global values. As an example, consider a parallel map construct mapP which applies a function f to each element of an array in parallel. By requiring that f be a global value so that child threads share no references, we can prevent mapP from running into the memory consistency problem. In this regard, λ_{\square}^{PC} serves as a unified framework for achieving type safety for both data parallelism and task parallelism when mutable references are allowed.

Removing mutable references simplifies the implementation of a parallel language because lack of mutable references implies automatic data separation in parallel computations. For example, Manticore (Fluet *et al.*, 2007) excludes mutable references from its base language (a subset of Standard ML) in order to simplify its implementation. Parallel dialects of Haskell, such as pH (Nikhil & Arvind, 2001) and Data Parallel Haskell (Chakravarty *et al.*, 2007), also benefit from lack of mutable references. In comparison, λ_{\square}^{PC} permits mutable references in sequential computations, but its type system ensures that mutable references never interfere with parallel computations. Thus the type system of λ_{\square}^{PC} wins us both flexibility in sequential programming and ease of implementing parallel computations.

```
A,B,C
                                        ::=  unit |A \rightarrow A|A \times A|A+A| ref A \mid \Box A
type
primitive type
                          M,N
                                        ::= x \mid \lambda x : A.M \mid MM \mid (M,M) \mid \text{fst } M \mid \text{snd } M \mid
term
                                                 \operatorname{inl} M \mid \operatorname{inr} M \mid \operatorname{case} M \text{ of } \operatorname{inl} x \Rightarrow M \mid \operatorname{inr} x \Rightarrow M \mid
                                                 () \mid \text{ fix } x:A.M \mid \text{ ref } M \mid !M \mid M:=M \mid l \mid
                                                 box M \mid letbox x = M in M
                          V
                                                 () |\lambda x:A.M|(V,V)| inl V| inr V|l| box M
value
                                                 G \mid L
locality
                          L
                                        ::=
                          Γ
typing context
                                        := \cdot | \Gamma, x : A_{@L}
                                        ::= \cdot \mid \psi, l \mapsto V
                                        ::= \cdot \mid \Psi, l \mapsto A
store typing
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Fig. 1. Abstract syntax for λ_{\square} .

1.4 Organization of the paper

Section 2 presents the base language λ_{\square} for our work. Section 3 discusses the logical meaning of the modality \square . Although, the type system differentiates global values from local values, λ_{\square} is just a sequential language to which global values are of no use. Hence, we develop a parallel operational semantics for modeling parallel computations. Section 4 presents the resultant language λ_{\square}^{P} which comes with a new construct spawn $\{M\}$ for creating new threads. Section 5 extends λ_{\square}^{P} with new constructs for higher-order channels to obtain λ_{\square}^{PC} . Section 6 illustrates how to implement various communication constructs in λ_{\square}^{PC} such as futures, bidirectional communications, shared references, remote evaluation, code on demand, and hot code replacement. Section 7 discusses two extensions of λ_{\square}^{PC} (including type safety for data parallelism). Section 8 discusses related work, and Section 9 concludes. Most proofs are given in Appendix.

2 Base language λ_{\square}

This section presents the base language λ_{\square} which is essentially a reformulation of the call-by-value language with modal types $\square A$ defined in our previous work (Park, 2006).

2.1 Definition of λ_{\square}

Figure 1 shows the abstract syntax for λ_{\square} which is based on the simply typed lambda-calculus with product types $A \times A$, sum types A + A, reference types ref A, and the fixed point construct fix x : A. M. Primitive types P are a subset of ordinary types A whose values are all inherently global. (We will define the set of primitive types later.) ref M allocates a fresh reference, M dereferences an existing reference, and M := N assigns a new value to a reference. A location M, of type ref M, is a value for a reference. A new construct box M has a modal type M, and another new construct letbox M in M expects M to be of type M.

We use a typing judgment $\Gamma \mid \Psi \vdash M : A_{@L}$ to mean that under typing context Γ and store typing Ψ , term M evaluates to a value of type A with locality L. The resultant value is local if L = L and global if L = G. (That is, L means "here only"

$$\frac{x:A_{@L} \in \Gamma \quad L \leq L'}{\Gamma \mid \Psi \vdash x:A_{@L'}} \text{ Var}$$

$$\frac{\Gamma,x:A_{@L} \mid \Psi \vdash M:B_{@L}}{\Gamma \mid \Psi \vdash \lambda x:A.M:A \to B_{@L}} \to I \quad \frac{\Gamma \mid \Psi \vdash M:A \to B_{@L} \quad \Gamma \mid \Psi \vdash N:A_{@L}}{\Gamma \mid \Psi \vdash MN:B_{@L}} \to E$$

$$\frac{\Gamma \mid \Psi \vdash (): \text{unit}_{@L}}{\Gamma \mid \Psi \vdash h:\lambda x:A.M:A \to B_{@L}} \quad \text{Unit} \quad \frac{\Gamma,x:A_{@L} \mid \Psi \vdash M:A_{@L}}{\Gamma \mid \Psi \vdash h:x:A.M:A_{@L}} \quad \text{Fix} \quad \frac{\Gamma \mid \Psi \vdash M:A_{@L}}{\Gamma \mid \Psi \vdash ref M:ref A_{@L}} \quad \text{Ref}$$

$$\frac{\Gamma \mid \Psi \vdash M: \text{ref } A_{@L}}{\Gamma \mid \Psi \vdash M:A_{@L}} \quad \text{Drf} \quad \frac{\Gamma \mid \Psi \vdash M: \text{ref } A_{@L}}{\Gamma \mid \Psi \vdash M:B_{@L}} \times I \quad \text{mit}_{@L} \quad \text{Asn} \quad \frac{\Psi(l) = A}{\Gamma \mid \Psi \vdash l: \text{ref } A_{@L}} \quad \text{Loc}$$

$$\frac{\Gamma \mid \Psi \vdash M:A_{@L}}{\Gamma \mid \Psi \vdash M:A_{@L}} \quad \times I \quad \frac{\Gamma \mid \Psi \vdash M:A \times B_{@L}}{\Gamma \mid \Psi \vdash \text{fst } M:A_{@L}} \times E_{L} \quad \frac{\Gamma \mid \Psi \vdash M:A \times B_{@L}}{\Gamma \mid \Psi \vdash \text{snd } M:B_{@L}} \times E_{R}$$

$$\frac{\Gamma \mid \Psi \vdash M:A_{@L}}{\Gamma \mid \Psi \vdash \text{inl } M:A + B_{@L}} \quad + I_{L} \quad \frac{\Gamma \mid \Psi \vdash M:B_{@L}}{\Gamma \mid \Psi \vdash \text{inr } M:A + B_{@L}} \quad + I_{R}$$

$$\frac{\Gamma,x:A_{@L'} \mid \Psi \vdash N:C_{@L}}{\Gamma \mid \Psi \vdash \text{case } M \text{ of inl } x \Rightarrow N \text{ inr } x' \Rightarrow N':C_{@L}} \quad + E$$

$$\frac{\Gamma \mid \Psi \vdash M:A_{@G}}{\Gamma \mid \Psi \vdash \text{box } M:\Omega_{@L}} \quad = \Gamma \mid \Psi \vdash M:\Omega_{@L} \quad \Gamma,x:A_{@G} \mid \Psi \vdash N:C_{@L}} \quad - E$$

$$\frac{\Gamma \mid \Psi \vdash M:A_{@G}}{\Gamma \mid \Psi \vdash \text{box } M:\Omega_{@L}} \quad = \Gamma \mid \Psi \vdash M:\Omega_{@L}}{\Gamma \mid \Psi \vdash \text{box } M:\Omega_{@L}} \quad - \Gamma \mid \Psi \vdash \text{letbox } x = M \text{ in } N:C_{@L}} \quad - E$$

Fig. 2. Type system of λ_{\square} .

and G "everywhere.") A binding $x:A_{@L}$ in a typing context Γ means that x holds a local value of type A if L=L, or a global value of type A if L=G. A store ψ maps locations to values, and a store typing Ψ maps locations to types of these values. We assume a relation G<L to reflect the fact that a global value may be used as a local value, but not vice versa.

Figure 2 shows the type system of λ_{\square} . The rule Var uses $L \leq L'$ to mean either L = L' or L < L'. The rules \rightarrow I through Loc are all derived from typing rules in the simply typed lambda-calculus by annotating each typing judgment with "here only" locality L. In order to relate terms in these rules with "everywhere" locality G, we need a separate application of the rule GVal or Prim (to be explained later) which connects the two localities L and G. In contrast, the rules $\times I$, $\times E_L$, and $\times E_R$ for product types use the same unspecified locality L in their premise and conclusion, which makes sense only because we assume the call-by-value semantics (where (M, N) is a value only when both M and N are also values). For example, if both M and N evaluate to global values V and V', respectively, we may associate (M, N) with locality G because (M, N) indeed evaluates to a global value (V, V'). If we assume the call-by-name semantics and consider (M, N) as a value for any term M and N, however, all these rules become invalid when L = G. For example, if both $!l_1$ and $!l_2$ evaluate to global values such as (), the rule $\times I$ incorrectly deduces that $(!l_1, !l_2)$ (which contains two locations and thus is not a global value) is a global

value. Similarly the call-by-value semantics allows the rules $+I_L$, $+I_R$, and +E for sum types to use the same unspecified locality L in their premise and conclusion.

The modality \square has an introduction rule \square I which states that M in box M always evaluates to a global value. The conclusion of the rule \square I uses locality \square because box M itself may not be a global value if M contains references. For example, if l is a location of type ref int, box !l is not a global value although it has a modal type \square int. The elimination rule \square E uses an unspecified locality L in the conclusion because M in letbox x = M in N does not specify whether N evaluates to a local value or a global value. That is, regardless of the type of M, the value to which N evaluates can still be either local or global.

The rule GVal is the main rule for connecting the two localities L and G. Its premise uses the following definition of Γ_G which extracts bindings for variables holding global values from Γ :

$$\Gamma_{\mathsf{G}} = \{ x : A_{@\mathsf{G}} \mid x : A_{@\mathsf{G}} \in \Gamma \}$$

Then the premise $\Gamma_G \mid \cdot \vdash V : A_{@L}$ states that V is a value that uses no local values (because of Γ_G) and no references (because of an empty store typing); hence V is a global value. Since V is already a value and thus requires no further evaluation, we may say that V evaluates to a global value, which is expressed in the conclusion $\Gamma \mid \Psi \vdash V : A_{@G}$.

The rule Prim uses the notion of primitive type to provide another way to connect the two localities L and G. A type is primitive if all its values are inherently global. For example, unit is a primitive type because its only value, (), typechecks under any typing context and store typing, as shown in the rule Unit. (Other examples of primitive types would be int for integers and bool for boolean values.) Formally, we define primitive types as follows:

Definition 2.1

$$P$$
 is a primitive type if $\Gamma \mid \Psi \vdash V : P_{@L}$ implies $\Gamma_{G} \mid \cdot \vdash V : P_{@L}$.

Then terms of primitive types always evaluate to global values, and $\Gamma \mid \Psi \vdash M : P_{@L}$ automatically implies $\Gamma \mid \Psi \vdash M : P_{@G}$ as shown in the rule Prim.

Under the type system in Figure 2, the rule Unit justifies the use of unit as a primitive type. In addition, $P_1 \times P_2$ and $P_1 + P_2$ are primitive types if both P_1 and P_2 are primitive types, since values of product type $P_1 \times P_2$ have the form (V_1, V_2) , and values of sum type $P_1 + P_2$ have the form inl V or inr V. Thus, we use the following set of primitive types for λ_{\square} :

primitive type
$$P ::= \text{unit} \mid P \times P \mid P + P$$

Proposition 2.2 justifies the relation G < L.

Proposition 2.2

The rule
$$\frac{\Gamma \mid \Psi \vdash M : A_{@G}}{\Gamma \mid \Psi \vdash M : A_{@L}}$$
 Global is admissible.

Proof

By induction on the structure of $\Gamma \mid \Psi \vdash M : A_{@G}$.

Fig. 3. Operational semantics for λ_{\square} .

Figure 3 shows the operational semantics for λ_{\square} . It uses a reduction judgment $M \mid \psi \to M' \mid \psi'$ which means that term M with store ψ reduces to term M' with store ψ' . A β -reduction $M \to_{\beta} M'$ uses a capture-avoiding substitution [N/x]M defined in a standard way. $\phi[M]$ fills the hole [M] in evaluation context ϕ with term M. $[l \mapsto V]\psi$ replaces $l \mapsto V'$ in store ψ by $l \mapsto V$. $\psi(l)$ denotes the value to which l is mapped under ψ ; $dom(\psi)$ denotes the set of locations mapped under ψ .

Since letbox $x = box \phi$ in N is an evaluation context, letbox x = box M in N further evaluates M so as to substitute the resultant value for x in N, even though box M is already a value. Thus, letbox x = M' in N first evaluates M' to identify how to obtain a value to be substituted for x, and then obtains such a value by evaluating M, if M' evaluates to box M. Since M evaluates to a global value, all occurrences of x in N become bound to a global value, as required by the rules $\square E$.

2.2 Examples

We illustrate the use of the modality \Box with the two examples given in Section 1.1. We assume a primitive type int, a typing rule Int for integers, and an infix operator + for adding two integers.

The first term

$$M_1 = (\lambda v : \text{int. } \lambda x : \text{int. } x + v) 0$$

has type int \rightarrow int and indeed evaluates to a global value λx : int. x + 0, but cannot be used to build a term of type \Box (int \rightarrow int):

$$\frac{(\textit{no typing rule applicable})}{\Gamma \mid \Psi \vdash (\lambda v : \mathsf{int}. \, \lambda x : \mathsf{int}. \, x + v) \; 0 : \mathsf{int} \to \mathsf{int}_{@G}}{\Gamma \mid \Psi \vdash \mathsf{box} \; (\lambda v : \mathsf{int}. \, \lambda x : \mathsf{int}. \, x + v) \; 0 : \Box (\mathsf{int} \to \mathsf{int})_{@L}} \; \Box \mathsf{I}$$

The reason why box M_1 fails to have type $\Box(\operatorname{int} \to \operatorname{int})$ is that the type of λv : $\operatorname{int.} \lambda x : \operatorname{int.} x + v$, namely $\operatorname{int} \to (\operatorname{int} \to \operatorname{int})$, does not express that a global value of type $\operatorname{int} \to \operatorname{int}$ is returned. That is, the type system is unaware that the inner λ -abstraction $\lambda x : \operatorname{int.} x + v$ can be a global value, since a binding $v : \operatorname{int}_{@G}$ is not added to the typing context. In order to build an operationally equivalent term of type $\Box(\operatorname{int} \to \operatorname{int})$, we need to explicitly specify that 0 is a global value as follows:

$$M_1' = \text{letbox } v = \text{box } 0 \text{ in } \lambda x : \text{int. } x + v$$

Note that typechecking box M'_1 adds a binding $v : int_{@G}$ to the typing context:

$$\frac{\frac{\Gamma \mid \Psi \vdash 0 : \mathsf{int}_{@L}}{\Gamma \mid \Psi \vdash 0 : \mathsf{int}_{@L}} \quad \mathsf{Int}}{\frac{\Gamma \mid \Psi \vdash 0 : \mathsf{int}_{@L}}{\Gamma \mid \Psi \vdash \mathsf{box} \ 0 : \Box \mathsf{int}_{@L}}} \quad \mathsf{Prim} \quad \frac{\frac{\vdots}{\Gamma_{\mathsf{G}}, v : \mathsf{int}_{@G}, x : \mathsf{int}_{@L}} \mid \cdot \vdash x + v : \mathsf{int} \to \mathsf{int}_{@L}}{\frac{\Gamma_{\mathsf{G}}, v : \mathsf{int}_{@G}}{\Gamma, v : \mathsf{int}_{@G}} \mid \Psi \vdash \lambda x : \mathsf{int}. x + v : \mathsf{int} \to \mathsf{int}_{@G}}}{\frac{\Gamma \mid \Psi \vdash \mathsf{letbox} \ v = \mathsf{box} \ 0 \ \mathsf{in} \ \lambda x : \mathsf{int}. x + v : \mathsf{int} \to \mathsf{int}_{@G}}{\Gamma \mid \Psi \vdash \mathsf{box} \ \mathsf{letbox} \ v = \mathsf{box} \ 0 \ \mathsf{in} \ \lambda x : \mathsf{int}. x + v : \Box (\mathsf{int} \to \mathsf{int})_{@L}}} \quad \Box \mathsf{E}$$

The second term

$$M_2 = (\lambda r : \text{ref int. } \lambda x : \text{int. } x + !r) \text{ (ref 0)}$$

has type int \rightarrow int and does not evaluate to a global value. The type system also prevents M_2 from being used to build a term of type \Box (int \rightarrow int) because there is no typing derivation of $\Gamma \mid \Psi \vdash M_2 : \text{int} \rightarrow \text{int}_{@G}$:

$$\frac{(\textit{no typing rule applicable})}{\Gamma \mid \Psi \vdash (\lambda r : \mathsf{ref int}. \lambda x : \mathsf{int}. x + !r) \; (\mathsf{ref 0}) : \mathsf{int} \to \mathsf{int}_{@G}}{\Gamma \mid \Psi \vdash \mathsf{box} \; (\lambda r : \mathsf{ref int}. \lambda x : \mathsf{int}. x + !r) \; (\mathsf{ref 0}) : \Box (\mathsf{int} \to \mathsf{int})_{@L}} \; \Box \mathsf{log}$$

Rewriting M_2 as letbox r = box ref 0 in $\lambda x : int. x + !r$ does not help because ref 0 does not return a global value.

Note that although M_2 does not evaluate to a global value, M_2 itself is closed and contains no free references. Hence, for example, λ_- : unit. M_2 is a global value (where $_-$ denotes a fresh variable) and box λ_- : unit. M_2 has type \square (unit \rightarrow (int \rightarrow int)):

$$\frac{\vdots}{\Gamma_{\mathsf{G}}\mid \cdot \vdash \lambda_{-} : \mathsf{unit}. (\lambda r : \mathsf{ref} \; \mathsf{int}. \; \lambda x : \mathsf{int}. \; x + !r) \; (\mathsf{ref} \; 0) : \mathsf{unit} \to (\mathsf{int} \to \mathsf{int})_{@L}}{\Gamma\mid \Psi \vdash \lambda_{-} : \mathsf{unit}. \; (\lambda r : \mathsf{ref} \; \mathsf{int}. \; \lambda x : \mathsf{int}. \; x + !r) \; (\mathsf{ref} \; 0) : \mathsf{unit} \to (\mathsf{int} \to \mathsf{int})_{@G}} \; \mathsf{GVal}}{\Gamma\mid \Psi \vdash \mathsf{box} \; \lambda_{-} : \mathsf{unit}. \; (\lambda r : \mathsf{ref} \; \mathsf{int}. \; \lambda x : \mathsf{int}. \; x + !r) \; (\mathsf{ref} \; 0) : \Box (\mathsf{unit} \to (\mathsf{int} \to \mathsf{int}))_{@L}} \; \Box \mathsf{Int}$$

2.3 Type safety of λ_{\square}

The proof of type safety of λ_{\square} needs a store typing judgment $\Psi \vdash \psi$ okay which means that store ψ conforms to store typing Ψ . $\Psi(l)$ denotes the type to which l is

mapped under Ψ ; $dom(\Psi)$ denotes the set of locations mapped under Ψ .

$$\frac{\mathit{dom}(\Psi) = \mathit{dom}(\psi)}{\Psi \vdash \psi \text{ okay}} \xrightarrow{ \begin{subarray}{c} \mathit{for every } l \in \mathit{dom}(\psi) \\ & \cdot \mid \Psi \vdash \psi(l) : \Psi(l)_{@L} \end{subarray}} \mathsf{Store}$$

The proof of progress (Theorem 2.3) uses a canonical forms lemma, as usual. The proof of type preservation (Theorem 2.6) uses a substitution theorem (Theorem 2.4). It also uses Lemma 2.5 whose proof uses the definition of primitive types (Definition 2.1).

Theorem 2.3 (Progress)

Suppose $\cdot \mid \Psi \vdash M : A_{@L}$. Then either

- (1) M is a value, or
- (2) for any store ψ such that $\Psi \vdash \psi$ okay, there exist some term M' and store ψ' such that $M \mid \psi \to M' \mid \psi'$.

Theorem 2.4 (Substitution)

If
$$\Gamma \mid \Psi \vdash N : A_{@L}$$
, then $\Gamma, x : A_{@L} \mid \Psi \vdash M : C_{@L}$ implies $\Gamma \mid \Psi \vdash [N/x]M : C_{@L}$.
 If $\Gamma_{G} \mid \cdot \vdash V : A_{@L}$, then $\Gamma, x : A_{@G} \mid \Psi \vdash M : C_{@L}$ implies $\Gamma \mid \Psi \vdash [V/x]M : C_{@L}$.

Lemma 2.5

If
$$\cdot \mid \Psi \vdash V : A_{@G}$$
, then $\cdot \mid \cdot \vdash V : A_{@L}$.

Proof

By induction on the structure of the proof of $\cdot \mid \Psi \vdash V : A_{@G}$. We need to consider the rules Prim, GVal, \times I, +I_L, and +I_R. In the case of the rule Prim, $\cdot \mid \Psi \vdash V : A_{@L}$ holds where A is a primitive type, and $\cdot \mid \cdot \vdash V : A_{@L}$ follows by Definition 2.1.

Theorem 2.6 (Type preservation)

Suppose $\cdot \mid \Psi \vdash M : A_{@L}, \ \Psi \vdash \psi$ okay, and $M \mid \psi \to M' \mid \psi'$. Then there exists a store typing Ψ' such that $\cdot \mid \Psi' \vdash M' : A_{@L}, \ \Psi \subset \Psi'$, and $\Psi' \vdash \psi'$ okay.

3 Logic for λ_{\Box}

The type system for modal types $\Box A$ is unusual in that it differentiates values (i.e., terms in weak head normal form) from ordinary terms, as shown in the rule GVal. This differentiation implies that the logic corresponding to the modality \Box via the Curry–Howard isomorphism requires a judgment that inspects not only hypotheses in a proof but also the proof structure itself (e.g., inference rules used in the proof). Thus, the modality \Box sets itself apart from other modalities and is not found in any other logic. Later we will compare the modality \Box with other modalities in modal logic.

In the pure fragment of λ_{\square} without primitive types and reference types, the modality \square shows similarities with modal possibility \diamond and lax modality \square in Pfenning and Davies (2001). Specifically a proof-theoretic analysis of \square gives rise to a new form of substitution $\langle M/x\rangle N$ which is defined inductively on the structure

of the term being substituted (namely M) instead of the term being substituted into (namely N). Let us interpret a β -reduction as the reduction of a typing derivation in which an introduction rule is followed by a corresponding elimination rule. For example, the β -reduction for the connective \rightarrow may be seen as the reduction of the following typing derivation in the pure fragment of λ_{\square} where we omit following store typings:

$$\frac{\Gamma, x: A_{@L} \vdash M: B_{@L}}{\Gamma \vdash \lambda x: A. M: A \to B_{@L}} \xrightarrow{\rightarrow | \Gamma \vdash N: A_{@L}} \xrightarrow{} \vdash \mathsf{E}$$

$$\Gamma \vdash (\lambda x: A. M) \ N: B_{@L}$$

Likewise we obtain a β -reduction for \square from the reduction of a typing derivation in which the introduction rule \square l is followed by the elimination rule \square E:

$$\frac{\Gamma \vdash M : A_{@\mathsf{G}}}{\Gamma \vdash \mathsf{box} \ M : \boxdot A_{@\mathsf{L}}} \ \ \Box \mathsf{I} \qquad \Gamma, x : A_{@\mathsf{G}} \vdash N : C_{@L}}{\Gamma \vdash \mathsf{letbox} \ x = \mathsf{box} \ M \ \mathsf{in} \ N : C_{@L}} \ \ \Box \mathsf{E} \qquad \to_{\beta} \qquad \Gamma \vdash \langle M/x \rangle N : C_{@L}$$

To see why $\langle M/x \rangle N$ should be defined inductively on the structure of M, observe that the reduction of letbox $x = \mathsf{box}\ M$ in N requires an analysis of M instead of N. The reason is that only a value can be substituted for x, but M may not be a value yet; therefore we have to analyze M to decide how to transform the whole term so that x is eventually replaced by a value. Conceptually N should be replicated at those subterms within M that can be taken as the result of evaluating M, so that M and N are evaluated exactly once and in that order. If M is already a value V, we reduce the whole term to [V/x]N. Thus we are led to define $\langle M/x \rangle N$ as follows:

$$\langle V/x\rangle N=[V/x]N$$
 $\langle {\rm letbox}\ x'=M'\ {\rm in}\ M''/x\rangle N={\rm letbox}\ x'=M'\ {\rm in}\ \langle M''/x\rangle N$

Note that we cannot define $\langle M_1 \ M_2/x \rangle N$ because in the absence of primitive types and the rule Prim, there is no typing derivation of $\Gamma \vdash M_1 \ M_2 : A_{@G}$ and thus box $M_1 \ M_2$ cannot be well typed.

In the presence of primitive types, the β -reduction

letbox
$$x = box M in N \rightarrow_{\beta} \langle M/x \rangle N$$

is no longer valid because letbox $x = \mathsf{box}\ M$ in N may typecheck while $\langle M/x \rangle N$ is undefined. For example, $M = M_1\ M_2$ of type unit satisfies $\Gamma \vdash M$: unit_{@G} by the rule Prim, but $\langle M_1\ M_2/x \rangle N$ is undefined. Intuitively the rule Prim disguises an unanalyzable term of a primitive type as an analyzable term. Thus, in order to reduce letbox $x = \mathsf{box}\ M$ in N, the operational semantics of λ_\square is forced to reduce M into a value V first, instead of analyzing M to transform the whole term. Then an ordinary substitution [V/x]N suffices for the reduction of letbox $x = \mathsf{box}\ V$ in N.

We close this section with a brief discussion of the properties of the modality \Box . For a given type in λ_{\Box} , we give a term of that type if it is inhabited. We ignore the fixed point construct, in the presence of which all types are inhabited.

 \bullet $A \not\rightarrow \Box A$

An ordinary term does not necessarily evaluate to a global value. This implies

that \boxdot is different from modal possibility \diamond and lax modality \bigcirc because both $A \to \diamond A$ and $A \to \bigcirc A$ hold.

 $\bullet \ \Box A \to A$

 $\lambda x : \Box A$. letbox y = x in y

A global value is a special case of an ordinary term. A special case $\Box \Box A \rightarrow \Box A$ means that \Box is an idempotent modality.

 $\bullet \quad \boxdot A \rightarrow \boxdot\boxdot A$

 $\lambda x : \Box A$. letbox y = x in box box y

A global value itself is global.

• $\Box(A \to B) \not\to (\Box A \to \Box B)$

A global λ -abstraction does not necessarily return a global value. This implies that \Box is different from modal necessity \Box because $\Box(A \to B) \to (\Box A \to \Box B)$ holds (Pfenning & Davies, 2001).

 $\bullet \ (\Box A \to \Box B) \not\to \Box (A \to B)$

A λ -abstraction that accepts and returns global values is not necessarily global.

• $\Box(A \times B) \to (\Box A \times \Box B)$

 $\lambda x : \Box (A \times B)$. letbox y = x in (box fst y, box snd y)

This type is inhabited because the typing rules $\times E_L$ and $\times E_R$ use an unspecified locality L instead of a specific locality L or G.

- $\bullet \ (\boxdot A \times \boxdot B) \to \boxdot (A \times B)$
 - $\lambda x : \Box A \times \Box B$. letbox $y_1 = \text{fst } x \text{ in letbox } y_2 = \text{snd } x \text{ in box } (y_1, y_2)$

This type is inhabited because the typing rule $\times I$ uses an unspecified locality L.

 $\bullet \ \Box (A+B) \to (\Box A + \Box B)$

 $\lambda x : \Box (A+B)$. letbox y=x in case y of inl $z_1 \Rightarrow$ inl box $z_1 \mid$ inr $z_2 \Rightarrow$ inr box z_2 . This type is inhabited because the typing rule +E uses an unspecified locality L.

• $(\Box A + \Box B) \rightarrow \Box (A + B)$ $\lambda x : \Box A + \Box B$.

box (case x of inl $y_1 \Rightarrow \text{letbox } z_1 = y_1 \text{ in inl } z_1 \mid \text{inr } y_2 \Rightarrow \text{letbox } z_2 = y_2 \text{ in inr } z_2$) This type is inhabited because the typing rules $+I_L$ and $+I_R$ use an unspecified locality L.

4 λ_{\square}^{P} with a parallel operational semantics

Although, its type system uses localities and modal types to differentiate global values from local values, λ_{\square} is just a base language for sequential computations to which global values are of no use. That is, its operational semantics focuses only on the sequential computation at a hypothetical thread and there is no way to exploit global values for communications between threads. This section extends λ_{\square} with a parallel operational semantics which models multiple threads running concurrently (but not communicating with each other yet). The resultant language is called $\lambda_{\square}^{\rm p}$.

Figure 4 shows the definition of λ^{P}_{\square} . At the syntax level, λ^{P}_{\square} augments λ_{\square} with a new construct spawn $\{M\}$. The typing rule Spawn requires M in spawn $\{M\}$ to

$$\begin{array}{c} \operatorname{term} & M ::= \cdots \mid \operatorname{spawn} \ \{M\} \\ \operatorname{thread} & \gamma \\ \operatorname{configuration} & \pi ::= \cdot \mid \pi, \{M \mid \psi @ \gamma\} \\ \operatorname{configuration} & \operatorname{typing} & \Pi ::= \cdot \mid \Pi, \gamma : A \\ \\ & \frac{\Gamma_{\mathsf{G}} \mid \cdot \vdash M : A_{@\mathsf{L}}}{\Gamma \mid \Psi \vdash \operatorname{spawn} \ \{M\} : \operatorname{unit}_{@\mathsf{L}}} \operatorname{Spawn} \\ & \frac{\operatorname{dom}(\Pi) = \operatorname{dom}(\pi)}{\operatorname{for each} \ \{M \mid \psi @ \gamma\} \in \pi, \ \operatorname{there \ exist} A_{\gamma}, \Psi_{\gamma} \ \operatorname{such \ that}}{\gamma : A_{\gamma} \in \Pi \ \operatorname{and} \Psi_{\gamma} \vdash \psi \ \operatorname{okay} \ \operatorname{and} \cdot \mid \Psi_{\gamma} \vdash M : A_{\gamma @\mathsf{L}}} \operatorname{Conf} \\ & \frac{H \mid \psi \to M' \mid \psi'}{\pi, \{M \mid \psi @ \gamma\} \Rightarrow \pi, \{M' \mid \psi' @ \gamma\}} \operatorname{Red} \\ & \frac{\operatorname{fresh} \gamma'}{\pi, \{\phi \llbracket \operatorname{spawn} \ \{M\} \rrbracket \mid \psi @ \gamma\} \Rightarrow \pi, \{\phi \llbracket () \rrbracket \mid \psi @ \gamma\}, \{M \mid \cdot @ \gamma'\}} \operatorname{Spawn} \end{array}$$

Fig. 4. Definition of λ_{\square}^{P} .

contain no dangling references so that it is safe to evaluate M at a fresh thread with an empty store, as shown in the rule Spawn. (It does not matter whether M evaluates to a global value or not.)

A configuration π , as an unordered set, represents the state of a parallel computation by associating each thread γ with a term M being evaluated at γ and a store ψ allocated at γ , written as $\{M \mid \psi @ \gamma\}$. A configuration typing Π records the type of the term being evaluated at each thread. The type system of $\lambda_{\square}^{\mathsf{P}}$ uses the same typing judgment as λ_{\square} , except that it also uses a configuration typing judgment $\Pi \vdash \pi$ okay to mean that configuration π has configuration typing Π . The rule Conf may be regarded as the definition of the configuration typing judgment, where $dom(\Pi)$ and $dom(\pi)$ denote the set of threads in Π and π , respectively. For each thread γ such that $\{M \mid \psi @ \gamma\} \in \pi$, it checks the existence of a store typing Ψ_{γ} whose domain includes all locations present in M and ψ .

The parallel operational semantics uses a configuration transition judgment $\pi \Rightarrow \pi'$ to mean that configuration π reduces (by the rule Red) or evolves (by the rule Spawn) to configuration π' . The rule Red says that a parallel computation consists primarily of sequential computations performed at individual threads. The rule Spawn starts a new thread γ' by evaluating V (). Since box V guarantees that V is a global value, it is safe to evaluate V () with an empty store at γ' . Note that a configuration transition is nondeterministic because a configuration may choose an arbitrary thread to which either rule is applied.

Type safety of λ_{\square}^{P} consists of *configuration progress* (Theorem 4.1) and *configuration typing preservation* (Theorem 4.2).

Theorem 4.1 (Configuration progress)

Suppose $\Pi \vdash \pi$ okay. Then either

- (1) π consists only of $\{V \mid \psi \otimes \gamma\}$, or
- (2) there exists π' such that $\pi \Rightarrow \pi'$.

Theorem 4.2 (Configuration typing preservation)

Suppose $\Pi \vdash \pi$ okay and $\pi \Rightarrow \pi'$. Then there exists a configuration typing Π' such that $\Pi \subset \Pi'$ and $\Pi' \vdash \pi'$ okay.

5 λ_{\square}^{PC} with higher-order channels

This section extends $\lambda^{\mathsf{P}}_{\square}$ with higher-order channels to obtain $\lambda^{\mathsf{PC}}_{\square}$. In order to achieve channel locality, $\lambda^{\mathsf{PC}}_{\square}$ splits channels into two kinds: read channels and write channels. A read channel a^r is assigned a read channel type $\langle A \rangle^r$, and is treated as a local value so that it cannot escape from the thread at which it is created. A write channel a^w is assigned a write channel type $\langle A \rangle^w$, and is treated as a global value so that it can be transmitted to another thread. Since values written to write channels travel between threads, they cannot be local values. That is, only global values are allowed to be written to write channels, and consequently all values read from read channels are also global.

Figure 5 shows the definition for λ_{\square}^{PC} . A read channel a^r of type $\langle A \rangle^r$ receives a global value of type A from its corresponding write channel a^w via a channel read a^r ?. A write channel a^w of type $\langle A \rangle^w$ transmits a global value V of type A to its corresponding read channel a^r via a channel write $a^w ! V$. A new construct $\text{new}_{\langle A \rangle}$ dynamically creates a pair of read channel a^r and write channel a^w to open unidirectional communications from a^w to a^r . $a^r \mapsto A$ in a read channel typing Φ^r means that a^r has type $\langle A \rangle^w$; similarly $a^w \mapsto A$ in a write channel typing Φ^w means that a^w has type $\langle A \rangle^w$.

The type system of λ_{\square}^{PC} uses a read channel typing Φ^r and a write channel typing Φ^w in each typing judgment:

$$\Gamma \mid \Phi^w : \Phi^r : \Psi \vdash M : A_{\odot L}$$

All the previous typing rules are extended in a straightforward way by adding Φ^r and Φ^w to each typing judgment. The only exception is the rule GVal whose premise $(\Gamma_G \mid \Phi^w; \cdot; \cdot \vdash V : A_{@L})$ uses an empty read channel typing because read channels are local values. Accordingly we redefine primitive types as follows:

Definition 5.1

P is a primitive type if and only if $\Gamma \mid \Phi^w; \Phi^r; \Psi \vdash V : P_{@L}$ implies $\Gamma_{G} \mid \Phi^w; \cdot; \cdot \vdash V : P_{@L}$.

Under the new definition of primitive types, a write channel type $\langle A \rangle^w$ becomes a primitive type (see the rule ChanW):

primitive type
$$P ::= \cdots | \langle A \rangle^w$$

The rule R? uses an unspecified locality L in the conclusion so that Proposition 2.2 continues to hold. The rule W! says that channel writes accept only global values. The rule Store uses an extended store typing judgment Φ^w ; Φ^r ; $\Psi \vdash \psi$ okay to check that store ψ conforms to store typing Ψ under Φ^r and Φ^w .

The rule Conf is of particular importance in λ_{\square}^{PC} because it verifies channel locality in a given configuration. Note that an extended configuration typing judgment

$$\begin{array}{c} \operatorname{type} & A & ::= & \cdots \mid \langle A \rangle^r \mid \langle A \rangle^w \\ \operatorname{term} & M & ::= & \cdots \mid \operatorname{new}_{\langle A \rangle} \mid a^r \mid a^w \mid M? \mid M!M \\ \operatorname{value} & V & ::= & \cdots \mid a^r \mid a^w \\ \operatorname{evaluation context} & \phi & ::= & \cdots \mid a^r \mid a^w \mid M \mid V! \phi \\ \operatorname{read channel typing} & \Phi^r & ::= & \cdot \mid \Phi^r, a^r \mapsto A \\ \operatorname{write channel typing} & \Phi^w & ::= & \cdot \mid \Phi^w, a^w \mapsto A \\ \\ \hline \frac{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash V : A_{@\mathsf{G}}}{\Gamma \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{New}_{\langle A \rangle} \mid A \mid \mathsf{New}} & \frac{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{New}_{\langle A \rangle} \mid A \mid \mathsf{New}}{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{New}_{\langle A \rangle} \mid A \mid \mathsf{New}} & \frac{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{Mex}_{\langle A \rangle} \mid A \mid \mathsf{New}}{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{Mex}_{\langle A \rangle} \mid A \mid \mathsf{New}} & \frac{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{Mex}_{\langle A \rangle} \mid A \mid \mathsf{New}}{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{Mex}_{\langle A \rangle} \mid A \mid \mathsf{New}} & \frac{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{Mex}_{\langle A \rangle} \mid A \mid \mathsf{New}}{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{Mex}_{\langle A \rangle} \mid A \mid \mathsf{New}} & \frac{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{Mex}_{\langle A \rangle} \mid A \mid \mathsf{New}}{\Gamma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{Mex}_{\langle A \rangle} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{Mex}_{\langle A \rangle} \mid \Phi^w; \varphi^r; \psi \vdash \mathsf{Mex}_{\langle A \rangle} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}} & \frac{\sigma_{\mathsf{G}} \mid \Phi^w; \varphi^r; \psi \vdash \psi \mid \mathsf{New}}{\Phi^w; \varphi^r; \psi$$

Fig. 5. Definition of λ_{\square}^{PC} .

 Φ^w ; $\Pi \vdash \pi$ okay shares a write channel typing Φ^w for all threads (because write channels are global values), but does not assume a specific read channel typing. Instead, for each thread γ such that $\{M \mid \psi \circledcirc \gamma\} \in \pi$, it infers a *unique* read channel typing Φ^r_{γ} , in addition to a store typing Ψ_{γ} , by typechecking all read channels present in M and ψ . The uniqueness of Φ^r_{γ} for each thread γ , as stated in the third premise, implies that no two threads share common read channels, and thus amounts to channel locality in π .

The parallel operational semantics uses the same configuration transition judgment $\pi \Rightarrow \pi'$ as in λ^P_\square . The premise of the rule *New* means that read channel a^r and write channel a^w are unique across the entire set of threads. The rule *Sync* says that a channel read a^r ? and a channel write $a^w ! V$ occur synchronously. (An asynchronous version of λ^{PC}_\square would require a different parallel operational semantics, but the same type system would continue to work.) The rule *Sync* is nondeterministic in that it does not specify how to choose a pair of channels read and write to be synchronized together.

As with λ_{\square}^{P} , type safety of λ_{\square}^{PC} consists of configuration progress (Theorem 5.3) and configuration typing preservation (Theorem 5.5). Proofs of Theorems 5.3 and 5.5 use type safety for sequential computations in λ_{\square}^{PC} (Propositions 5.2 and 5.4). Note that in Theorem 5.5, a configuration transition $\pi \Rightarrow \pi'$ preserves channel locality: Φ^{w} ; $\Pi \vdash \pi$ okay and Φ'^{w} ; $\Pi' \vdash \pi'$ okay imply that π and π' satisfy channel locality, respectively.

Proposition 5.2 (Progress)

Suppose $\cdot \mid \Phi^w; \Phi^r; \Psi \vdash M : A_{@L}$. Then either

- (1) M is a value,
- (2) $M = \phi \llbracket \mathsf{new}_{\langle A \rangle} \rrbracket$,
- (3) $M = \phi [a^r ?],$
- (4) $M = \phi [a^w ! V],$
- (5) $M = \phi \llbracket \text{spawn } \{M\} \rrbracket$, or
- (6) for any store ψ such that Φ^w ; Φ^r ; $\Psi \vdash \psi$ okay, there exist some term M' and store ψ' such that $M \mid \psi \to M' \mid \psi'$.

Theorem 5.3 (Configuration progress)

Suppose Φ^w ; $\Pi \vdash \pi$ okay. Then either

- (1) π consists only of $\{V \mid \psi @ \gamma\}$, $\{\phi \llbracket a^r ? \rrbracket \mid \psi @ \gamma\}$, and $\{\phi \llbracket a^w ! V \rrbracket \mid \psi @ \gamma\}$, or
 - (2) there exists π' such that $\pi \Rightarrow \pi'$.

Proposition 5.4 (Type preservation)

Suppose $\cdot \mid \Phi^w; \Phi^r; \Psi \vdash M : A_{@L}, \Phi^w; \Phi^r; \Psi \vdash \psi$ okay, and $M \mid \psi \to M' \mid \psi'$. Then there exists a store typing Ψ' such that $\cdot \mid \Phi^w; \Phi^r; \Psi' \vdash M' : A_{@L}, \Psi \subset \Psi'$, and $\Phi^w; \Phi^r; \Psi' \vdash \psi'$ okay.

Theorem 5.5 (Configuration typing preservation)

Suppose Φ^w ; $\Pi \vdash \pi$ okay and $\pi \Rightarrow \pi'$. Then there exist a write channel typing Φ'^w and a configuration typing Π' such that $\Phi^w \subset \Phi'^w$, $\Pi \subset \Pi'$, and Φ'^w ; $\Pi' \vdash \pi'$ okay.

6 Examples

This section presents examples of implementing various communication constructs in λ_{\square}^{PC} : futures, bidirectional communications, shared references, remote evaluation, code on demand, and hot code replacement. Throughout the examples, we use the following syntactic sugar:

$$\begin{array}{rcl} \operatorname{let} \, x = M \, \operatorname{in} \, N & \equiv & (\lambda x : ... N) \, M \\ \operatorname{let} \, (x,y) = M \, \operatorname{in} \, N & \equiv & \operatorname{let} \, z = M \, \operatorname{in} \\ & & \operatorname{let} \, x = \operatorname{fst} \, z \, \operatorname{in} \, \operatorname{let} \, y = \operatorname{snd} \, z \, \operatorname{in} \, N \\ \operatorname{let} \, \operatorname{rec} \, x = M \, \operatorname{in} \, N & \equiv & \operatorname{let} \, x = \operatorname{fix} \, x : ... M \, \operatorname{in} \, N \end{array}$$

6.1 Futures

Future (Baker & Hewitt, 1977; Halstead 1985) is a communication construct which can be thought of as a pointer to a thread. After the thread finishes its computation,

the pointer is implicitly dereferenced whenever the result is requested. We use read channels to simulate a restricted variant of the future that should be explicitly dereferenced by the programmer.

In order to create a future, first we need to evaluate a term M at a fresh thread. We implement the future with a read channel a^r such that the result of evaluating M is written to the corresponding write channel a^w . Since the result of evaluating M is written to a write channel, M must evaluate to a global value; hence box M must typecheck:

box
$$M: \Box A$$

Moreover, M is to be evaluated at a remote thread and must contain no references. Hence, box M itself must be a global value, otherwise M cannot be transmitted to a remote thread

box box
$$M: \Box \Box A$$

Thus future, our construct for futures, expects a value of type $\Box\Box A$ and returns a read channel of type $\langle A \rangle^r$ to which the result of the computation at a fresh thread is sent

```
\begin{array}{ll} \mathrm{future} &: \boxdot\boxdot A \to \langle A \rangle^r{}_{@\mathsf{G}} \\ \mathrm{future} &= \lambda x : \boxdot\boxdot A. \ \ \mathrm{letbox} \ x' = x \ \mathrm{in} \\ & \mathrm{let} \ (y_r, y_w) = \mathrm{new}_{\langle A \rangle} \ \mathrm{in} \\ & \mathrm{letbox} \ y_w' = \mathrm{box} \ y_w \ \mathrm{in} \\ & \mathrm{let} \ \_ = \mathrm{spawn} \ \big\{ \mathrm{letbox} \ z = x' \ \mathrm{in} \ y_w' \, ! z \big\} \ \mathrm{in} \\ & y_r \end{array}
```

As an example, consider future box box M which evaluates M at a fresh thread. First x' is bound to a global value box M, which is then transmitted to the fresh thread. By evaluating M at the fresh thread, we obtain a global value z, which is finally written to write channel y'_w . Note that even though y_w is actually bound to a global value (namely a write channel), we need to explicitly mark it as a global value by introducing another variable y'_w because it is used inside spawn (see the rule Spawn). Typically we use a term of the form letbox x' = box x in M to inform the type system that variable x has a primitive type and is thus bound to a global value (because the type system does not automatically keep track of the locality of variables of primitive type).

6.2 Bidirectional communications

Communications from a child thread back to its parent thread are easy to implement because write channels are global values—we start the child thread by evaluating a term containing write channels whose corresponding read channels belong to the parent thread. However, for communications in the other direction, the child thread needs to somehow return a write channel to the parent thread, which can be done only via another write channel originating from the parent thread. Thus, in order to receive a write channel of type $\langle A \rangle^w$ from a child thread, the parent thread creates a pair of read channel a^r of type $\langle \langle A \rangle^w \rangle^r$ and write channel a^w of type $\langle \langle A \rangle^w \rangle^w$. Then

the parent thread sends the child thread a^w , through which a write channel of type $\langle A \rangle^w$ is sent back to the parent thread.

We design spawn^{BI}, our construct for bidirectional communications (or for unidirectional communications from a parent thread to a child thread), in such a way that given a value of type $\Box(\langle A \rangle^r \to C)$, say box $\lambda x_r : \langle A \rangle^r . M$, it creates a child thread evaluating M with x_r bound to a read channel a^r of type $\langle A \rangle^r$ and returns a corresponding write channel a^w to the parent thread

```
\begin{array}{ll} \operatorname{spawn^{BI}} : \boxdot(\langle A \rangle^r \to C) \to \langle A \rangle^w_{@G} \\ \operatorname{spawn^{BI}} = \lambda z : \boxdot(\langle A \rangle^r \to C). \quad \operatorname{let} \ (x_r, x_w) = \operatorname{new}_{\langle \langle A \rangle^w \rangle} \ \operatorname{in} \\ \operatorname{letbox} \ x_w' = \operatorname{box} \ x_w \ \operatorname{in} \\ \operatorname{letbox} \ z' = z \ \operatorname{in} \\ \operatorname{let} \ _- = \operatorname{spawn} \{ \ \operatorname{let} \ (y_r, y_w) = \operatorname{new}_{\langle A \rangle} \ \operatorname{in} \\ \operatorname{let} \ _- = x_w' \ ! \ y_w \ \operatorname{in} \\ z' \ y_r \} \\ \operatorname{in} \\ x_r \ ? \end{array}
```

Note that even though y_w is not marked explicitly as a global value, channel write $x'_w ! y_w$ still typechecks by the rule W! because write channels have primitive types.

A minor drawback of spawn^{BI} is that by the rule Sync, the channel write $x'_w ! y_w$ blocks the child thread until it synchronizes with the corresponding channel read x_r ?. Thus the evaluation of z' y_r , the core of the child thread, may not start immediately after $\text{new}_{\langle A \rangle}$ returns a pair of read and write channels. To start the evaluation of z' y_r immediately, the child thread could delegate the channel write to another new thread. In general, a channel write M!N can be replaced by the following term which spawns a new thread for performing the channel write and returns immediately (unless an evaluation of M or N gets stuck with a channel read or a channel write):

letbox
$$x_M = \text{box } M$$
 in letbox $x_N = \text{box } N$ in spawn $\{x_M \, | \, x_N\}$

The same idea can be applied to all instances of channel writes in the examples given later.

6.3 Shared references

We implement a shared reference for type A as a thread with two components: a read channel of type $\langle\langle A\rangle^w + A\rangle^r$, as an interface, and a global value of type A, as its current content. Any thread may request the current content of the reference by sending a value inl a^w (of type $\langle A\rangle^w + A$) to the read channel, in which case the current content is written back to a^w . To update the content of the reference, it sends a value inr V (also of type $\langle A\rangle^w + A$) to the read channel, in which case the current content is updated with a new value V.

As an illustration, we use spawn^{BI} to create a shared reference for type intinitialized with a value 0

```
\begin{split} \det cell_w = & \text{spawn}^{\text{BI}} \text{ box } \lambda cell_r : \langle \langle \text{int} \rangle^w + \text{int} \rangle^r. \ \ \text{let rec } f = \lambda n : \text{int.} \\ & \text{case } cell_r ? \text{ of inl } ch_w \Rightarrow \text{let } \_ = ch_w ! n \text{ in } f n \\ & \text{in} \\ & f 0 \end{split}
```

Then we use a write channel in $cell_w$ to implement get, of type unit \rightarrow int, for requesting the current content of the reference and set, of type int \rightarrow unit, for updating the content of the reference; note that each call to get creates a fresh pair of read channel and write channel

letbox
$$cell_w' = \text{box } cell_w$$
 in letbox $get = \text{box } \lambda_-$: unit. let $(x_r, x_w) = \text{new}_{\langle \text{int} \rangle}$ in let $_- = cell_w'$! (inl x_w) in $_- x_r$? in letbox $set = \text{box } \lambda n$: int. $cell_w'$! (inr n) in (get, set)

Since both *get* and *set* are global values, they may be present at any thread, which means that the reference can be shared by all threads.

6.4 Remote evaluation

Remote evaluation (Stamos & Gifford, 1990) is a mechanism for exploiting a set of services exported by a server in a flexible way. A client sends to the server a program whose execution by the server may invoke these services. That is, it sends not arguments for these services but a program utilizing these services. We demonstrate how to implement remote evaluation in λ_{\square}^{PC} with a server providing a service for calculating the successor of an integer.

We use a write channel of type $\langle \text{int} \times \langle \text{int} \rangle^w \rangle^w$ as an interface to the service. The idea is that the service responds to (n, a^w) written to the write channel by writing n+1 back to a^w . A call to spawn^{BI} with the following global value $V_{service}$ as an argument starts the service and returns its interface:

$$\begin{array}{l} V_{service} : \boxdot(\langle \mathsf{int} \times \langle \mathsf{int} \rangle^w \rangle^r \to \mathsf{unit})_{@\mathsf{G}} \\ V_{service} = \mathsf{box} \ \lambda s_r : \langle \mathsf{int} \times \langle \mathsf{int} \rangle^w \rangle^r. \ \ \mathsf{let} \ \mathsf{rec} \ f = \lambda_- : \mathsf{unit}. \ \ \mathsf{let} \ (n, ch) = s_r \ ? \ \mathsf{in} \\ & \mathsf{let} \ _ = ch \ ! (n+1) \ \mathsf{in} \\ & f \ () \\ & \mathsf{in} \\ & f \ () \end{array}$$

The following term starts the server and returns its interface which is a write channel req_w of type $\langle \langle int \times \langle int \rangle^w \rangle^w \rightarrow unit \rangle^w$. Since req_w is a global value, any

client can use the service by sending a global value $\lambda s : \langle \operatorname{int} \times \langle \operatorname{int} \rangle^w \rangle^w$. M. Upon receiving such a global value, the server binds s to the interface to the service and evaluates M:

```
\begin{split} \text{let } req_w = \text{spawn}^{\text{BI}} & \text{box } \lambda req_r : \langle \langle \text{int} \times \langle \text{int} \rangle^w \rangle^w \rightarrow \text{unit} \rangle^r. \\ & \text{let } s_w = \text{spawn}^{\text{BI}} \ V_{service} \text{ in} \\ & \text{let rec } f = \lambda_- : \text{unit. let } \_ = (req_r \ ?) \ s_w \text{ in } f \ () \text{ in } \\ & f \ () \end{split}
```

Here is an example of a client which calculates the successor of the successor of 0. Note that it sends a global value f to the server only once while the server invokes the service twice. The client waits for the result by performing a channel read c_r ?

```
\begin{split} & | \operatorname{let}\left(c_{r},c_{w}\right) = \operatorname{new}_{\langle \operatorname{int} \rangle} \operatorname{in} \\ & | \operatorname{letbox}\left(c_{w}' = \operatorname{box}\left(c_{w}' \operatorname{in}\right) \right) \\ & | \operatorname{letbox}\left(f = \operatorname{box}\left(\lambda s : \langle \operatorname{int} \times \langle \operatorname{int} \rangle^{w}\right)^{w}. \right. \\ & | \operatorname{let}\left(ch_{r},ch_{w}\right) = \operatorname{new}_{\langle \operatorname{int} \rangle} \operatorname{in} \\ & | \operatorname{let}_{-} = s ! (0,ch_{w}) \operatorname{in} \\ & | \operatorname{let}_{-} = s ! (ch_{r} ?,ch_{w}) \operatorname{in} \\ & | \operatorname{let}_{-} = c_{w}' ! (ch_{r} ?) \operatorname{in} \\ & | \operatorname{let}_{-} = req_{w} ! f \operatorname{in} \\ & c_{r} ? \end{split}
```

6.5 Code on demand

Code on demand is the opposite mechanism of remote evaluation in that a client can download code from a code server instead of sending code to a remote server. As an example, we implement a code server which, upon request, returns the global value $V_{service}$ of type $\Box(\langle \operatorname{int} \times \langle \operatorname{int} \rangle^w \rangle^r \to \operatorname{unit})$ given in Section 6.4. It expects from a client a write channel of type $\langle \Box(\langle \operatorname{int} \times \langle \operatorname{int} \rangle^w \rangle^r \to \operatorname{unit}) \rangle^w$, to which $V_{service}$ is sent back:

$$\begin{split} \det \mathit{req}_{\mathit{w}} &= \mathsf{spawn}^{\mathsf{BI}} \quad \mathsf{box} \; \lambda \mathit{req}_{\mathit{r}} : \langle \langle \Box (\langle \mathsf{int} \times \langle \mathsf{int} \rangle^{\mathit{w}} \rangle^{\mathit{r}} \to \mathsf{unit}) \rangle^{\mathit{w}} \rangle^{\mathit{r}}. \\ &\quad \mathsf{let} \; \mathsf{rec} \; f = \lambda_{-} : \mathsf{unit}. \, \mathsf{let}_{\; -} = (\mathit{req}_{\mathit{r}} \, ?) \, ! \, V_{\mathit{service}} \; \mathsf{in} \; f \, () \; \mathsf{in} \\ &\quad f \, () \end{split}$$

The following client downloads $V_{service}$ and starts the service for its own use:

$$\begin{array}{l} \mathrm{let}\;(c_r,c_w) = \mathrm{new}_{\langle \Box(\langle \mathrm{int} \times \langle \mathrm{int} \rangle^w \rangle^r \to \mathrm{unit})\rangle}\;\mathrm{in}\\ \mathrm{let}\;_{-} = req_w\,!\,c_w\;\mathrm{in}\\ \mathrm{let}\;s_w = \mathrm{spawn^{BI}}\;(c_r\,?)\;\mathrm{in}\\ \mathrm{let}\;(ch_r,ch_w) = \mathrm{new}_{\langle \mathrm{int} \rangle}\;\mathrm{in}\\ \mathrm{let}\;_{-} = s_w\,!\,(0,ch_w)\;\mathrm{in}\\ ch_r\,? \end{array}$$

6.6 Hot code replacement

The capability to transmit code between threads enables us to implement a server whose code can be replaced at runtime without stopping it. The following term starts a server which accepts a pair of integer n and write channel ch and writes to ch the result of applying a certain function f to n. Initially f is given as an identity function, but we can change the behavior of the server at runtime by performing a channel write s_w ! (inr f_{new}) for a certain global value f_{new} of type int \rightarrow int.

```
\begin{split} \text{let } s_w = \text{spawn}^{\text{BI}} & \text{box } \lambda s_r : \langle (\text{int} \times \langle \text{int} \rangle^w) + (\text{int} \to \text{int}) \rangle^r. \\ & \text{let rec } loop = \lambda f : \text{int} \to \text{int}. \\ & \text{case } s_r ? \text{ of } \text{inl } (n, ch) \Rightarrow \text{ let } \underline{\ \ } \underline{\ \ } = ch \: ! (f \ n) \text{ in} \\ & loop \: f \\ & \text{ in} \\ & loop \: (\lambda x : \text{int.} \: x) \end{split}
```

7 Extensions to λ_{\square}^{PC}

This section discusses two extensions to λ_{\square}^{PC} . As it is routine to incorporate these extensions into the definition of λ_{\square}^{PC} , we only sketch the main ideas and omit the details.

7.1 Local threads

Consider two read channels a_1^r and a_2^r (of the same type $\langle A \rangle^r$) belonging to different threads γ_1 and γ_2 , respectively. a_1^r wishes to forward to a_2^r every incoming message V addressed to it with a channel write a_2^w ! V where a_2^w is a write channel corresponding to a_2^r . (As write channels are global values, we assume that a_2^w has already been transmitted to thread γ_1 .) An easy solution is to call the following construct forward with arguments a_1^r and a_2^w at thread γ_1 :

```
forward : \langle A \rangle^r \to (\langle A \rangle^w \to \text{unit})_{@G}
forward = \lambda x : \langle A \rangle^r . \lambda y : \langle A \rangle^w. let rec f = \lambda_-: unit. let _- = y ! (x ?) in f () in
```

The problem with such a call to forward is that thread γ_1 goes into an infinite loop and degenerates to a trivial thread that only repeats a channel read x? followed by a channel write y!(x?). Hence a better solution would be to spawn a separate thread with a call to the following construct forward with arguments a_1^r and a_2^w :

```
forward': \langle A \rangle^r \to (\langle A \rangle^w \to \text{unit})_{@G}
forward' = \lambda x : \langle A \rangle^r . \lambda y : \langle A \rangle^w.
letbox x' = \text{box } x \text{ in } (* does not typecheck }^*)
letbox y' = \text{box } y \text{ in }
spawn{ let rec f = \lambda_-: unit. let _- = y' ! (x' ?) \text{ in } f () \text{ in } f ()}
```

$$\frac{fresh\ \gamma'}{\pi,\{\phi[\hspace{-0.05cm}[spawn\ \{M\}]\hspace{-0.05cm}],M_1,\ldots,M_n\ |\ \psi\ @\ \gamma\}\Rightarrow\pi,\{\phi[\hspace{-0.05cm}[()]\hspace{-0.05cm}],M_1,\ldots,M_n\ |\ \psi\ @\ \gamma\},\{M\ |\ @\ \gamma'\}\}} Sync'} \frac{\pi,\{\phi[\hspace{-0.05cm}[a^w\,!\,V]\hspace{-0.05cm}],\phi'[\hspace{-0.05cm}[a^w\,!\,V]\hspace{-0.05cm}],M_1,\ldots,M_n\ |\ \psi\ @\ \gamma\}\Rightarrow\pi,\{\phi[\hspace{-0.05cm}[V]\hspace{-0.05cm}],\phi'[\hspace{-0.05cm}[()]\hspace{-0.05cm}],M_1,\ldots,M_n\ |\ \psi\ @\ \gamma\}} {\pi,\{\phi[\hspace{-0.05cm}[lthread\ \{M\}]\hspace{-0.05cm}],M_1,\ldots,M_n\ |\ \psi\ @\ \gamma\}\Rightarrow\pi,\{\phi[\hspace{-0.05cm}[()]\hspace{-0.05cm}],M,M_1,\ldots,M_n\ |\ \psi\ @\ \gamma\}} LThread}$$

Fig. 6. Configuration transition rules for local threads.

Unfortunately forward' fails to typecheck because a read channel of type $\langle A \rangle^r$ cannot be a global value.

Instead of spawning an ordinary thread, therefore, we spawn a local thread that starts by evaluating forward a_1^r a_2^w . The underlying assumption is that a thread consists of one or more local threads running concurrently which share both the store and read channels. (There arises the memory consistency problem, but it is a separate issue beyond the scope of this paper.) Hence both references and read channels can be a part of a term for creating a new local thread. In other words, as far as creating local threads is concerned, both references and read channels can be regarded as global values. We do not, however, allow communications of read channels even between local threads, since the syntax for a channel write $a^w \,! \, V$ does not indicate whether the corresponding read channel a^r resides at the same thread (in which case V may be another read channel) or at a remote thread (in which case V may not be another read channel). Thus channel writes still expect global values which do not include read channels. Fournet et al. (1996) make a similar assumption in their study of the distributed join-calculus: every channel has a unique solution which is essentially a thread in λ^{PC}_{\square} , while a solution may run multiple *processes* which are essentially local threads in λ^{PC}_{\square} ; all these processes can interact with any message addressed to the channel.

We introduce a new construct Ithread $\{M\}$ for creating a local thread that starts by evaluating M

term
$$M$$
 ::= \cdots | Ithread $\{M\}$

Since local threads running at the same thread share references and read channels, Ithread $\{M\}$ typechecks whenever M typechecks

$$\frac{\Gamma \mid \Phi^w \, ; \Phi^r \, ; \Psi \vdash M \, : A_{@\mathsf{L}}}{\Gamma \mid \Phi^w \, ; \Phi^r \, ; \Psi \vdash \mathsf{lthread} \, \left\{ M \right\} \, : \mathsf{unit}_{@\mathsf{L}}} \, \, \mathsf{LThread}$$

Now that a thread may run multiple local threads, the state of a thread γ is represented by $\{M_1,\ldots,M_n\mid\psi\circledcirc\gamma\}$ where each term $M_i,\ 1\leqslant i\leqslant n$, is being evaluated by a local thread. All the previous rules for configuration transitions are extended in a straightforward way by rewriting $\{M\mid\psi\circledcirc\gamma\}$ as $\{M,M_1,\ldots,M_n\mid\psi\circledcirc\gamma\}$. An exception is the rule *Spawn* which creates a fresh thread consisting only of a single local thread, as shown in Figure 6. A new rule Sync' accounts for a channel read and a channel write occurring synchronously within the same thread γ . Another new rule LThread evaluates Ithread $\{M\}$ to create a fresh local thread.

Now we rewrite forward by exploiting Ithread

forward =
$$\lambda x : \langle A \rangle^r . \lambda y : \langle A \rangle^w$$
. Ithread {let rec $f = \lambda_-$: unit. let $_- = y ! (x ?)$ in $f ()$ in $f ()$ }

Another application of Ithread is to simulate channels not subject to channel locality, i.e., channels that can be read from and written to at any thread. The idea is the same as in implementing shared references in Section 6.3 except that each channel read from $cell_r$ starts a new local thread. Here is an example of creating such a channel for type int. We call read for channel reads and write for channel writes, both of which are global values and thus can be shared by all threads.

```
\begin{split} \det cell_w = \operatorname{spawn}^{\operatorname{BI}} & \operatorname{box} \lambda cell_r : \langle \langle \operatorname{int} \rangle^w + \operatorname{int} \rangle^r. \\ & \det (c_r, c_w) = \operatorname{new}_{\langle \operatorname{int} \rangle} \text{ in } \\ & \det \operatorname{rec} f = \lambda_- : \operatorname{unit.} \\ & \operatorname{case} cell_r ? \text{ of } \operatorname{inl} ch_w \Rightarrow \operatorname{Ithread} \{ch_w ! (c_r ?)\} \\ & | \operatorname{inr} v \Rightarrow \operatorname{Ithread} \{c_w ! v\} \\ & \operatorname{in} f () \\ & \operatorname{letbox} cell_w' = \operatorname{box} cell_w \text{ in } \\ & \operatorname{letbox} read = \operatorname{box} \lambda_- : \operatorname{unit.} & \det (x_r, x_w) = \operatorname{new}_{\langle \operatorname{int} \rangle} \text{ in } \\ & \operatorname{let} _- = cell_w' ! (\operatorname{inl} x_w) \text{ in } \\ & x_r ? \\ & \operatorname{in} \\ & \operatorname{letbox} write = \operatorname{box} \lambda x : \operatorname{int.} cell_w' ! (\operatorname{inr} x) \text{ in } \\ & (read, write) \end{split}
```

7.2 Type-safe data parallelism

The characteristic feature of the type system of λ_{\square}^{PC} is to be able to decide whether a given term evaluates to a global value or a local value. We can exploit this feature to provide type safety for data parallelism in λ_{\square}^{PC} , as follows.

Consider a parallel map construct mapP which applies a function f to each element of an array e in parallel (where we assume that e consists only of global values)

mapP
$$f$$
 e

The parent thread evaluating mapP f e spawns multiple child threads each of which applies f to an element of e. In order to avoid the memory consistency problem, we require that f do not inherit references from the parent thread (because f may attempt to update such references). On the other hand, in order to provide more flexibility in programming, we allow f to allocate new references, which cannot be transmitted among child threads and thus are safe to use.

Checking whether f satisfies the above two conditions is simple: we just test whether f is a global value or not. Note that although child threads do not share writable data, they can still share read-only data because variables bound to global

values can serve as shared read-only data. That is, if a binding $x : A_{@G}$ is available at the parent thread, all child threads may read variable x to obtain a global value.

If we want f to protect against not only references but also write channels being shared by child threads, we can introduce another locality G^* with a relation $G^* < G$ and the following typing rule:

$$\frac{\Gamma_{\mathsf{G}^\star} \mid \cdot; \cdot; \cdot \vdash V : A_{@\mathsf{L}}}{\Gamma \mid \Phi^w; \Phi^r; \Psi \vdash V : A_{@\mathsf{G}^\star}} \ \mathsf{GVal'}$$

where

$$\Gamma_{\mathsf{G}^{\star}} = \{ x : A_{@\mathsf{G}^{\star}} \mid x : A_{@\mathsf{G}^{\star}} \in \Gamma \}.$$

New constructs and types for G^* can be designed in an analogous way to those for G. Then a typing judgment $f:A_{@G^*}$ ensures that f contains neither references nor write channels.

8 Related work

8.1 Mutable references in parallel or distributed languages

As there is no definitive standard for shared memory model in parallel or distributed languages, different policies for mutable references or similar constructs have been proposed. X10 (Charles et al., 2005) permits remote mutable variables belonging to remote threads, but accessing the contents of a remote mutable variable results in a runtime exception. Fortress (Allan et al., 2007) permits shared objects accessible to every thread, but at the cost of implicitly maintaining the sharedness (either local or shared) of every object. Titanium (Hilfinger et al., 2005), which extends Java, takes a different approach by allowing shared memory, but also distinguishing between local references and global references at the type level. UPC (El-Ghazawi et al., 2003), which extends C, takes a similar approach by using two kinds of pointers: private pointers and global pointers. Facile (Knabe, 1995) and JoCAML (Fournet et al., 2003) dispense with remote references by sending copies of heap cells whenever their references are transmitted to remote threads. Erlang (Armstrong, 1997) and Manticore (Fluet et al., 2007) do not use mutable references at all. Alice (Rossberg et al., 2005) raises runtime exceptions when references are transmitted to remote threads.

 λ^{PC}_{\square} is similar to Facile and JoCAML in that it permits mutable references but assumes no shared memory (and also in that it is a dialect of ML). The main difference is that Facile and JoCAML rely on the runtime system to avoid remote references whereas λ^{PC}_{\square} relies on the type system to forestall remote references.

8.2 Channel locality

The issue of channel locality has been studied mainly for the pi-calculus and its relatives. Amadio (1997) develops a type system for a fragment of the pi-calculus in which typing judgments use channels linearly to ensure channel locality. The distributed join-calculus (Fournet *et al.*, 1996) assumes a syntactic restriction

to enforce channel locality. Specifically it considers only syntactically well-formed DRCHAMs (distributed reflexive chemical machines) in which every channel is defined in at most one RCHAM (reflexive chemical machine), where a DRCHAM corresponds to a configuration and a RCHAM to a thread in λ_{\square}^{PC} . Schmitt and Stefani (2003) present a higher-order version of the distributed join-calculus which uses a polymorphic type system to guarantee that the destination for each message is uniquely determined. It achieves channel locality in a slightly different sense than that in λ_{\square}^{PC} because, for example, a message may be intercepted before reaching its destination. The dynamic join-calculus (Schmitt, 2002) achieves channel locality in a similar vein, in which the destination for a message is determined according to its current position.

Yoshida and Hennessy (1999) present a calculus similar to λ_{\square}^{PC} in that it uses a *type* system to enforce channel locality. It combines the call-by-value lambda-calculus and a higher-order extension of the pi-calculus, and allows processes themselves to be transmitted via channels. The type system introduces sendable types whose values can be transmitted between processes without destroying channel locality, and exploits a subtyping relation to enforce channel locality. The type system, however, is not a general solution applicable to our setting for two reasons. First, there is a semantic restriction on function types which severely limits the generality of the calculus: for a function type $\tau \to \sigma$, if σ is sendable, τ must also be sendable, in which case $\tau \to \sigma$ is also automatically regarded as sendable. In our setting, such a restriction means, for example, that no term of type ref int \rightarrow int or $\langle int \rangle^r \rightarrow$ int is even allowed and that every function of type int → int must be global, neither of which is the case in λ_{\square}^{PC} . Second, the type system ignores the call-by-value semantics of the underlying lambda-calculus. Specifically it includes a typing rule (Term_l) assigning a sendable type to a term whenever it typechecks under a typing context using sendable types only. In our setting, the typing rule would correspond to the following rule which fails to comply with the call-by-value semantics:

$$\frac{\Gamma_{\mathsf{G}} \mid \Phi^{\scriptscriptstyle{W}}; \cdot; \cdot \vdash M : A_{@\mathsf{L}}}{\Gamma \mid \Phi^{\scriptscriptstyle{W}}; \Phi^{\scriptscriptstyle{F}}; \Psi \vdash M : A_{@\mathsf{G}}} \; (\textit{wrong})$$

In comparison, λ_{\square}^{PC} imposes no syntactic or semantic restriction on function types and properly accounts for the call-by-value semantics in the presence of mutable references.

8.3 Splitting channels

The idea of using two kinds of channels, namely read channels and write channels (or input channels and output channels), is not new. For example, the type system of Pierce and Sangiorgi (1993) can effectively distinguish between read channels and write channels because every channel is assigned a tag indicating its usage (read, write, or both). The polarized pi-calculus (Odersky, 1995) syntactically distinguishes between read channels and write channels. In the polarized pi-calculus, using read channels for channel writes or write channels for channel reads results in no reaction with other processes. The polar pi-calculus (Zhang & Potter, 2002)

also syntactically distinguishes between read channels and write channels with an additional requirement that only write channels can be transmitted via channels.

 λ_{\square}^{PC} is different from previous work in that it assigns different types to different kinds of channels and *exploits these types*, instead of the syntax, to enforce channel locality. In fact, such notions as local values, global values, and primitive types, all established in the base language λ_{\square} , have naturally led to the main idea for achieving channel locality in λ_{\square}^{PC} . Thus our decision to distinguish between read channels and write channels has a different motivation from previous work.

8.4 Modal types for parallel and distributed computations

There are a few distributed languages whose type systems are based on the spatial interpretation of modal logic.

Borghuis and Feijs (2000) present a typed lambda-calculus MTSN (Modal Type System for Networks) which assumes stationary services (i.e., stationary code) and mobile data. An indexed modal type $\Box^{\omega}(A \to B)$ represents services transforming data of type A into data of type B at node ω . MTSN is a task description language rather than a programming language, since services are all "black boxes" whose inner workings are unknown. For example, terms of type $tex \to dvi$ all describe procedures to convert tex files to dvi files. Thus reduction on terms is tantamount to simplifying procedures to achieve a certain task.

Jia and Walker (2004) present a modal language λ_{rpc} which is based on hybrid logic (Braüner, 2004) as every typing judgment explicitly specifies the node where typechecking takes place. The modalities \Box and \diamond are used for terms that can be evaluated at any node and at a certain node, respectively.

Murphy et al. (2004) present a modal language Lambda 5 which addresses both code mobility and resource locality. It is based on modal logic S5 where all judgments are relativized to nodes as in Simpson (1994). A value of type $\Box A$ contains a term that can be evaluated at any node, and a value of type $\Diamond A$ contains a label, a reference to a local resource. A label may appear at remote nodes, but the type system guarantees that it is dereferenced only at the node where it is valid. Murphy et al. (2007) later developed Lambda 5 into a distributed language ML5 whose type system also prevents local resources from being used at remote nodes while their references may travel between nodes. ML5 uses a typing judgment $M: A_{@w}$ to mean that M is a term of type A for locality w. As it permits not only constants but also variables as localities, the type system of ML5 is expressive enough to encode modalities \Box and \diamondsuit both.

Our previous work (Park, 2006) was the first departure from the typical spatial interpretation of modal logic in that it uses a new modality \square to focus on values rather than terms. In our previous work, we used two separate typing judgments to distinguish between local values and global values: an ordinary typing judgment M:A to mean that M evaluates to a local value, and a stronger typing judgment $M\sim A$ to mean that M evaluates to a global value. The base language λ_{\square} in the present work combines the two typing judgments into a single typing judgment $M:A_{@L}$ where locality L, either L or G, indicates whether M evaluates to a local value or a global value. This simplification of typing judgments is essential to

maintaining the complexity of the type system at a manageable level. For example, the introduction of sum types requires eight new typing rules in the previous work whereas λ_{\square} uses only three typing rules.

In comparison with previous work whose type system relativizes typing judgments to nodes, the type system of λ_\square^{PC} lacks the expressive power necessary for allowing references to escape from their host threads, even if it is known that they are not dereferenced at remote threads. This is essential because its typing judgment relies only on two fixed localities: every value is associated with either locality L, meaning that it is valid "here only," or locality G, meaning that it is valid "everywhere." As a result, the type system cannot distinguish remote threads particularly relevant to the current thread (e.g., parent threads or child threads) from remote threads irrelevant to the current thread, and every value traveling between threads must prove to be global regardless of its destination thread. We leave it to the future work to relax this limitation by further extending the type system.

9 Conclusion and future work

We present an ML-like parallel language λ_{\square}^{PC} which features type-safe higher-order channels with channel locality. Sequential programming in λ_{\square}^{PC} may exploit mutable references as usual, since the type system ensures that mutable references never interfere with parallel computations. Thus, implementing the parallel operational semantics of λ_{\square}^{PC} is no more complicated than implementing a similar parallel language without mutable references.

Our long-term goal is to build a programming system that supports three levels of parallelism within a unified framework. At the highest level, it implements distributed computations taking place in a network of nodes. Each node performs a stand-alone computation and also communicates with other nodes via higher-order channels. (Hence there is no distributed shared memory.) The next level implements task parallelism in parallel computations with higher-order channels as in λ_{\square}^{PC} . At the lowest level, multiple local threads with shared memory run within each thread. Task parallelism with shared memory as well as data parallelism can be implemented at this level. Then the type system of λ_{\square}^{PC} can be extended to provide type safety at all the three levels.

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Appendix

A Proofs about λ_{\square}

To simplify proofs, we omit product types, sum types, and the fixed point construct.

Lemma A.1 (Canonical forms)

If V is a value of type

unit $A \rightarrow B$ ref A

then V is

 $\begin{array}{l} () \\ \lambda x : A . M \\ l \end{array}$ box M

Proof

By case analysis of V.

Proof of Theorem 2.3

By induction on the structure of $\cdot \mid \Psi \vdash M : A_{@L}$.

If M is already a value, we need no further consideration. Therefore we assume that M is not a value. The cases for those constructs available in the simply typed lambda-calculus remain the same except that we use a judgment $\cdot \mid \Psi \vdash M : A_{@L}$. The case for the rule Prim follows immediately from induction hypothesis on the premise. Therefore the only interesting case is the rule $\Box E$.

$$\text{Case } \mathscr{D} :: \ \frac{\cdot \mid \Psi \vdash M : \boxdot A_{@\mathsf{L}} \quad x : A_{@\mathsf{G}} \mid \Psi \vdash N : C_{@L}}{\cdot \mid \Psi \vdash \mathsf{letbox} \ x = M \ \mathsf{in} \ N : C_{@L}} \ \boxdot \mathsf{E}$$

Subcase 1: *M* is a value

$$\begin{split} M &= \mathsf{box}\ M' & \mathsf{from} \cdot \mid \Psi \vdash M : \Box A_{@L} \ \mathsf{and} \ \mathsf{Lemma} \ \mathsf{A.1} \\ \mathscr{D} &:: & \frac{\cdot \mid \Psi \vdash M' : A_{@G}}{\cdot \mid \Psi \vdash \mathsf{box}\ M' : \Box A_{@L}} \ \Box \mathsf{I} \\ \hline & \cdot \mid \Psi \vdash \mathsf{letbox}\ x = \mathsf{box}\ M' \ \mathsf{in}\ N : C_{@L} \\ \mathsf{because} \ \mathsf{only} \ \mathsf{the} \ \mathsf{rule} \ \Box \mathsf{l} \ \mathsf{is} \ \mathsf{applicable} \ \mathsf{to} \ \mathsf{deduce} \ \cdot \mid \Psi \vdash \mathsf{box}\ M' : \Box A_{@l} \ . \end{split}$$

Subcase 1-1: M' is a value letbox x = box M' in $N \mid \psi \rightarrow [M'/x]N \mid \psi$

Subcase 1-2: M' is not a value

 $\begin{array}{ll} \Psi \vdash \psi \text{ okay} & \text{assumption} \\ M' \mid \psi \to M'' \mid \psi' & \text{by induction hypothesis on } \cdot \mid \Psi \vdash M' : A_{@G} \\ \text{letbox } x = \text{box } M' \text{ in } N \mid \psi \to \text{letbox } x = \text{box } M'' \text{ in } N \mid \psi' \end{array}$

Subcase 2: M is not a value

$$\begin{array}{ccc} \Psi \vdash \psi \text{ okay} & \text{assumption} \\ M \mid \psi \to M' \mid \psi' & \text{by induction hypothesis on } \cdot \mid \Psi \vdash M : \Box A_{@L} \\ \text{letbox } x = M \text{ in } N \mid \psi \to \text{letbox } x = M' \text{ in } N \mid \psi' & \Box \end{array}$$

Proof of Theorem 2.4

By induction on the structure of $\Gamma, x: A_{@L} \mid \Psi \vdash M: C_{@L}$ and $\Gamma, x: A_{@G} \mid \Psi \vdash M: C_{@L}$.

All rules except Var and GVal share the property that typing contexts are not used in premises or that typing contexts in premises extend typing contexts in the conclusion. We consider the rules Var and GVal as follows:

$$\begin{array}{ll} \text{Case} & \frac{y:C_{@L'}\in\Gamma \quad L\leqslant L'}{\Gamma,x:A_{@L''}\mid\Psi\vdash y:C_{@L}} \text{ Var} \quad \text{where } y\neq x \text{ and } L''=\text{G or } L''=\text{L} \\ \Gamma\mid\Psi\vdash y:C_{@L} & \text{from } y:C_{@L'}\in\Gamma \text{ and the rule Var} \\ \Gamma\mid\Psi\vdash [N/x]y:C_{@L} & \text{from } [N/x]y=y \end{array}$$

$$\begin{array}{ll} \operatorname{Case} & \frac{L \leqslant \mathsf{L}}{\Gamma, x: A_{@\mathsf{L}} \mid \Psi \vdash x: A_{@L}} \; \mathsf{Var} \\ \\ \Gamma \mid \Psi \vdash N: A_{@\mathsf{L}} & \operatorname{assumption} \\ \Gamma \mid \Psi \vdash [N/x]x: A_{@\mathsf{L}} & \operatorname{from} \; [N/x]x = N \end{array}$$

$$\begin{array}{lll} \operatorname{Case} & L \leqslant \operatorname{G} \\ \hline \Gamma, x : A_{@\operatorname{G}} \mid \Psi \vdash x : A_{@L} & \operatorname{assumption} \\ \hline \Gamma_{\operatorname{G}} \mid \cdot \vdash V : A_{@\operatorname{G}} & \operatorname{by the rule GVal} \\ \hline \Gamma \mid \Psi \vdash V : A_{@\operatorname{G}} & \operatorname{from } [V/x]x = V \\ \hline \Gamma \mid \Psi \vdash [V/x]x : A_{@\operatorname{G}} & \operatorname{by Proposition 2.2} \end{array}$$

$$\begin{array}{ll} \text{Case} & \frac{\Gamma_{\text{G}} \mid \cdot \vdash V' : C_{\text{@L}}}{\Gamma, x : A_{\text{@L}} \mid \Psi \vdash V' : C_{\text{@G}}} \text{ GVal} \\ \Gamma_{\text{G}} \mid \cdot \vdash [N/x]V' : C_{\text{@L}} & \text{from } x \text{ is not a free variable in } V' \text{ and } [N/x]V' = V' \\ \Gamma \mid \Psi \vdash [N/x]V' : C_{\text{@G}} & \text{by the rule GVal} \end{array}$$

$$\begin{array}{ll} \operatorname{Case} & \frac{\Gamma_{\operatorname{G}}, x: A_{\operatorname{@G}} \mid \cdot \vdash V': C_{\operatorname{@L}}}{\Gamma, x: A_{\operatorname{@G}} \mid \Psi \vdash V': C_{\operatorname{@G}}} \text{ GVal} \\ & \Gamma_{\operatorname{G}} \mid \cdot \vdash V: A_{\operatorname{@L}} & \operatorname{assumption} \\ & \Gamma_{\operatorname{G}} \mid \cdot \vdash [V/x]V': C_{\operatorname{@L}} & \operatorname{by induction hypothesis} \\ & \Gamma \mid \Psi \vdash [V/x]V': C_{\operatorname{@G}} & \operatorname{by the rule GVal} & \Box \\ \end{array}$$

Corollary A.2

If
$$\Gamma \mid \Psi \vdash V : P_{@L}$$
 and $\Gamma, x : P_{@G} \mid \Psi \vdash M : C_{@L}$, then $\Gamma \mid \Psi \vdash [V/x]M : C_{@L}$.

Proof

Proposition A.3

If
$$\cdot \mid \Psi \vdash (\lambda x : A.M) \ V : C_{@L}$$
, then $\cdot \mid \Psi \vdash [V/x]M : C_{@L}$.

Proof

By induction on the structure of $\cdot \mid \Psi \vdash (\lambda x : A.M) \ V : C_{@L}$. The proof uses Theorem 2.4 and the inversion property of λ_{\square} .

Proposition A.4

If $\cdot \mid \Psi \vdash \text{letbox } x = \text{box } V \text{ in } M : C_{@L}, \text{ then } \cdot \mid \Psi \vdash [V/x]M : C_{@L}.$

Proof

By induction on the structure of $\cdot \mid \Psi \vdash \text{letbox } x = \text{box } V \text{ in } M : C_{@L}$. The proof uses Theorem 2.4 and Lemma 2.5 and the inversion property of λ_{\square} .

Proof of Theorem 2.6

By induction on the structure of $M \mid \psi \to M' \mid \psi'$.

B Proofs about λ_{\square}^{P}

Proposition B.1 (Progress)

Suppose $\cdot \mid \Psi \vdash M : A_{@L}$. Then either

- (1) M is a value,
- (2) $M = \phi [[spawn \{N\}]], or$
- (3) for any store ψ such that $\Psi \vdash \psi$ okay, there exist some term M' and store ψ' such that $M \mid \psi \to M' \mid \psi'$.

Proof

By induction on the structure of $\cdot \mid \Psi \vdash M : A_{@L}$. If M is already a value, we need no further consideration. Assume that M is not a value. The cases for those constructs available in λ_{\square} remain the same, and the case for the rule Spawn is trivial: $M = \text{spawn } \{N\} = \phi \text{ [spawn } \{N\} \text{] where } \phi = \text{[]}$.

Proof of Theorem 4.1

By the case analysis of $\Pi \vdash \pi$ okay.

By Proposition B.1, there are only three cases for every $\{M \mid \psi @ \gamma\} \in \pi$. If π consists only of $\{V \mid \psi @ \gamma\}$, we are done. Assume that π contains $\{M \mid \psi @ \gamma\}$ such that M is not a value. Then either M is ϕ [spawn $\{N\}$], or there exist some term M' and store ψ' such that $M \mid \psi \to M' \mid \psi'$ where $\Psi \vdash \psi$ okay. We analyze these two cases.

Case
$$\pi = \pi_1, \{\phi \llbracket \operatorname{spawn} \{N\} \rrbracket \mid \psi @ \gamma \}$$

Let $\pi' = \pi_1, \{\phi \llbracket () \rrbracket \mid \psi @ \gamma \}, \{N \mid \cdot @ \gamma' \}$ where γ' is a fresh node $\pi \Rightarrow \pi'$ by the rule Spawn

Case
$$\pi = \pi_1, \{\phi[\![M]\!] \mid \psi @ \gamma\}$$
 where $M \mid \psi \to M' \mid \psi'$
Let $\pi' = \pi_1, \{\phi[\![M']\!] \mid \psi' @ \gamma\}$
 $\pi \Rightarrow \pi'$ by Red

Theorem B.2 (Substitution)

If
$$\Gamma \mid \Psi \vdash N : A_{@L}$$
, then $\Gamma, x : A_{@L} \mid \Psi \vdash M : C_{@L}$ implies $\Gamma \mid \Psi \vdash [N/x]M : C_{@L}$.
 If $\Gamma_{G} \mid \cdot \vdash V : A_{@L}$, then $\Gamma, x : A_{@G} \mid \Psi \vdash M : C_{@L}$ implies $\Gamma \mid \Psi \vdash [V/x]M : C_{@L}$.

Proof

By induction on the structure of $\Gamma, x: A_{@L} \mid \Psi \vdash M: C_{@L}$ and $\Gamma, x: A_{@G} \mid \Psi \vdash M: C_{@L}$.

The proof extends the proof of Theorem 2.4 with a new case for the rule Spawn. \Box

Proposition B.3 (Type preservation)

Suppose $\cdot \mid \Psi \vdash M : A_{@L}$, $\Psi \vdash \psi$ okay, and $M \mid \psi \to M' \mid \psi'$. Then there exists a store typing Ψ' such that $\cdot \mid \Psi' \vdash M' : A_{@L}$, $\Psi \subset \Psi'$, and $\Psi' \vdash \psi'$ okay.

Proof

By induction on the structure of $M \mid \psi \to M' \mid \psi'$. The proof is the same as the proof of Theorem 2.6 except that it uses Theorem B.2 instead of Theorem 2.4 for the substitution property.

Proof of Theorem 4.2

By the case analysis of $\pi \Rightarrow \pi'$. We show the case for the rule *Red* only which uses Proposition B.3.

C Proofs about λ_{\Box}^{PC}

Lemma C.1 (Canonical forms)

If V is a value of type

$$\langle A \rangle^r \ \langle A \rangle^w ,$$

then V is

$$\frac{a^r}{a^w}$$
.

Proof

By the case analysis of V.

Proof of Proposition 5.2

By induction on the structure of $|\Phi^w; \Phi^r; \Psi \vdash M : A_{@L}$.

If M is already a value, we need no further consideration. Assume that M is not a value. The cases for the constructs available in $\lambda^{\mathsf{P}}_{\square}$ remain the same except that we use $\cdot \mid \Phi^w \, ; \Phi^r \, ; \Psi \vdash M : A_{@L}$ in place of $\cdot \mid \Psi \vdash M : A_{@L}$. The case for the rule New is trivial: $M = \mathsf{new}_{\langle A \rangle} = \phi [\![\mathsf{new}_{\langle A \rangle}]\!]$ where $\phi = [\![]\!]$. Therefore the only interesting cases are the rules R? and W!. To illustrate the need for induction on the structure of $\cdot \mid \Phi^w \, ; \Phi^r \, ; \Psi \vdash M : A_{@L}$, we show the case for the rule R?. The case for the rule W! is similar.

Case
$$\frac{\cdot\mid\Phi^{w};\Phi^{r};\Psi\vdash N:\langle A\rangle^{r}_{@L}}{\cdot\mid\Phi^{w};\Phi^{r};\Psi\vdash N?:A_{@L}} \ \mathsf{R}? \ \ \mathsf{where} \ M=N?$$

Subcase 1: N is a value

 $N = a^r$ where $a^r \mapsto A \in \Phi^r$

because only the rule ChanR is applicable to deduce $\cdot \mid \Phi^w ; \Phi^r ; \Psi \vdash N : \langle A \rangle^r_{@L} M = \phi \llbracket a^r ? \rrbracket$ where $\phi = \llbracket$

Subcase 2: N is written as $\phi[[\operatorname{new}_{\langle B \rangle}]]$, $\phi[[a^r?]]$, $\phi[[a^w!V]]$, or $\phi[[\operatorname{spawn}\{N'\}]]$ N? is written as $\phi'[[\operatorname{new}_{\langle B \rangle}]]$, $\phi'[[a^r?]]$, $\phi'[[a^w!V]]$, or $\phi'[[\operatorname{spawn}\{N'\}]]$ where $\phi' = \phi$?

Subcase 3:
$$N \mid \psi \to N' \mid \psi'$$

 Φ^w ; Φ^r ; $\Psi \vdash \psi$ okay assumption
 $N? \mid \psi \to N'? \mid \psi'$

Proof of Theorem 5.3

By the case analysis of Φ^w ; $\Pi \vdash \pi$ okay.

By Proposition 5.2, there are only six cases for every $\{M \mid \psi @ \gamma\} \in \pi$. If π consists only of $\{V \mid \psi @ \gamma\}$, $\{\phi[\![a^r\,?]\!] \mid \psi @ \gamma\}$, or $\{\phi[\![a^w\,!\,V]\!] \mid \psi @ \gamma\}$, we are done. (If π happens to include both $\{\phi[\![a^r\,?]\!] \mid \psi @ \gamma\}$ and $\{\phi'[\![a^w\,!\,V]\!] \mid \psi' @ \gamma'\}$ for the same channel identifier a, a communication between nodes γ and γ' occurs by the rule Sync.)

If there exists $\{M \mid \psi @ \gamma\} \in \pi$ such that $M = \phi \llbracket \text{spawn } \{N\} \rrbracket$ or $M \mid \psi \to M' \mid \psi'$ for some term M' and store ψ' , the proof follows the same pattern as in $\lambda^{\mathsf{P}}_{\square}$. Therefore we consider the case in which there exists $\{\phi \llbracket \text{new}_{\langle A \rangle} \rrbracket \mid \psi @ \gamma\} \in \pi$.

Case
$$\pi = \pi_1, \{\phi[[new_{(A)}]] \mid \psi @ \gamma\}$$

Let $\pi' = \pi_1, \{\phi[[(a^r, a^w)]] \mid \psi @ \gamma\}$ where a^r and a^w are fresh $\pi \Rightarrow \pi'$ by the rule New

Theorem C.2 (Substitution)

If
$$\Gamma \mid \Phi^w; \Phi^r; \Psi \vdash N : A_{@L}$$
,
then $\Gamma, x : A_{@L} \mid \Phi^w; \Phi^r; \Psi \vdash M : C_{@L}$ implies $\Gamma \mid \Phi^w; \Phi^r; \Psi \vdash [N/x]M : C_{@L}$.
If $\Gamma_{\mathsf{G}} \mid \Phi^w; \cdot; \cdot \vdash V : A_{@L}$,
then $\Gamma, x : A_{@\mathsf{G}} \mid \Phi^w; \Phi^r; \Psi \vdash M : C_{@L}$ implies $\Gamma \mid \Phi^w; \Phi^r; \Psi \vdash [V/x]M : C_{@L}$.

Proof

By induction on the structure of $\Gamma, x: A_{@L} \mid \Phi^w; \Phi^r; \Psi \vdash M: C_{@L}$ and $\Gamma, x: A_{@G} \mid \Phi^w; \Phi^r; \Psi \vdash M: C_{@L}$. The proof extends the proof of Theorem B.2 by replacing each judgment of the form $\Gamma \mid \Psi \vdash M: A_{@L}$ by $\Gamma \mid \Phi^w; \Phi^r; \Psi \vdash M: A_{@L}$. Among new cases in λ_{\square}^{PC} , the cases for the rules New, ChanR, and ChanW are trivial because substitutions do not change terms. The cases for the rules R? and W! are also trivial because typing contexts are the same in both the premise and the conclusion. We consider the case for the rule GVal.

$$\text{Case } \frac{\Gamma_{\mathsf{G}} \mid \Phi^{\scriptscriptstyle{\mathsf{W}}} \, ; \cdot ; \cdot \vdash V' : C_{@\mathsf{L}}}{\Gamma, x : A_{@\mathsf{L}} \mid \Phi^{\scriptscriptstyle{\mathsf{W}}} \, ; \Phi' \, ; \Psi \vdash V' : C_{@\mathsf{G}}} \text{ GVal}$$

 $\Gamma_{\mathsf{G}} \mid \Phi^w; \cdot; \cdot \vdash [N/x]V' : C_{@L}$ from x is not a free variable in V' and [N/x]V' = V' $\Gamma \mid \Phi^w; \Phi^r; \Psi \vdash [N/x]V' : C_{@G}$ by the rule GVal

$$\begin{array}{ll} \operatorname{Case} & \frac{\Gamma_{\operatorname{G}}, x: A_{@\operatorname{G}} \mid \Phi^w\,; \cdot; \cdot \vdash V': C_{@\operatorname{L}}}{\Gamma, x: A_{@\operatorname{G}} \mid \Phi^w\,; \Phi^r\,; \Psi \vdash V': C_{@\operatorname{G}}} \; \operatorname{GVal} \\ & \Gamma_{\operatorname{G}} \mid \Phi^w\,; \cdot; \cdot \vdash V: A_{@\operatorname{L}} & \operatorname{assumption} \\ & \Gamma_{\operatorname{G}} \mid \Phi^w\,; \cdot; \cdot \vdash [V/x]V': C_{@\operatorname{L}} & \operatorname{by induction \ hypothesis} \\ & \Gamma \mid \Phi^w\,; \Phi^r\,; \Psi \vdash [V/x]V': C_{@\operatorname{G}} & \operatorname{by \ the \ rule \ GVal} & \square \end{array}$$

Proof of Proposition 5.4

By induction on the structure of $M \mid \psi \to M' \mid \psi'$. The proof is the same as the proof of Proposition B.3 except that we use $\cdot \mid \Phi^w ; \Phi^r ; \Psi \vdash M : A_{@L}$ in place of $\cdot \mid \Psi \vdash M : A_{@L}$. The proof uses Theorem C.2.

Proof of Theorem 5.5

By the case analysis of $\pi \Rightarrow \pi'$.

The cases for the rules Red and Spawn are the same as in Theorem 4.2 except that we use Φ^w ; $\Pi \vdash \pi$ okay in place of $\Pi \vdash \pi$ okay. Thus, we analyze the cases for the rules New and Sync. The proof uses a similar strategy as in Theorem 4.2 except that it frequently uses weakening, so we highlight only important points.

From these assumptions, we can prove Φ'^w ; $\Pi_1, \gamma : C \vdash \pi_1, \{\phi[[(a^r, a^w)]] \mid \psi @ \gamma\}$ okay which simplifies to Φ'^w ; $\Pi' \vdash \pi'$ okay where $\Phi^w \subset \Phi'^w$ and $\Pi = \Pi'$.

Case
$$\frac{\pi_{1}, \{\phi_{1}\llbracket a^{r}? \rrbracket \mid \psi_{1} @ \gamma_{1}\}, \{\phi_{2}\llbracket a^{w}!V \rrbracket \mid \psi_{2} @ \gamma_{2}\} \Rightarrow}{\pi_{1}, \{\phi_{1}\llbracket V \rrbracket \mid \psi_{1} @ \gamma_{1}\}, \{\phi_{2}\llbracket 0 \rrbracket \mid \psi_{2} @ \gamma_{2}\}}$$

$$\text{ where } \begin{cases} \pi = \pi_1, \{\phi_1 \llbracket a^r ? \rrbracket \mid \psi_1 @ \gamma_1\}, \{\phi_2 \llbracket a^w ! V \rrbracket \mid \psi_2 @ \gamma_2\} \\ \pi' = \pi_1, \{\phi_1 \llbracket V \rrbracket \mid \psi_1 @ \gamma_1\}, \{\phi_2 \llbracket 0 \rrbracket \mid \psi_2 @ \gamma_2\} \end{cases}$$

For this case, the proof exploits $a^r \mapsto B \in \Phi_1^r$ and $a^w \mapsto B' \in \Phi^w$.

$$\begin{cases} \gamma_1:A_1\in\Pi\\ \cdot\mid\Phi^w;\Phi_1^r;\Psi_1\vdash\phi_1[\![a^r\,?]\!]:A_{1@L} \end{cases} \qquad \text{from }\Phi^w;\Pi\vdash\pi\text{ okay}\\ \cdot\mid\Phi^w;\Phi_1^r;\Psi_1\vdash a^r\,?:B_{@L} \end{cases} \qquad \text{by the rule R? and }a^r\mapsto B\in\Phi_1^r\\ \begin{cases} \gamma_2:A_2\in\Pi\\ \cdot\mid\Phi^w;\Phi_2^r;\Psi_2\vdash\phi_2[\![a^w\,!\,V]\!]:A_{2@L} \end{cases} \qquad \text{from }\Phi^w;\Pi\vdash\pi\text{ okay}\\ \cdot\mid\Phi^w;\Phi_2^r;\Psi_2\vdash a^w\,!\,V:\text{unit}_{@L} \end{cases} \qquad \text{by the rule W! and }a^w\mapsto B'\in\Phi^w\\ \text{Let }\Pi=\Pi_1,\gamma_1:A_1,\gamma_2:A_2 \end{cases}$$

From these assumptions, we can prove

$$\Phi^w;\Pi_1,\gamma_1:A_1,\gamma_2:A_2\vdash\pi_1,\{\phi_1\llbracket V\rrbracket\mid\psi_1@\gamma_1\},\{\phi_2\llbracket()\rrbracket\mid\psi_2@\gamma_2\}\text{ okay}$$
 which simplifies to $\Phi'^w;\Pi'\vdash\pi'$ okay where $\Phi^w=\Phi'^w$ and $\Pi=\Pi'$.