Dillon Hall

Email: Dillon.hall@ucdenver.edu

Skills:

- Extensive in C/C++, Python, Rust, Java, Java script, PIC & Atmel assembly, Verilog, and, Linux Bash
- Experience designing Analog Circuits that are designed to operate at frequencies as high as giga bands, using passive and active components, Op amps, and transmission lines.
- Extensive experience in development using 8bit microcontrollers
- Proficient in electrical design suites such as QUCS, eagle CAD ,Agilent ADS and, Spice.
- Knowledgeable about standard bench kit such as oscilloscopes, signal generators and VNA's

Education:

CU Denver, 2019, Electrical Engineering, concentration in radio frequency design with a minor in computer Engineering.

School Projects:

- Big assent tracker(2019), Senior design project. Designed software and electronics for a 3G assented GPS tracker. Device contains a long-lasting battery and sensors to monitor environment.
- Designed a 2.0 Ghz low pass filter for 4134: intro to microwave circuit design
- Developed a convolutional neural network base image classification algorithm for ELEC 4800
- Implemented a limited MIPS CPU in Verilog for ELEC 3651: Digital hardware design.
- Developed a bandpass filter for ELEC:2500
- Designed a VGA video generator for DE1-SOC board.

Employment History:

- HDR, Summer 2019 Electrical Engineering intern.

   Designed PLC control systems and DC electronics for sensor systems

- HOSTING. Summer 2015 to Fall of 2015. Software engineering intern.

   Working on bugs for on going projects.

   Gained practical experience with mocking and Unit testing.

- Motocol LLC, Software engineering intern Fall 2014 to Summer 2015
    - Worked under direction of senior Software engineers to build UI elements for web services.
    - Wrote documentation for ongoing projects
- Analog engineering LLC: Engineering intern. Summer 2013 to Summer 2014
    - Developed firmware for PIC16F1455 microcontroller for sending telemetry to a host PC over USB.

- Wrote documentation for board and firmware.