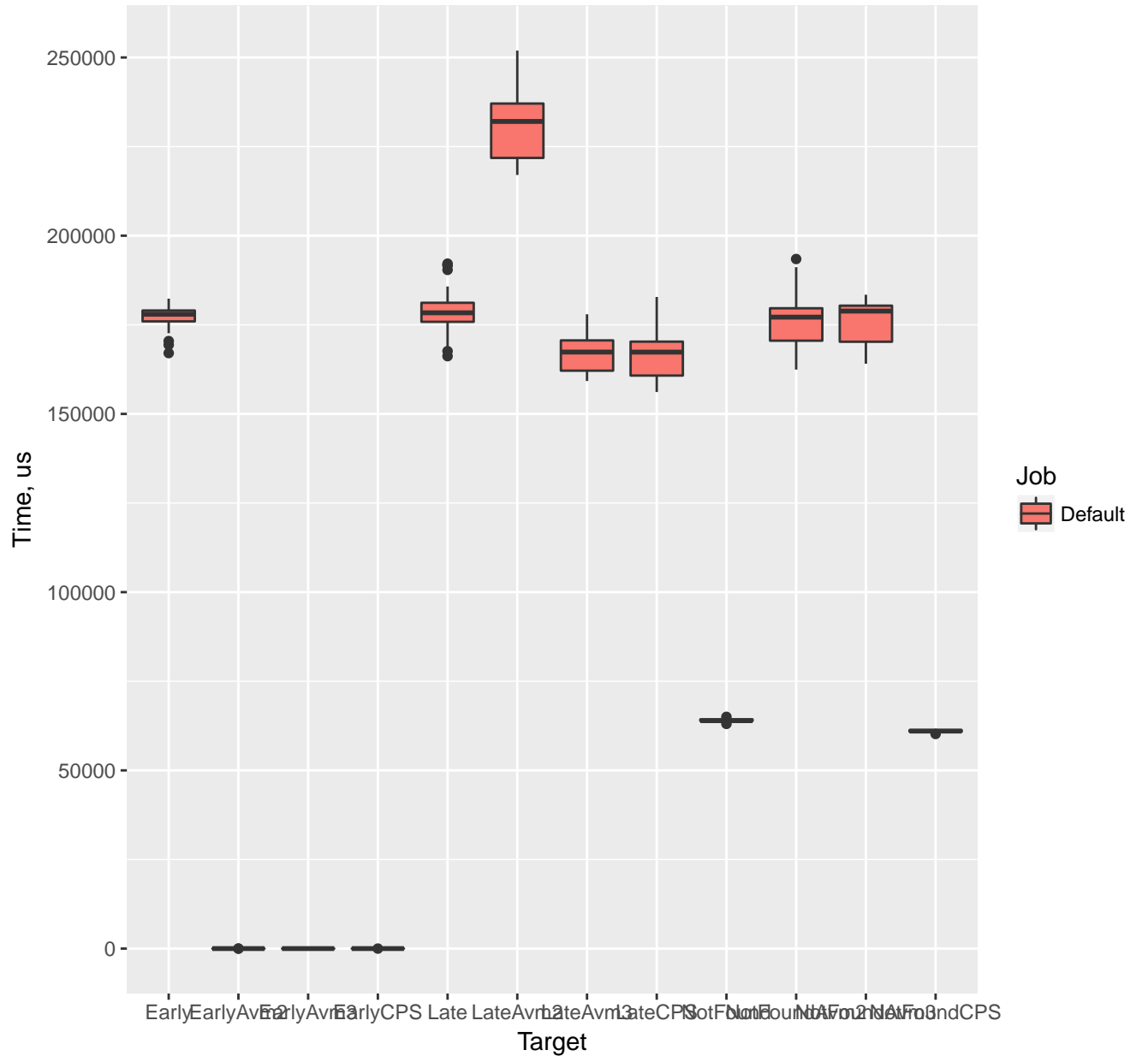
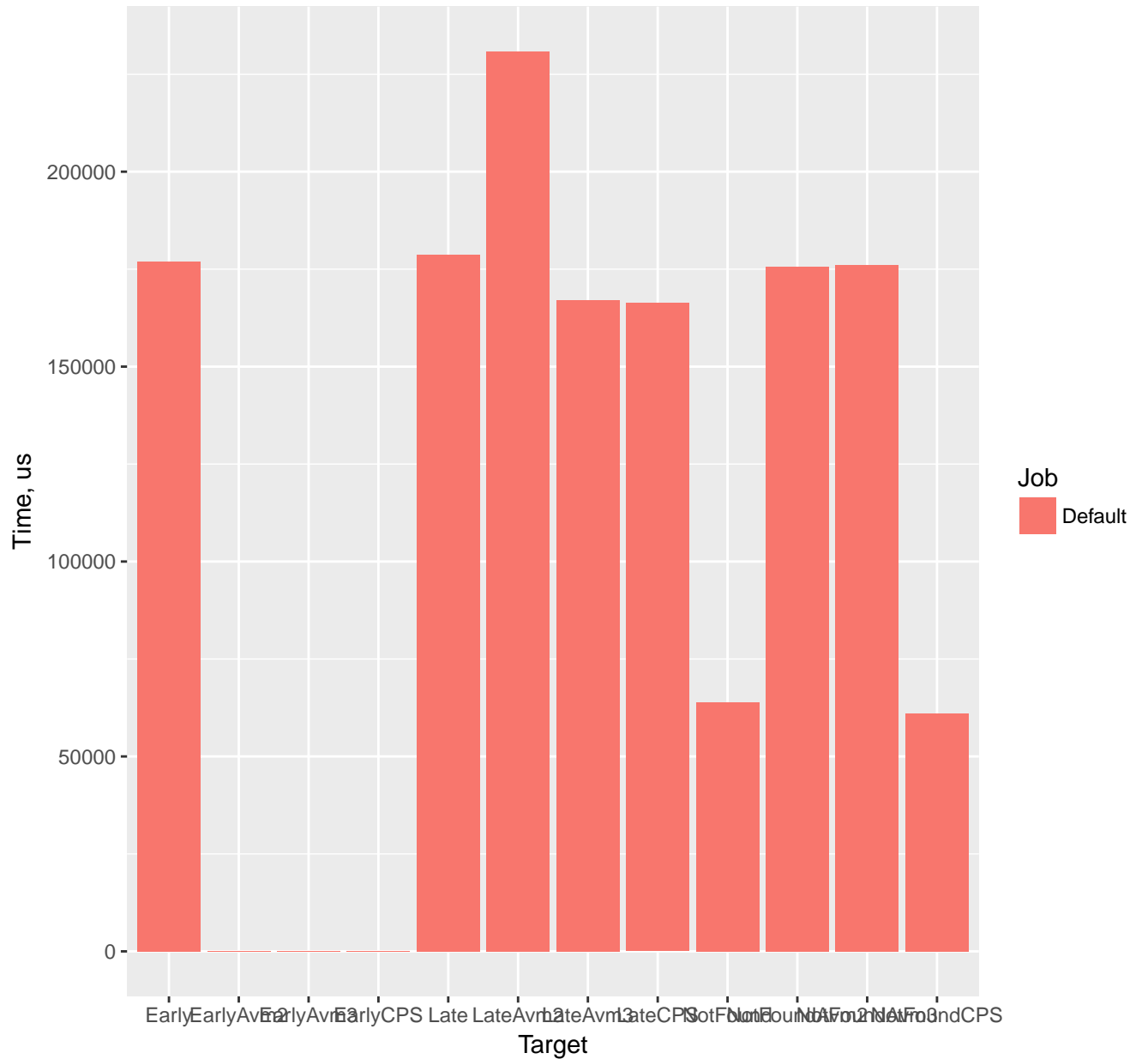


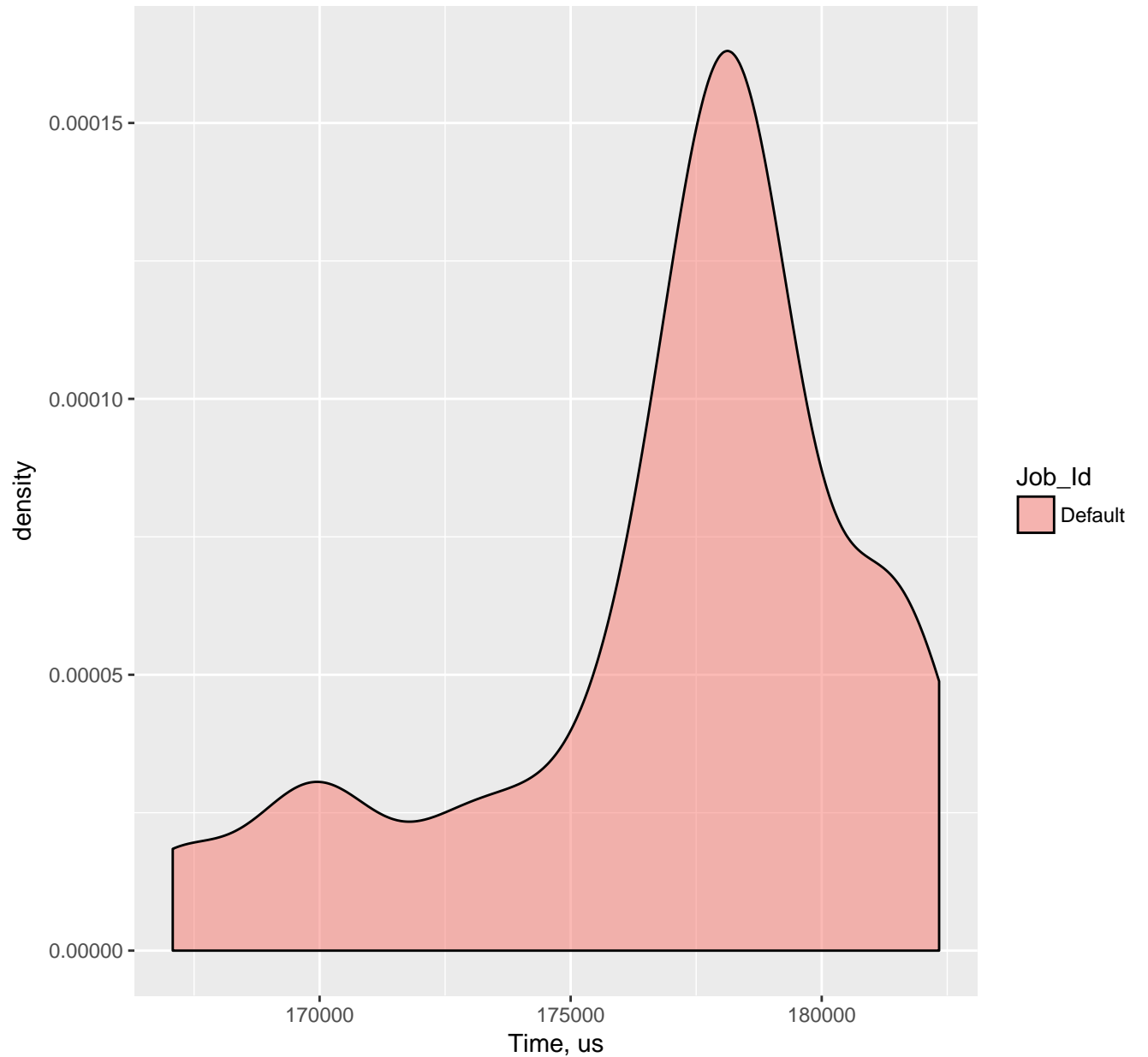
# CpsBenchmark+CpsPerformanceTests



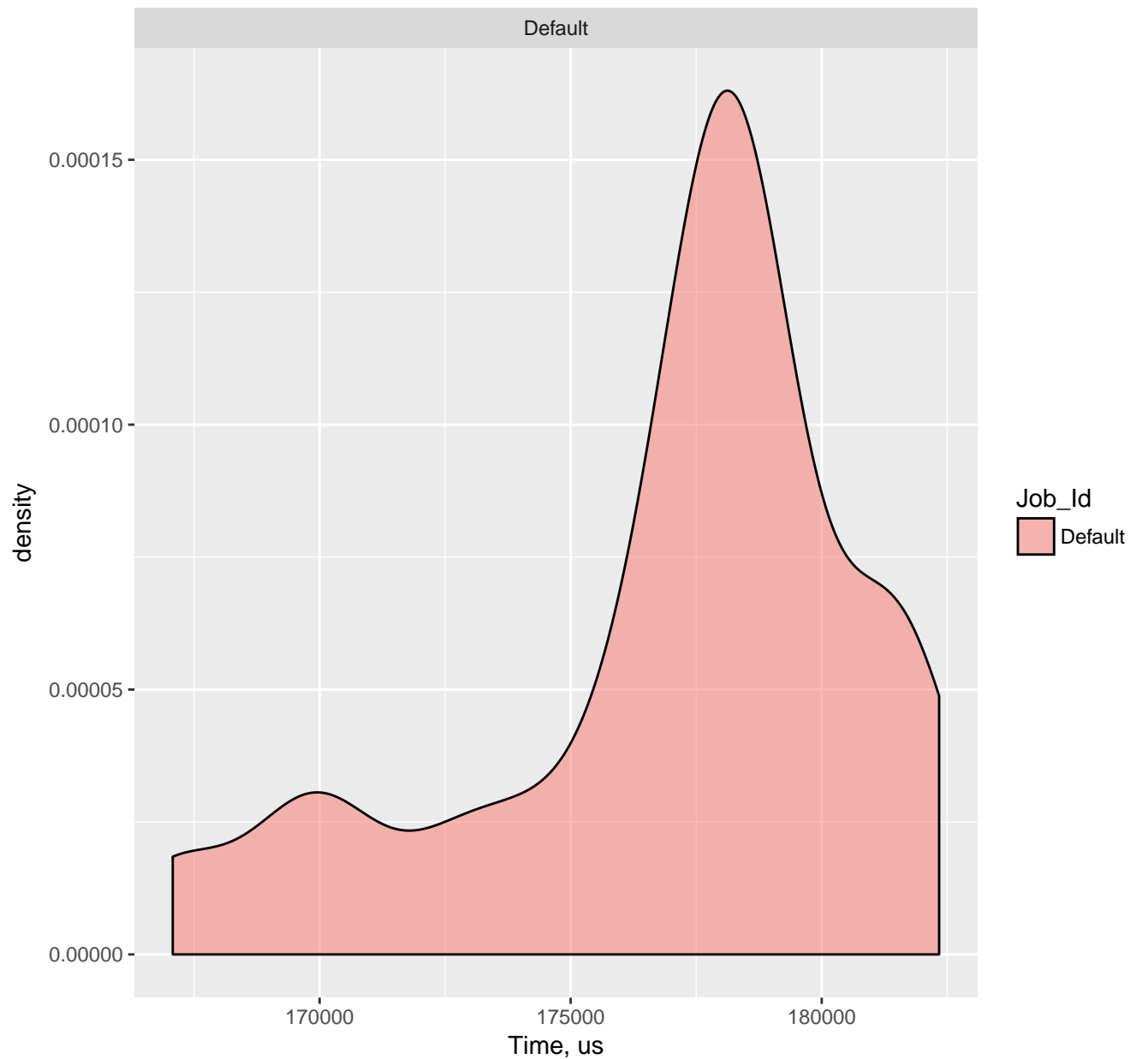
# CpsBenchmark+CpsPerformanceTests

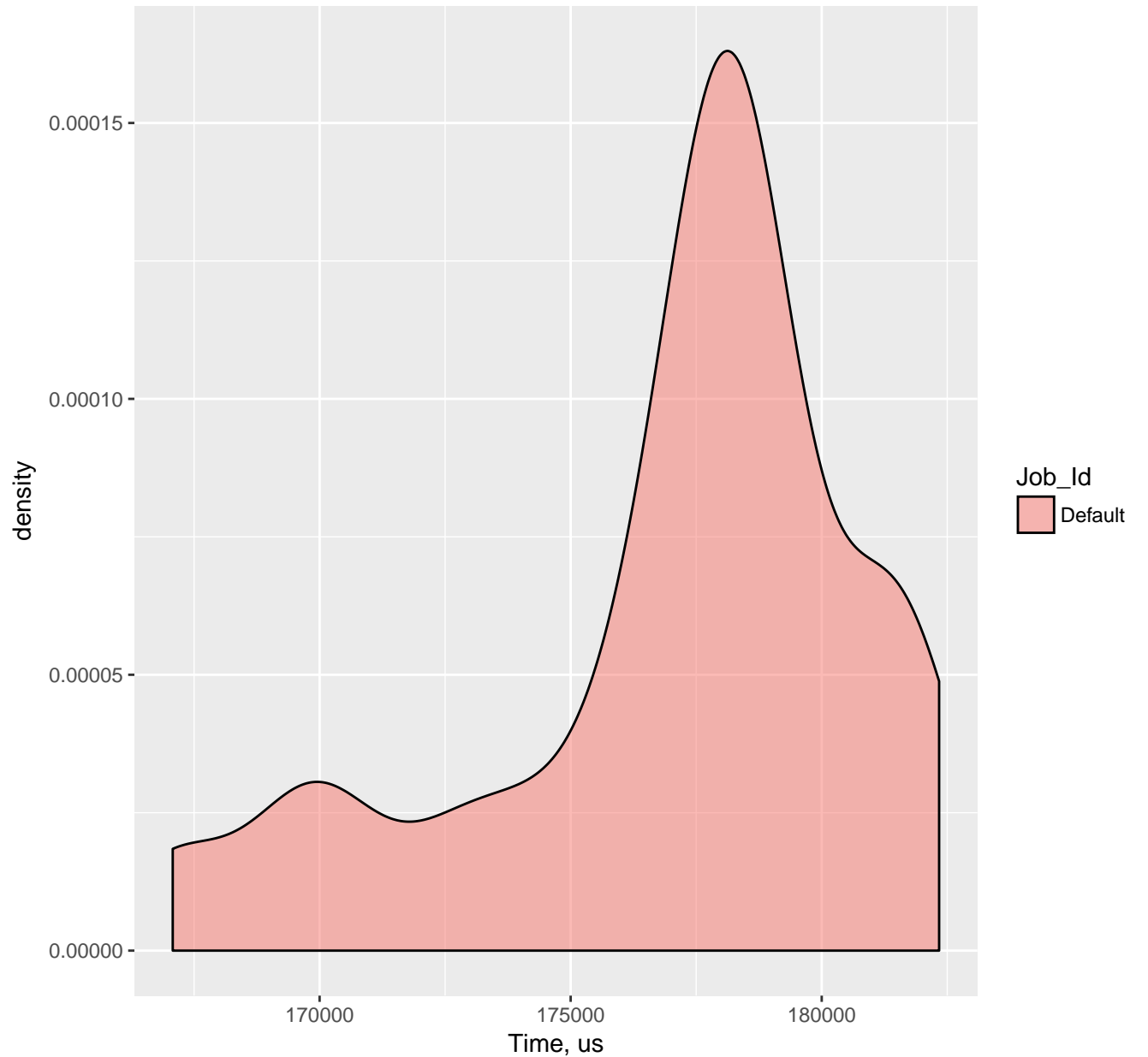


# CpsBenchmark+CpsPerformanceTests / Early

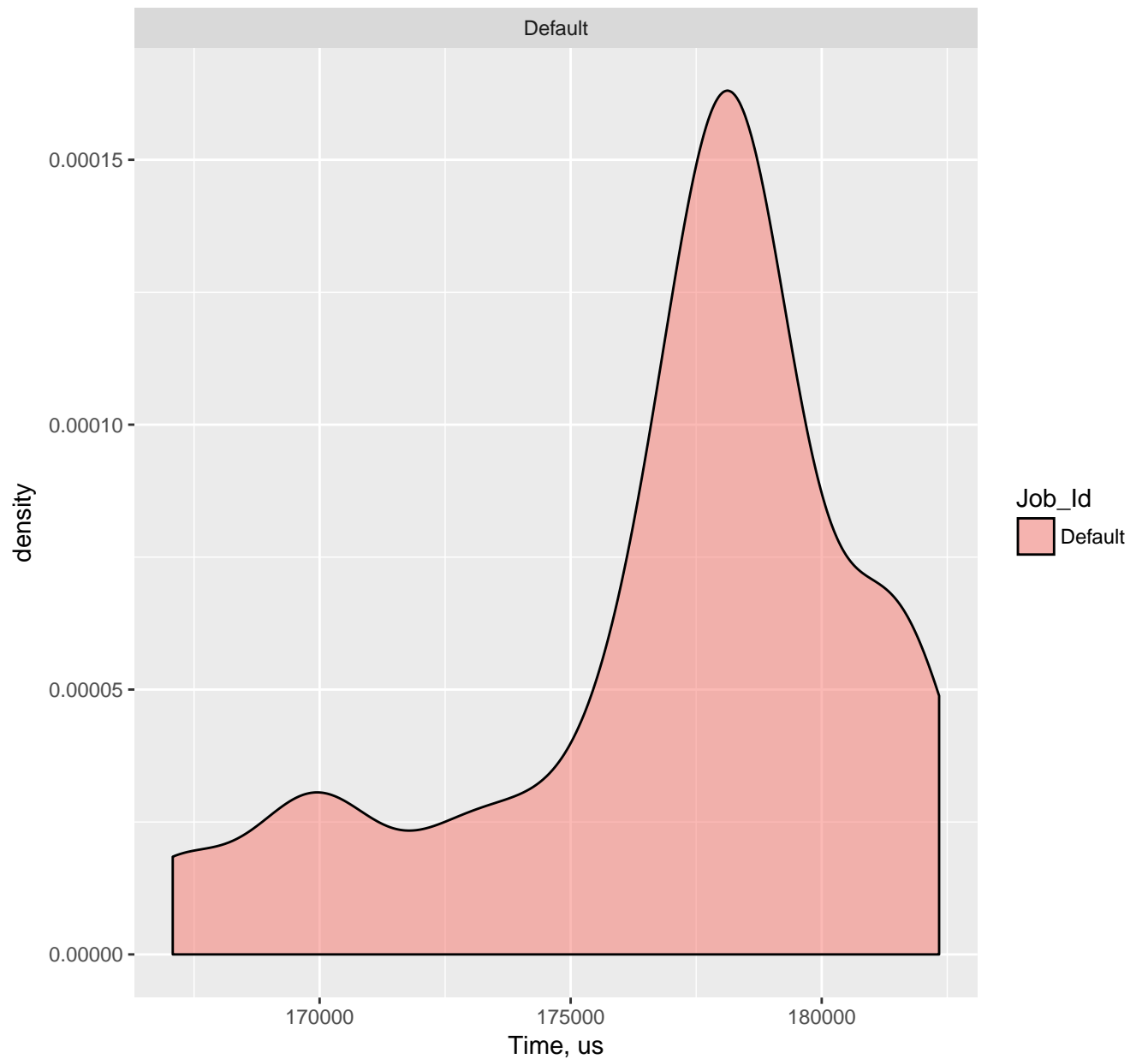


# CpsBenchmark+CpsPerformanceTests / Early

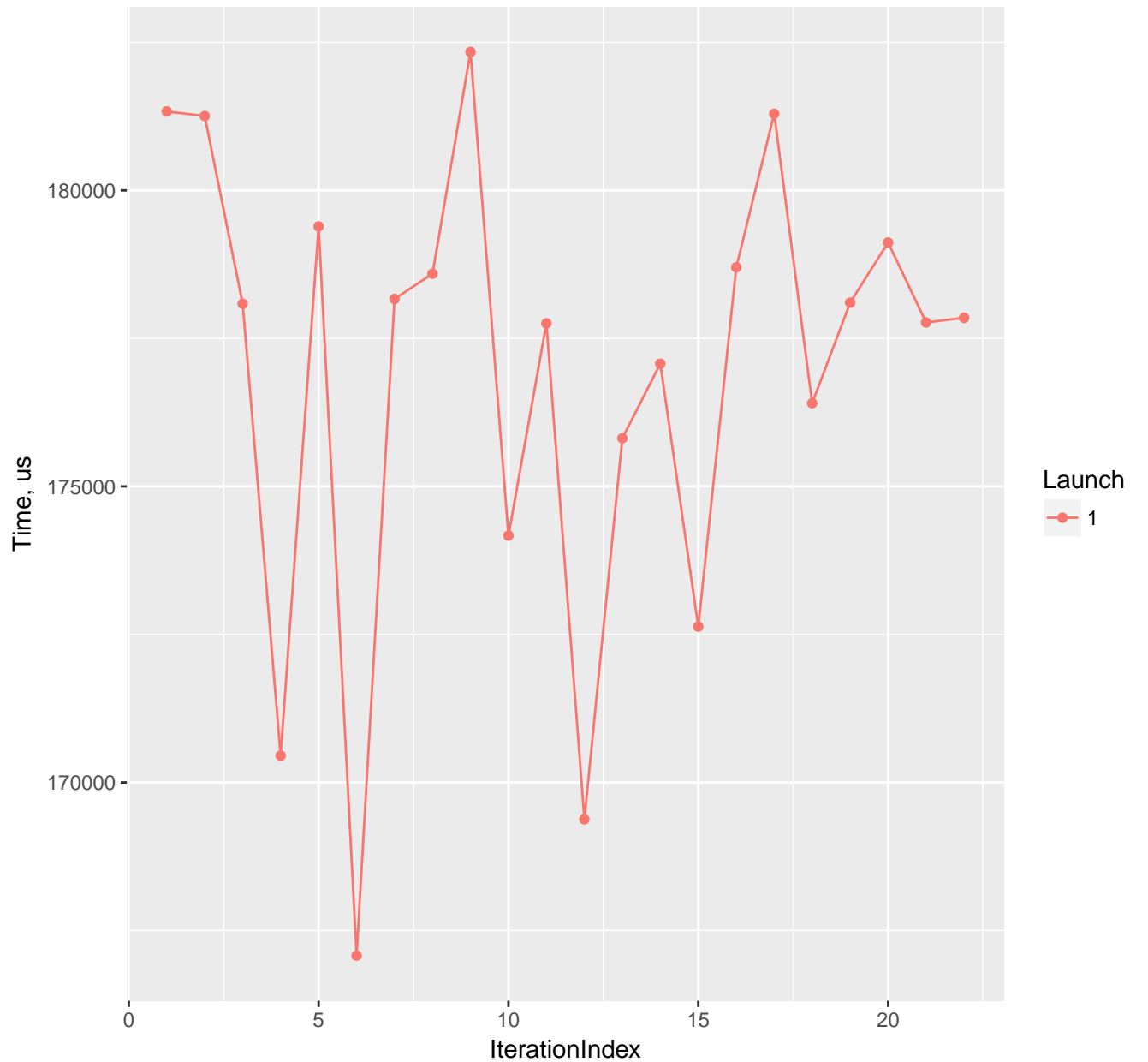




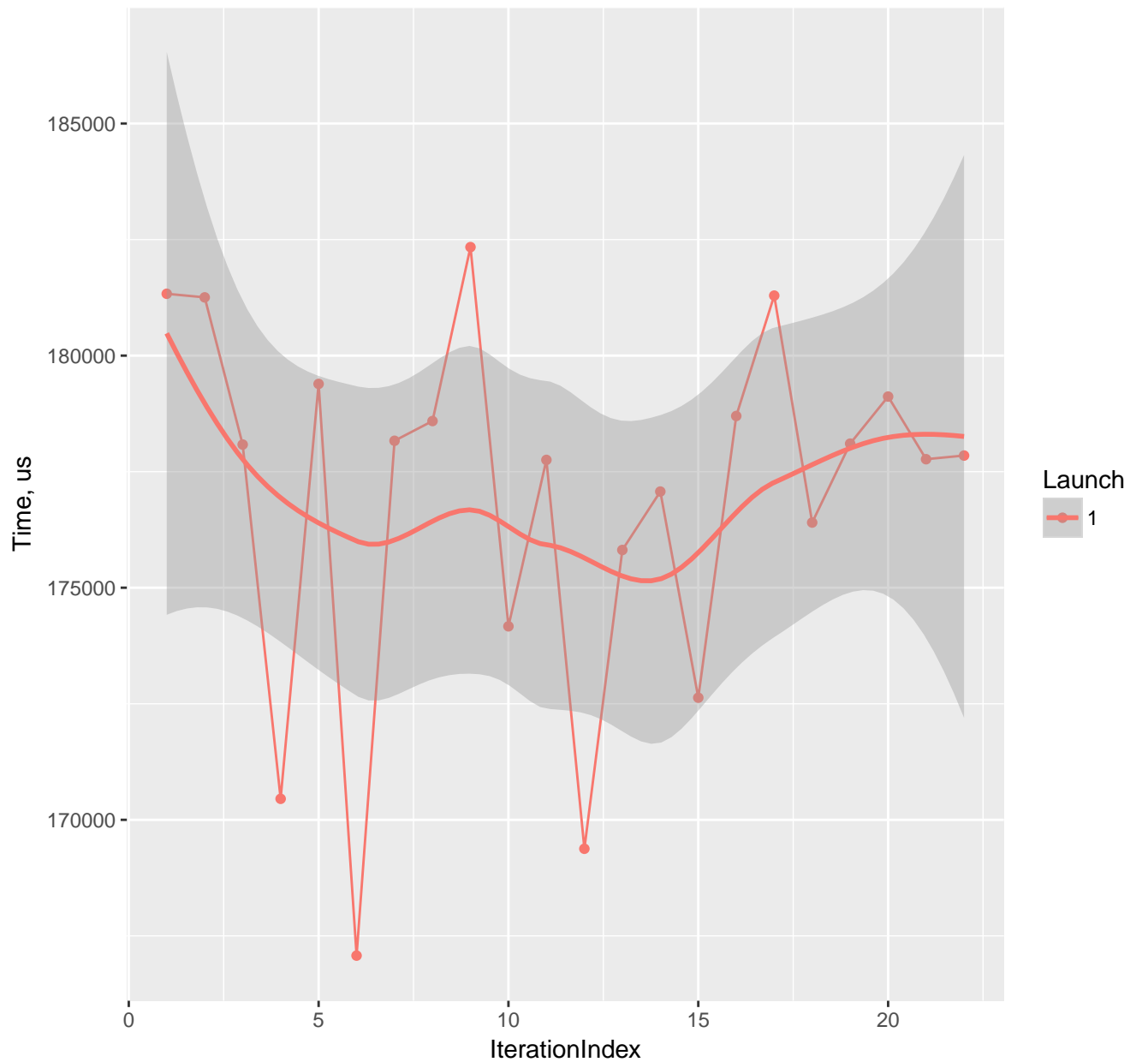
# CpsBenchmark+CpsPerformanceTests / Early /



# CpsBenchmark+CpsPerformanceTests / Early / Default

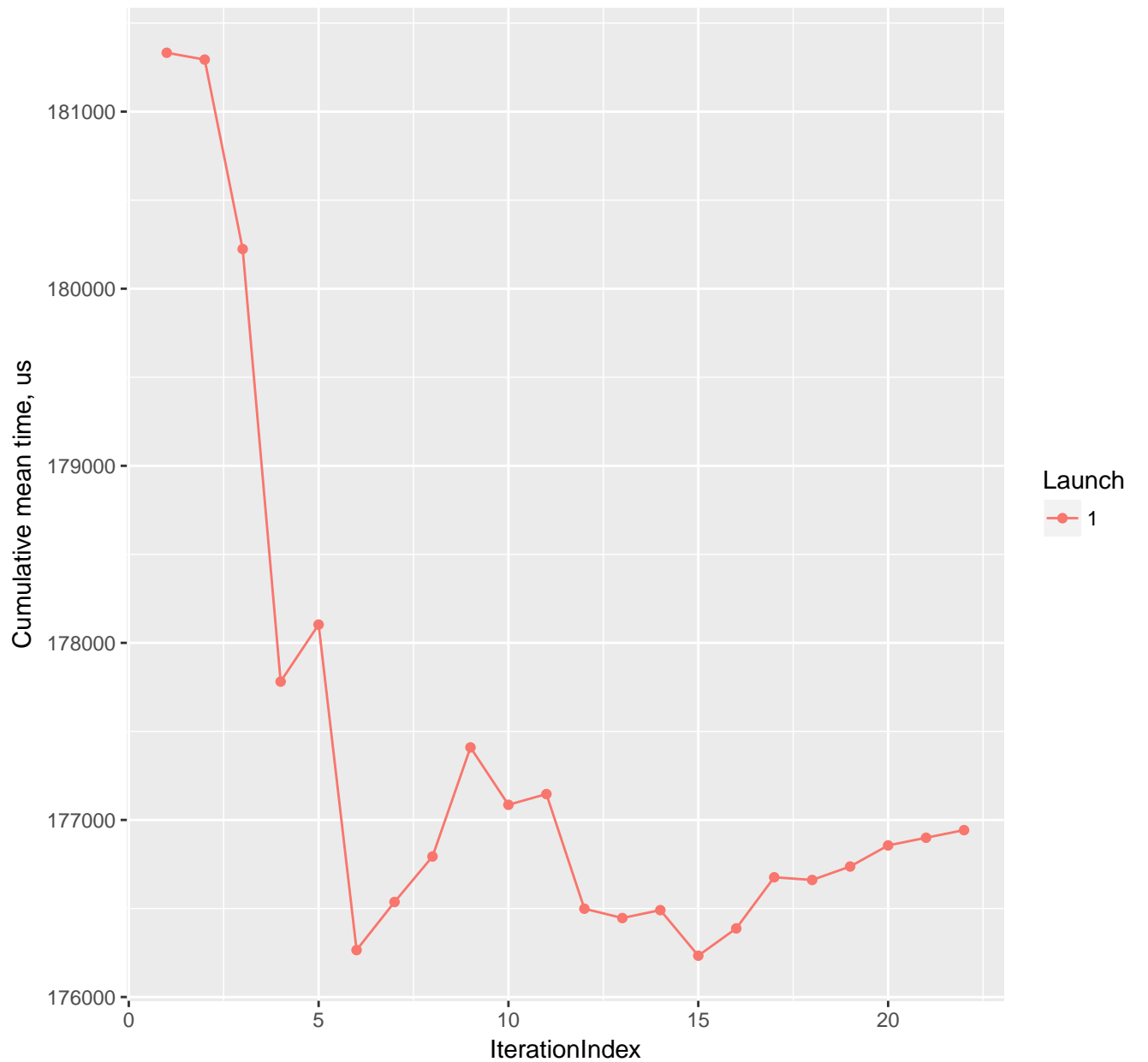


# CpsBenchmark+CpsPerformanceTests / Early / Default

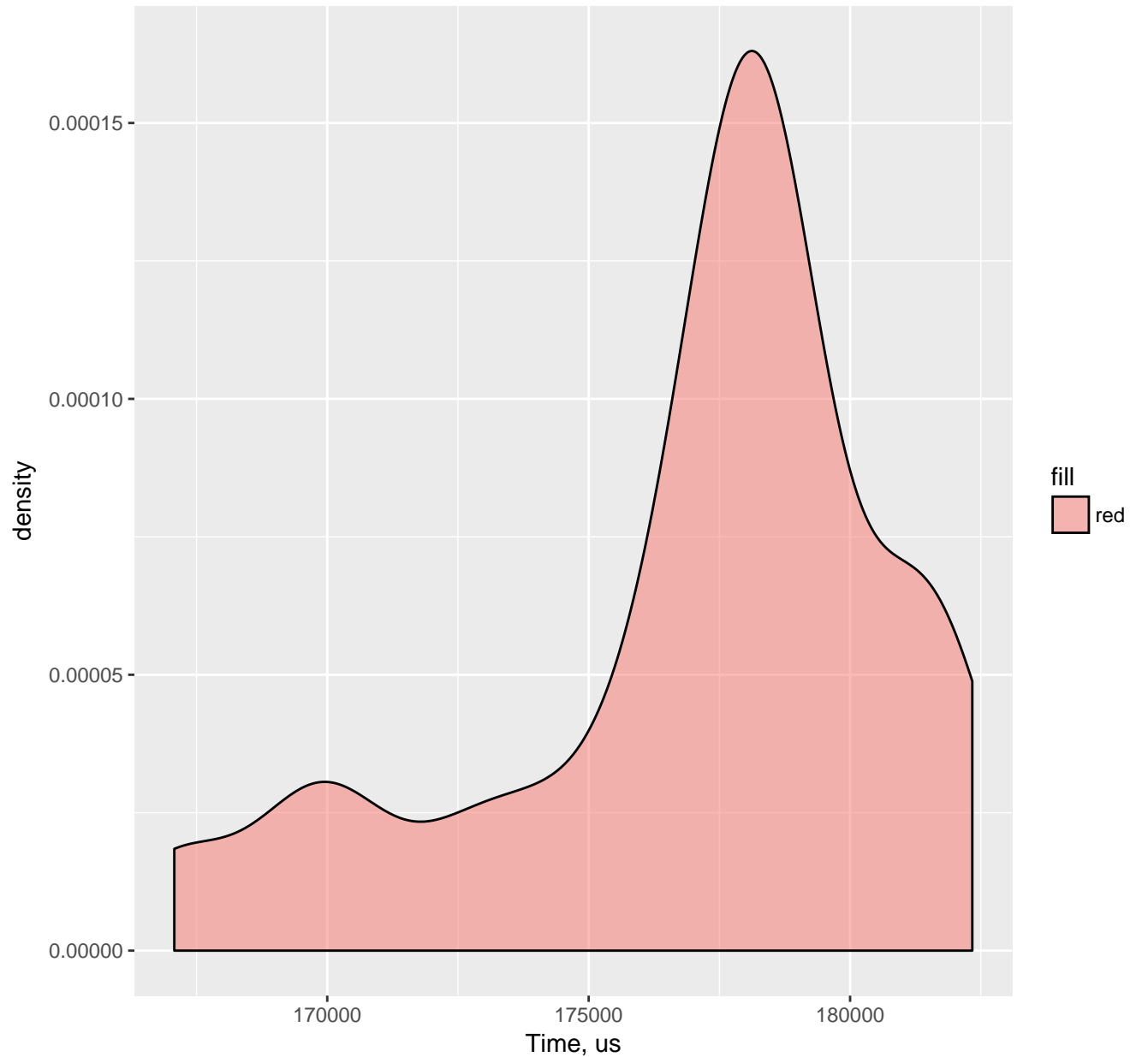




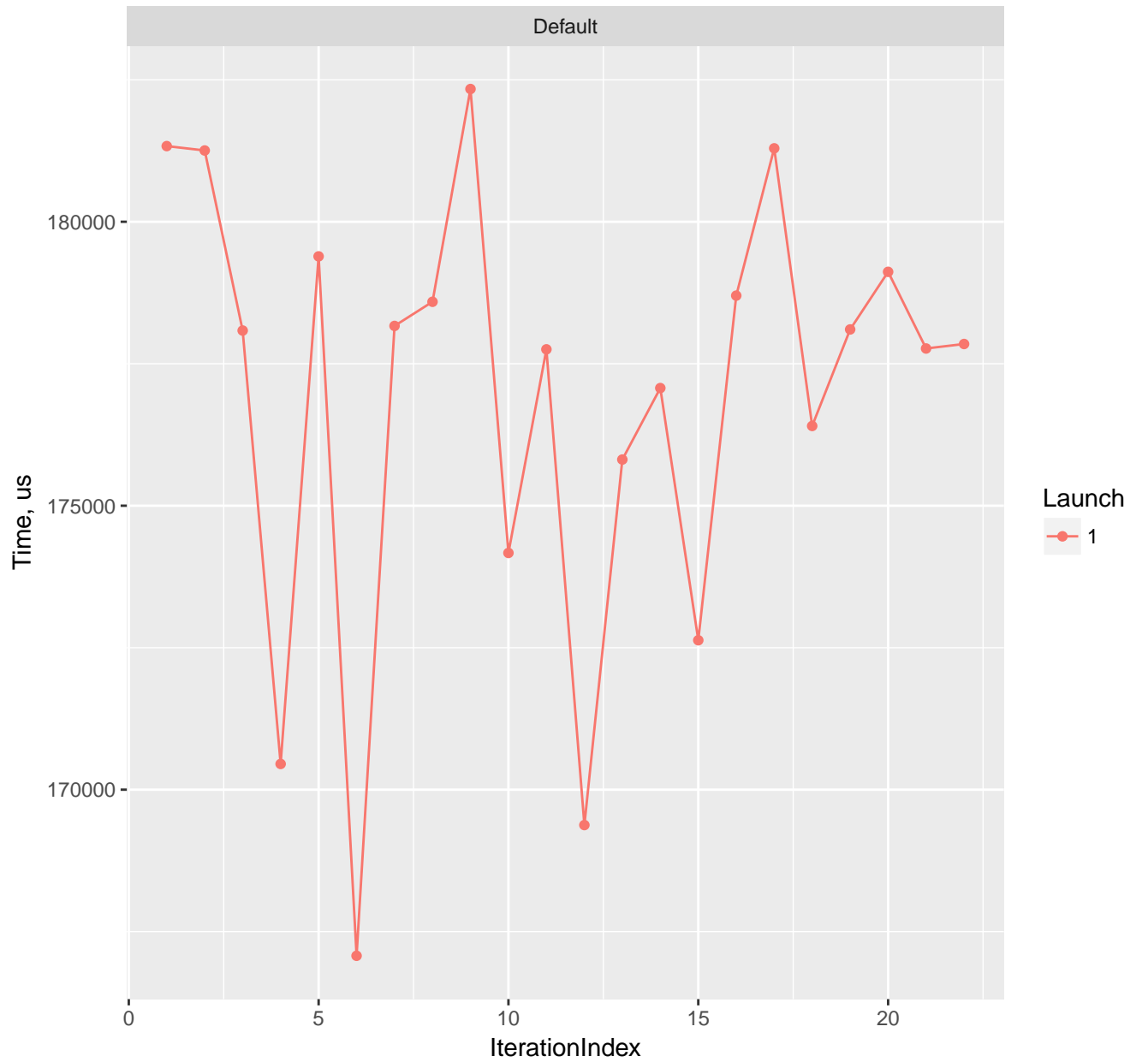
# CpsBenchmark+CpsPerformanceTests / Early / Default



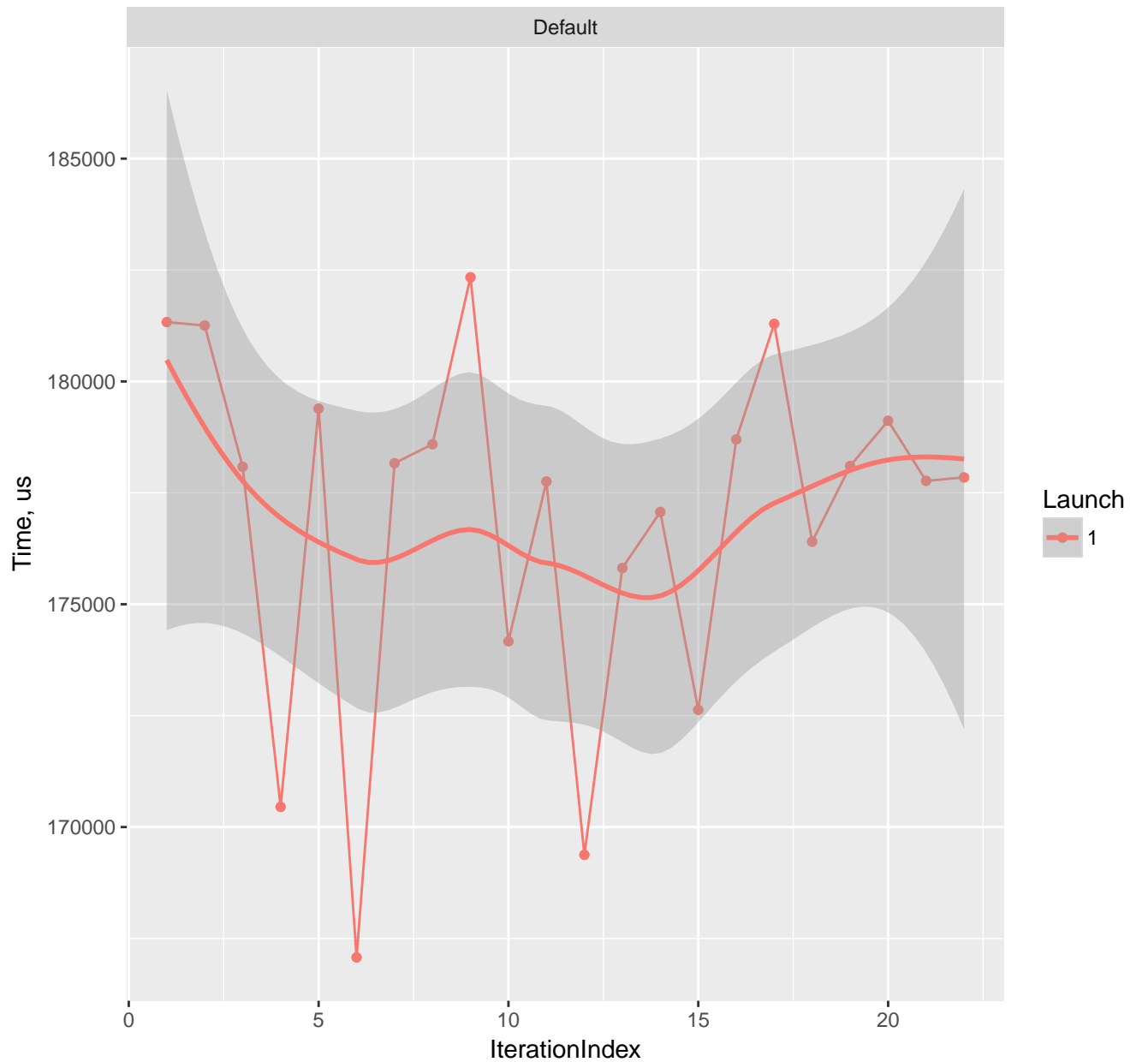
# CpsBenchmark+CpsPerformanceTests / Early / Default



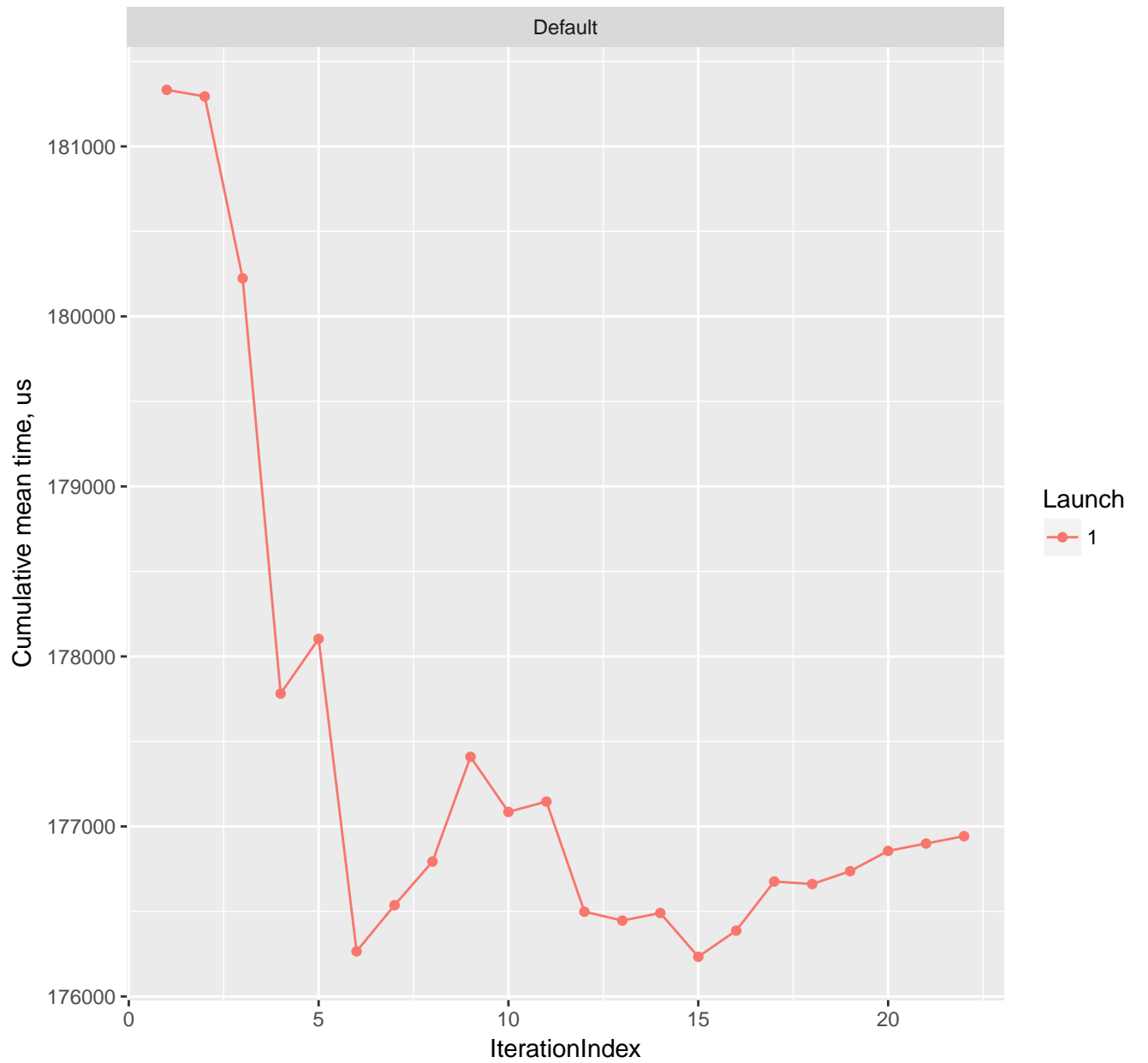
# CpsBenchmark+CpsPerformanceTests / Early



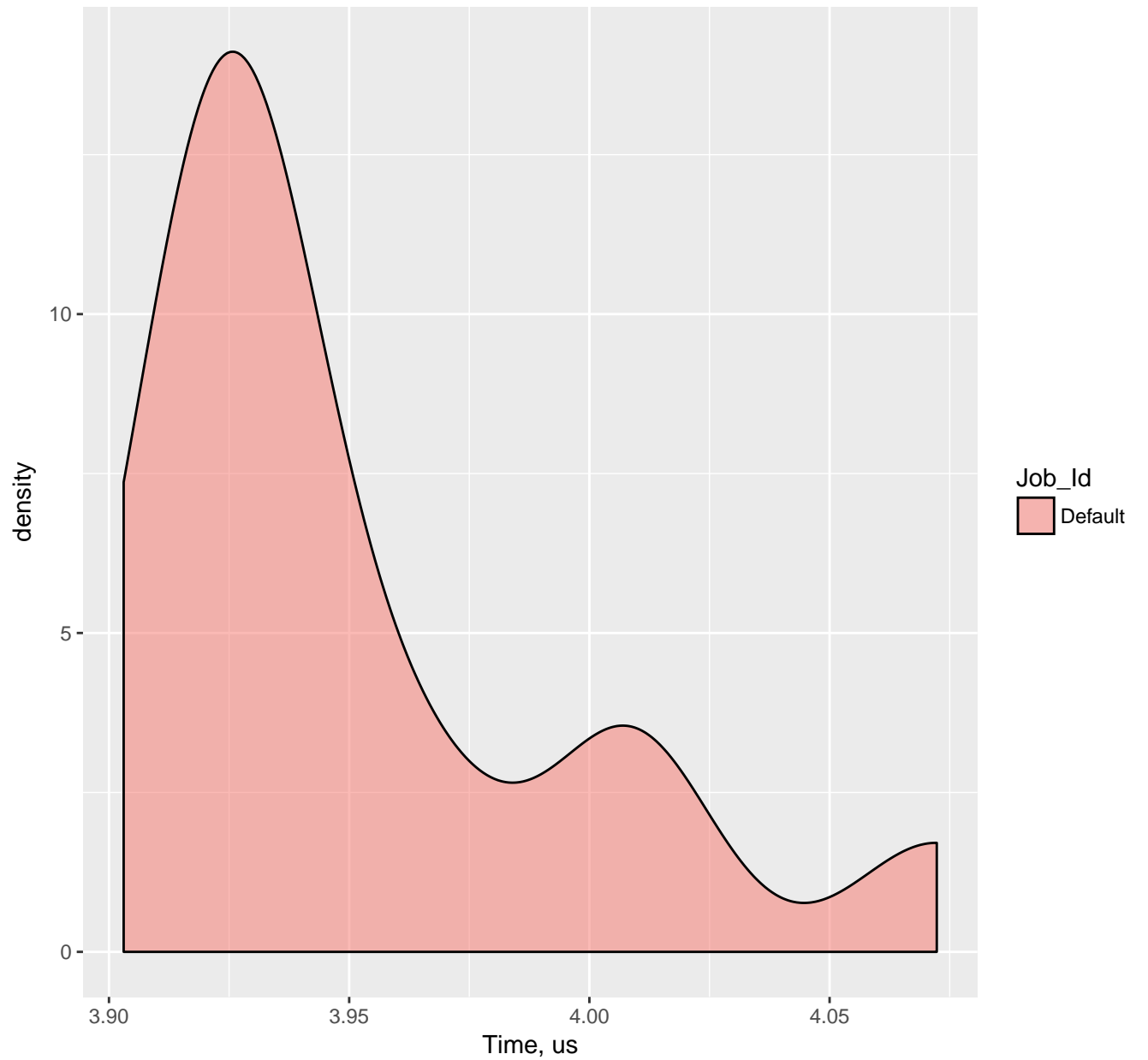
# CpsBenchmark+CpsPerformanceTests / Early



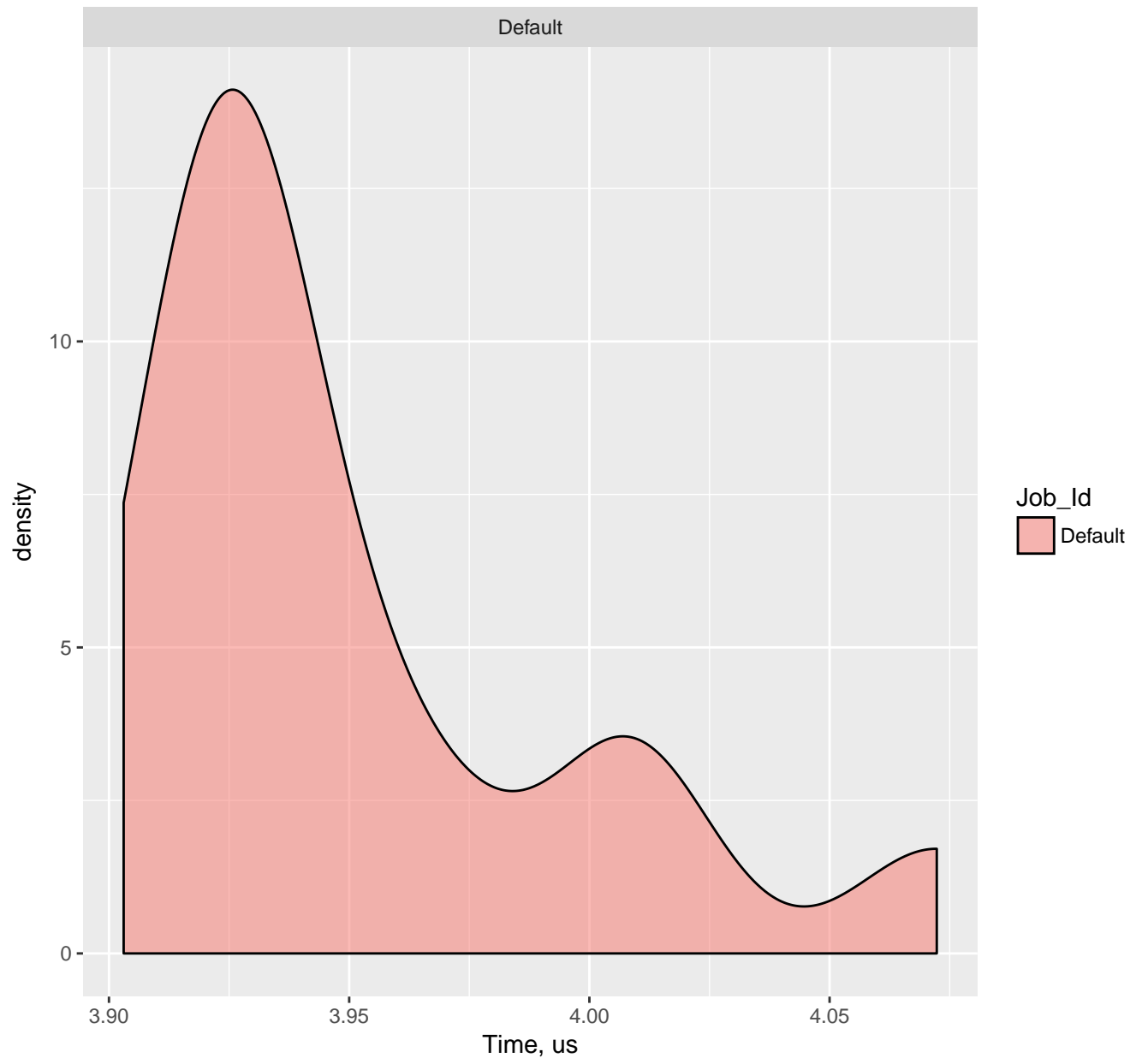
# CpsBenchmark+CpsPerformanceTests / Early

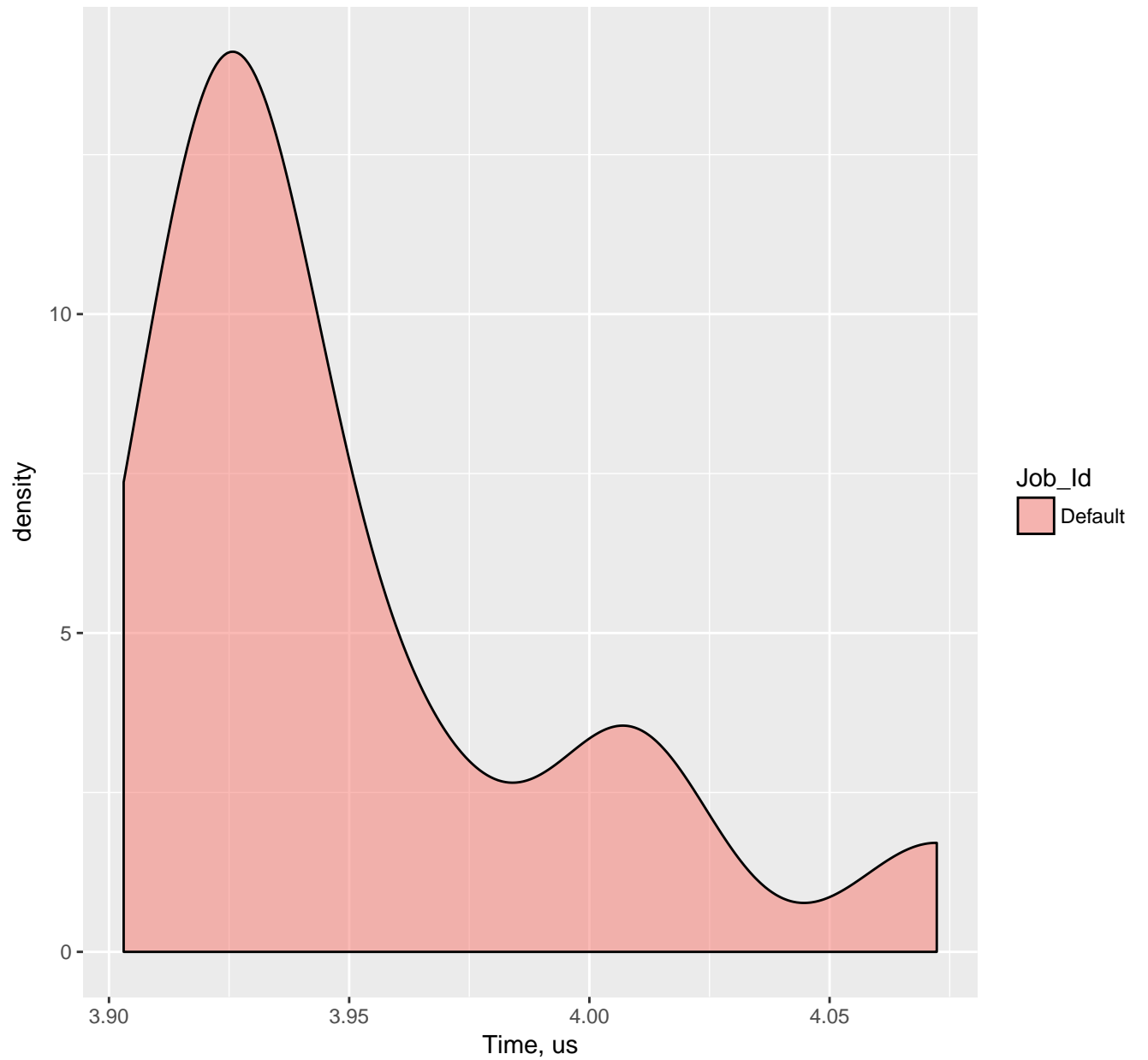


# CpsBenchmark+CpsPerformanceTests / EarlyCPS

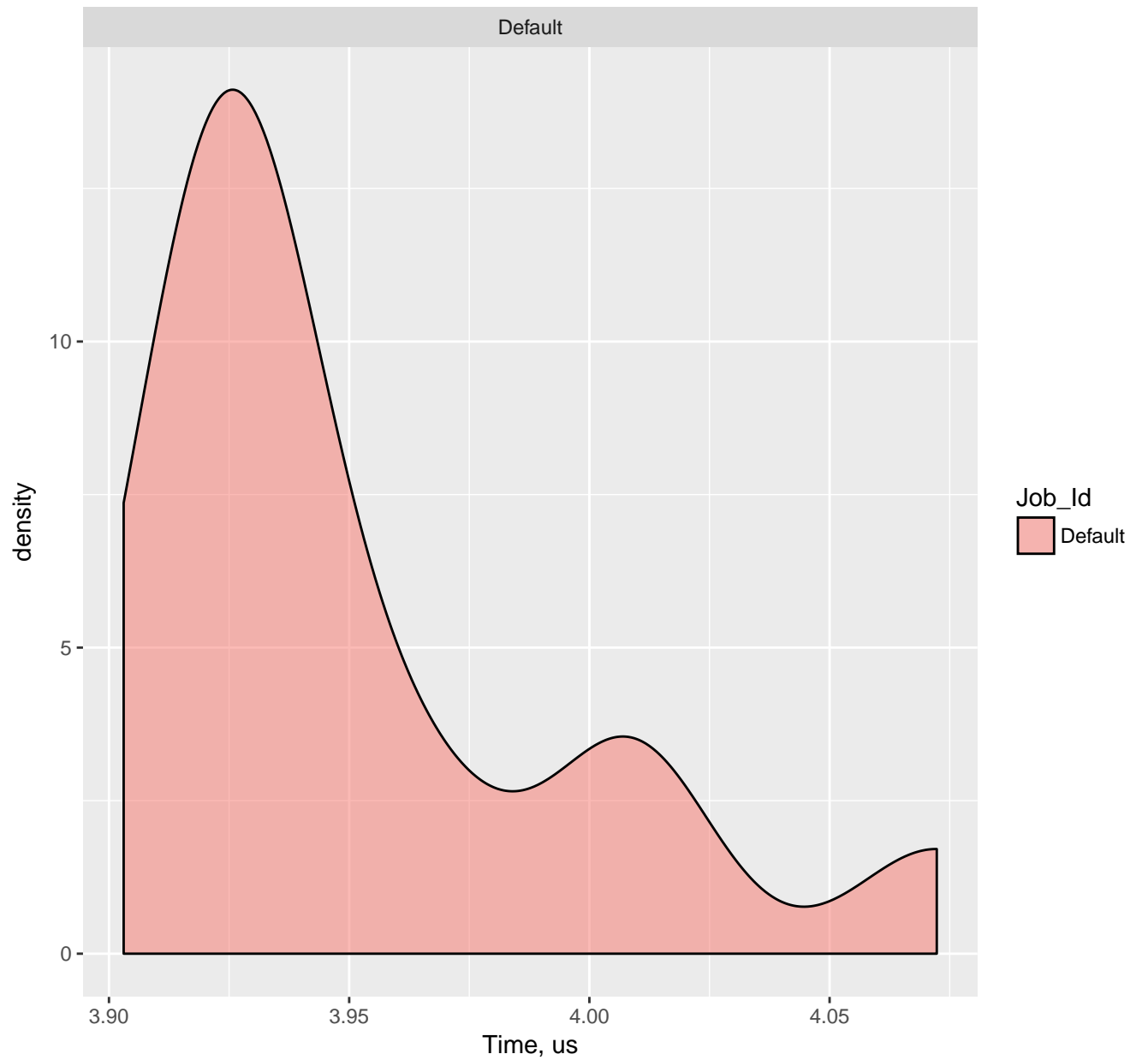


# CpsBenchmark+CpsPerformanceTests / EarlyCPS

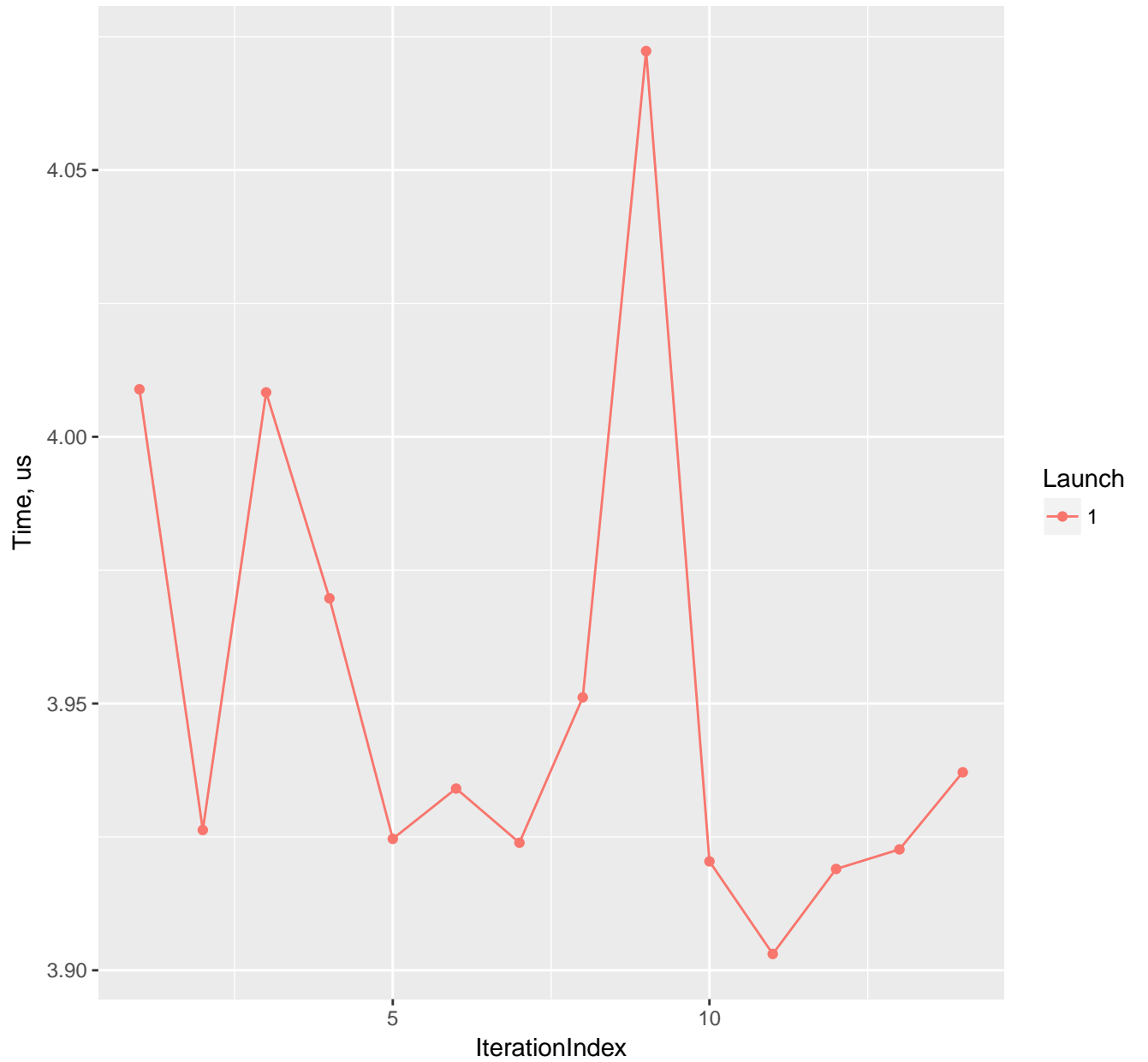


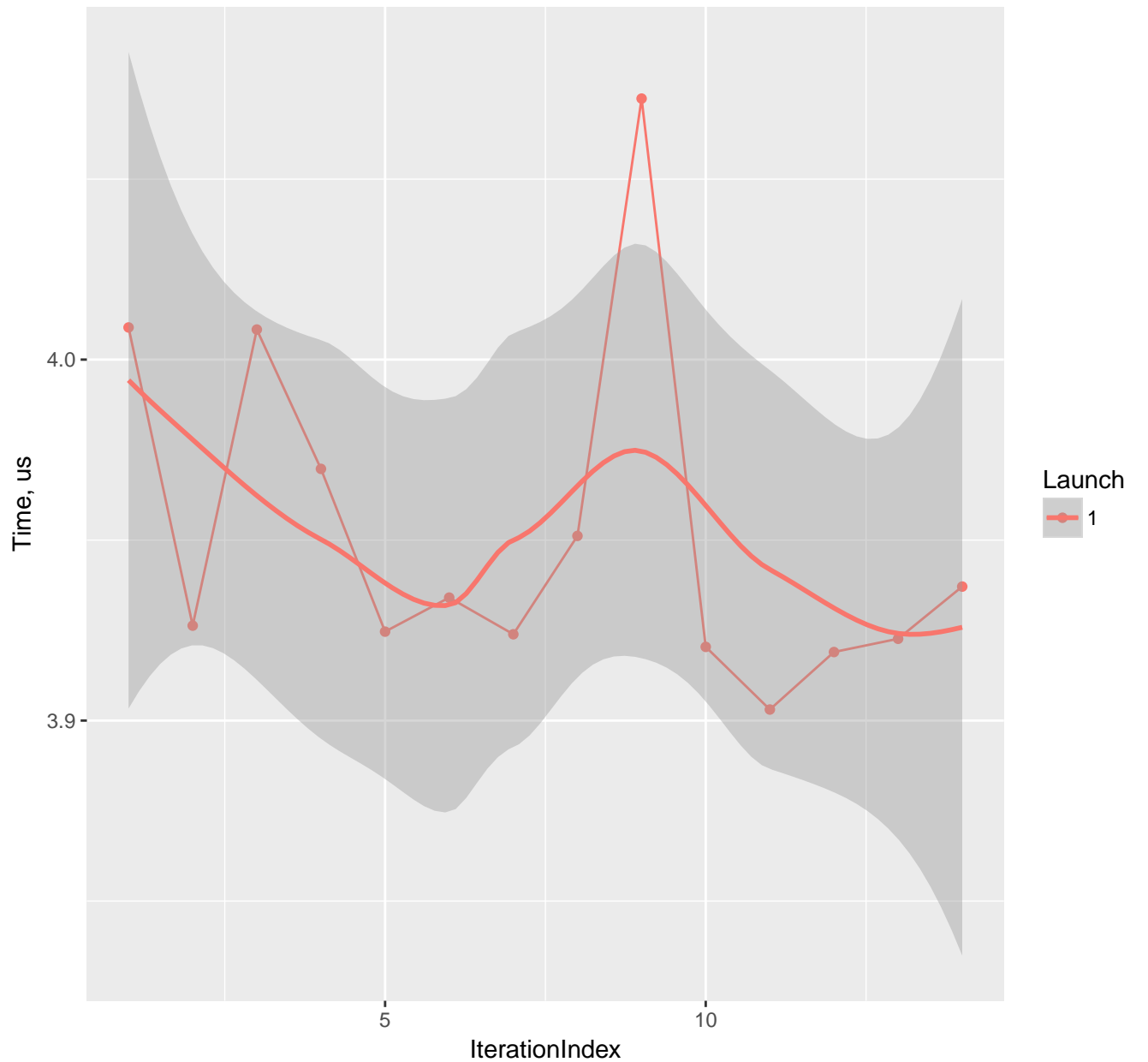




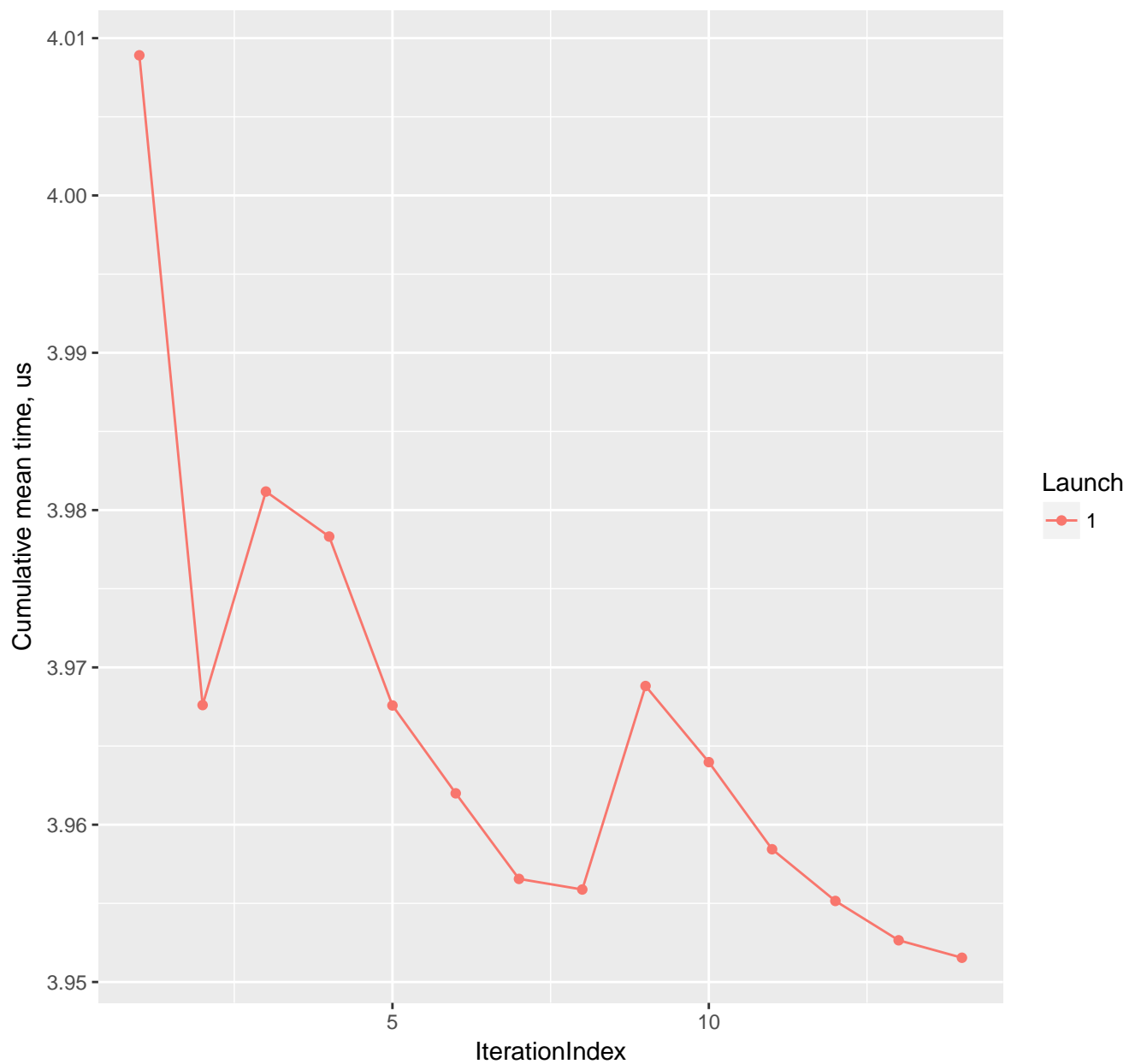


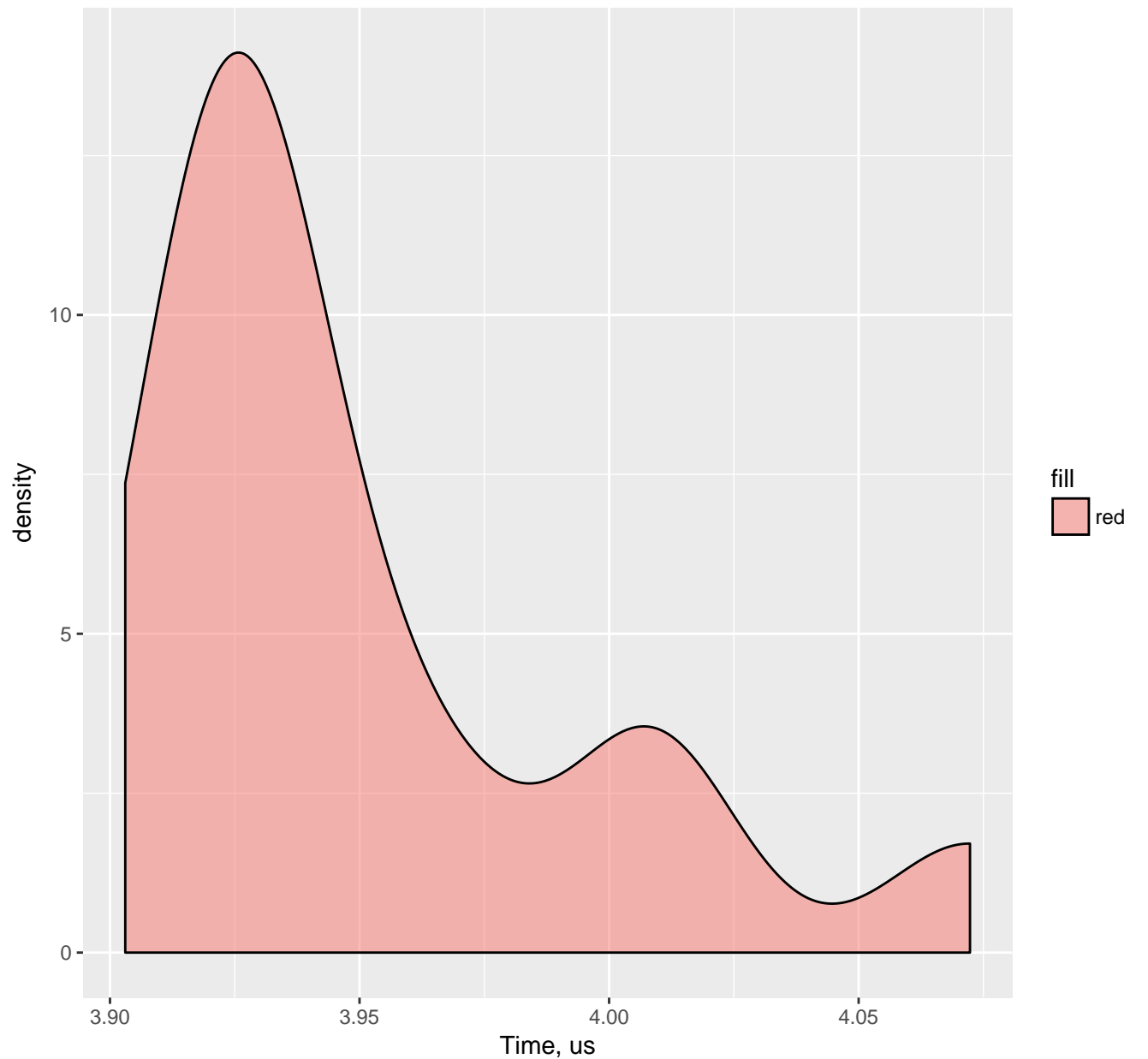
# CpsBenchmark+CpsPerformanceTests / EarlyCPS / Default



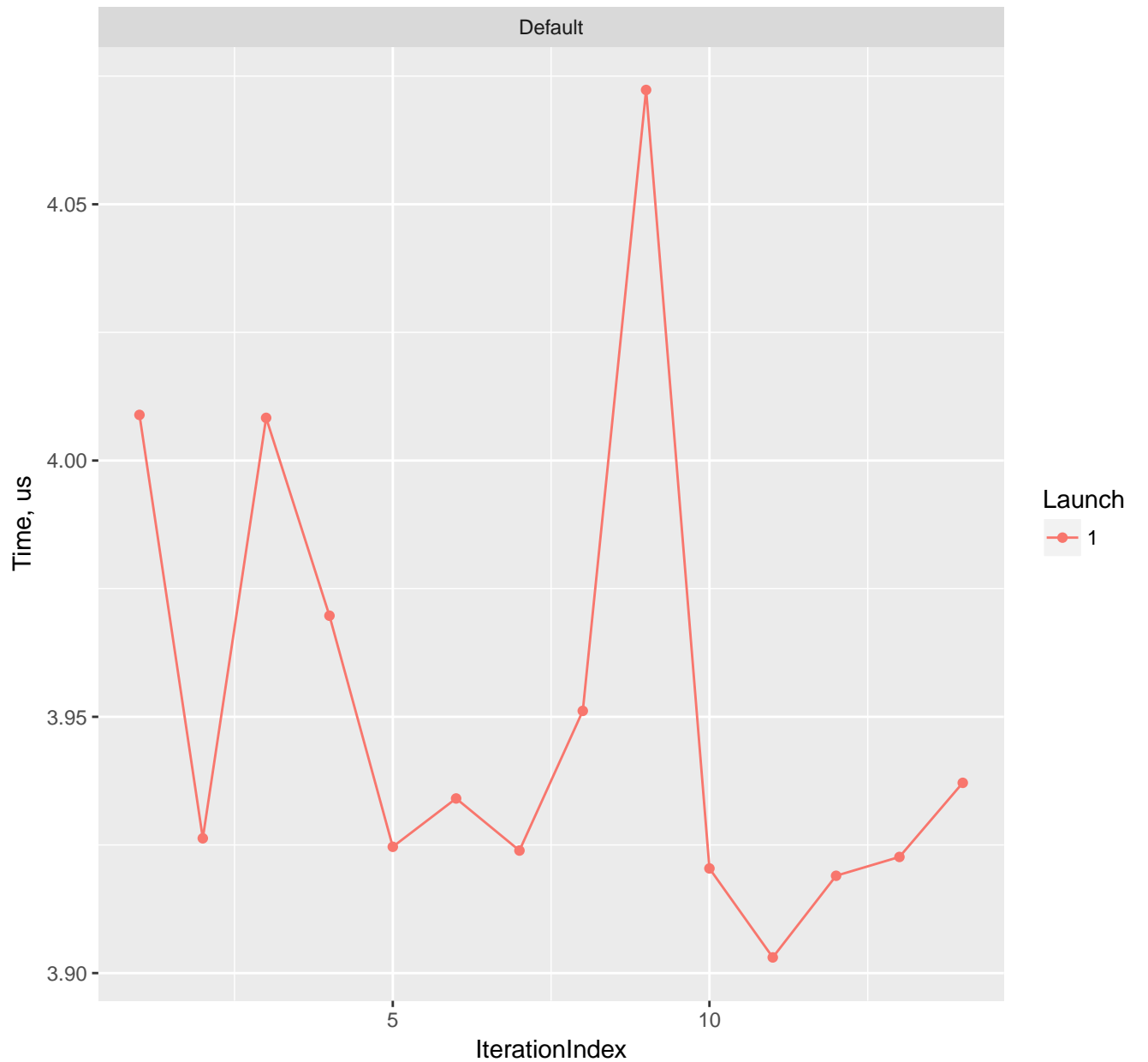


# CpsBenchmark+CpsPerformanceTests / EarlyCPS / Default

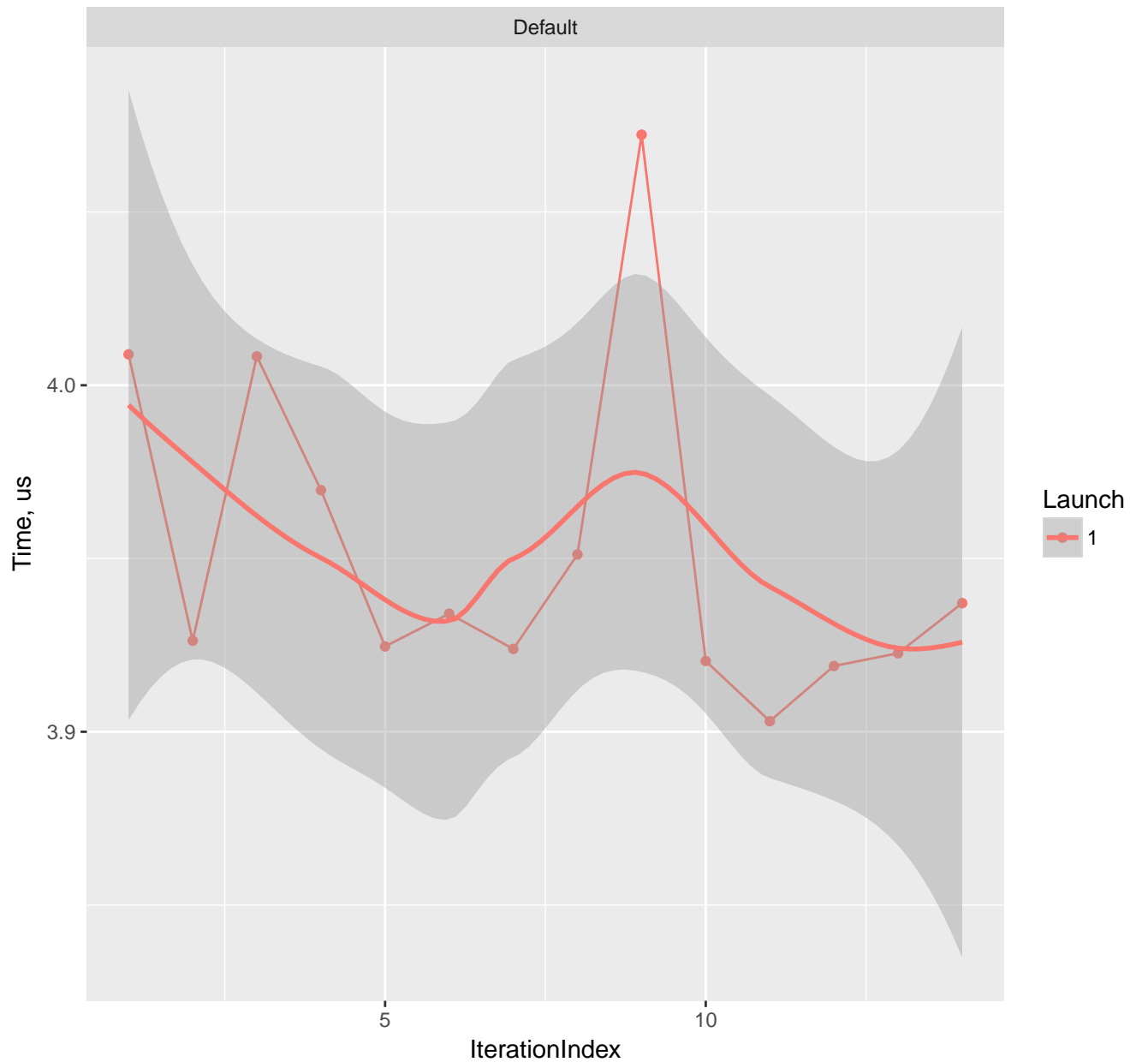




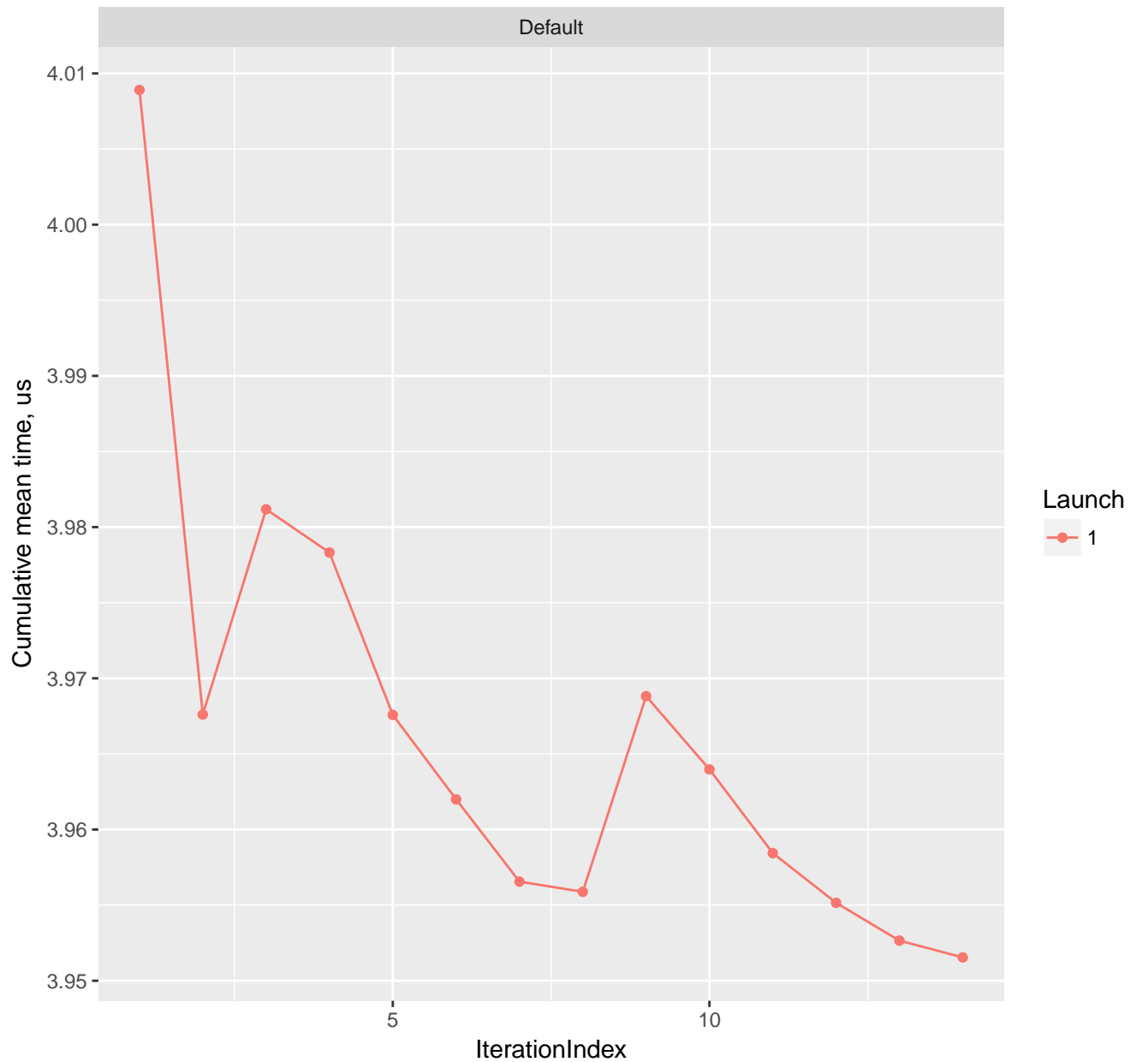
# CpsBenchmark+CpsPerformanceTests / EarlyCPS



# CpsBenchmark+CpsPerformanceTests / EarlyCPS

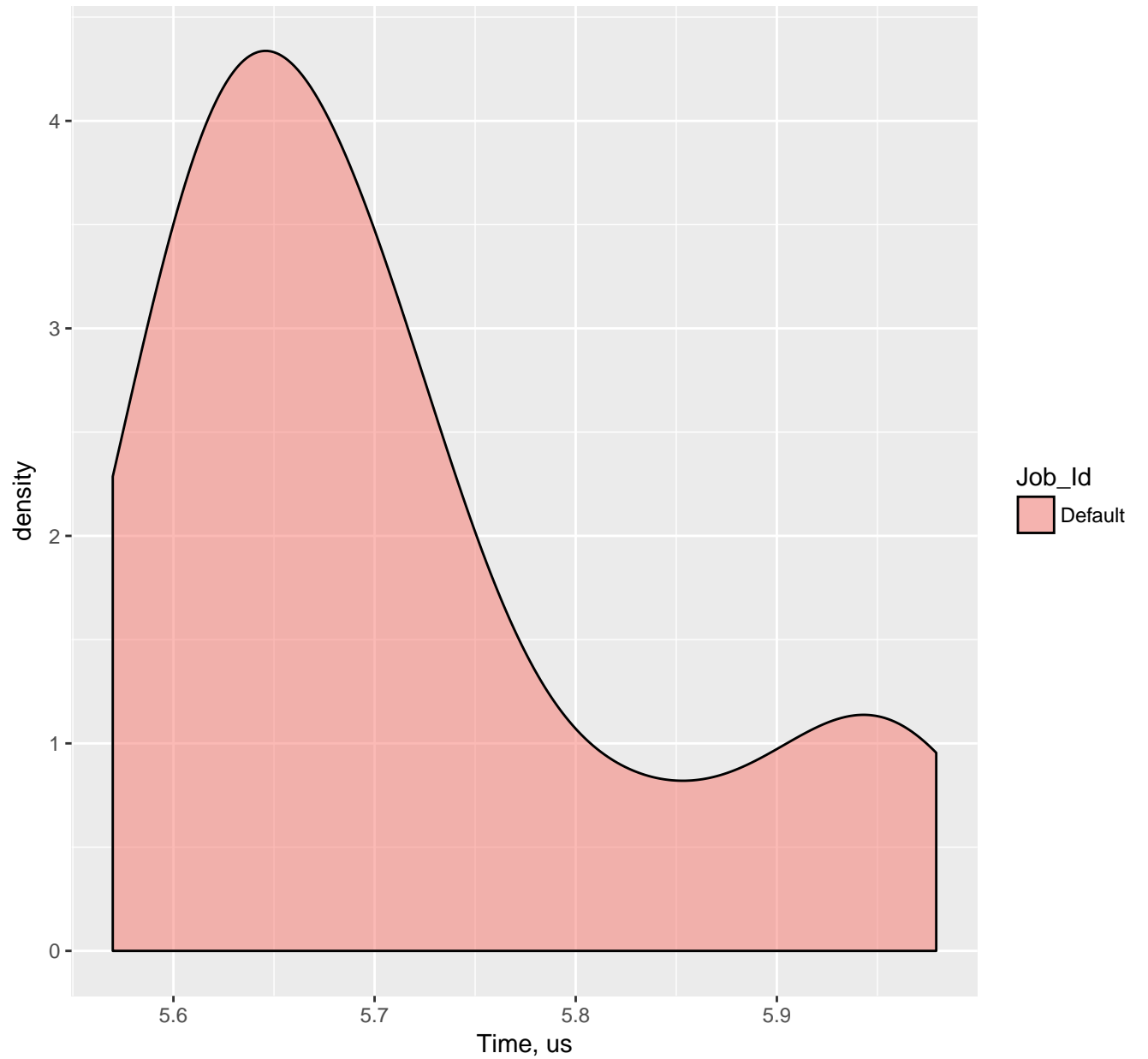


# CpsBenchmark+CpsPerformanceTests / EarlyCPS

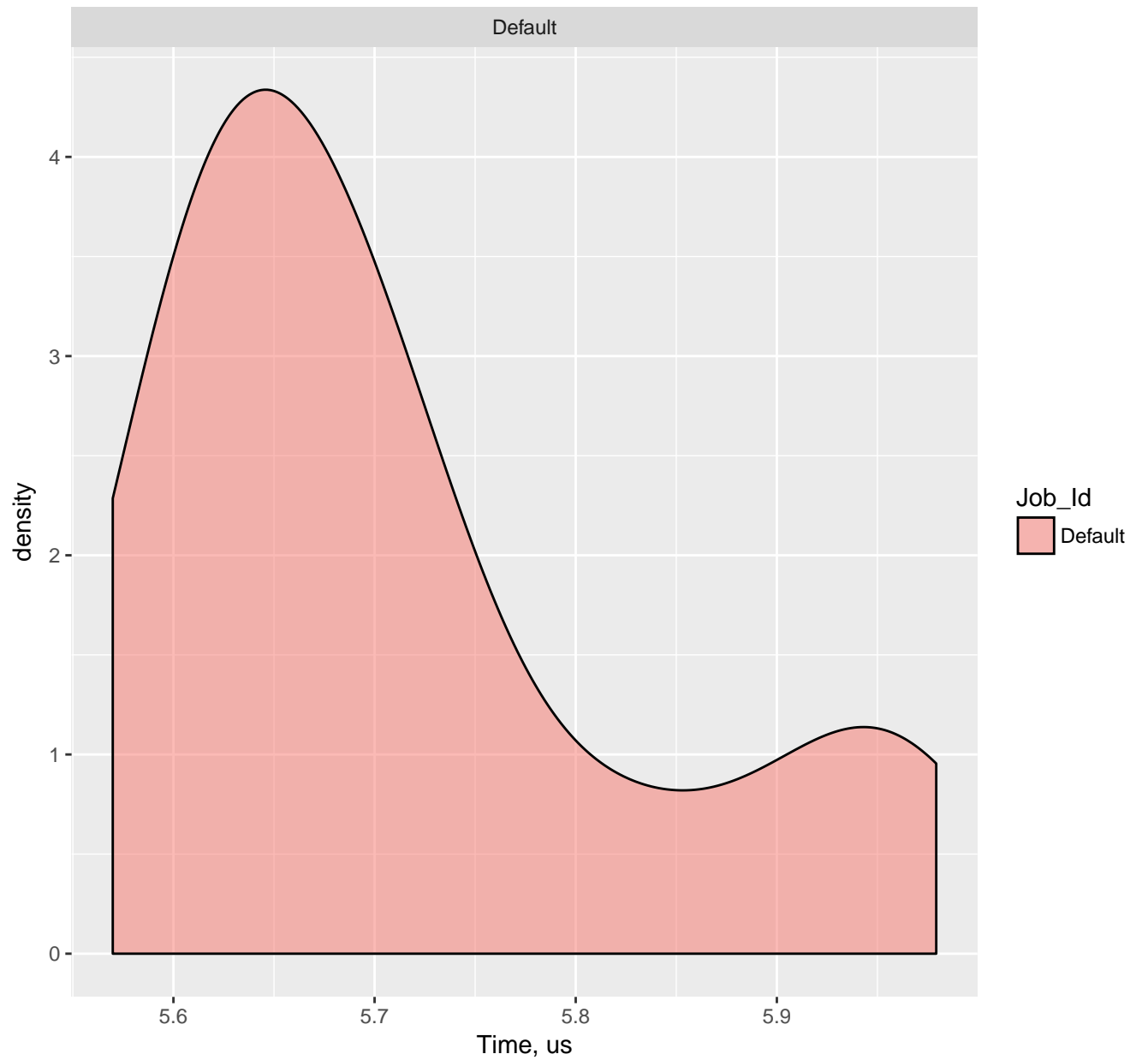


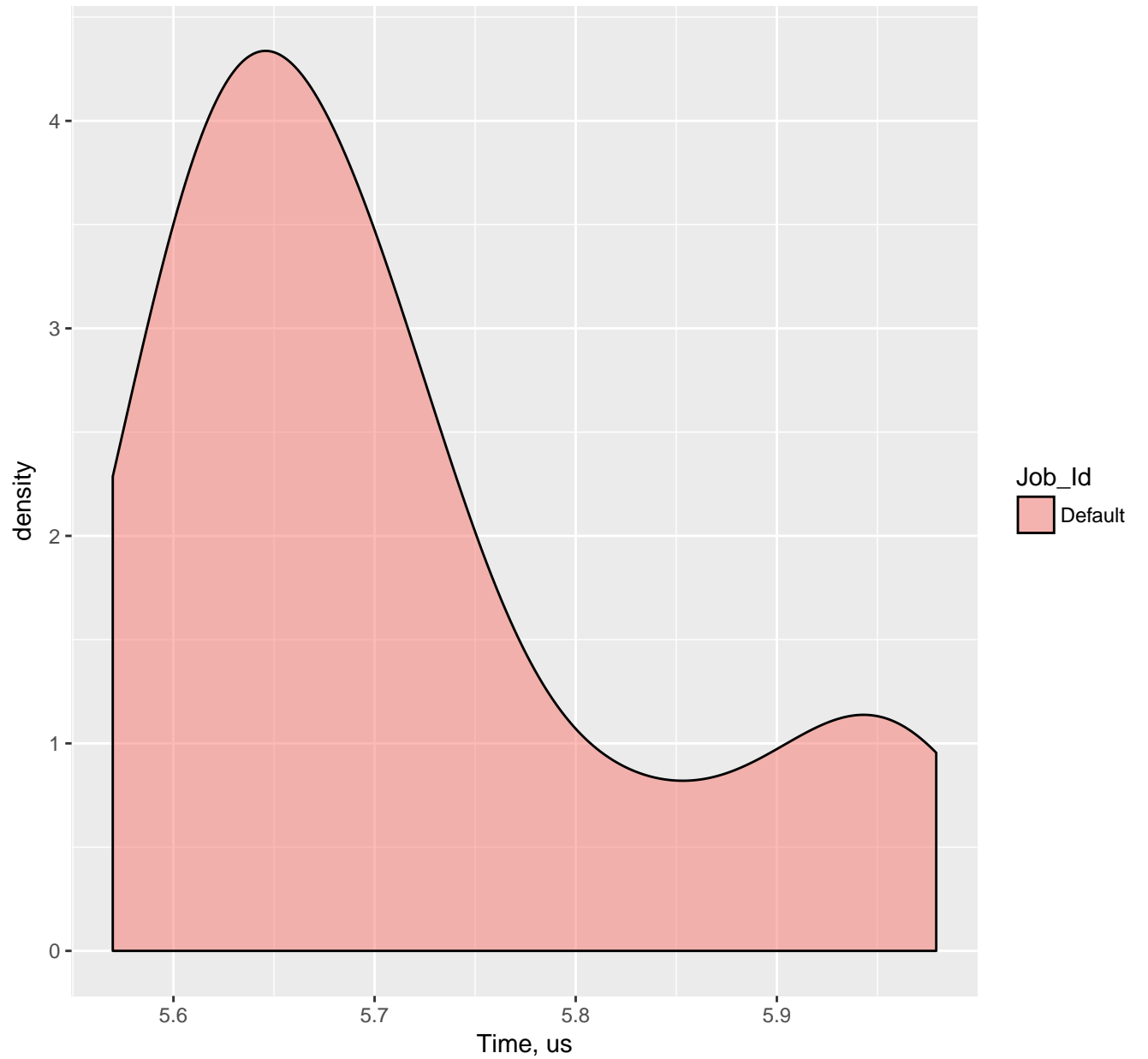


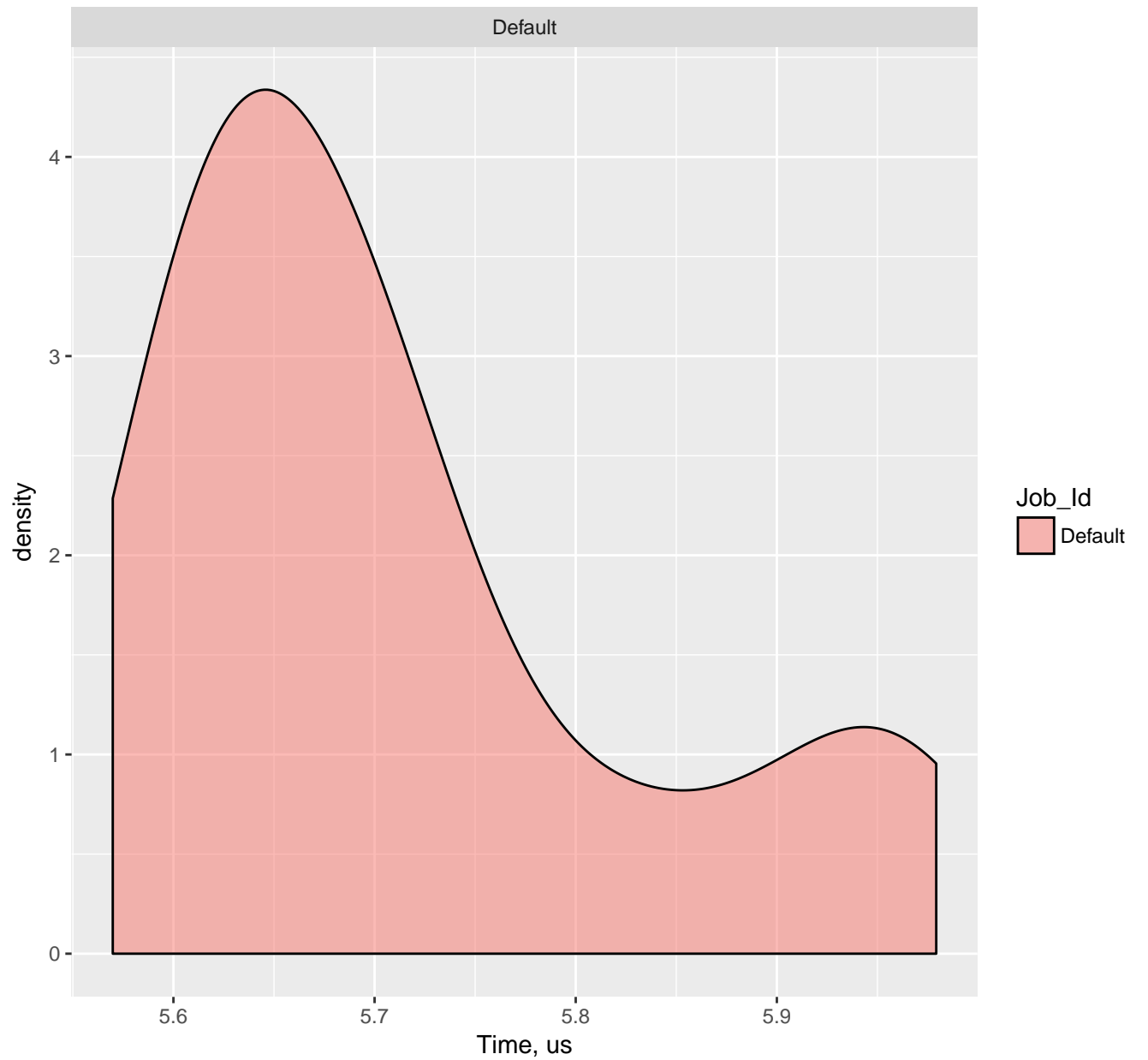
# CpsBenchmark+CpsPerformanceTests / EarlyAvm2



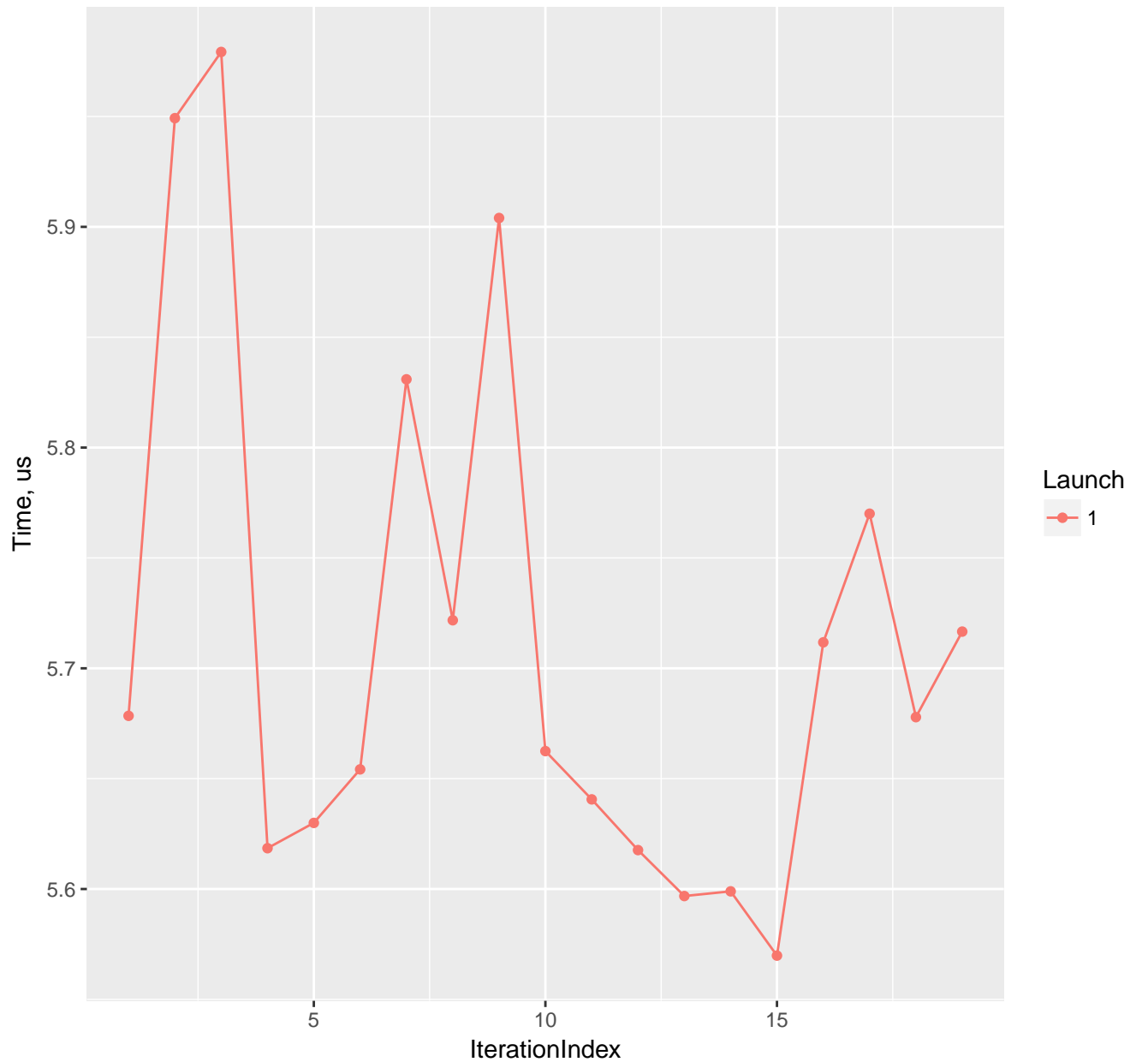
# CpsBenchmark+CpsPerformanceTests / EarlyAvm2



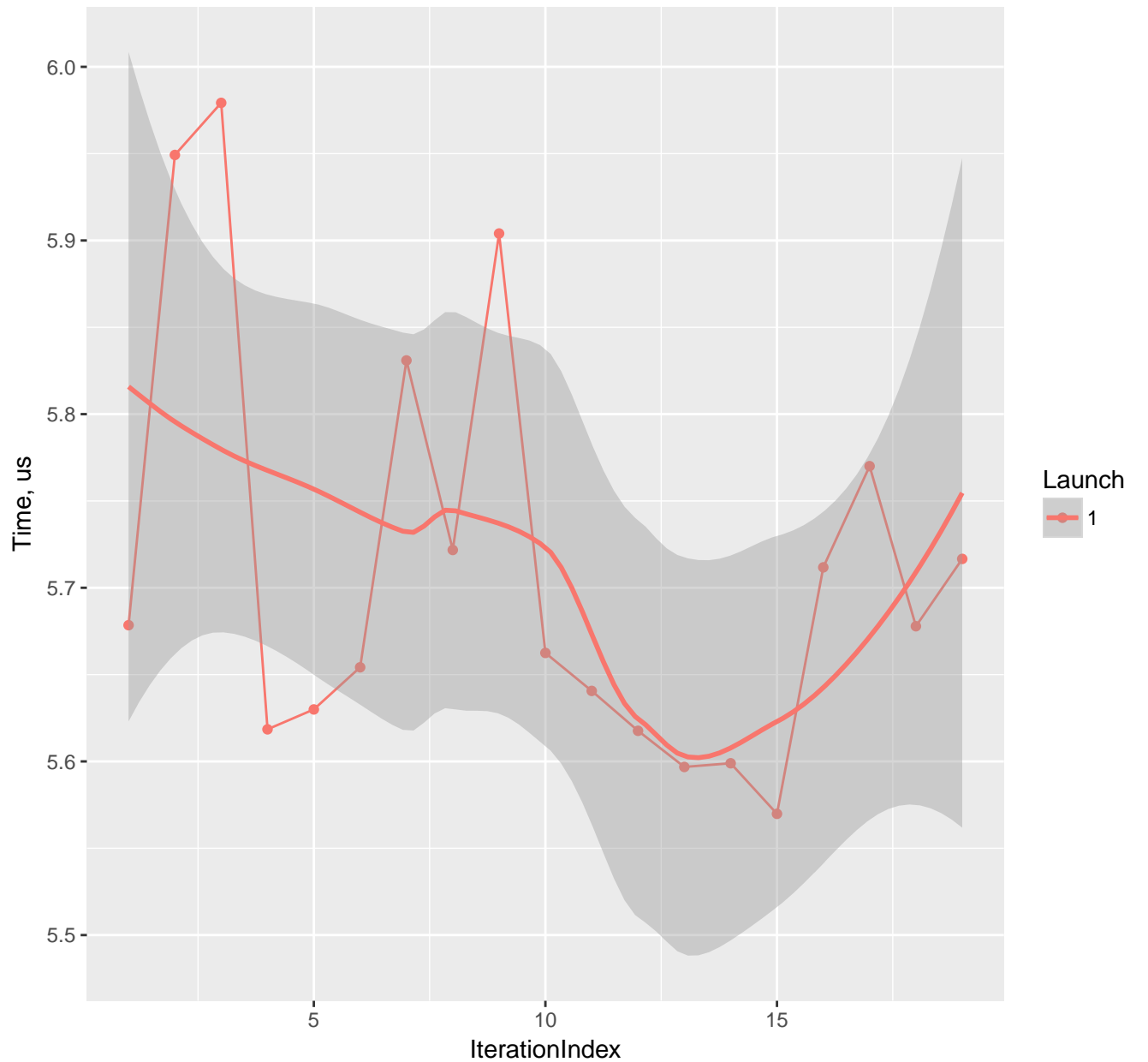




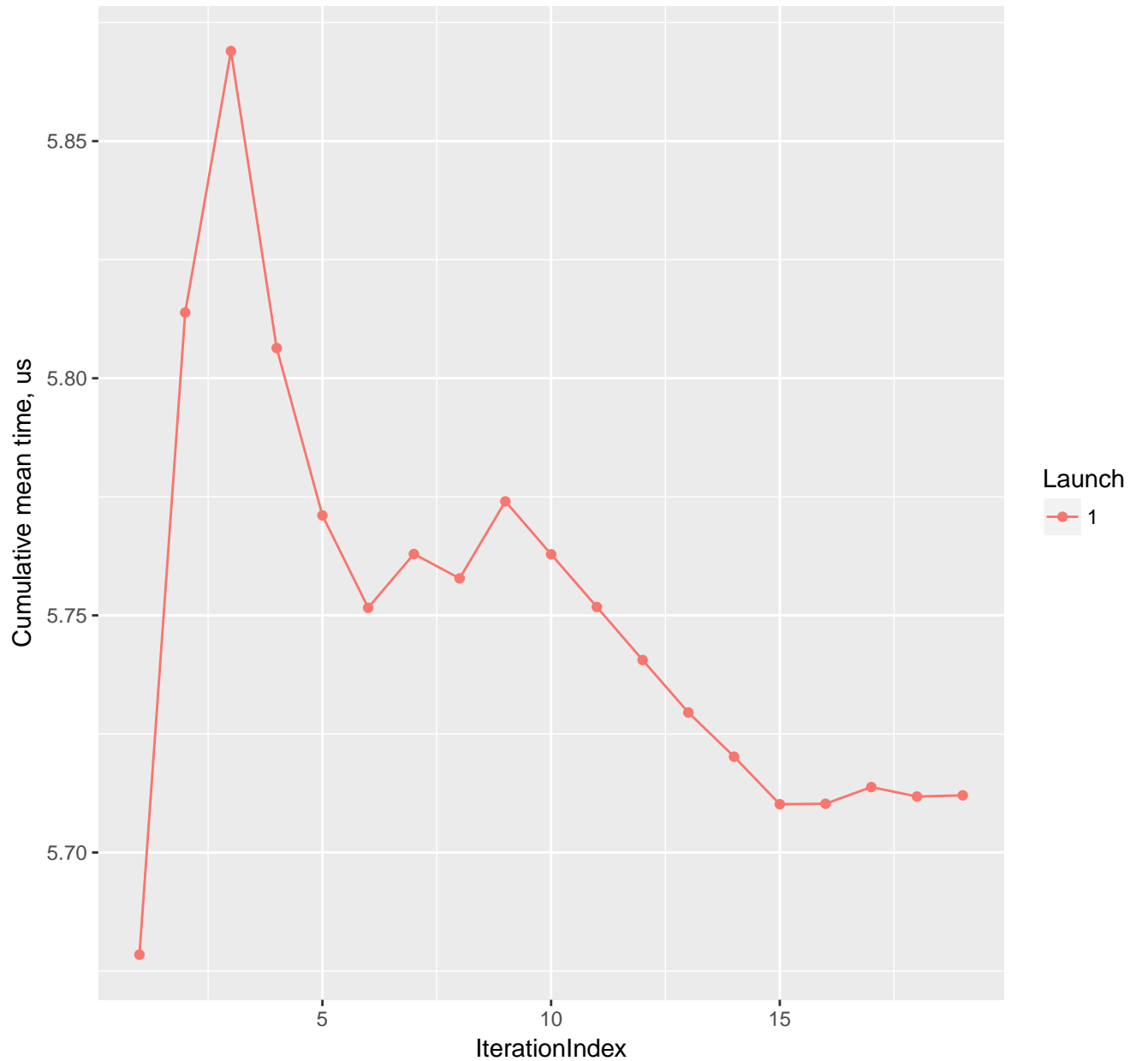
# CpsBenchmark+CpsPerformanceTests / EarlyAvm2 / Default

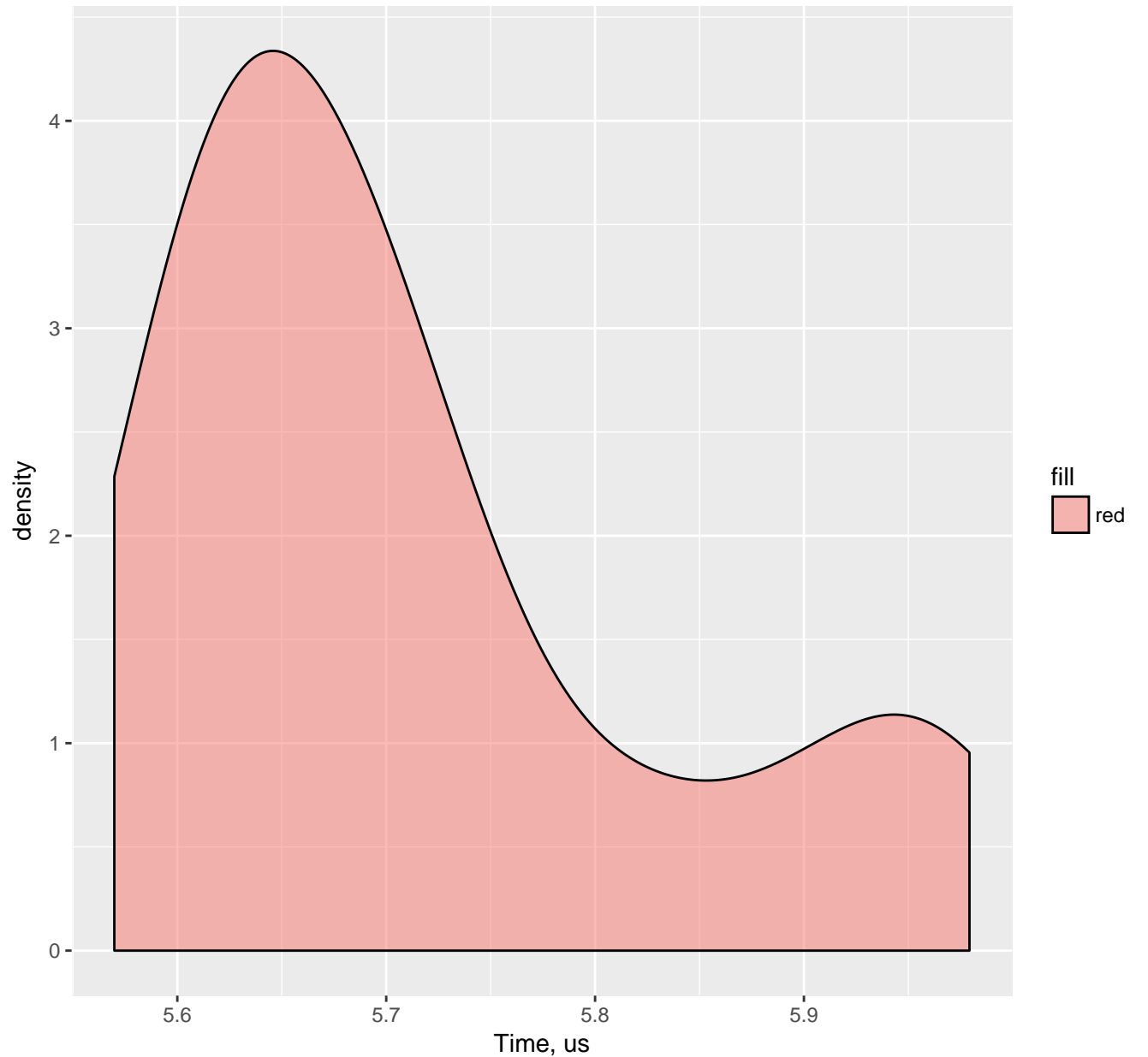


# CpsBenchmark+CpsPerformanceTests / EarlyAvm2 / Default



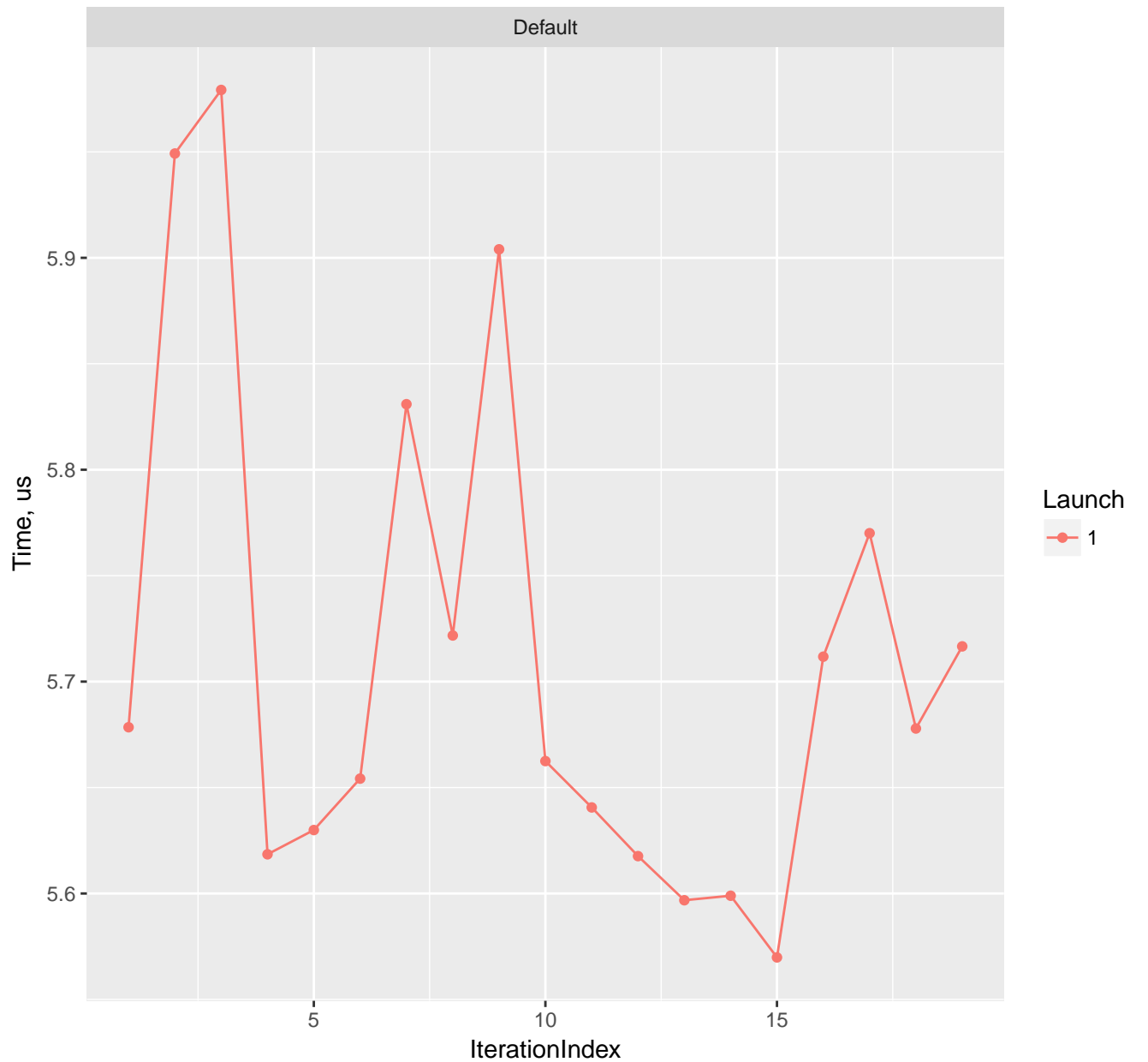
# CpsBenchmark+CpsPerformanceTests / EarlyAvm2 / Default



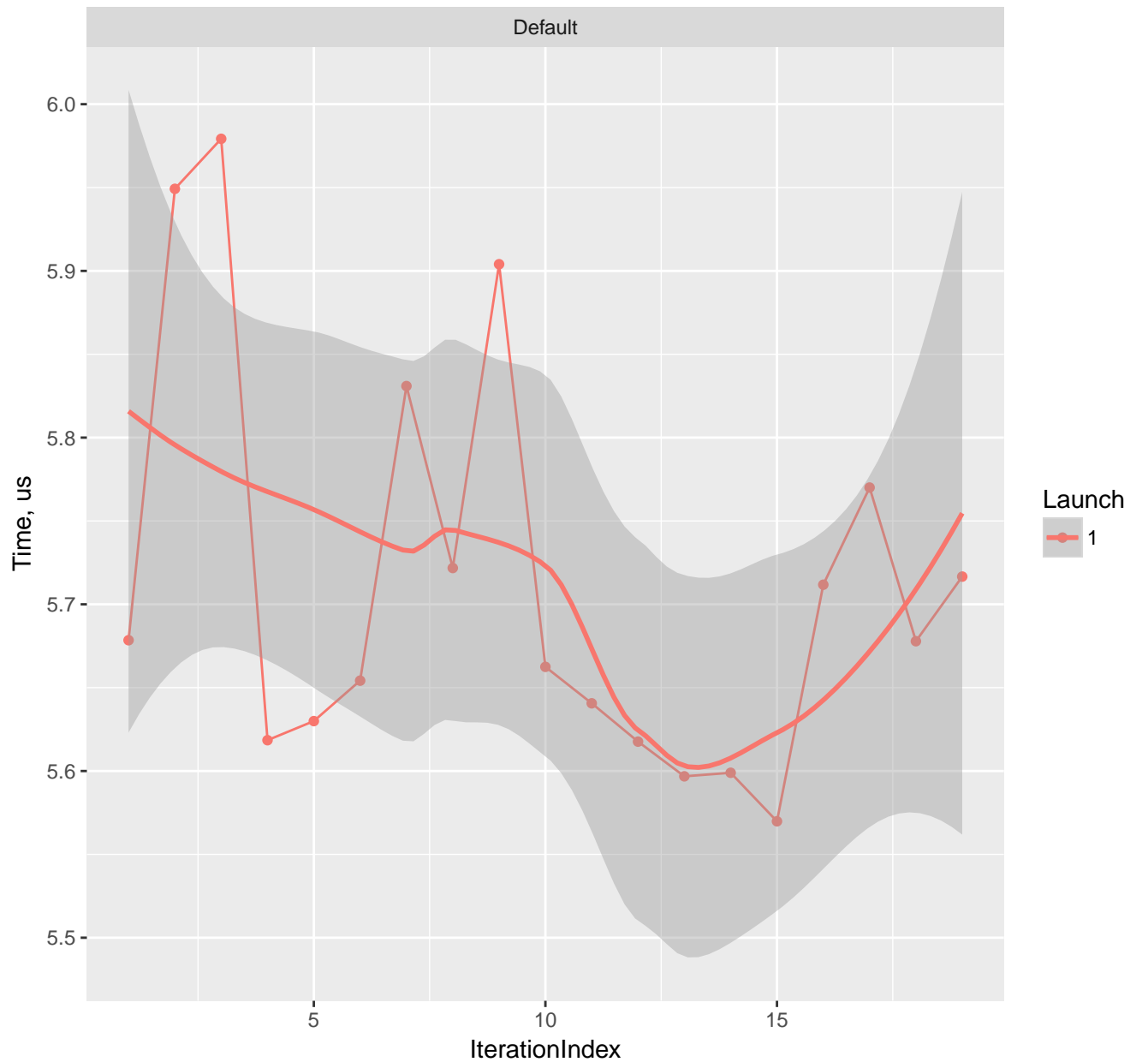




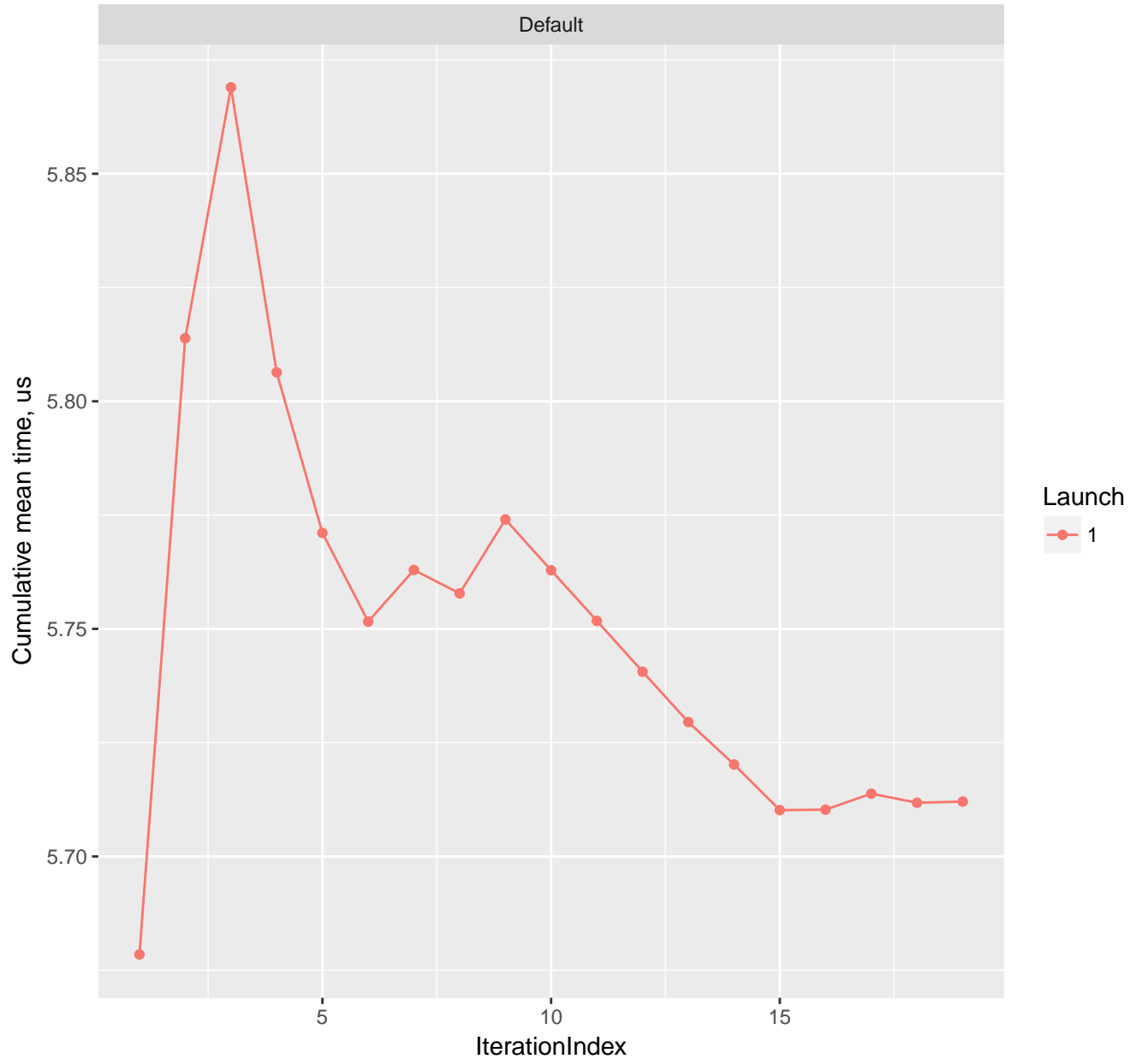
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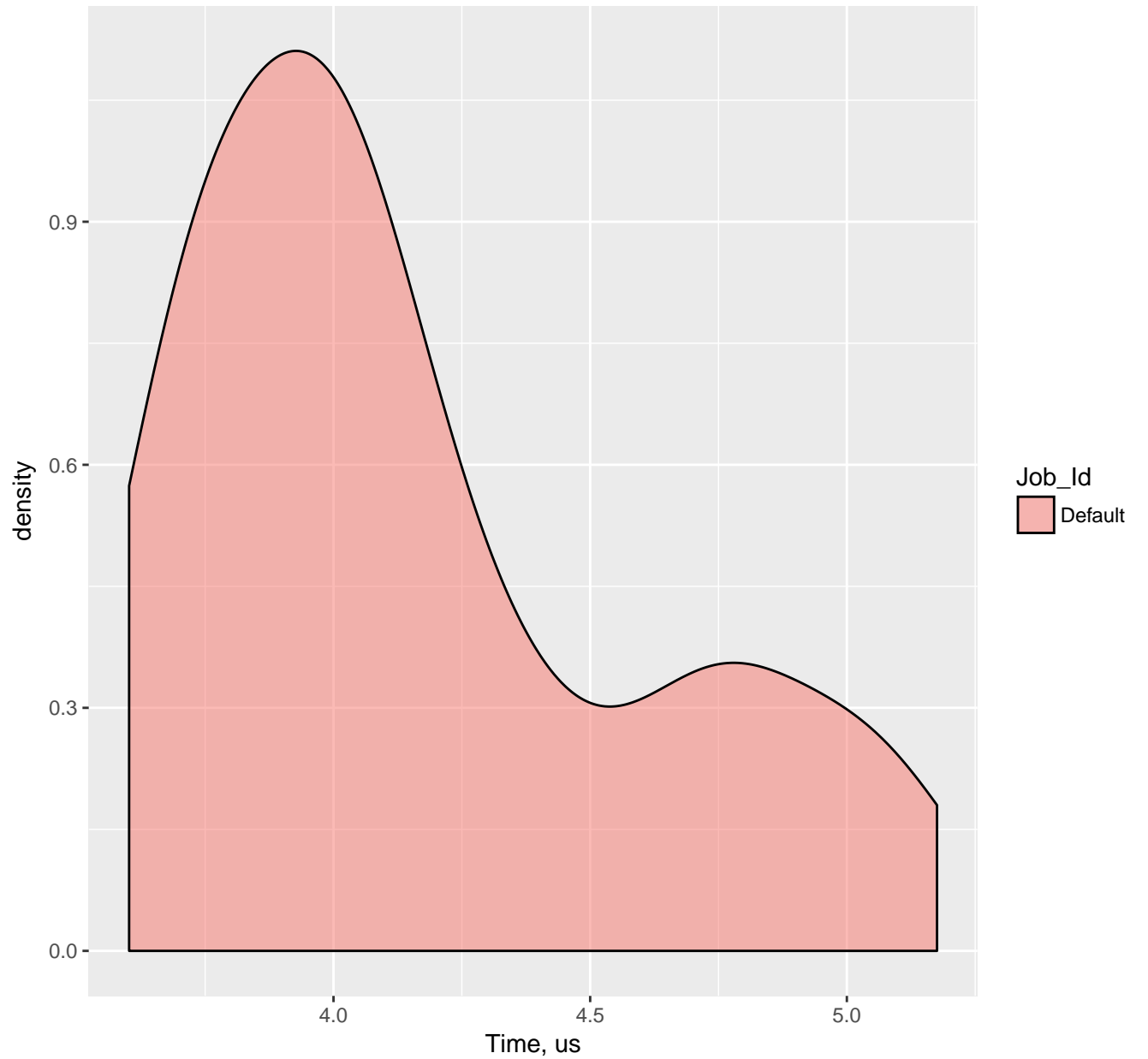


# CpsBenchmark+CpsPerformanceTests / EarlyAvm2

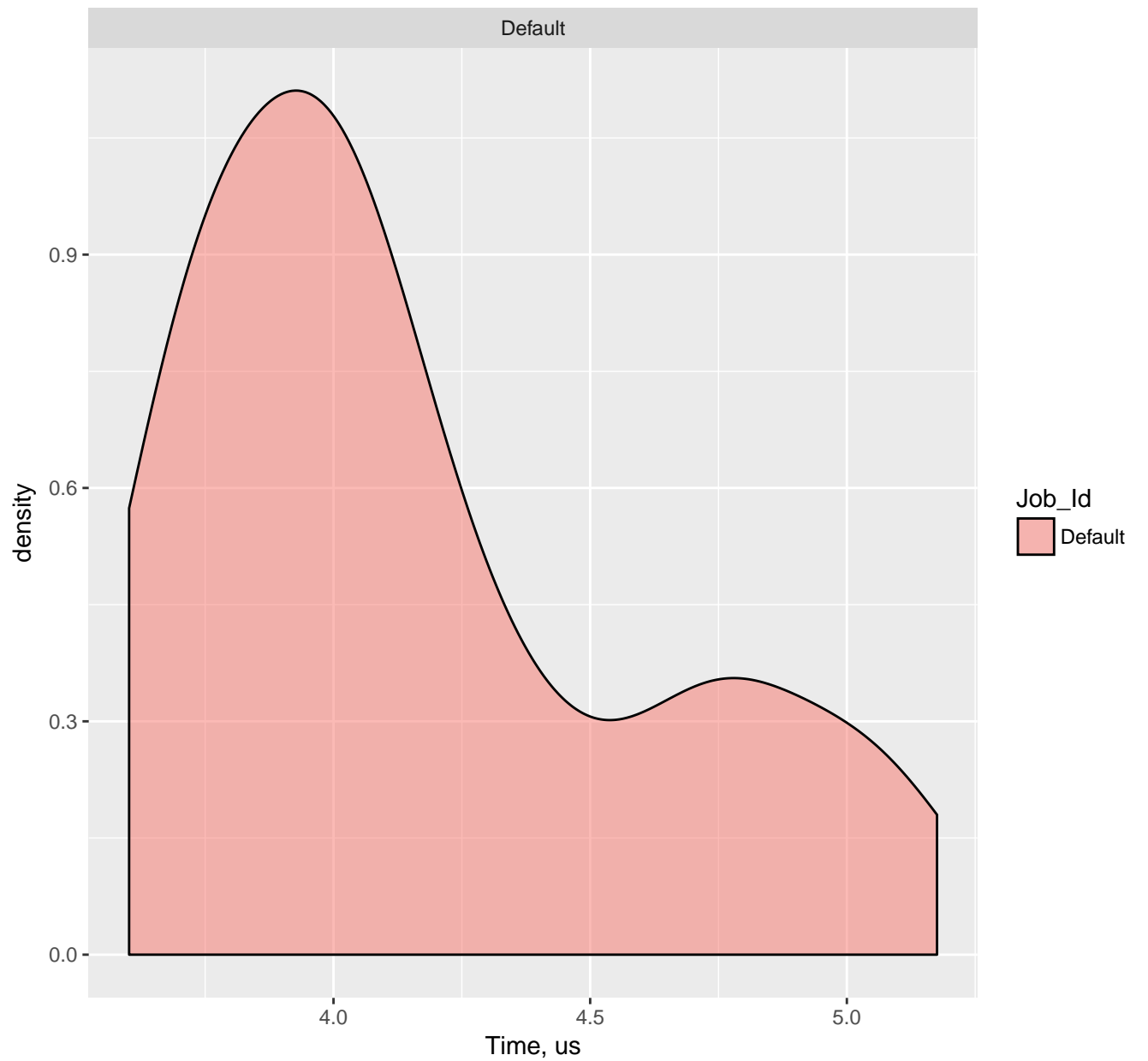


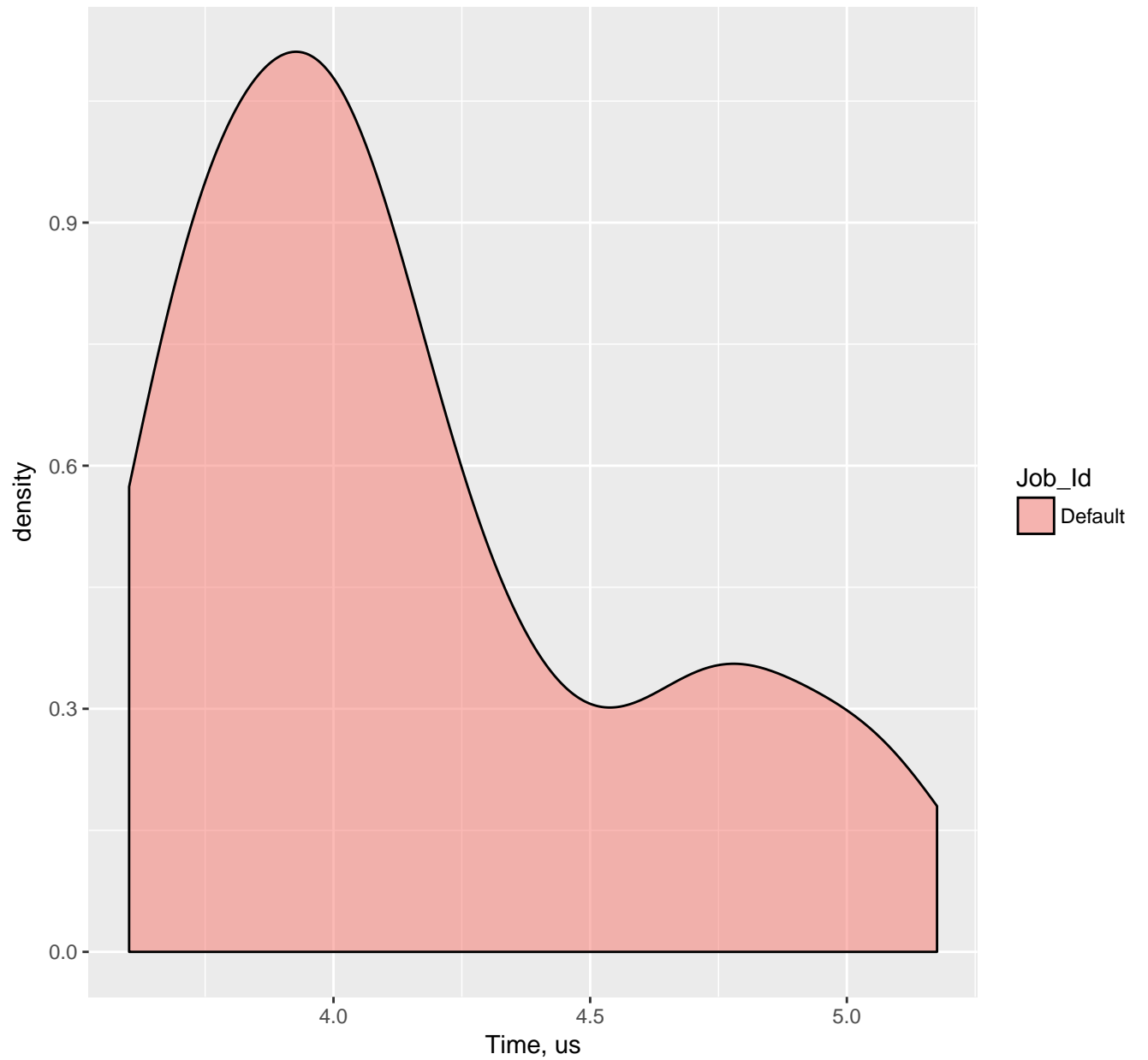
# CpsBenchmark+CpsPerformanceTests / EarlyAvm2

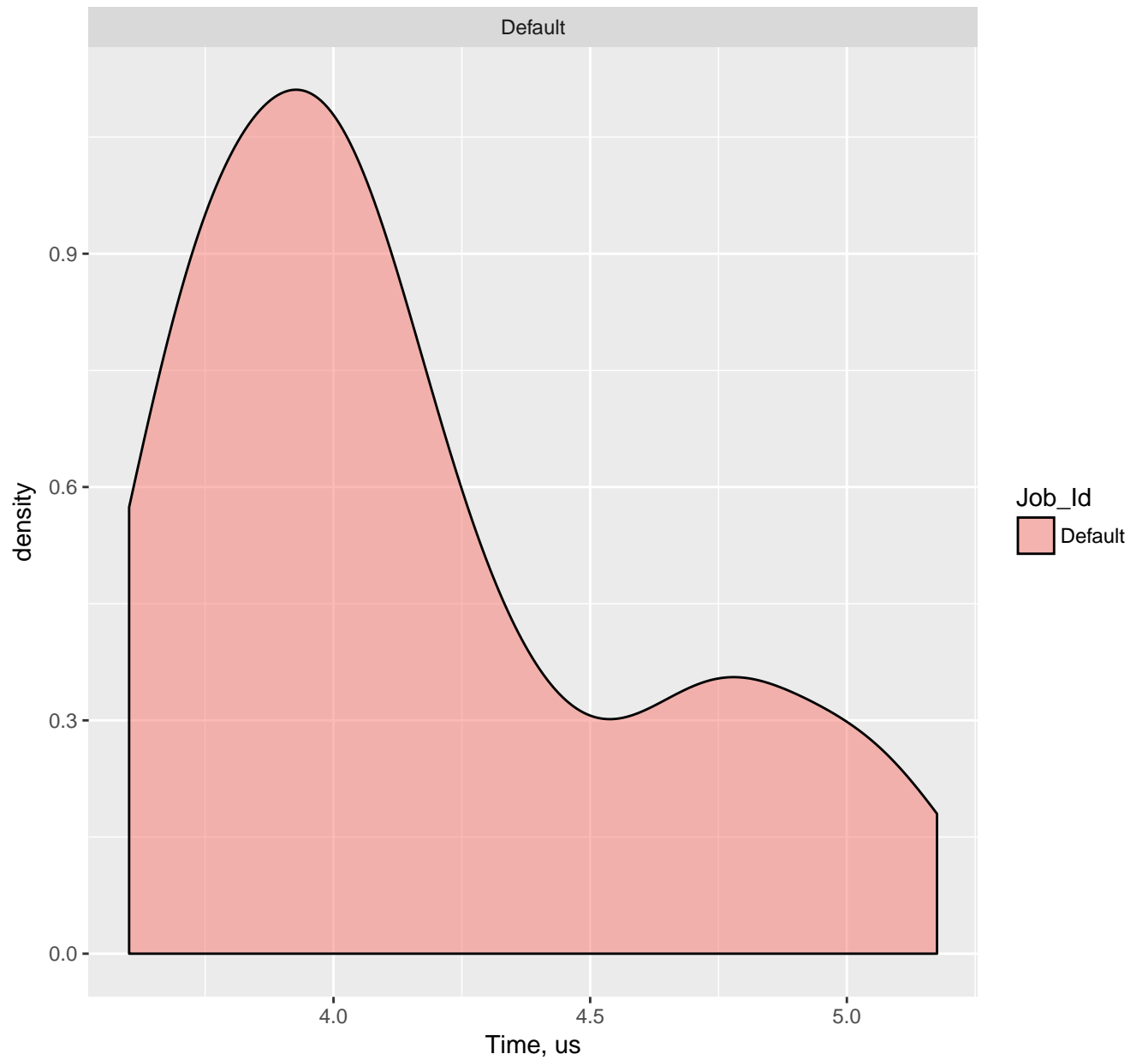




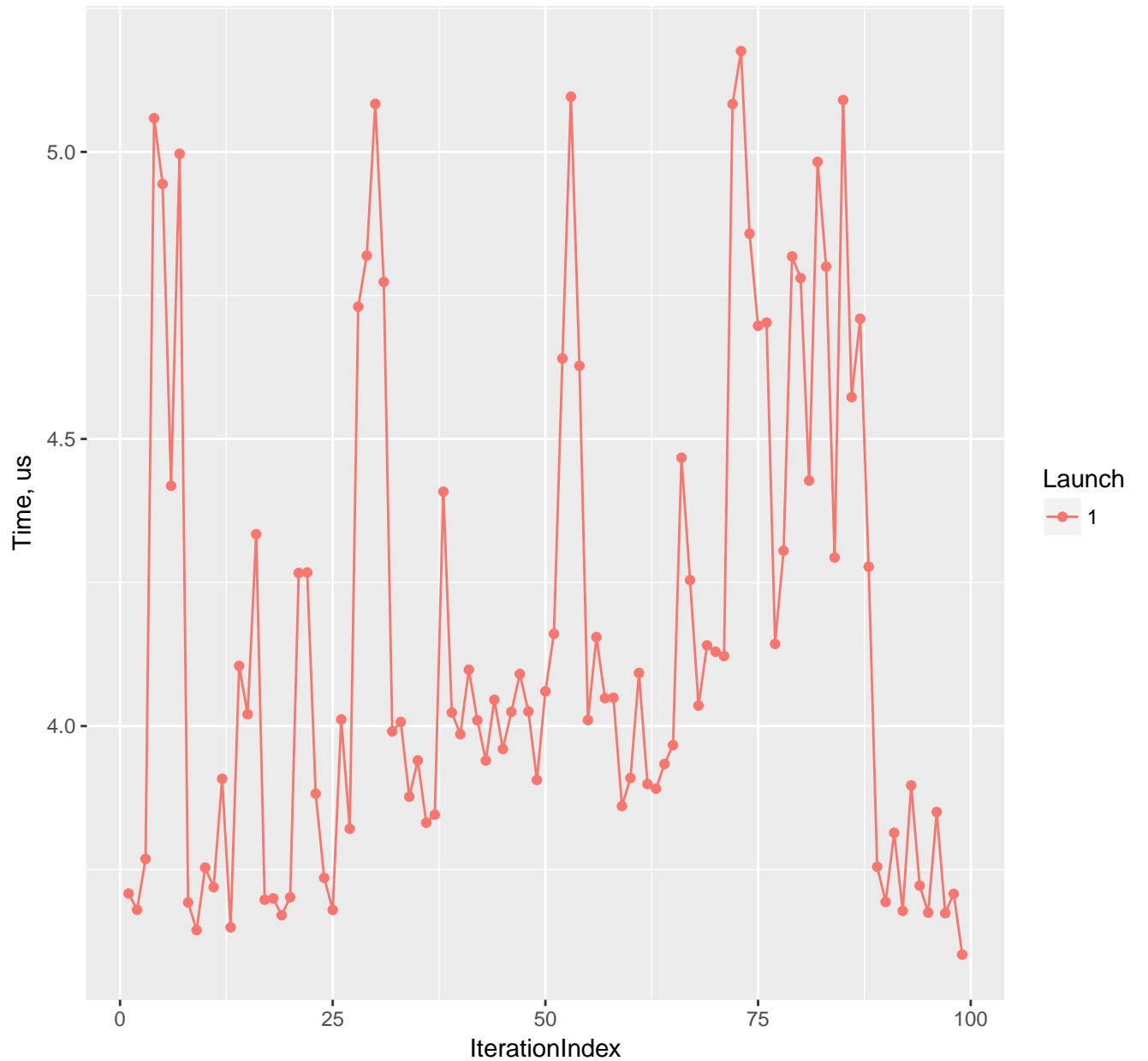
# CpsBenchmark+CpsPerformanceTests / EarlyAvm3





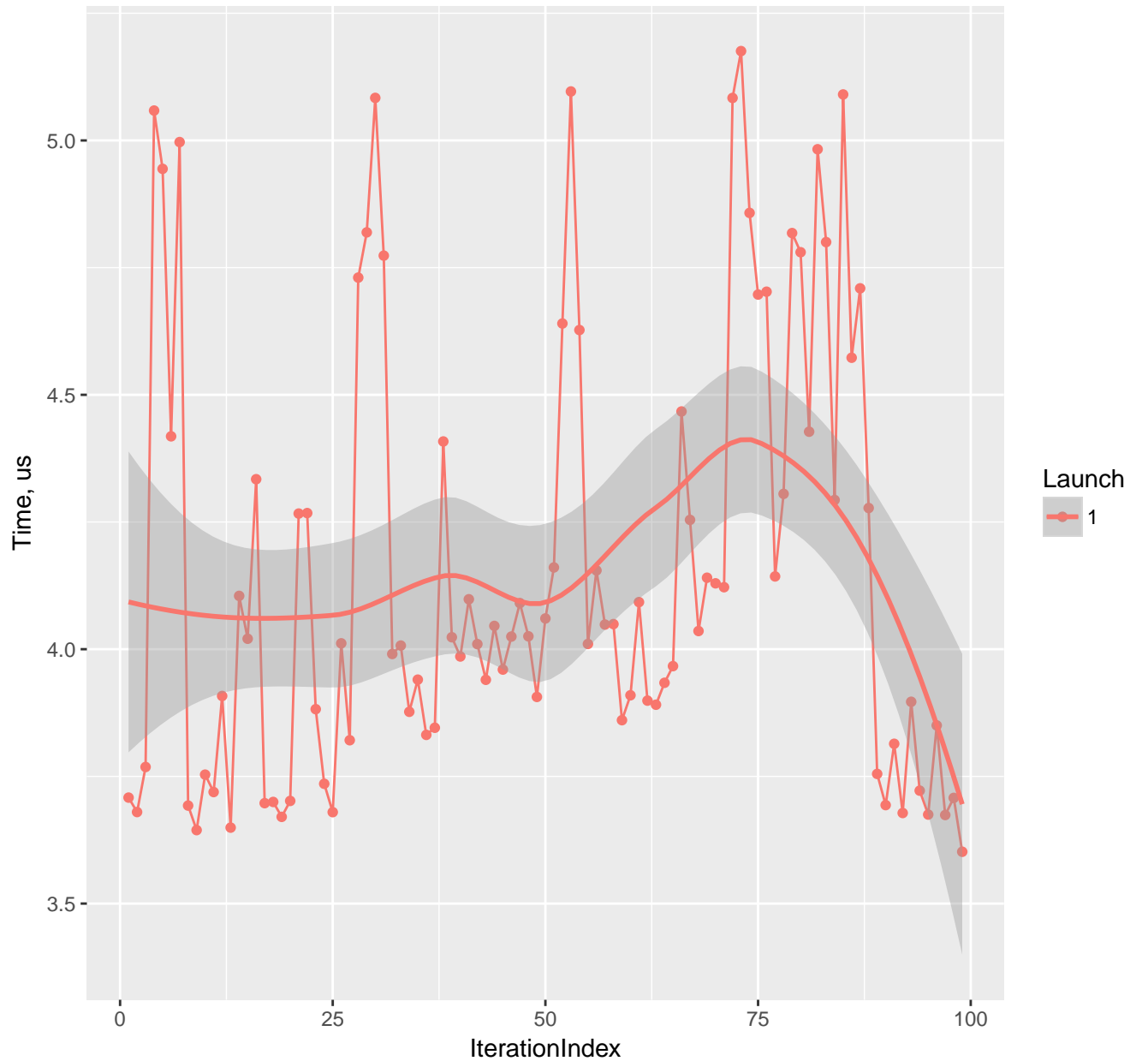


# CpsBenchmark+CpsPerformanceTests / EarlyAvm3 / Default

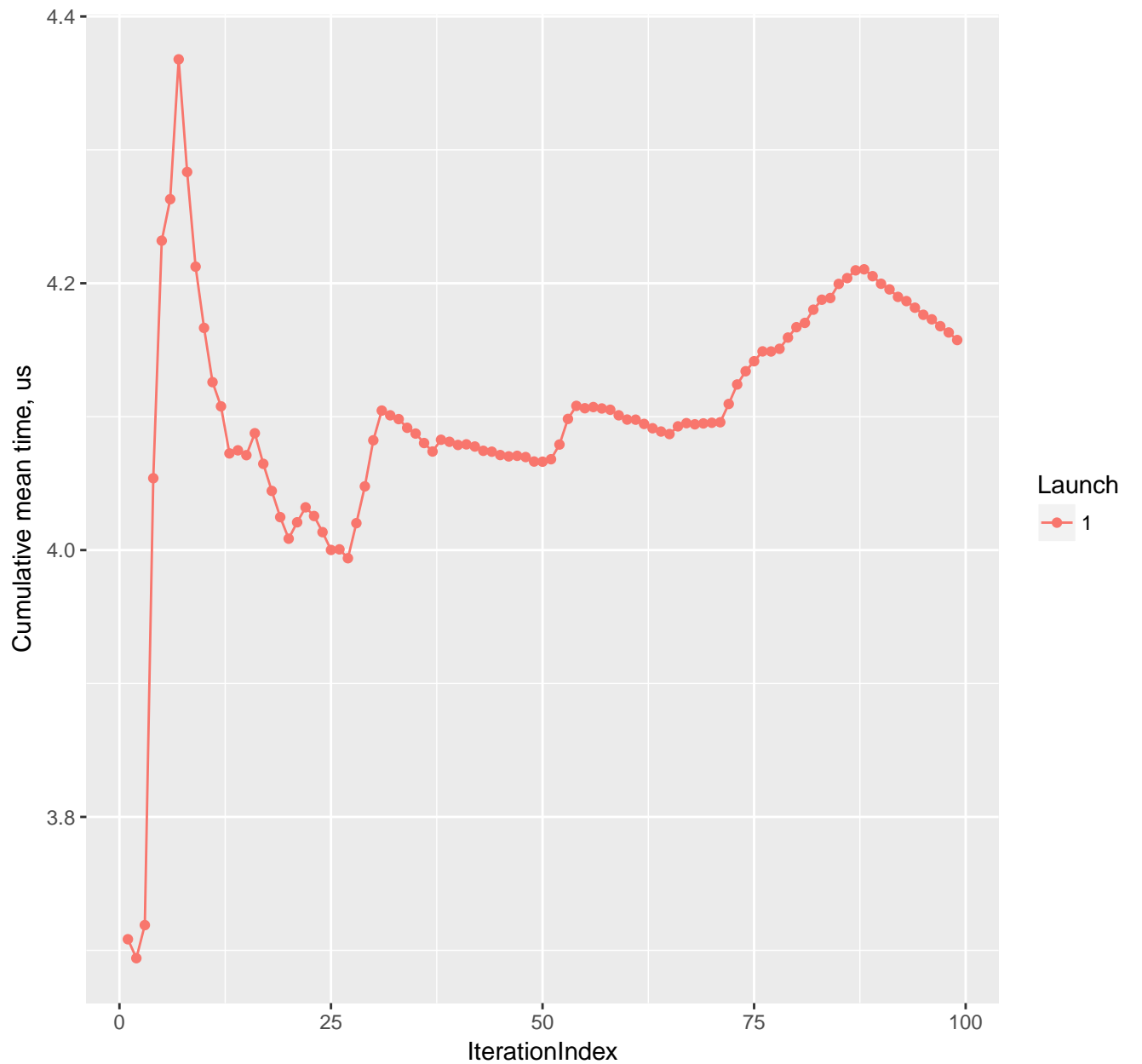


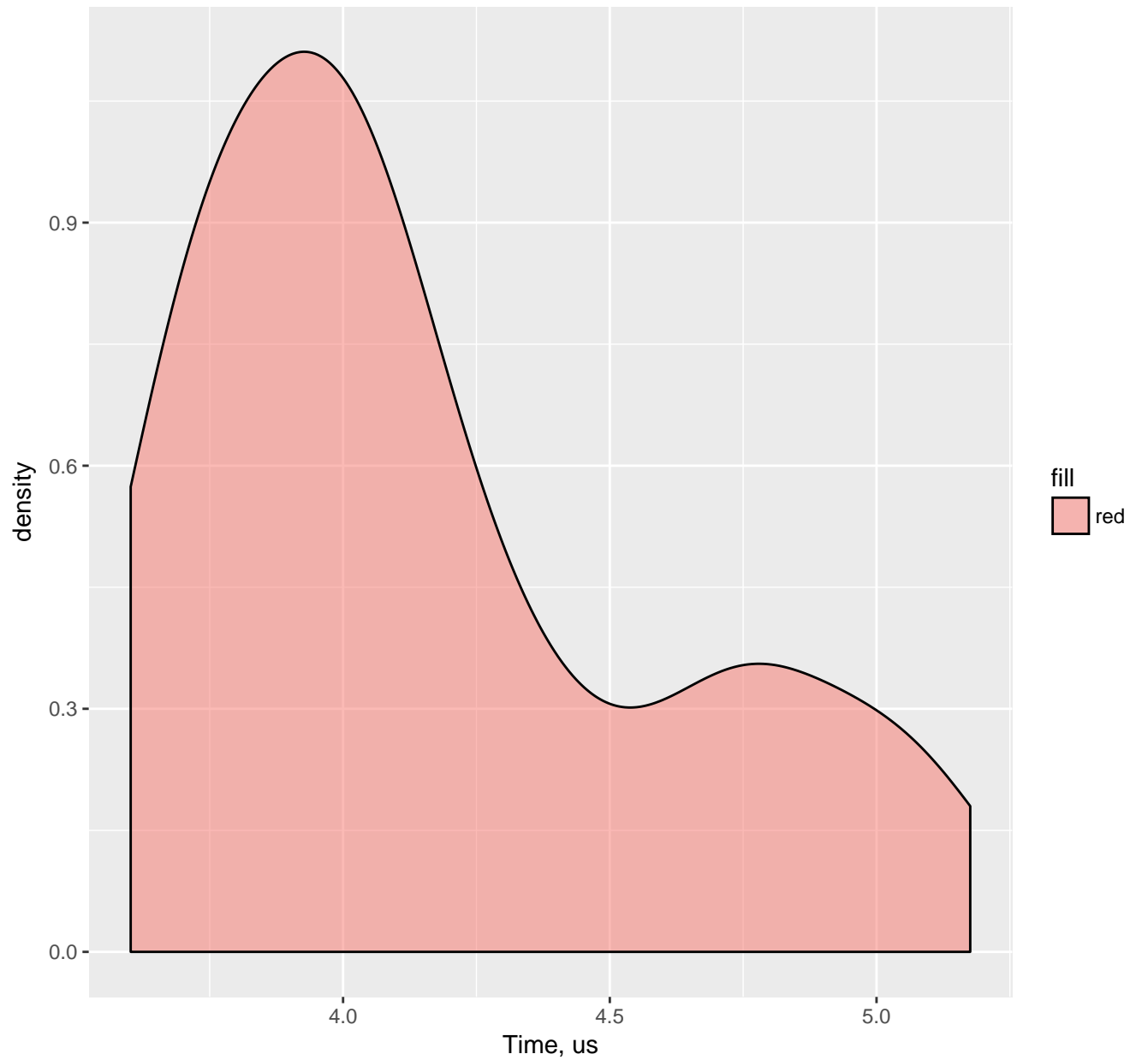


# CpsBenchmark+CpsPerformanceTests / EarlyAvm3 / Default

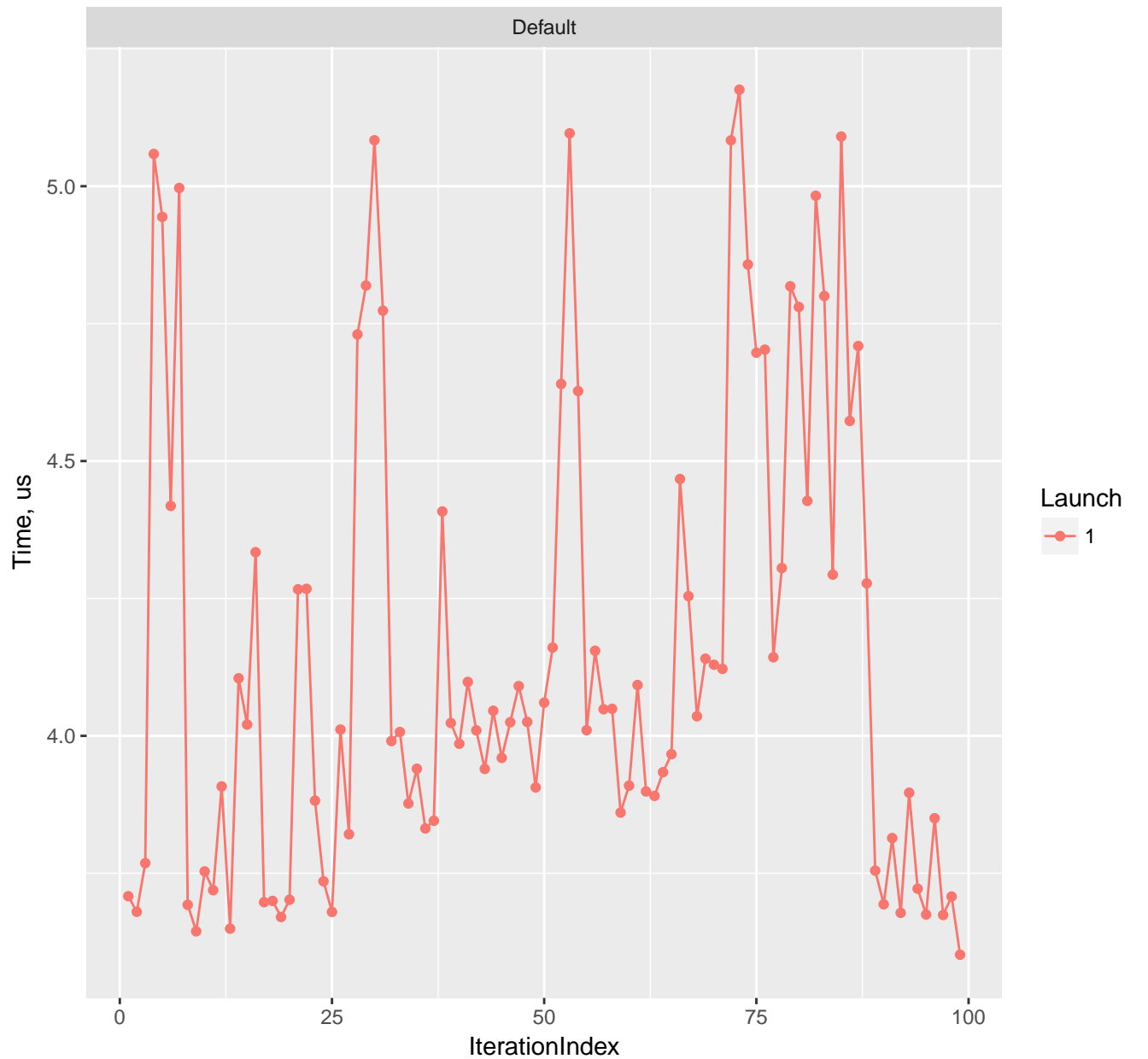


# CpsBenchmark+CpsPerformanceTests / EarlyAvm3 / Default

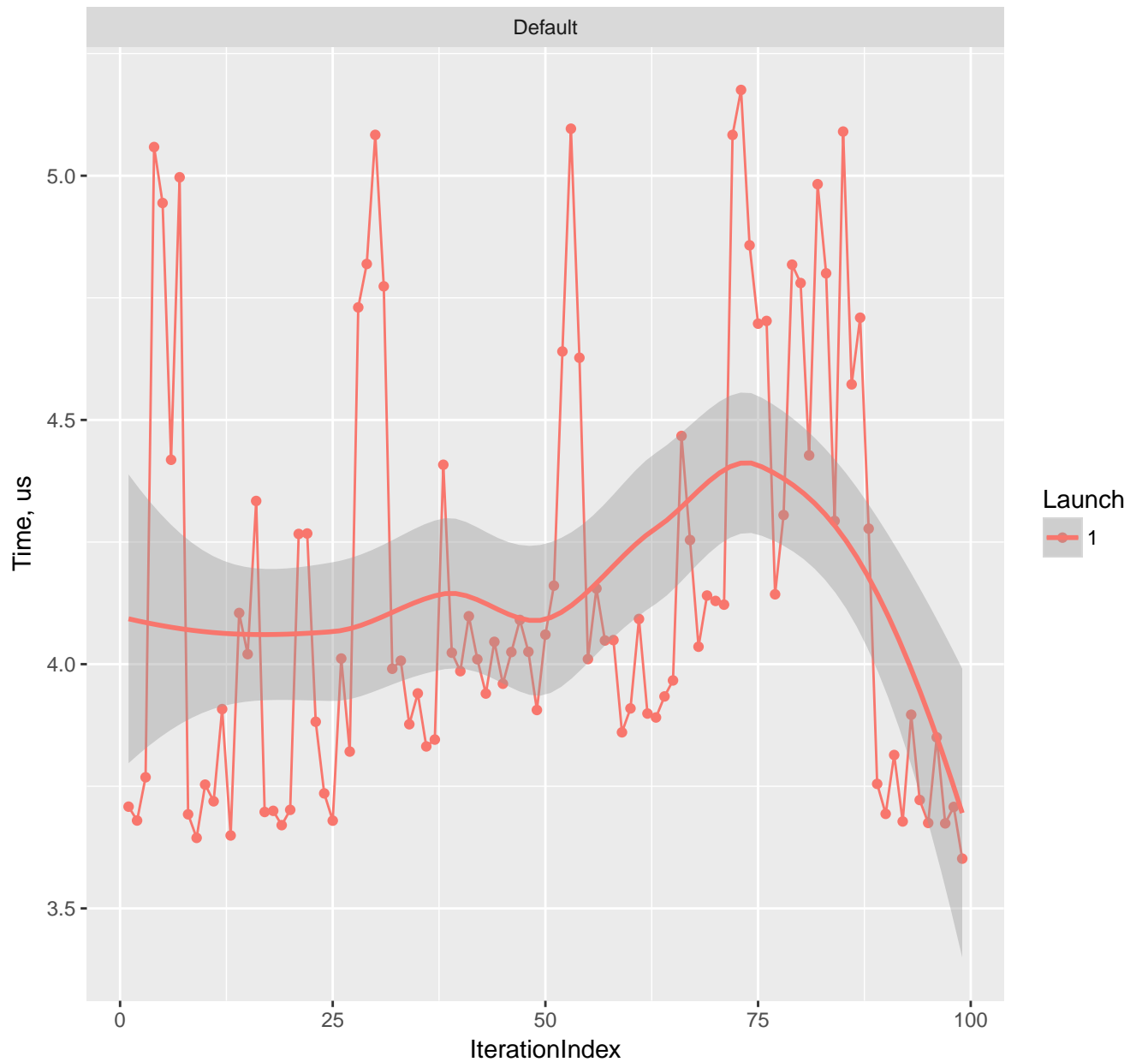




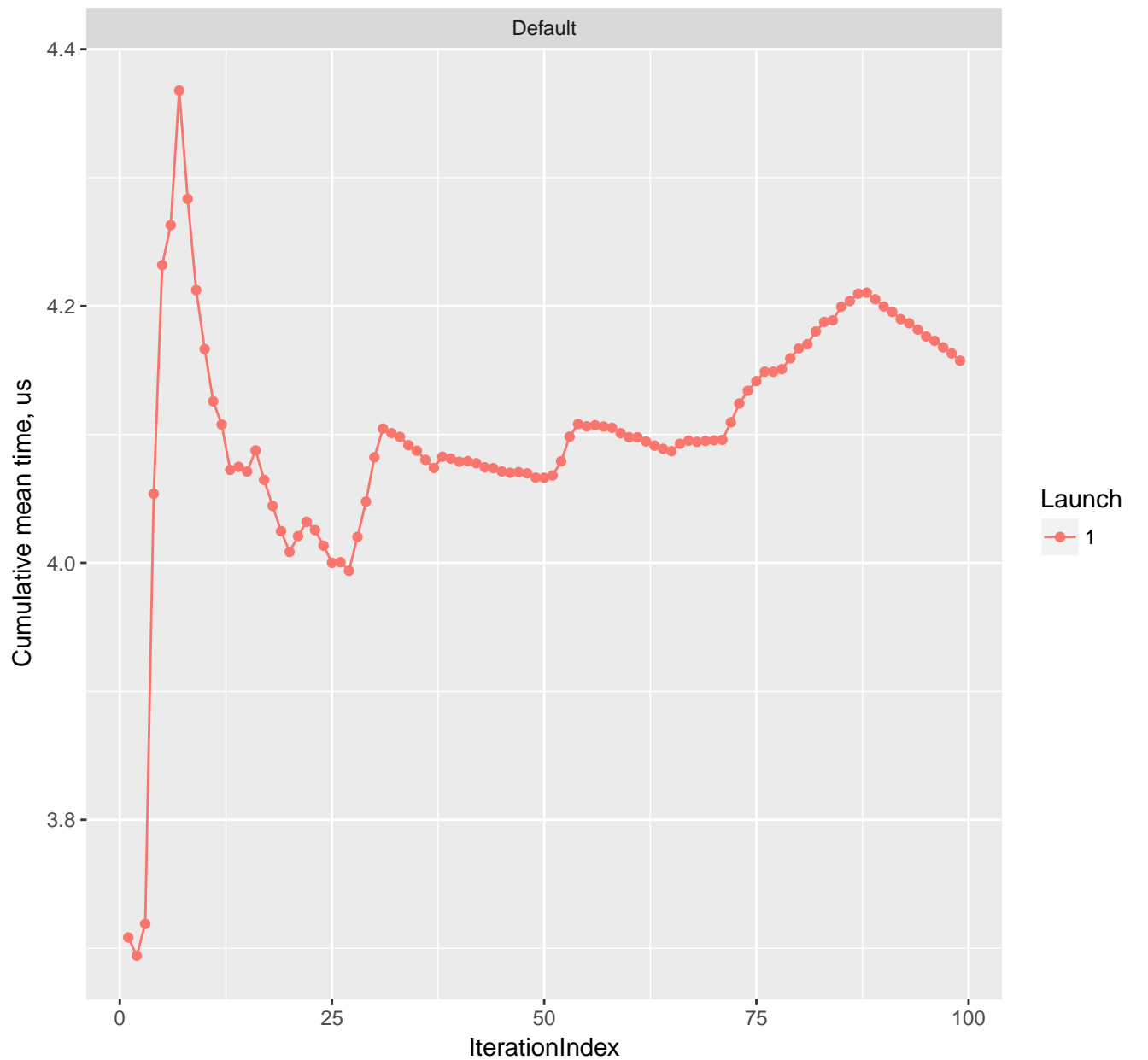
# CpsBenchmark+CpsPerformanceTests / EarlyAvm3



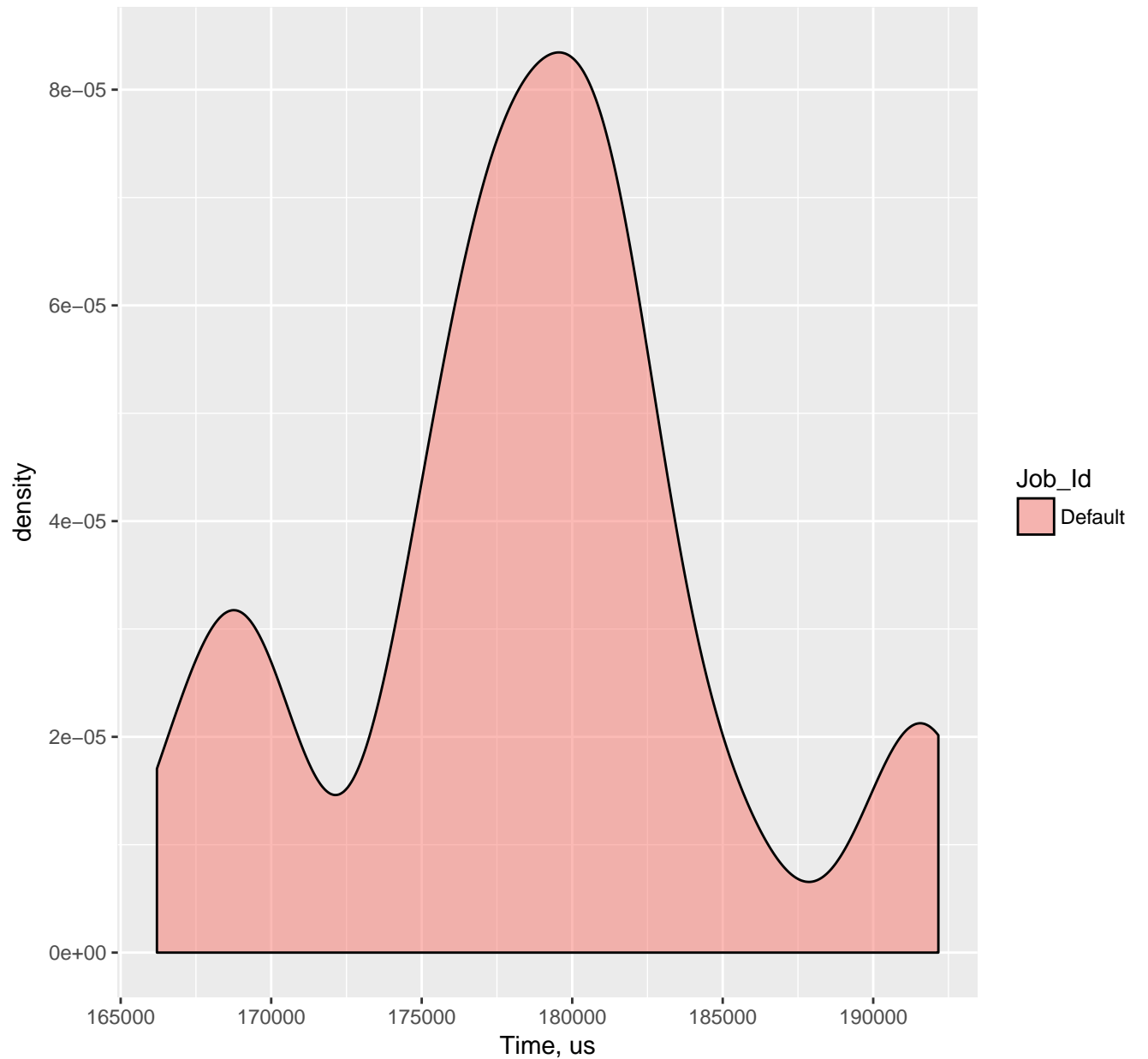
# CpsBenchmark+CpsPerformanceTests / EarlyAvm3



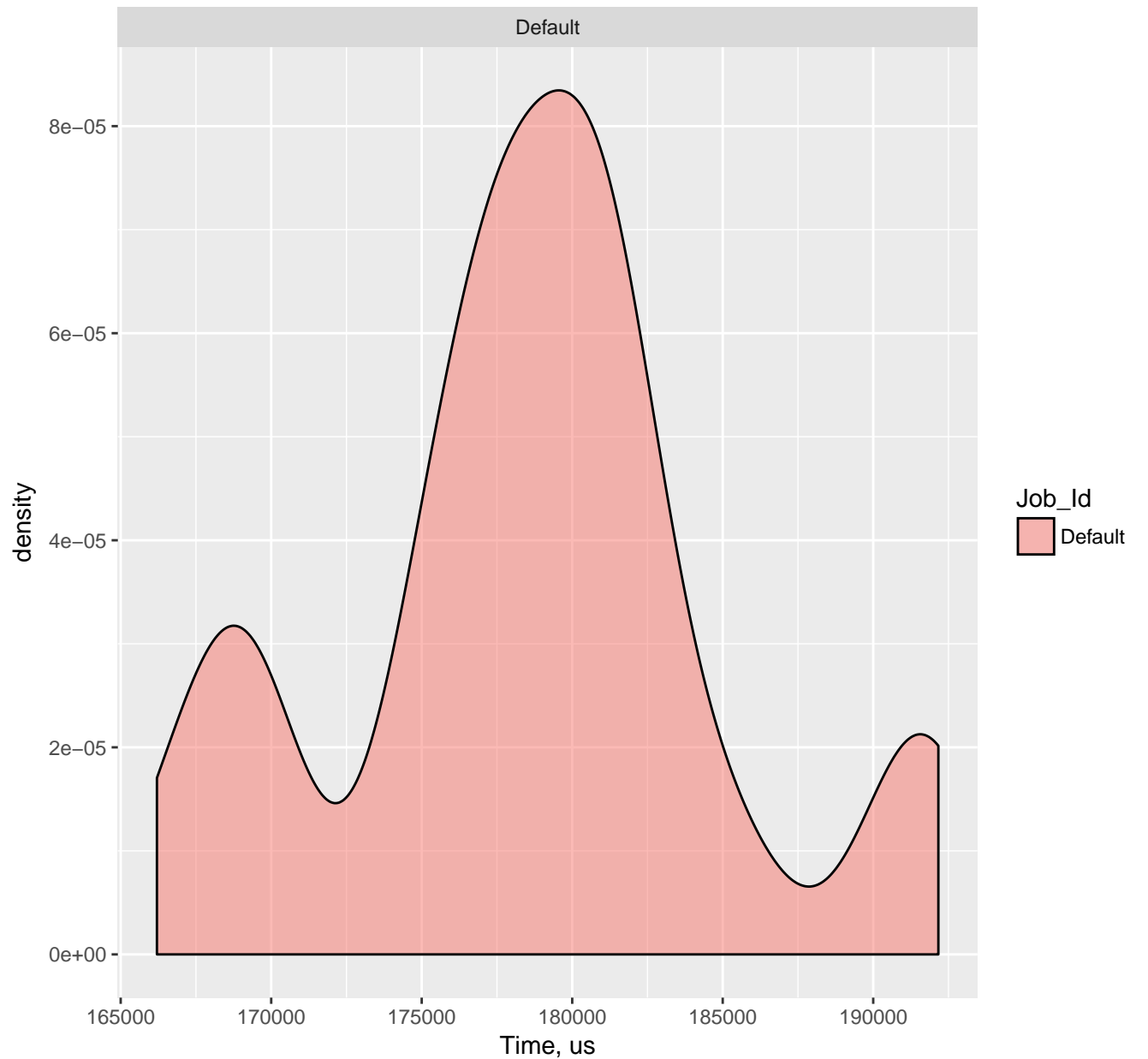
# CpsBenchmark+CpsPerformanceTests / EarlyAvm3



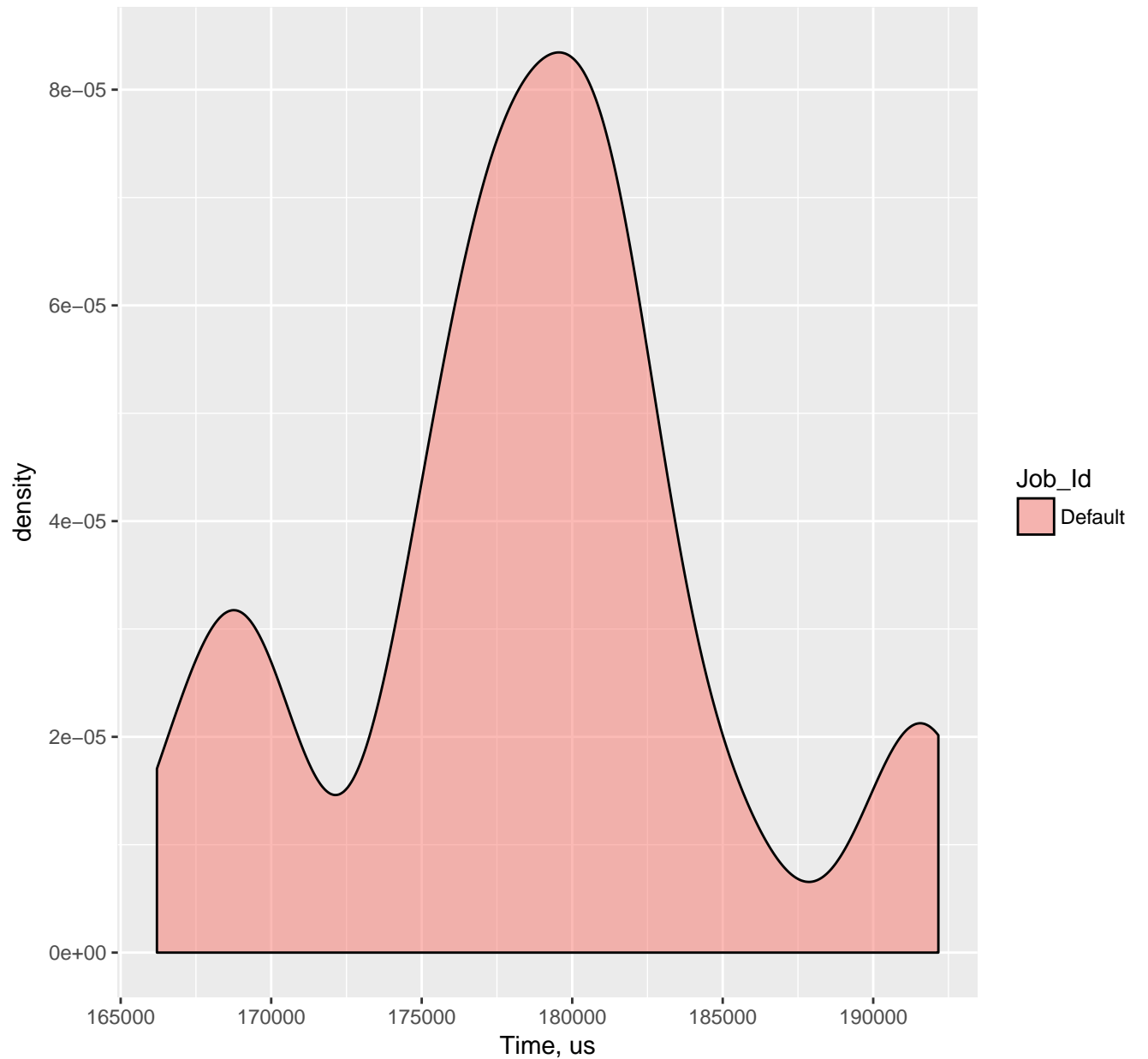
# CpsBenchmark+CpsPerformanceTests / Late

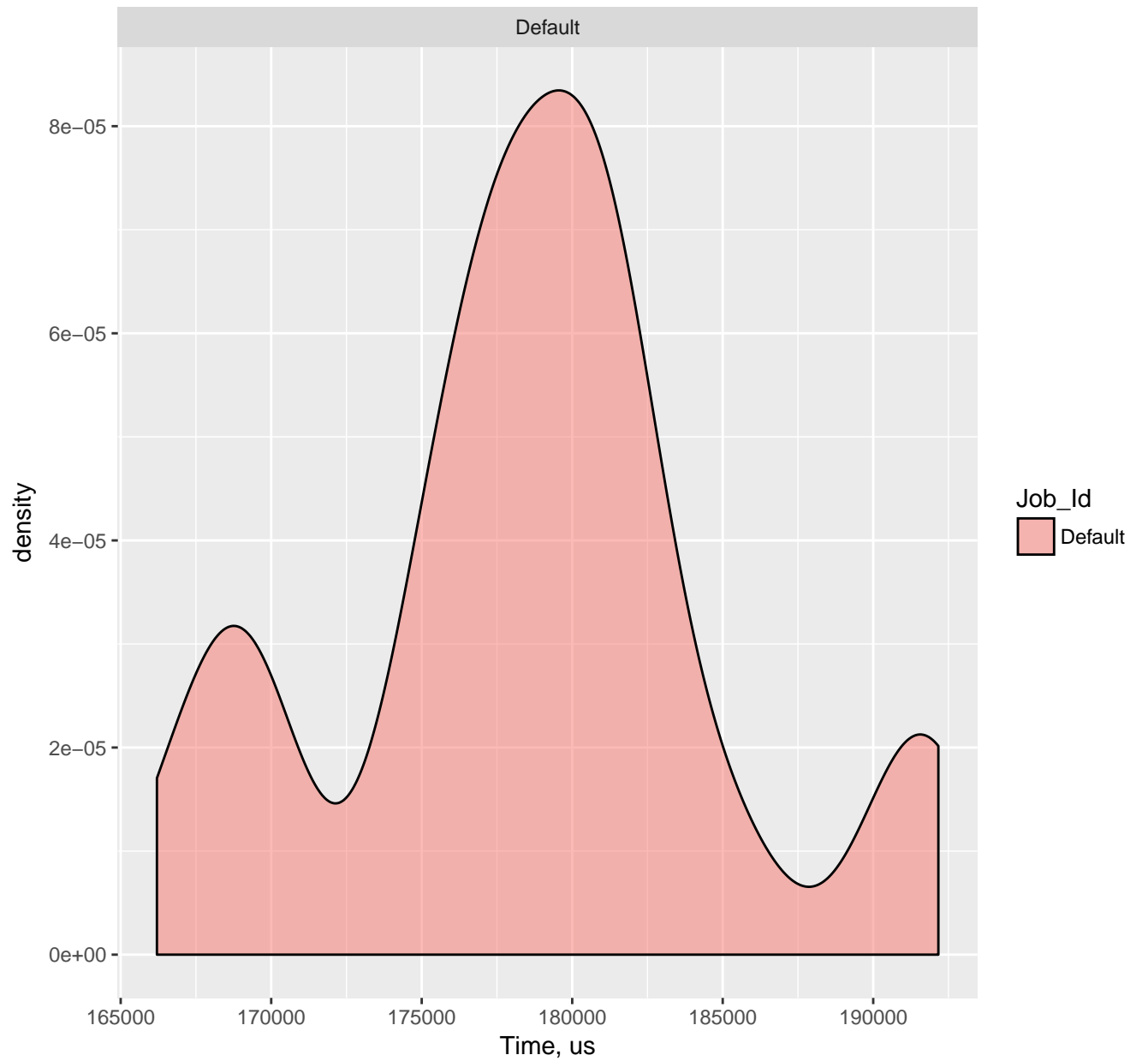


# CpsBenchmark+CpsPerformanceTests / Late

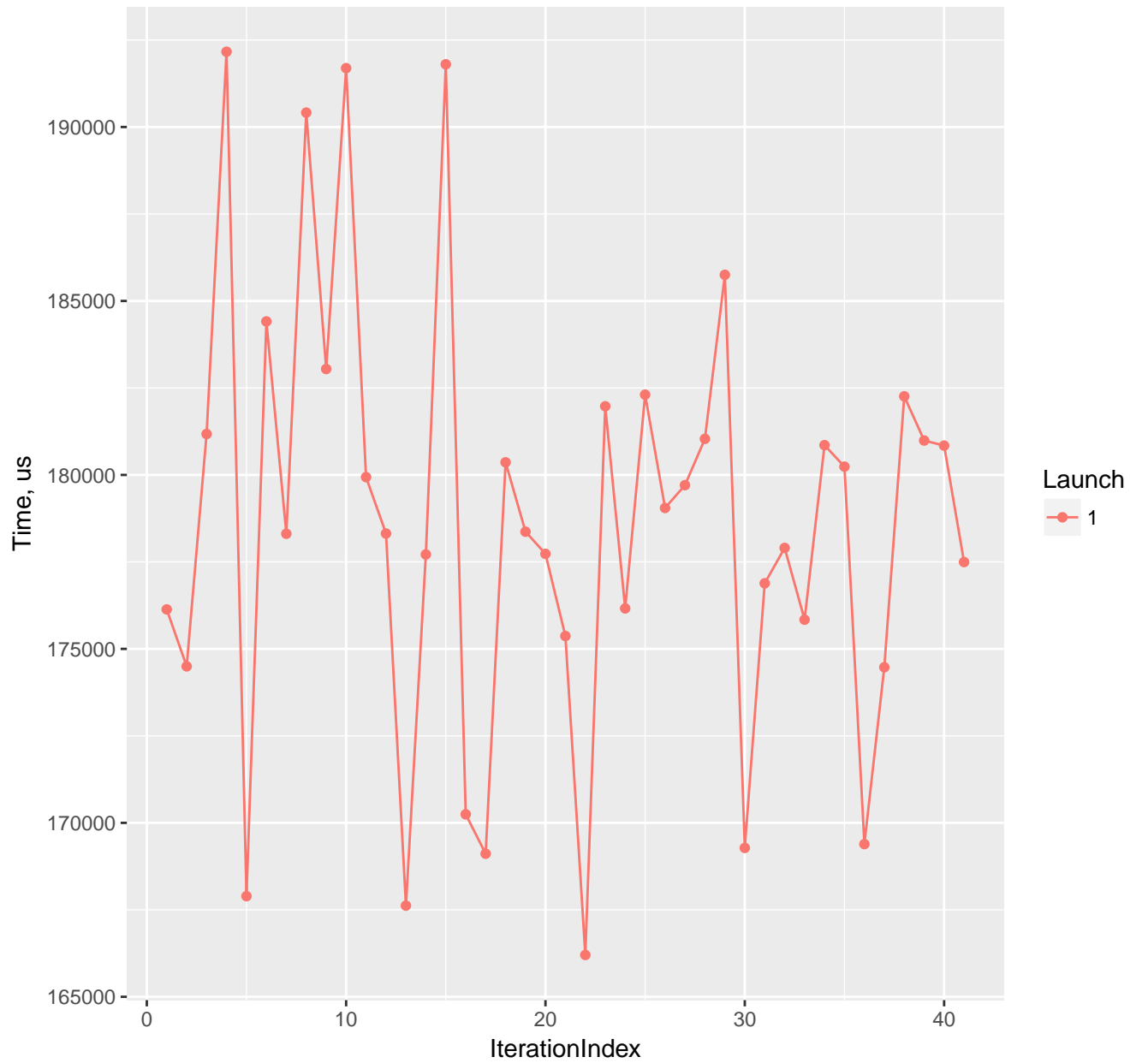




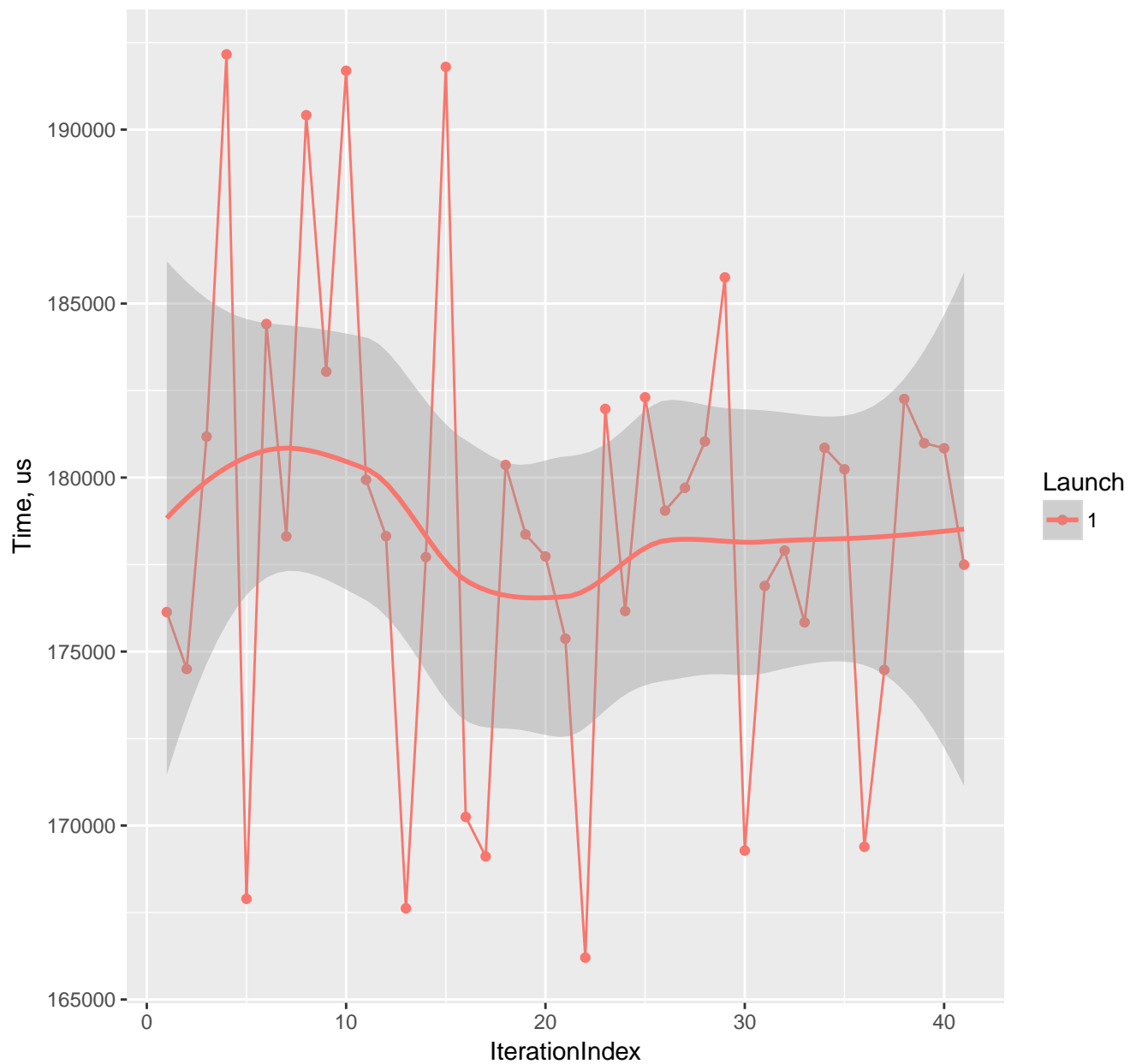




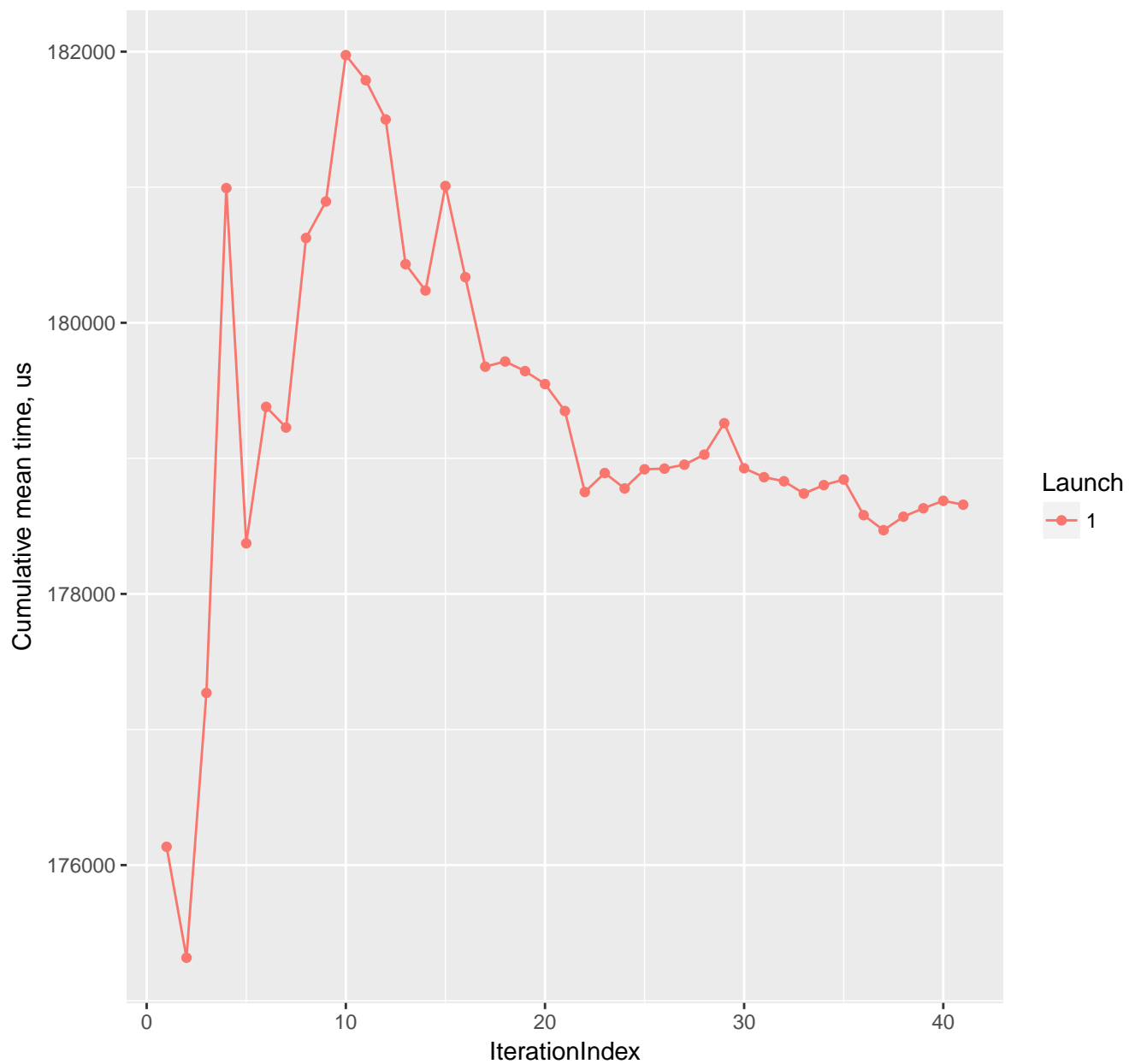
# CpsBenchmark+CpsPerformanceTests / Late / Default



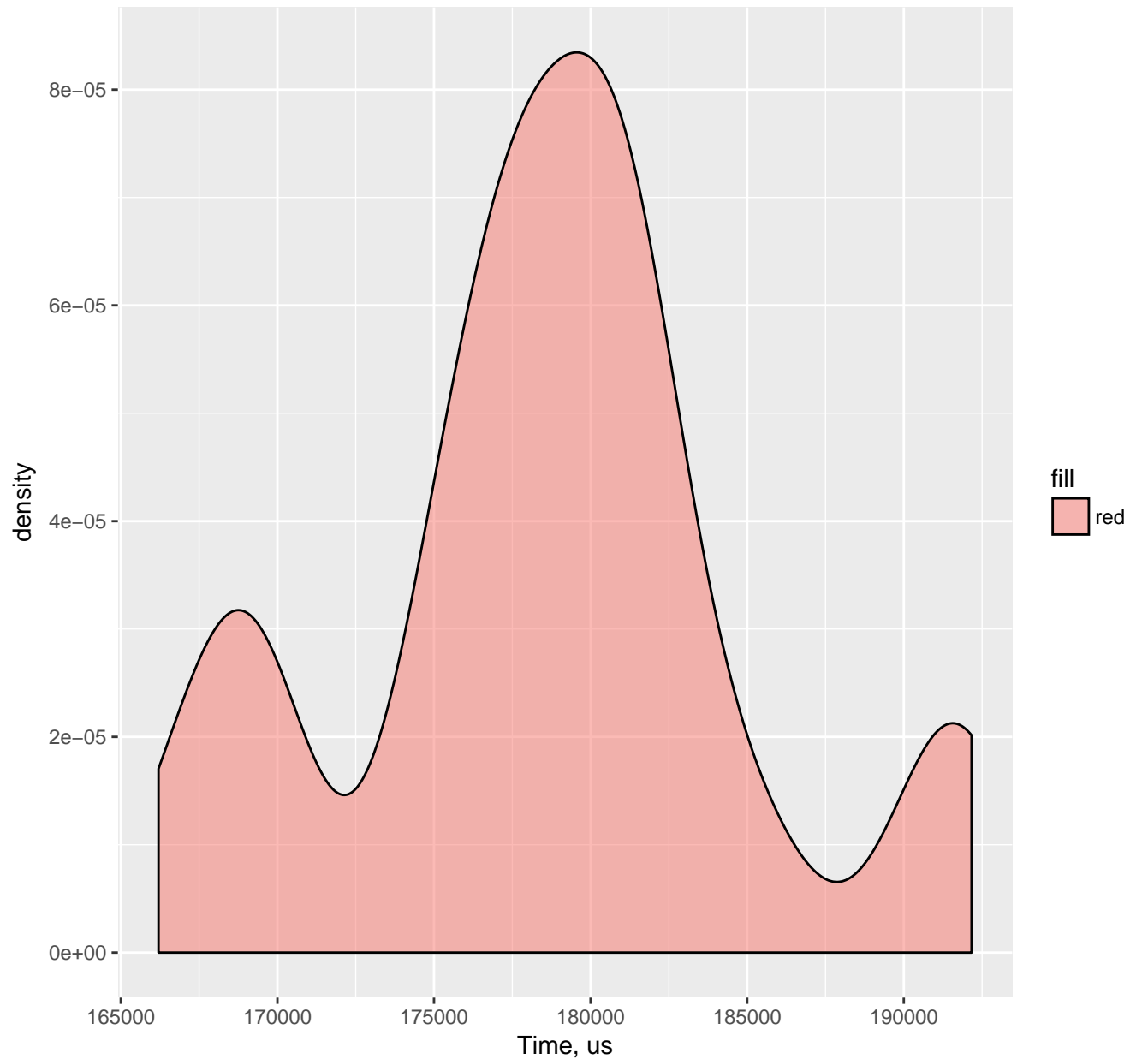
# CpsBenchmark+CpsPerformanceTests / Late / Default



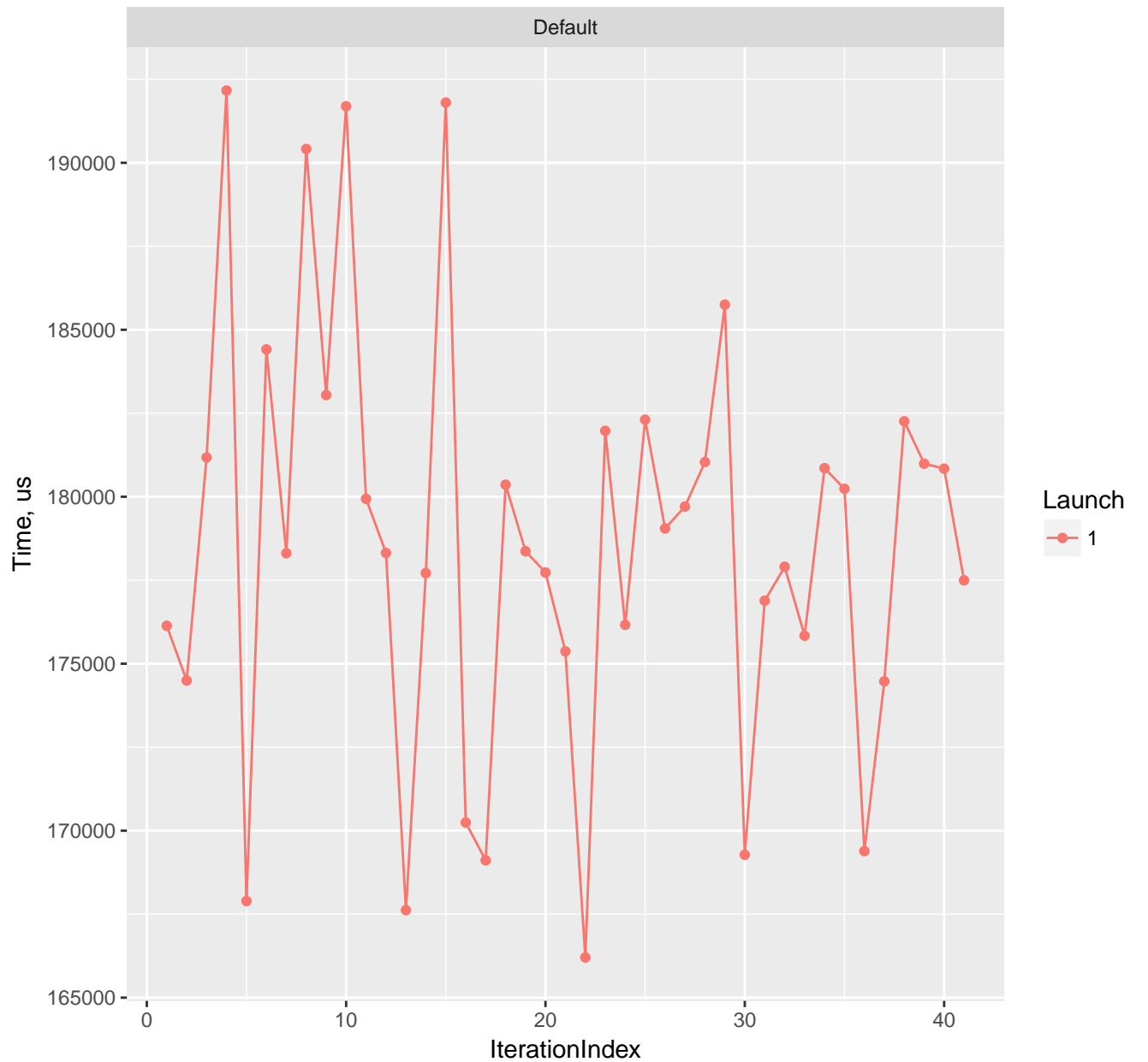
# CpsBenchmark+CpsPerformanceTests / Late / Default



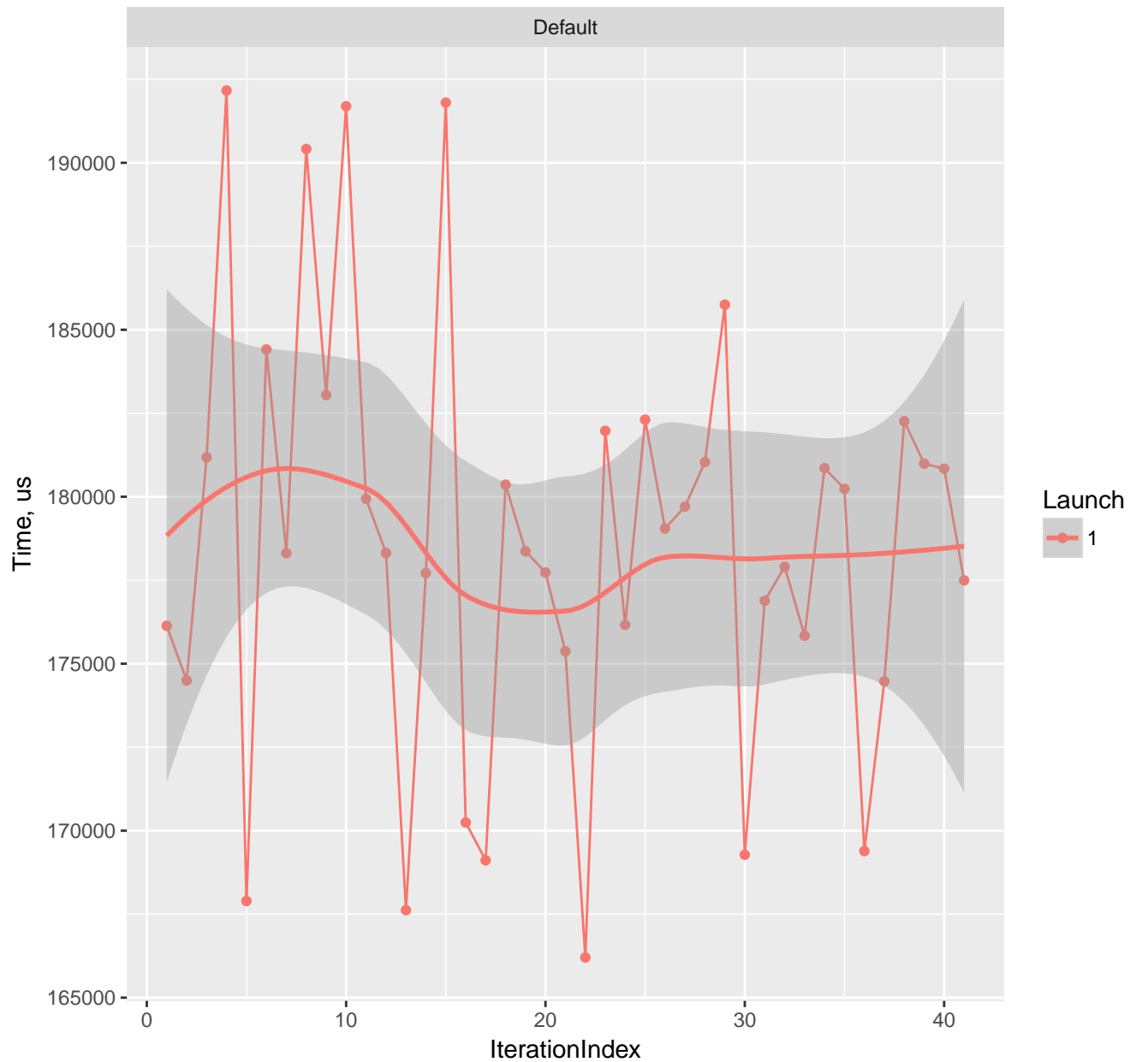
# CpsBenchmark+CpsPerformanceTests / Late / Default



# CpsBenchmark+CpsPerformanceTests / Late

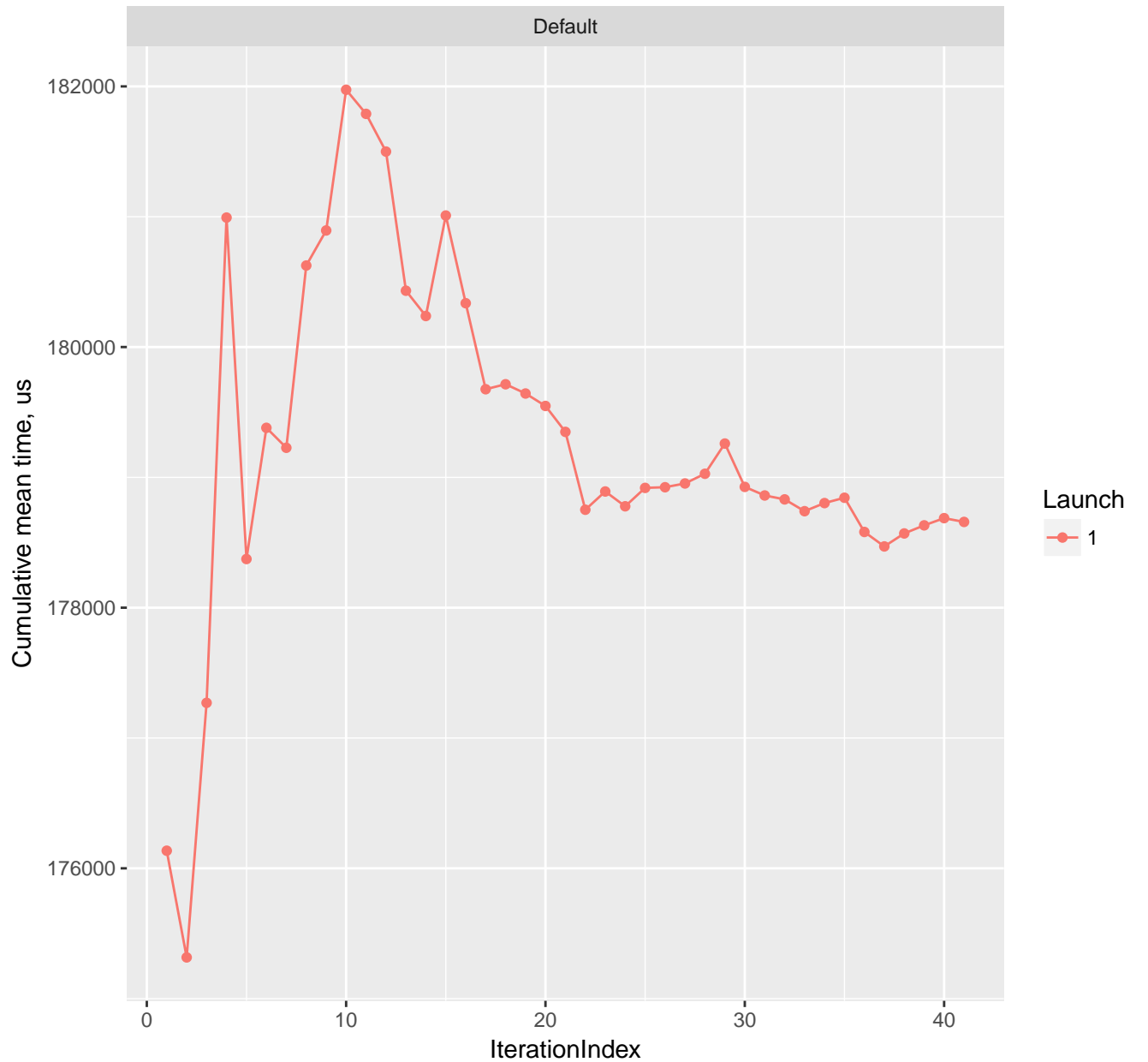


# CpsBenchmark+CpsPerformanceTests / Late

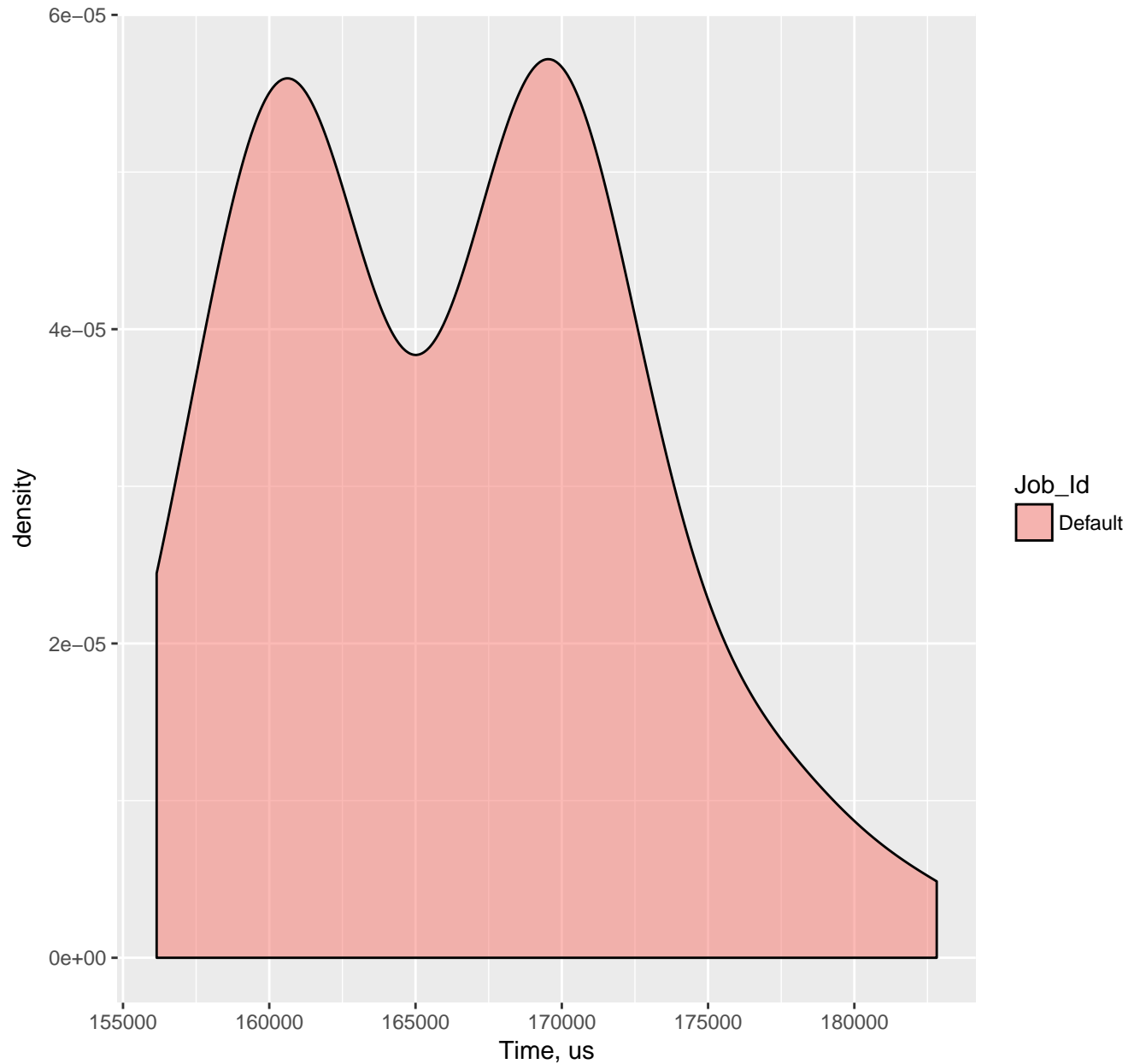




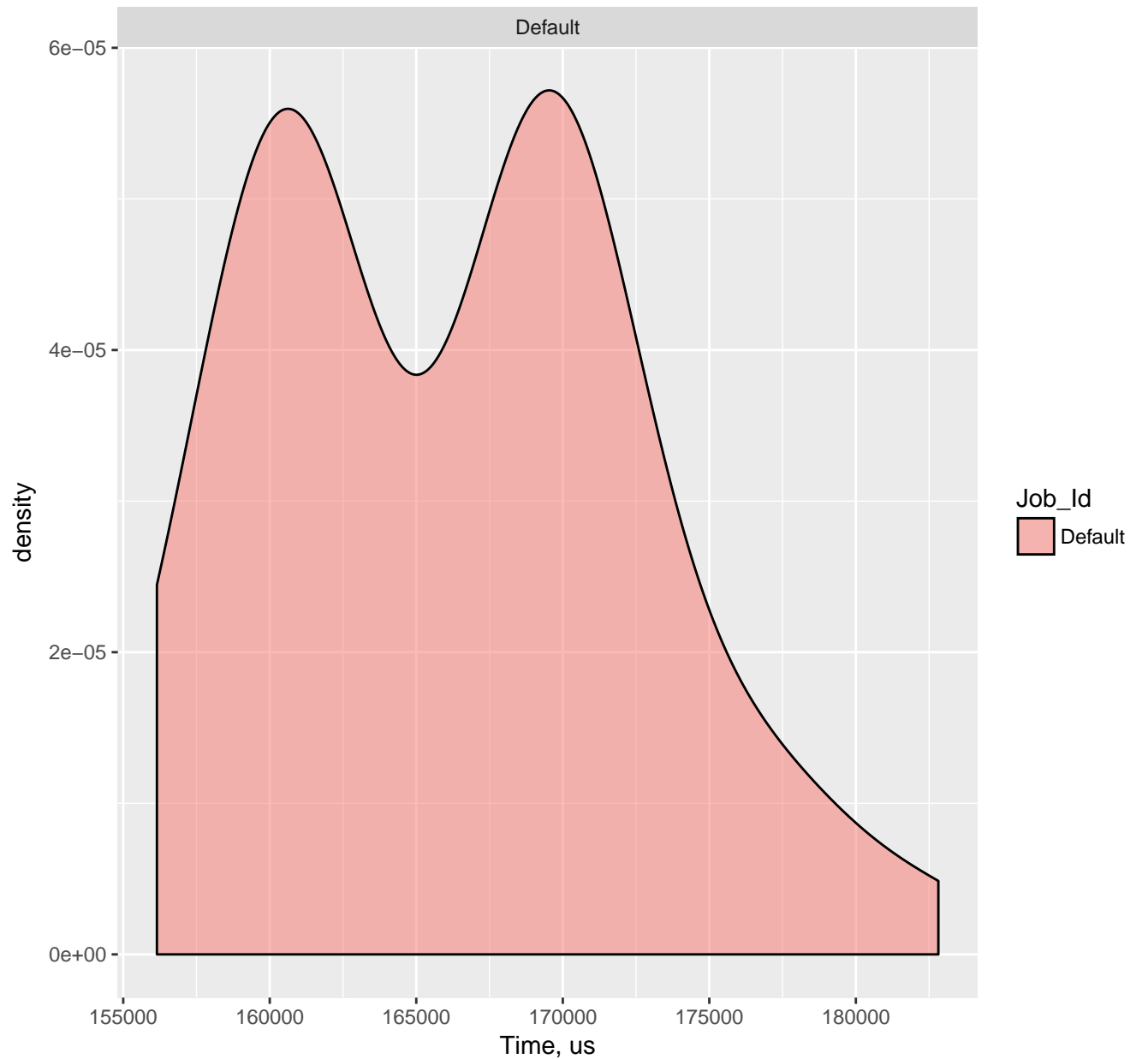
# CpsBenchmark+CpsPerformanceTests / Late



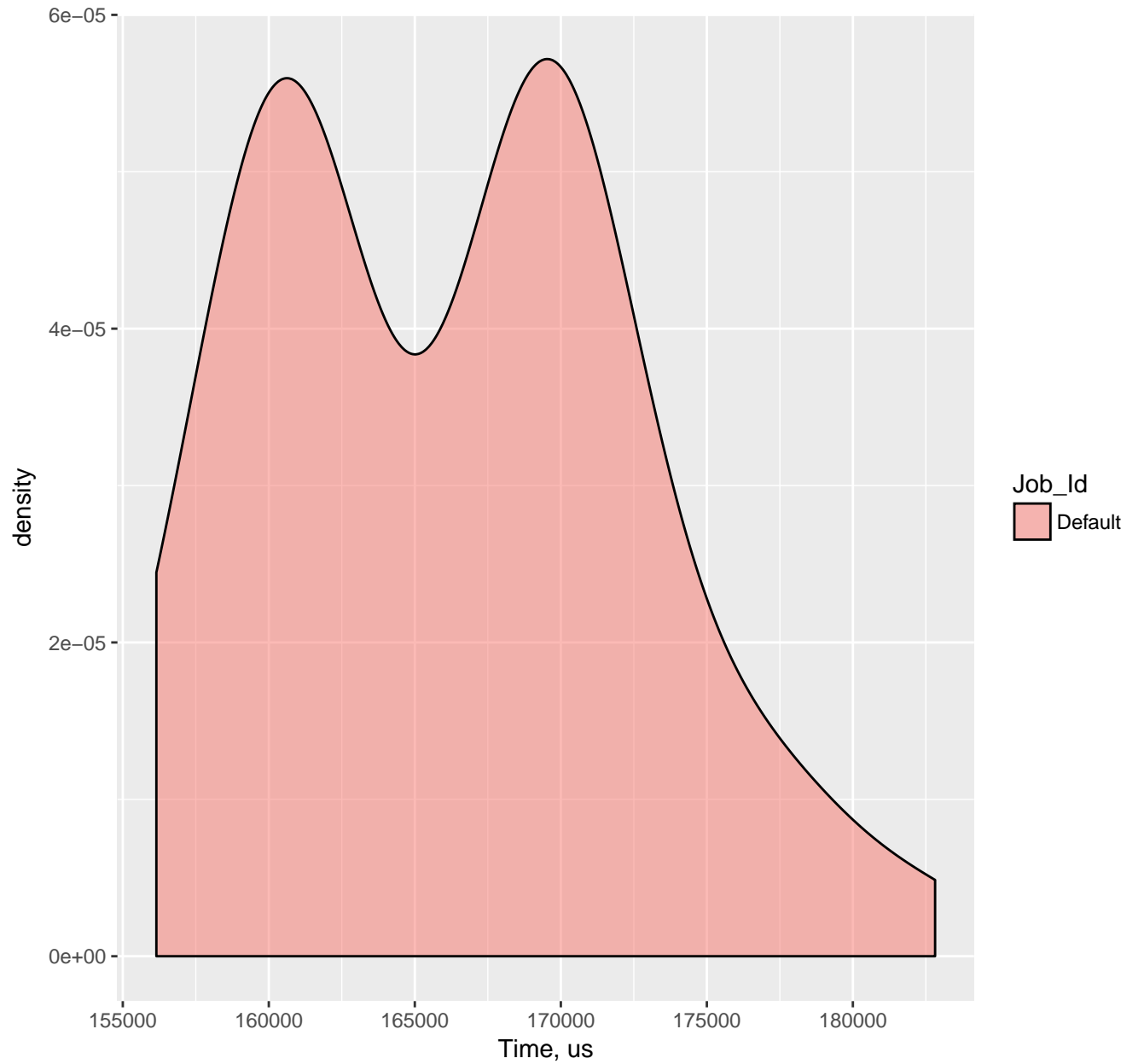
# CpsBenchmark+CpsPerformanceTests / LateCPS

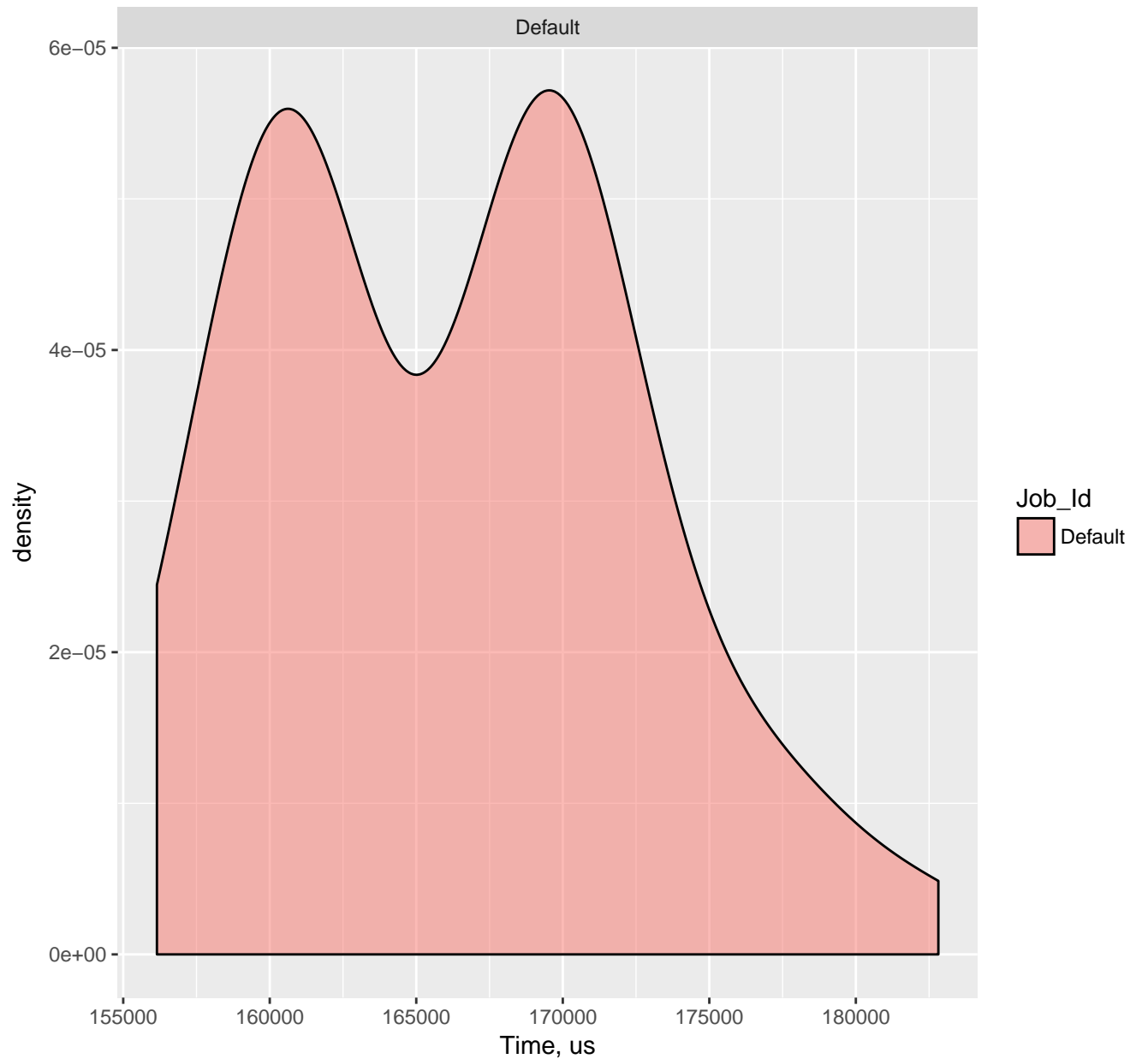


# CpsBenchmark+CpsPerformanceTests / LateCPS

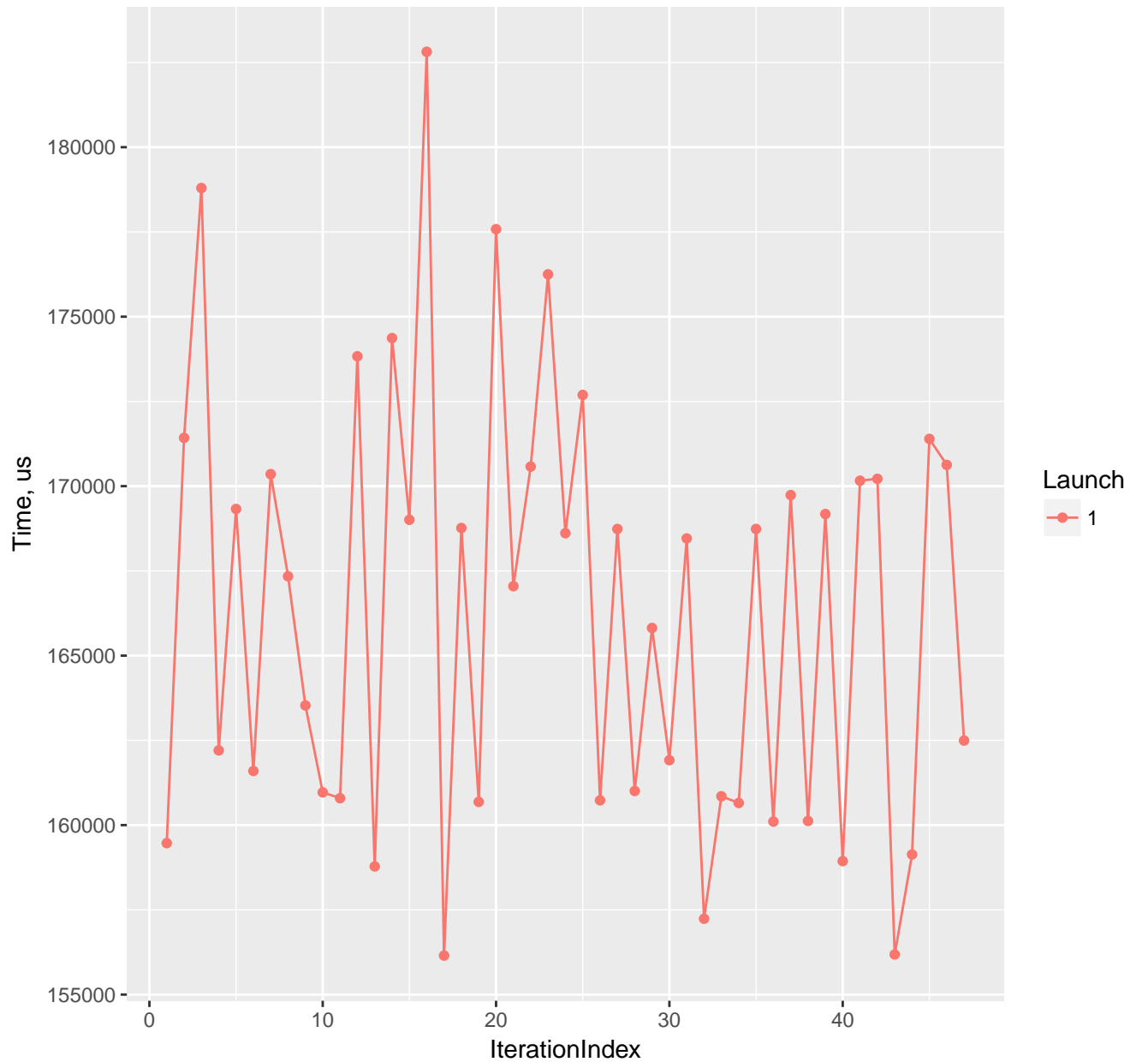


# CpsBenchmark+CpsPerformanceTests / LateCPS /

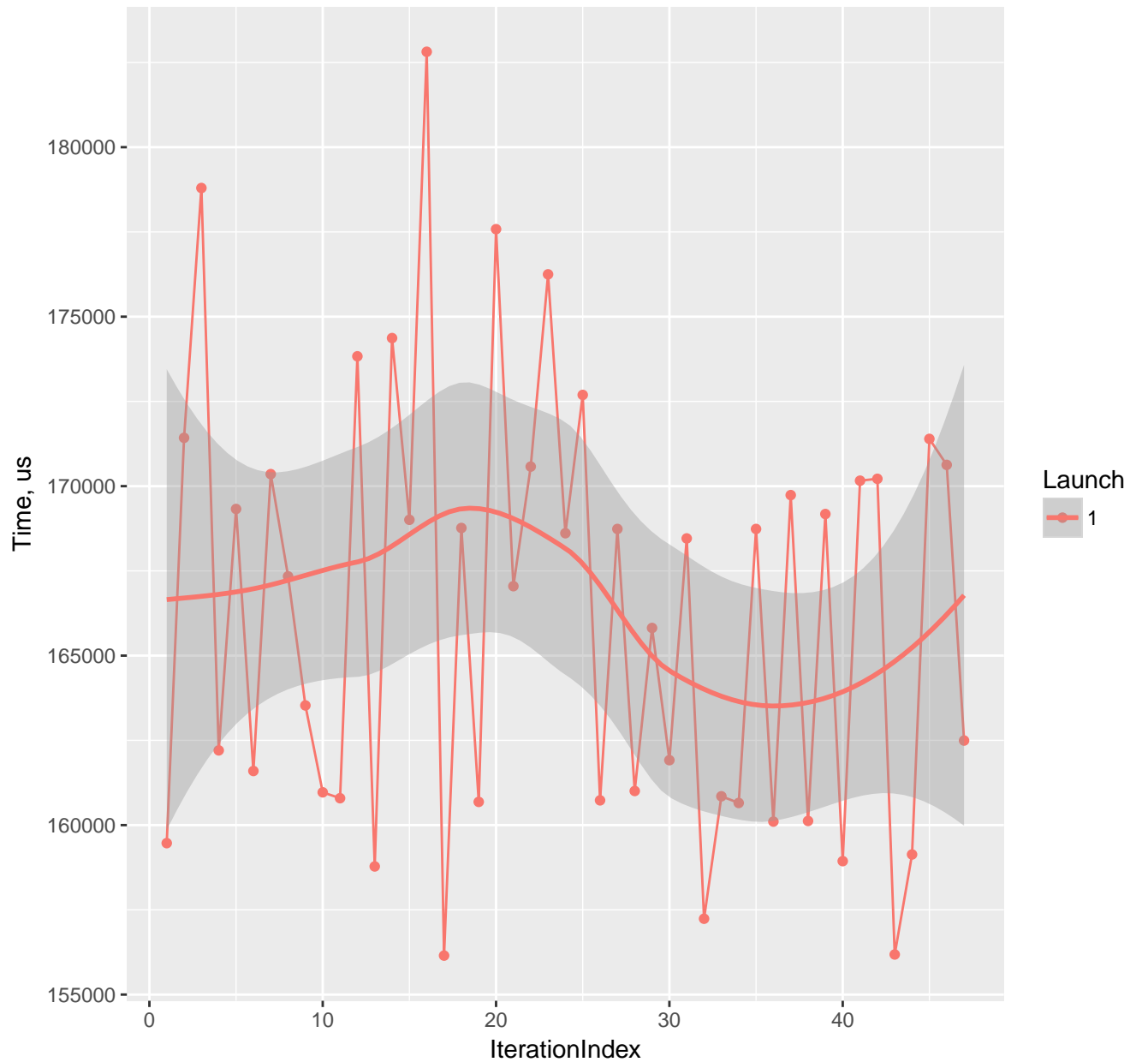




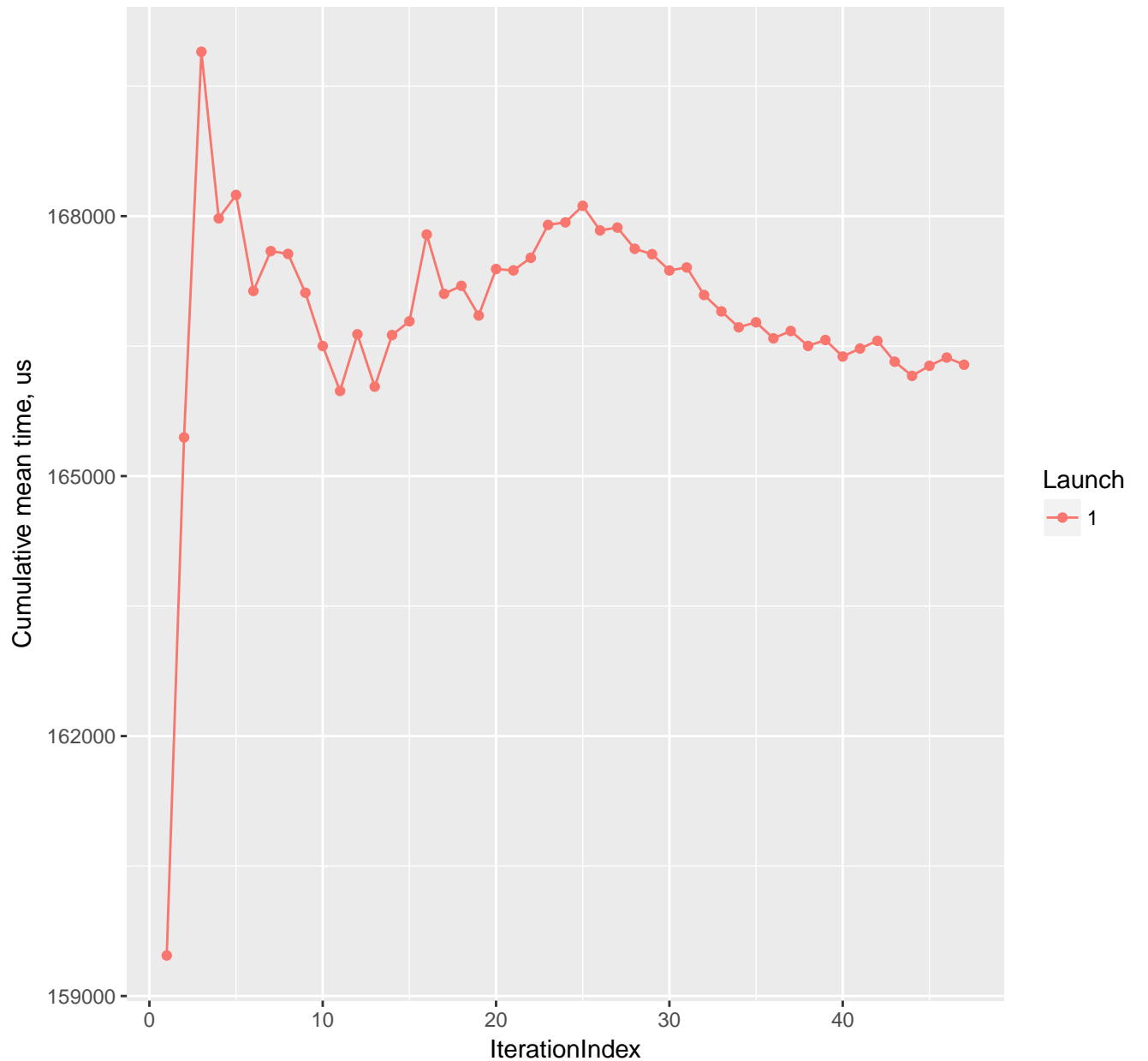
# CpsBenchmark+CpsPerformanceTests / LateCPS / Default



# CpsBenchmark+CpsPerformanceTests / LateCPS / Default

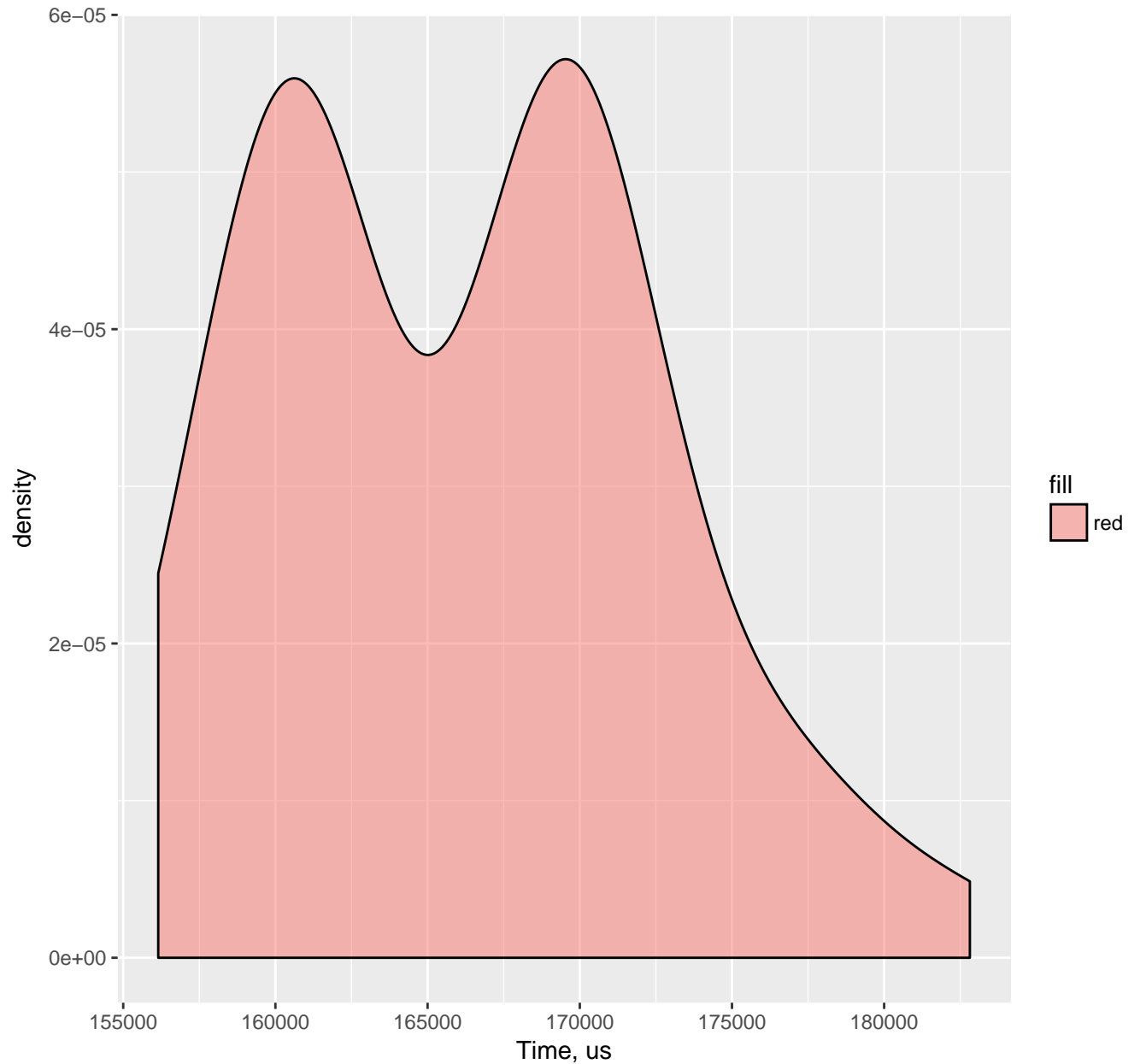


# CpsBenchmark+CpsPerformanceTests / LateCPS / Default

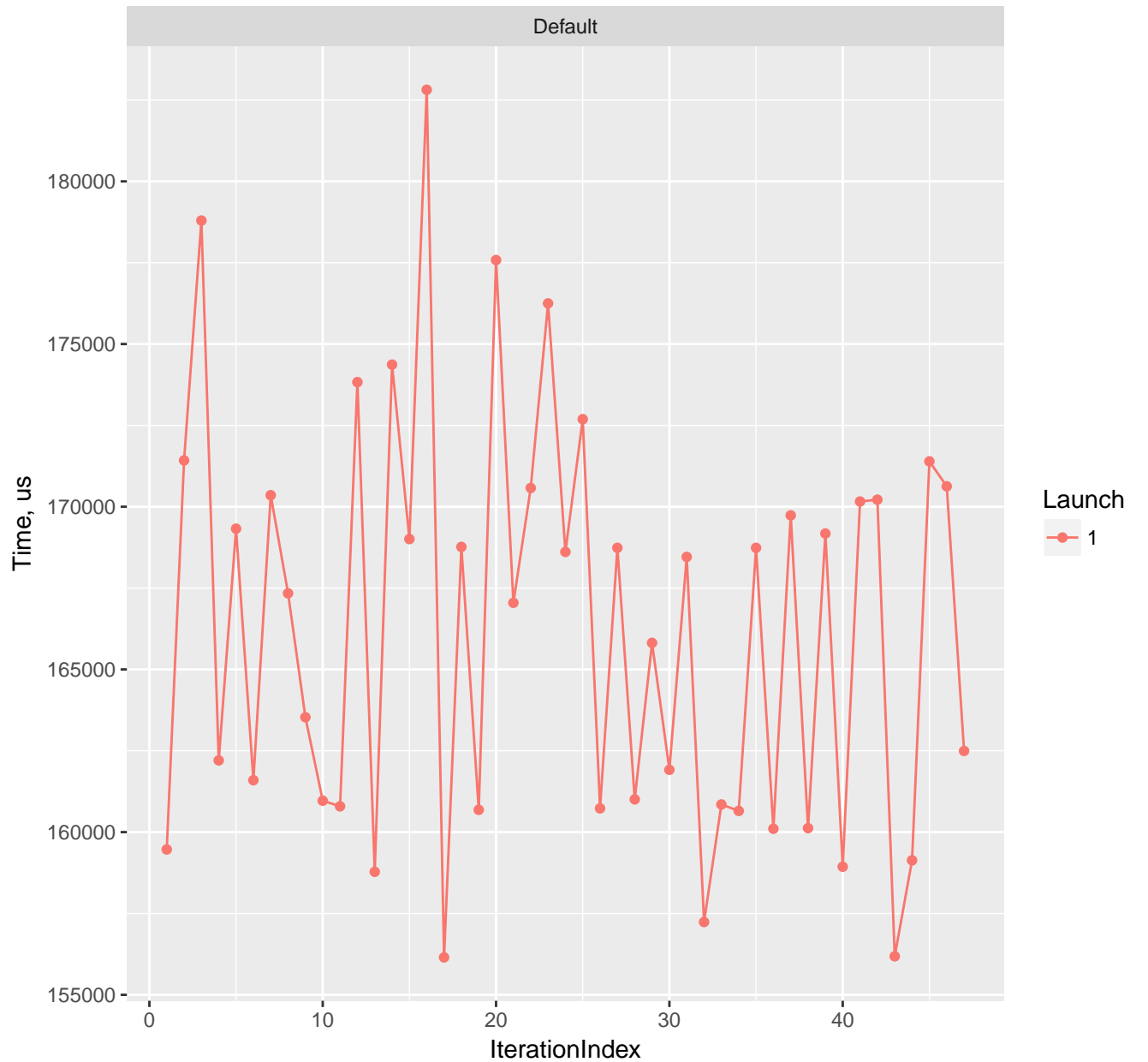




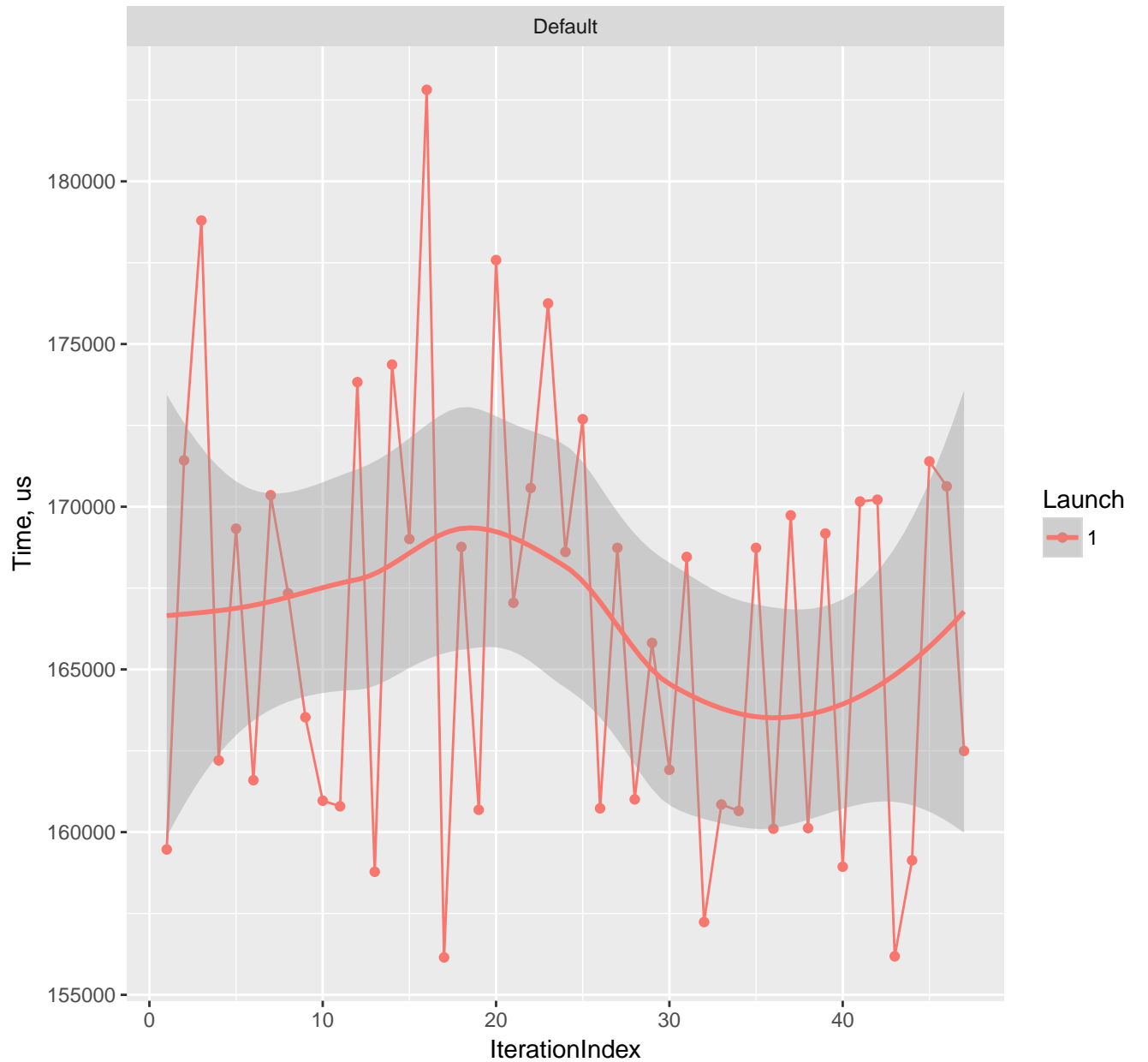
# CpsBenchmark+CpsPerformanceTests / LateCPS / Default



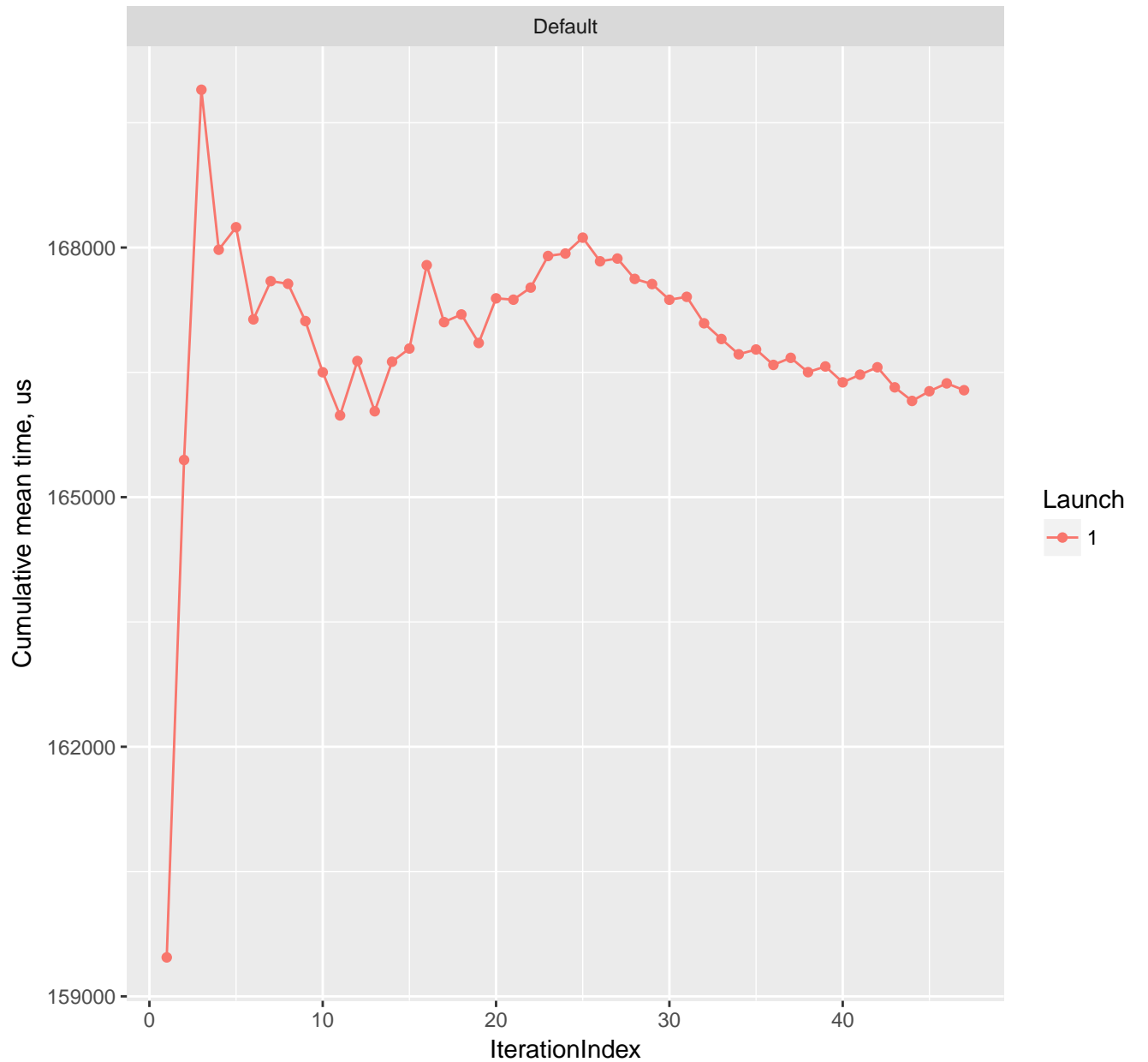
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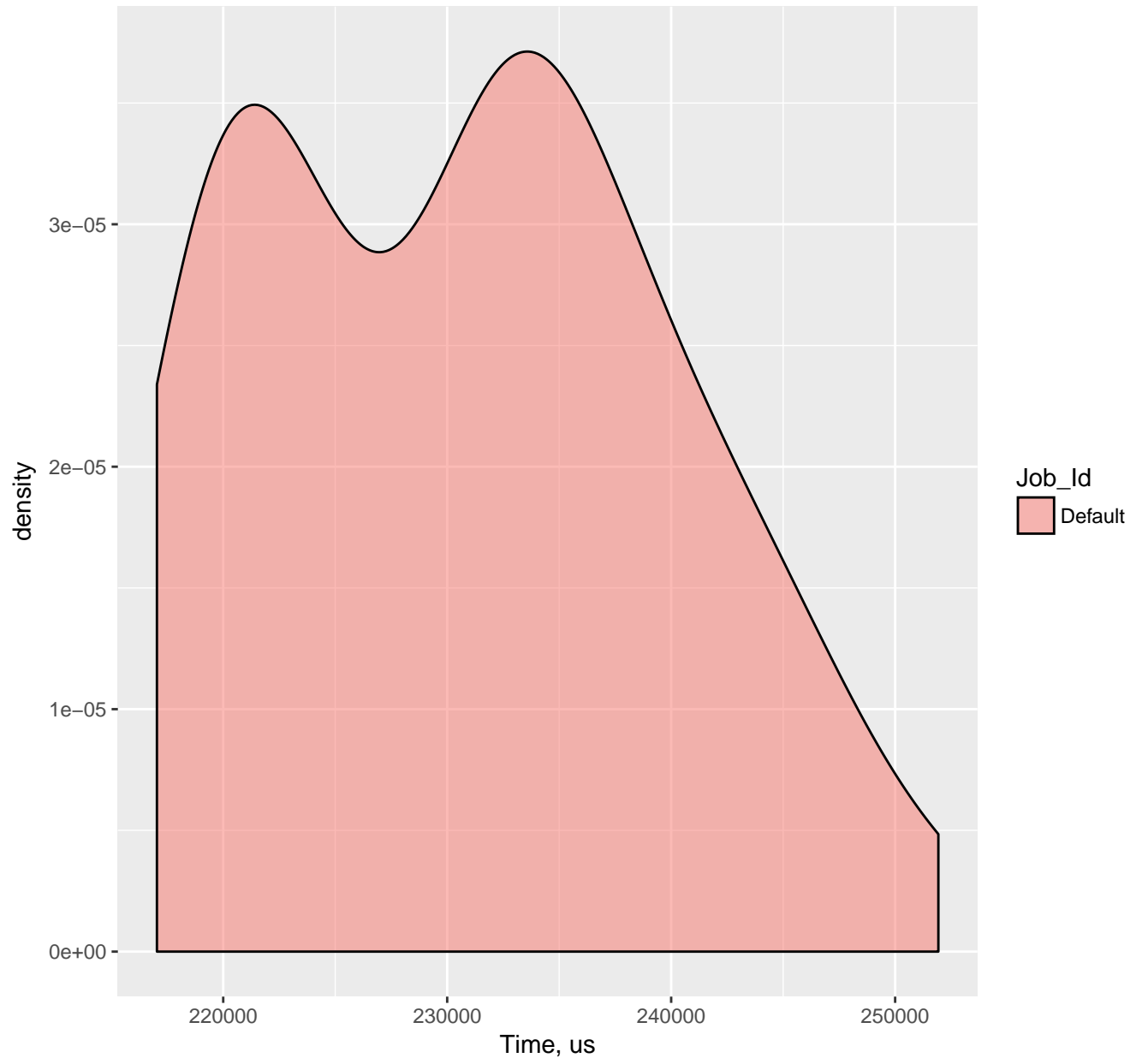


# CpsBenchmark+CpsPerformanceTests / LateCPS

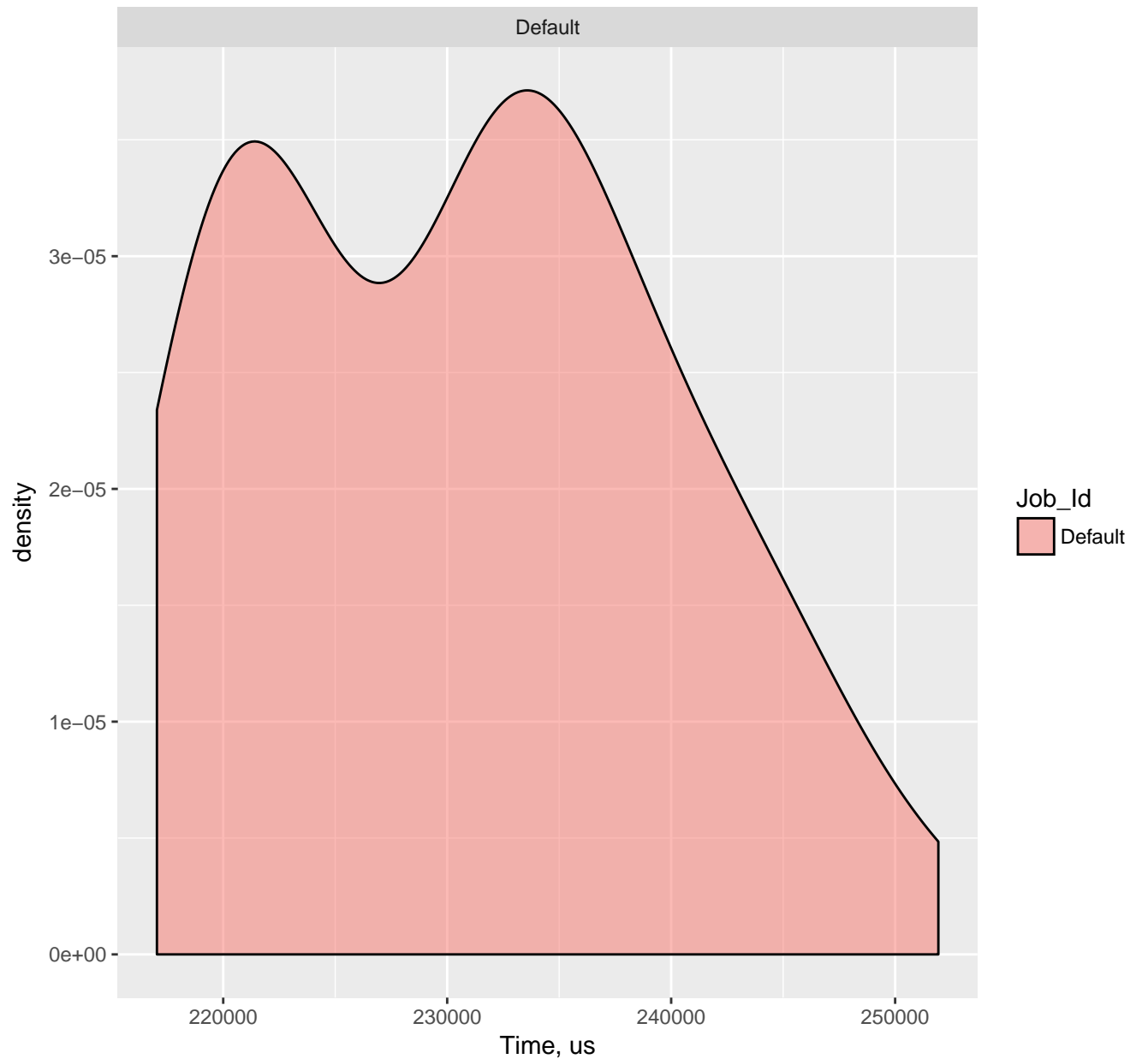


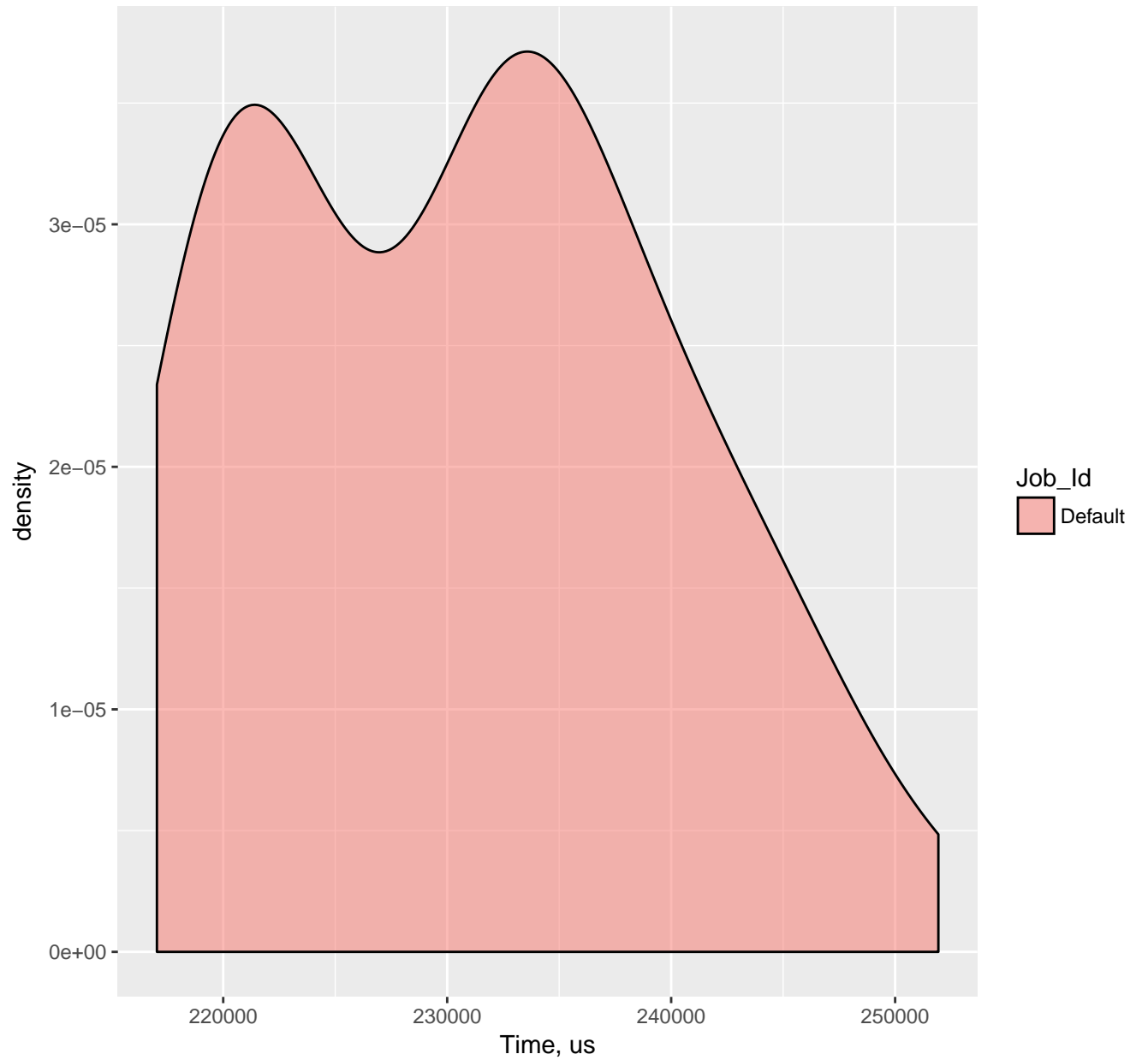
# CpsBenchmark+CpsPerformanceTests / LateCPS

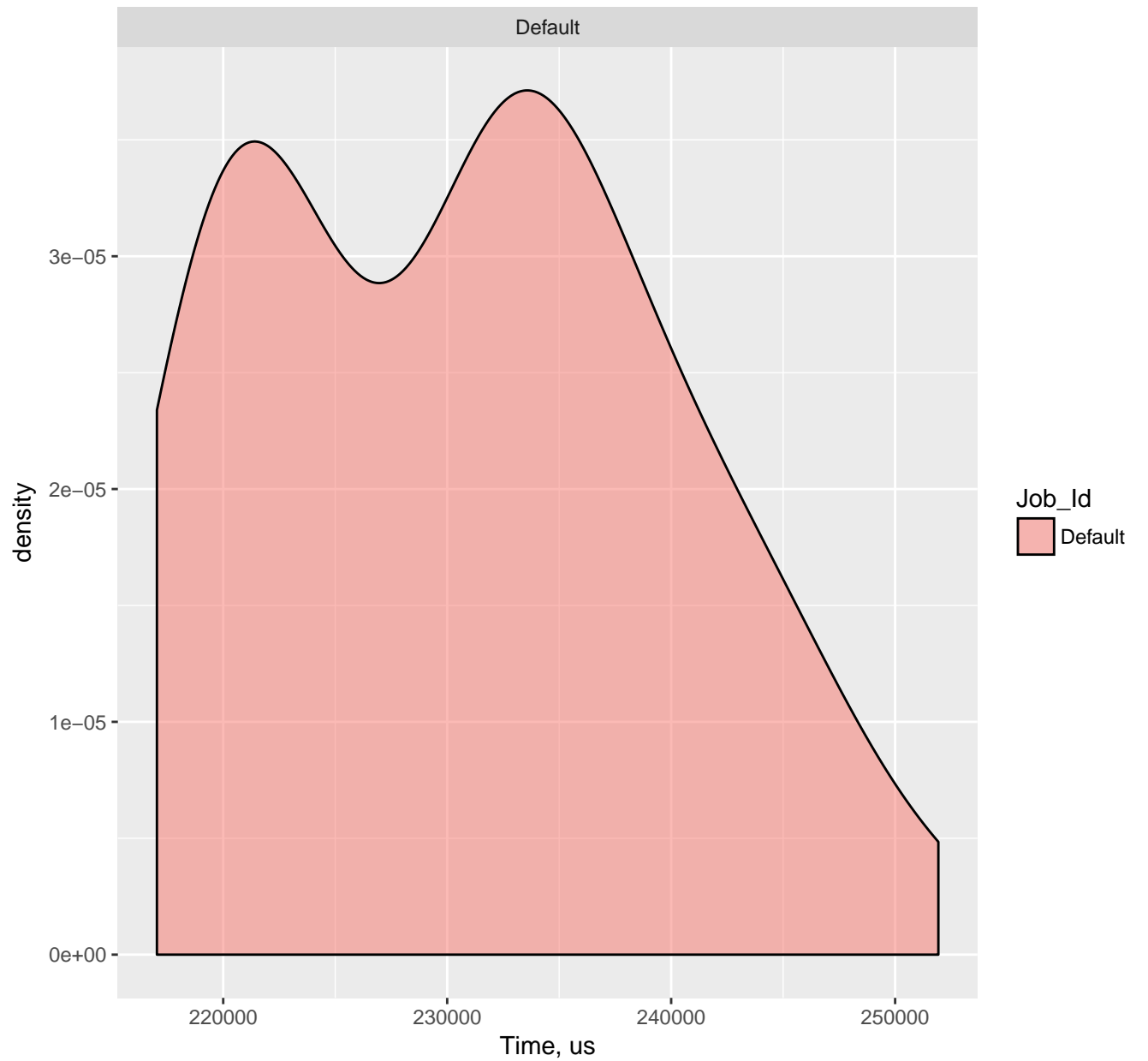




# CpsBenchmark+CpsPerformanceTests / LateAvm2

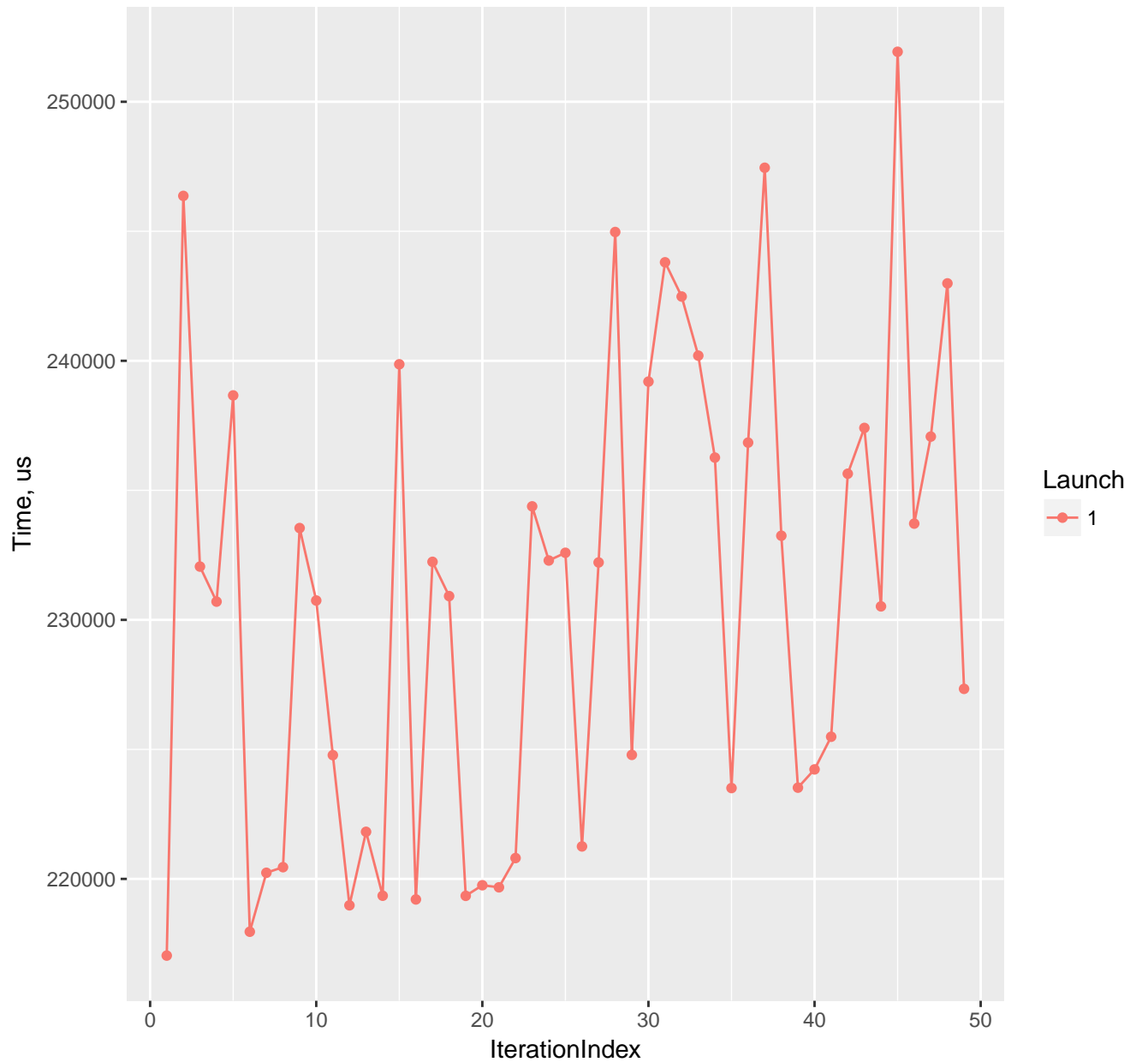




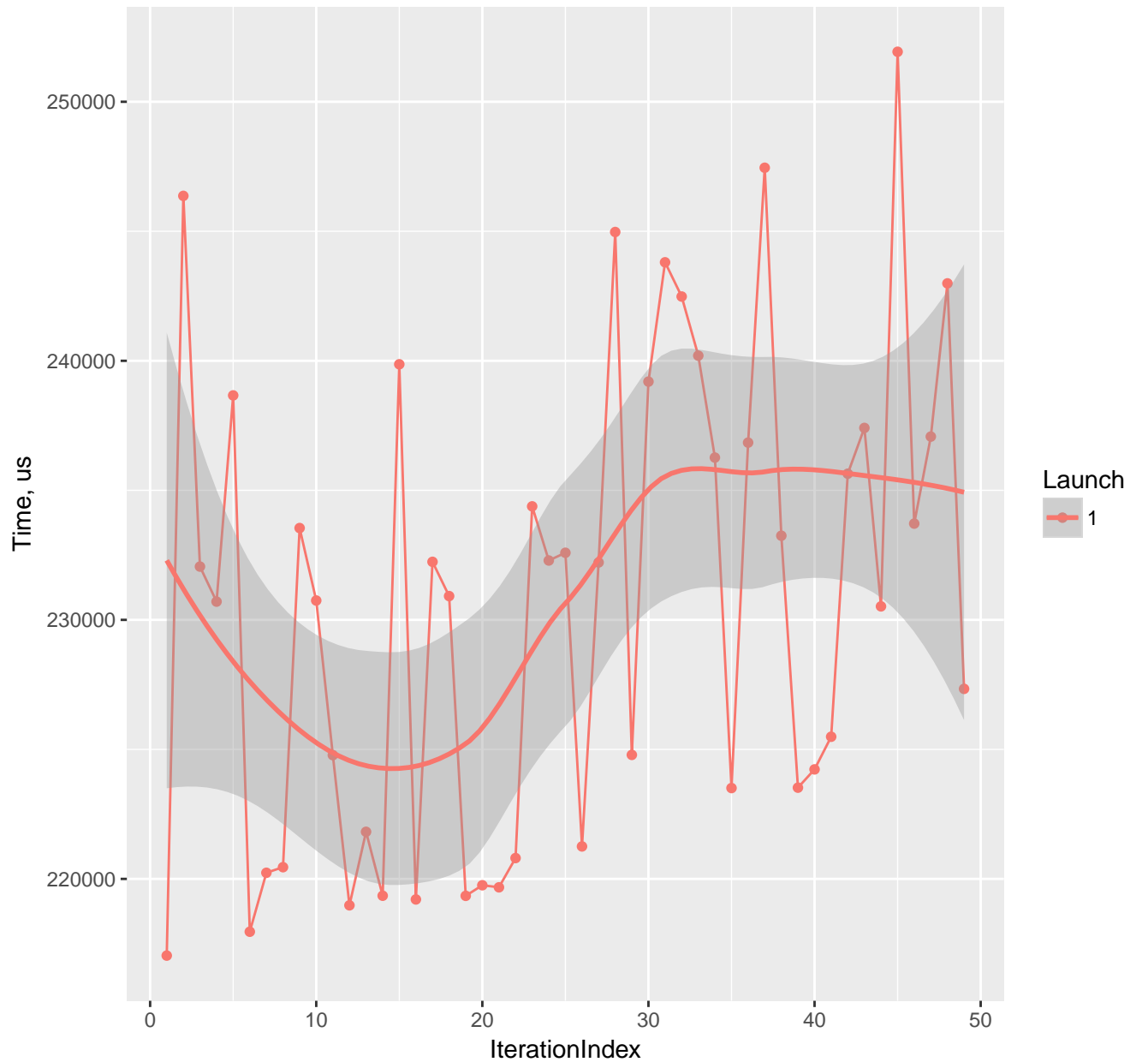




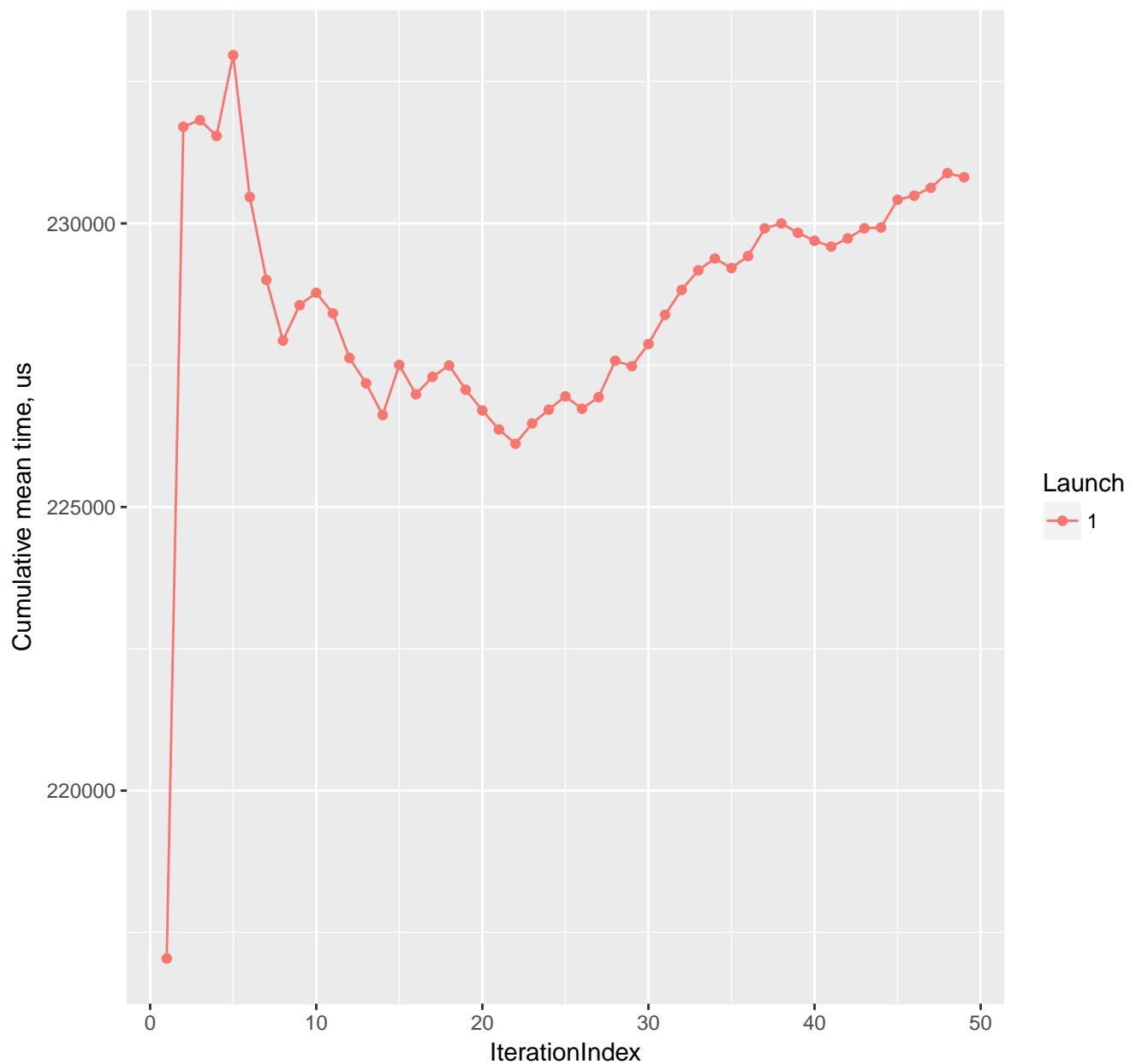
# CpsBenchmark+CpsPerformanceTests / LateAvm2 / Default

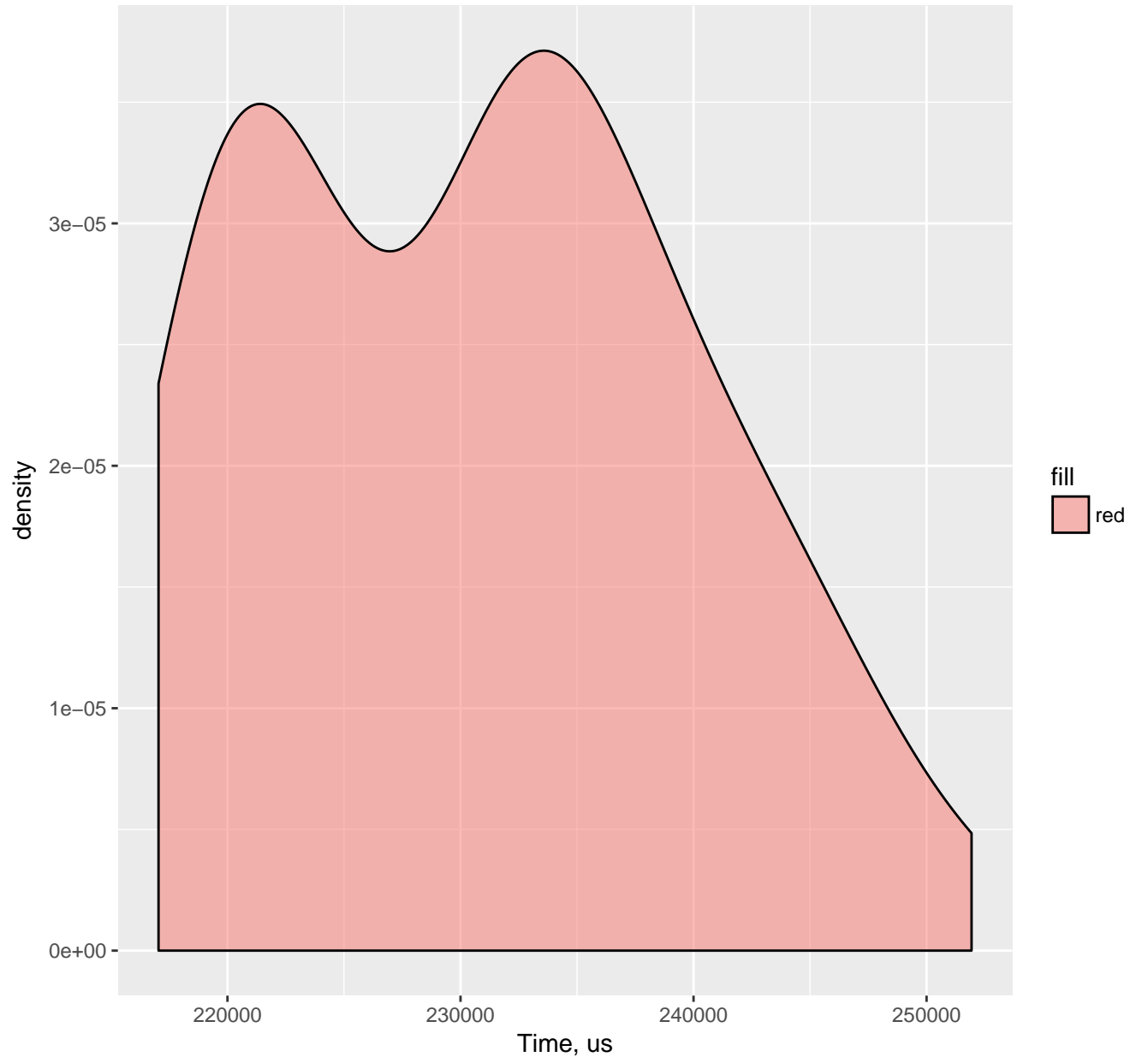


# CpsBenchmark+CpsPerformanceTests / LateAvm2 / Default

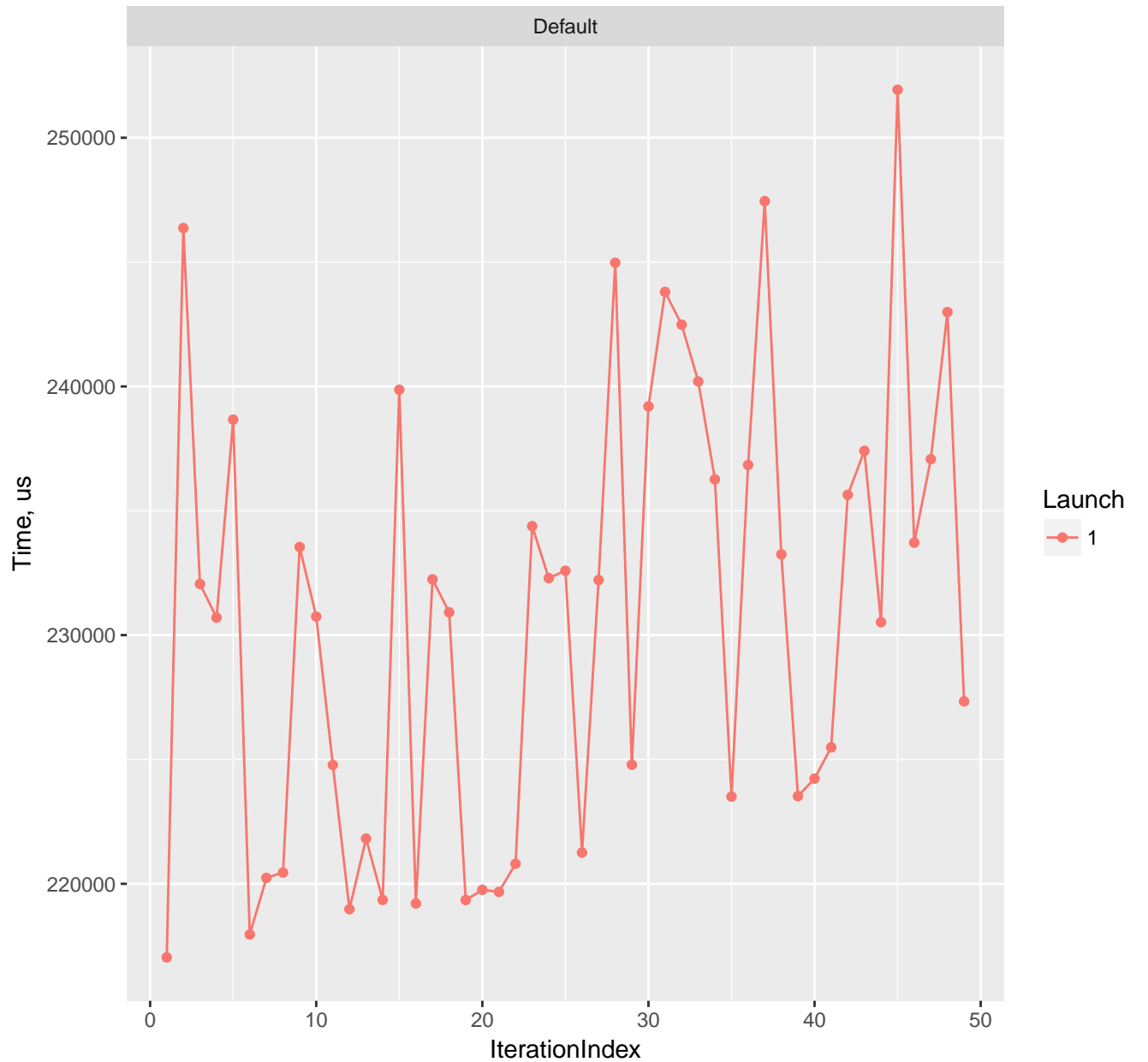


# CpsBenchmark+CpsPerformanceTests / LateAvm2 / Default

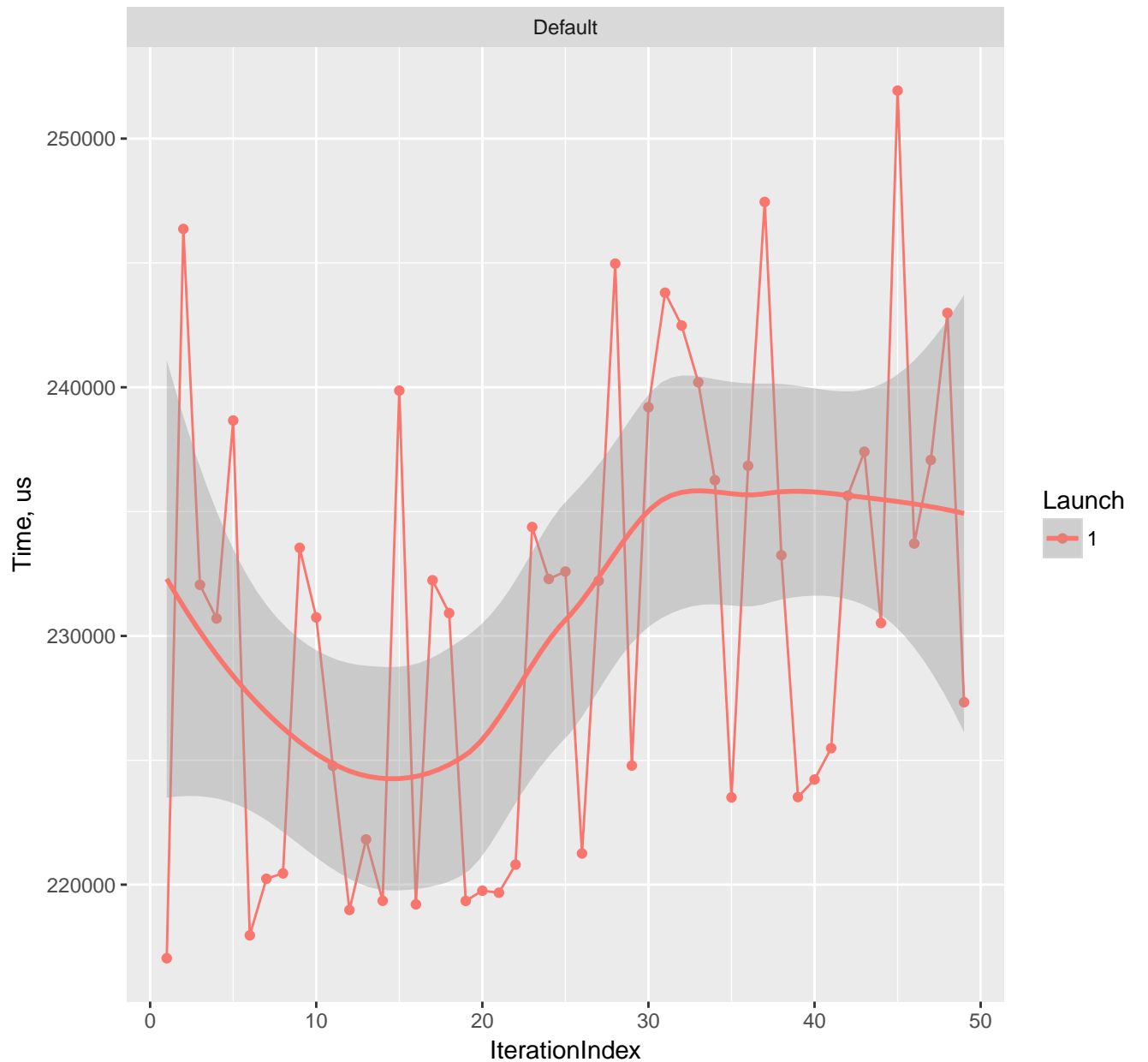




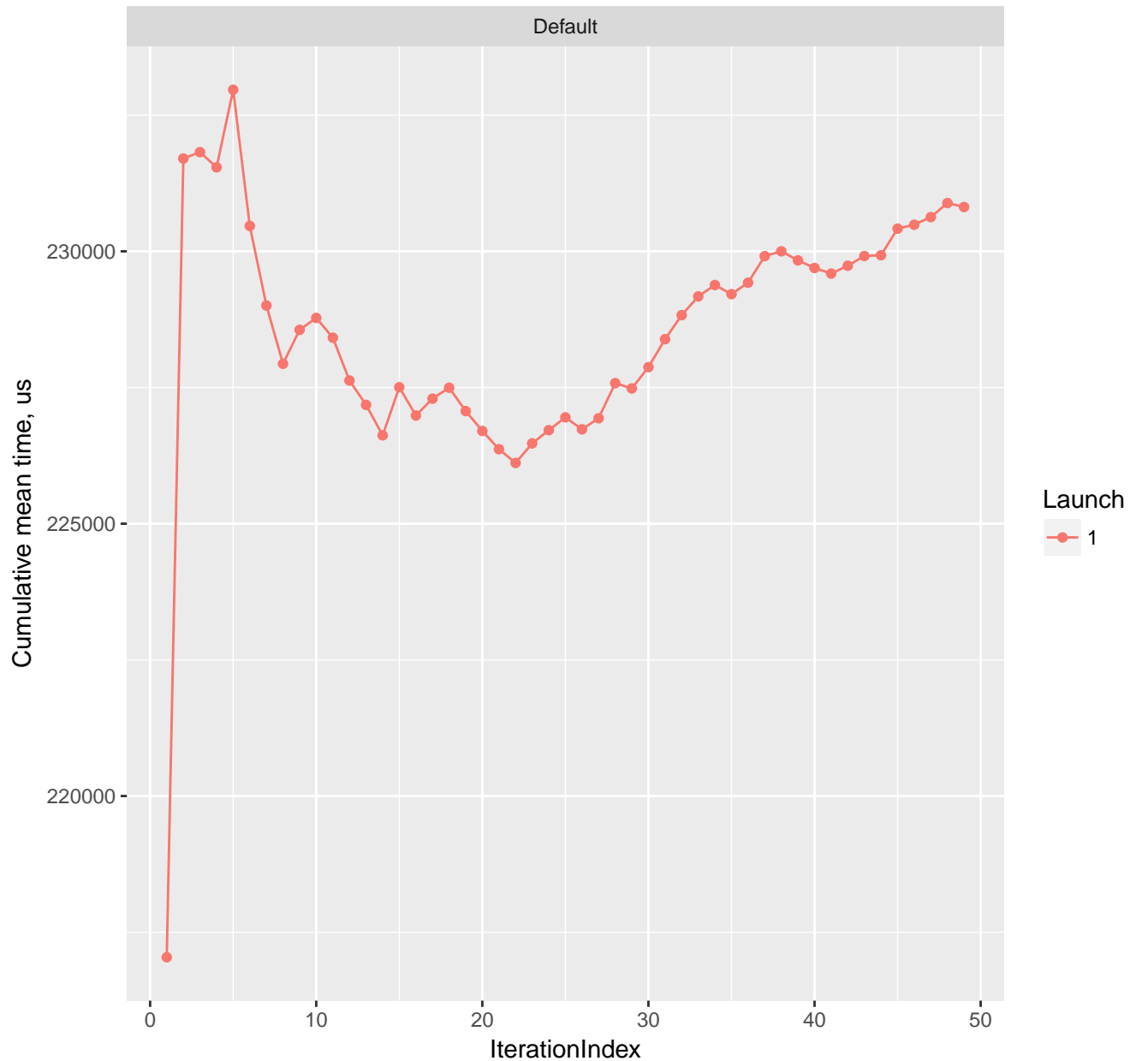
# CpsBenchmark+CpsPerformanceTests / LateAvm2



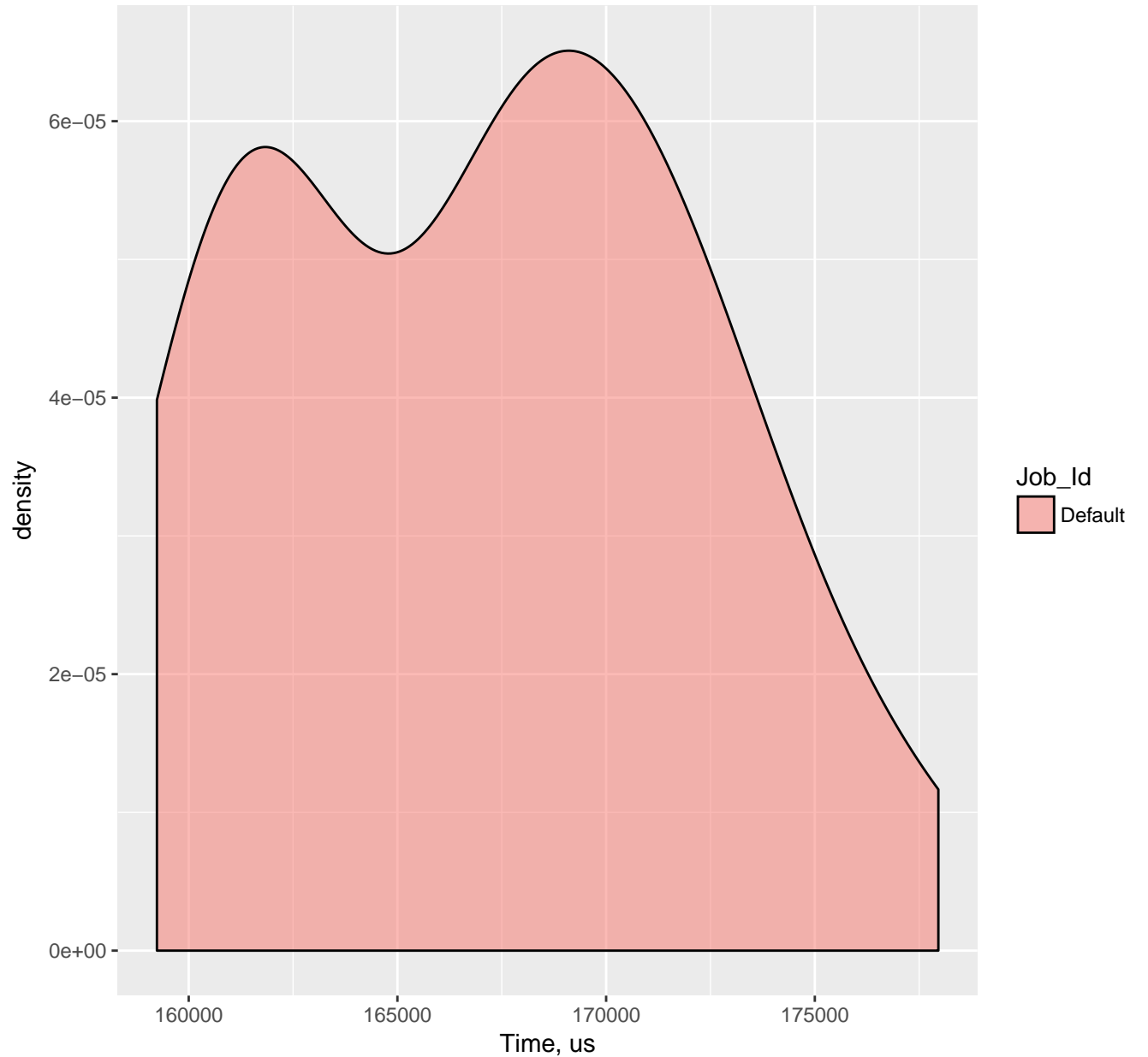
# CpsBenchmark+CpsPerformanceTests / LateAvm2



# CpsBenchmark+CpsPerformanceTests / LateAvm2

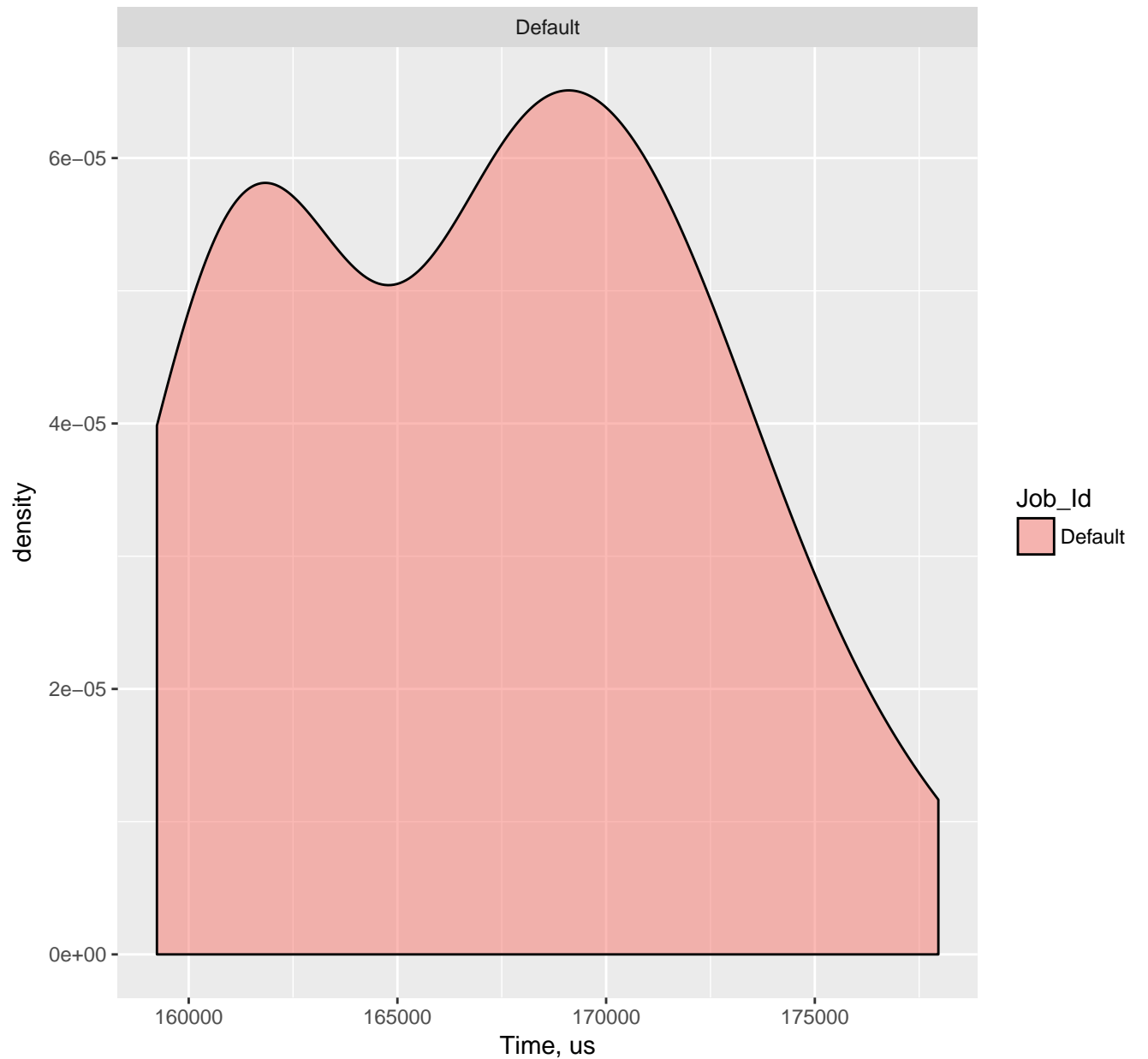


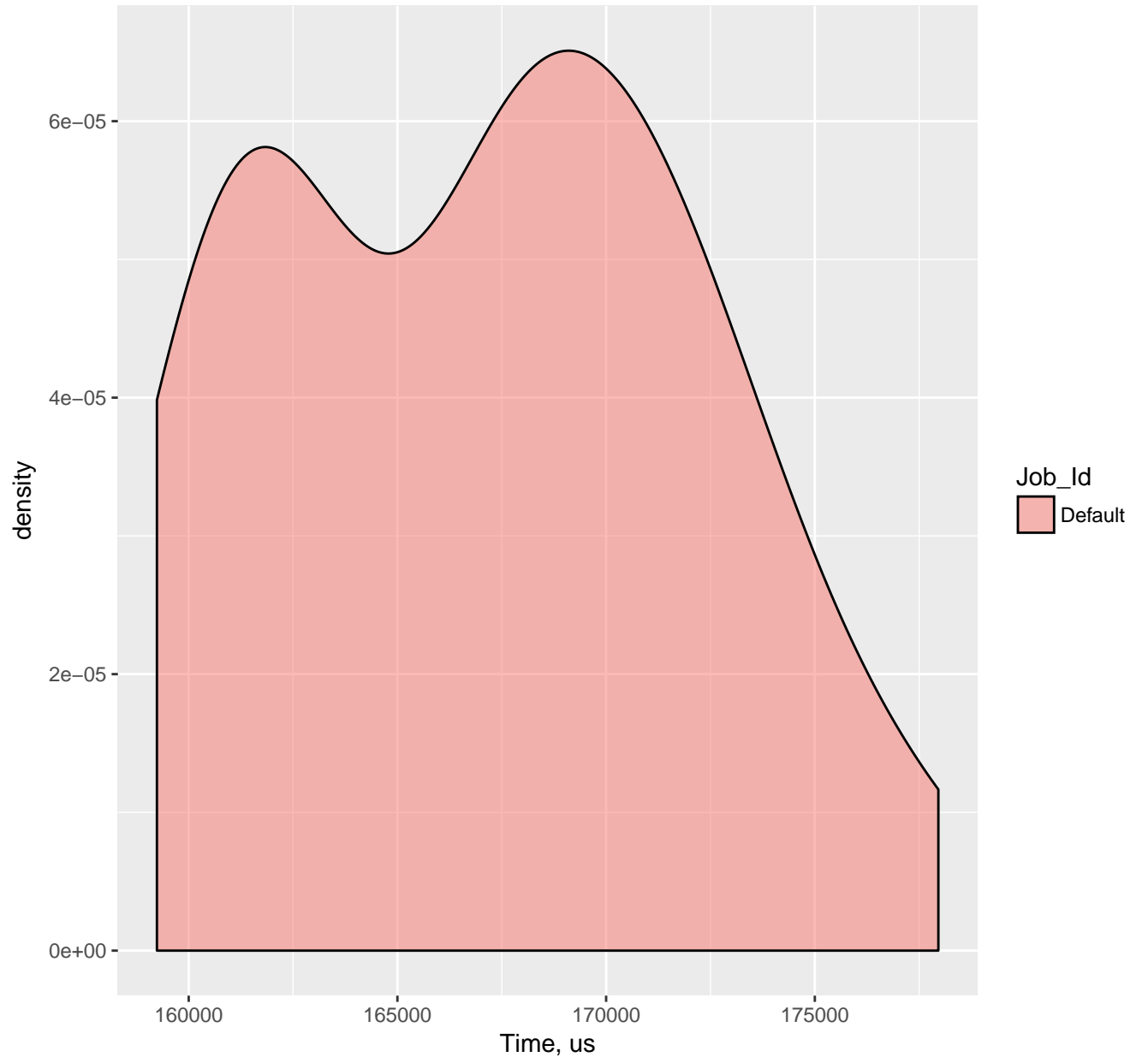
# CpsBenchmark+CpsPerformanceTests / LateAvm3

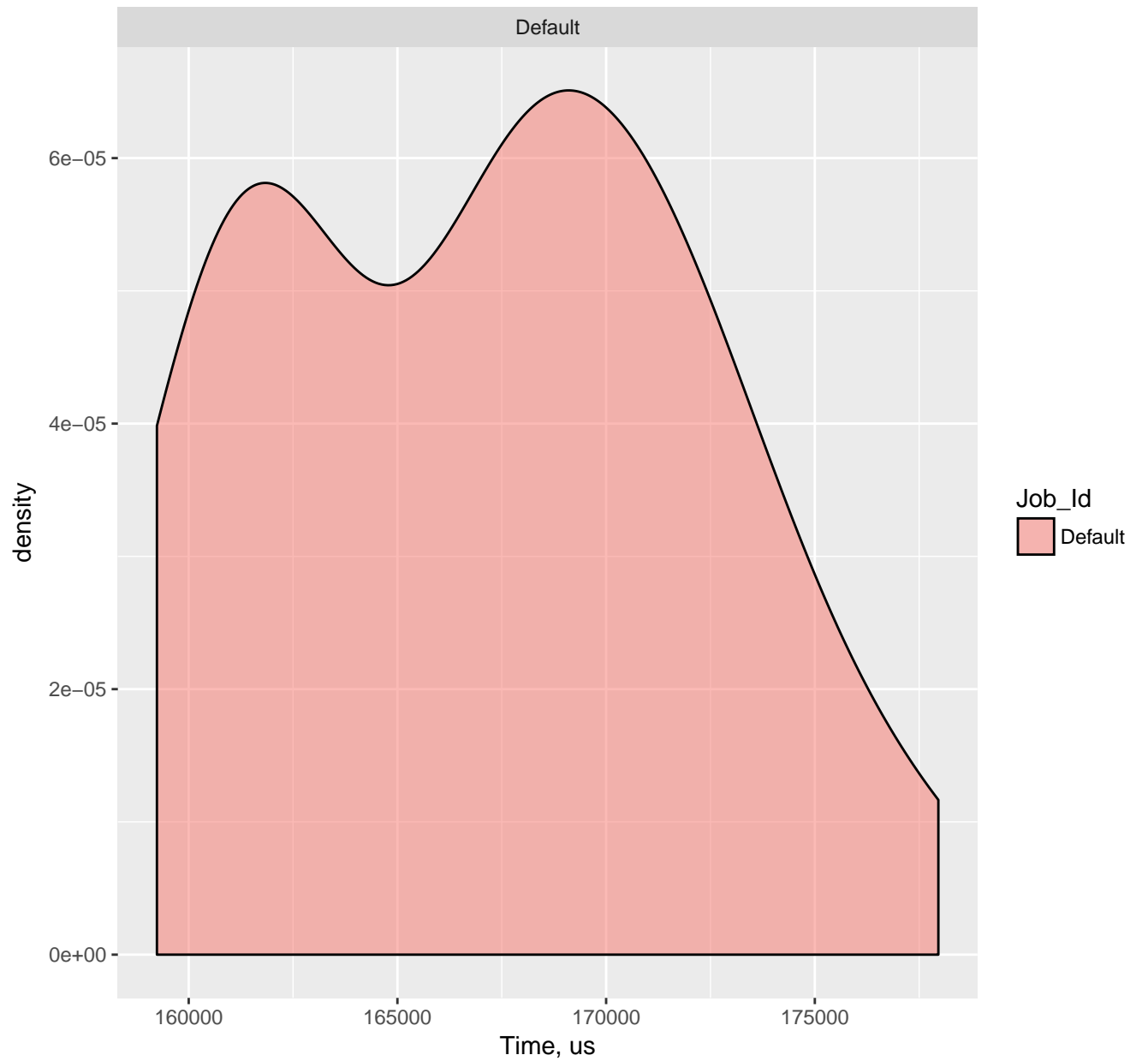




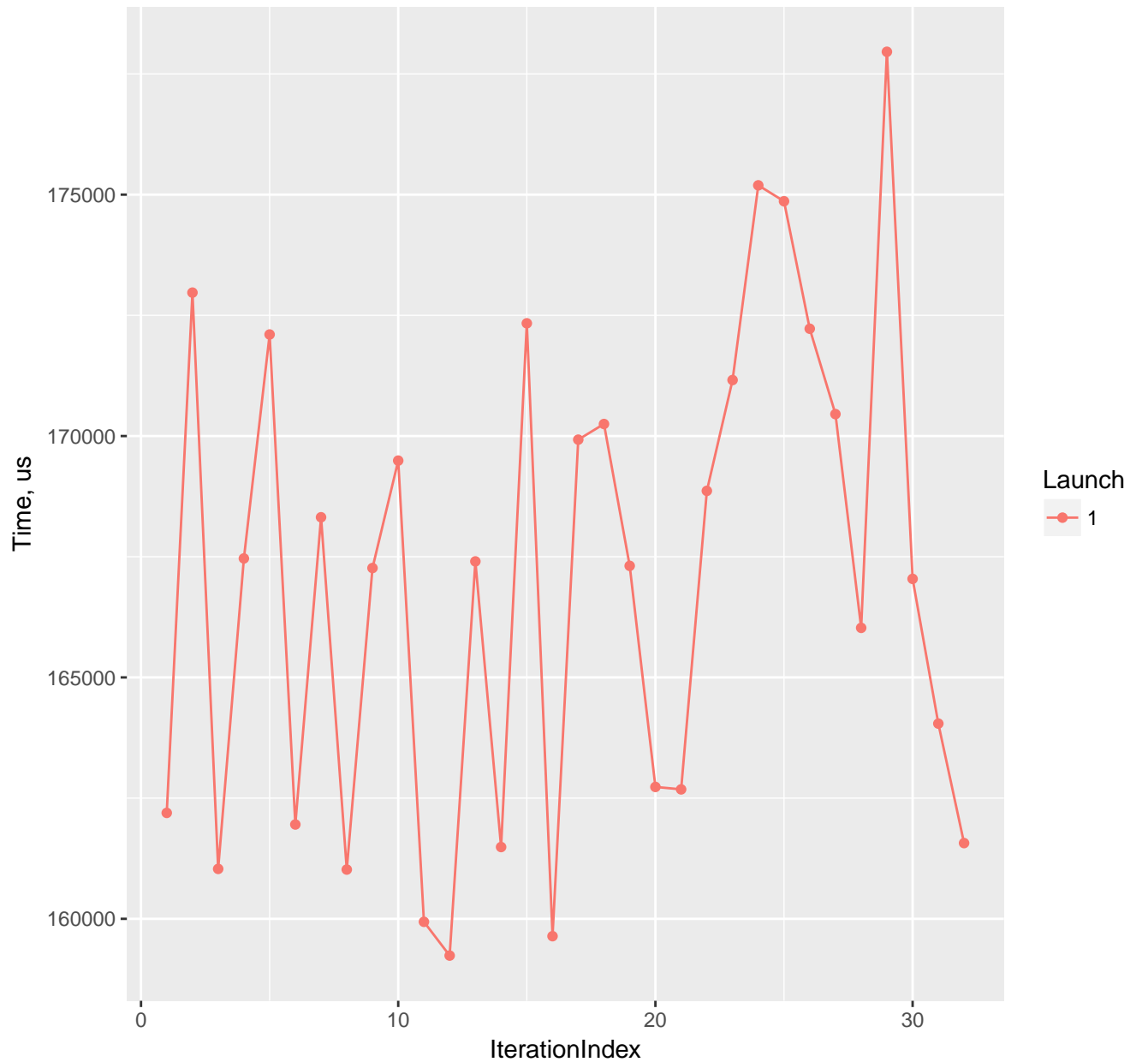
# CpsBenchmark+CpsPerformanceTests / LateAvm3



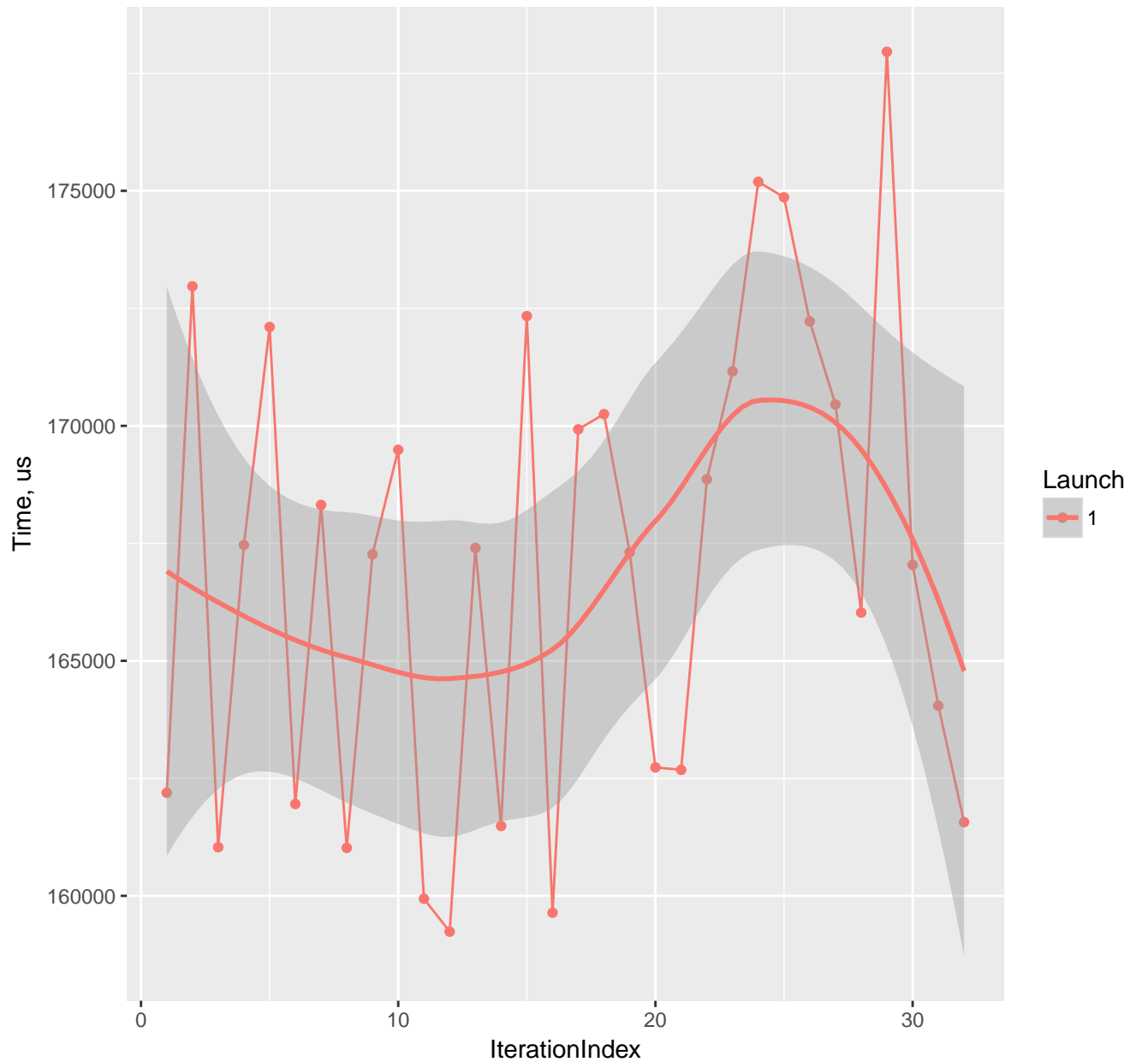




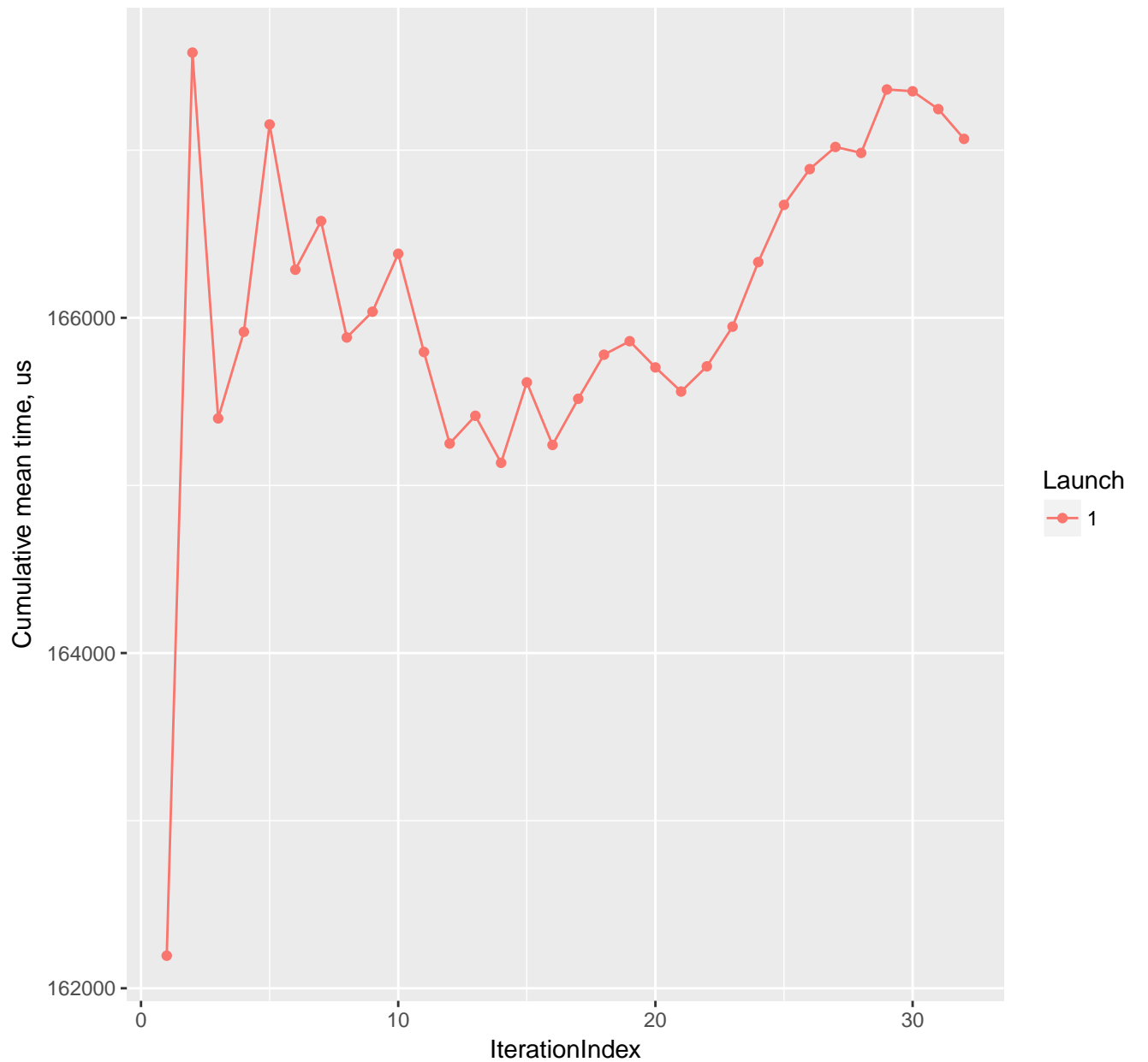
# CpsBenchmark+CpsPerformanceTests / LateAvm3 / Default

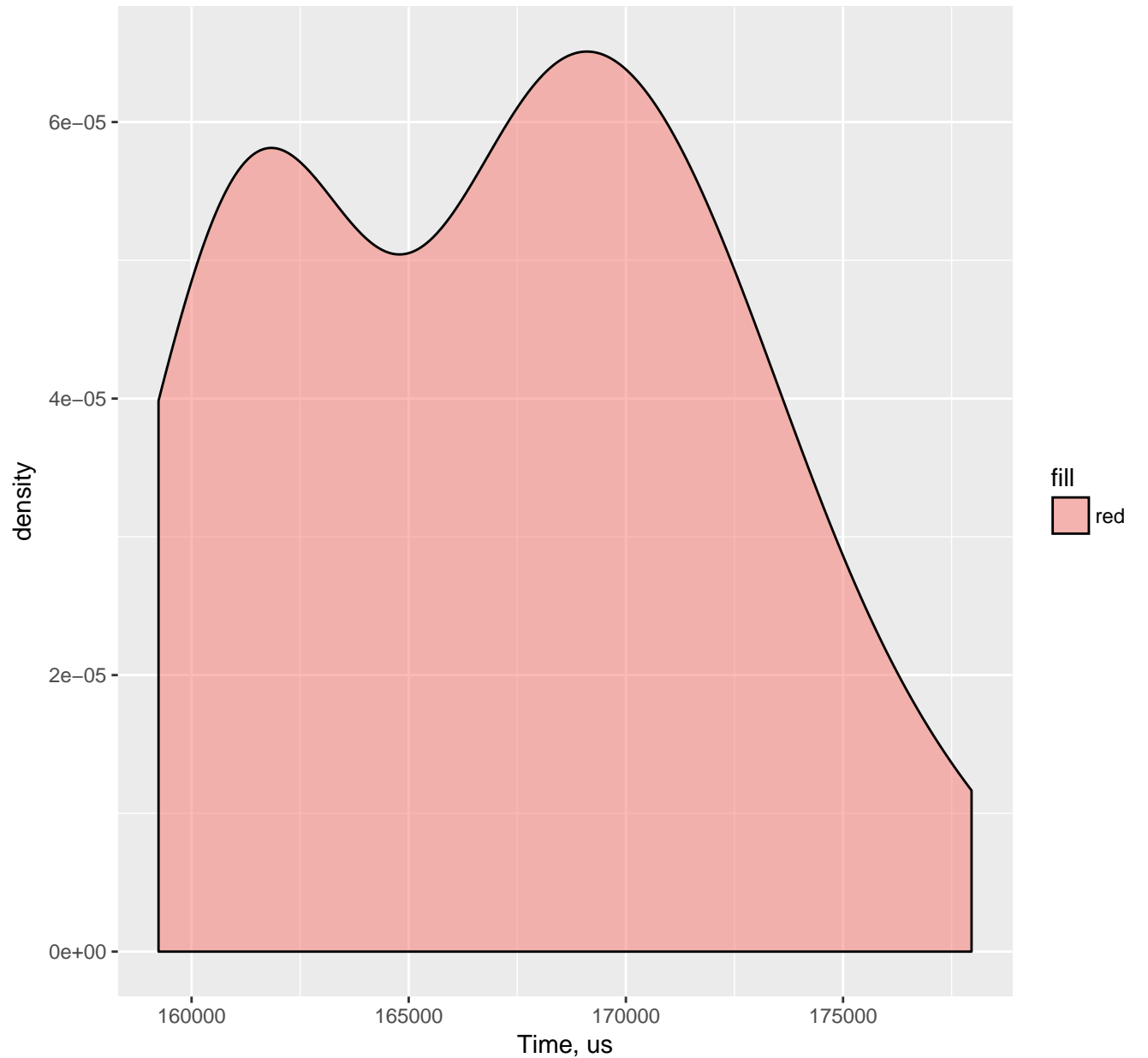


# CpsBenchmark+CpsPerformanceTests / LateAvm3 / Default

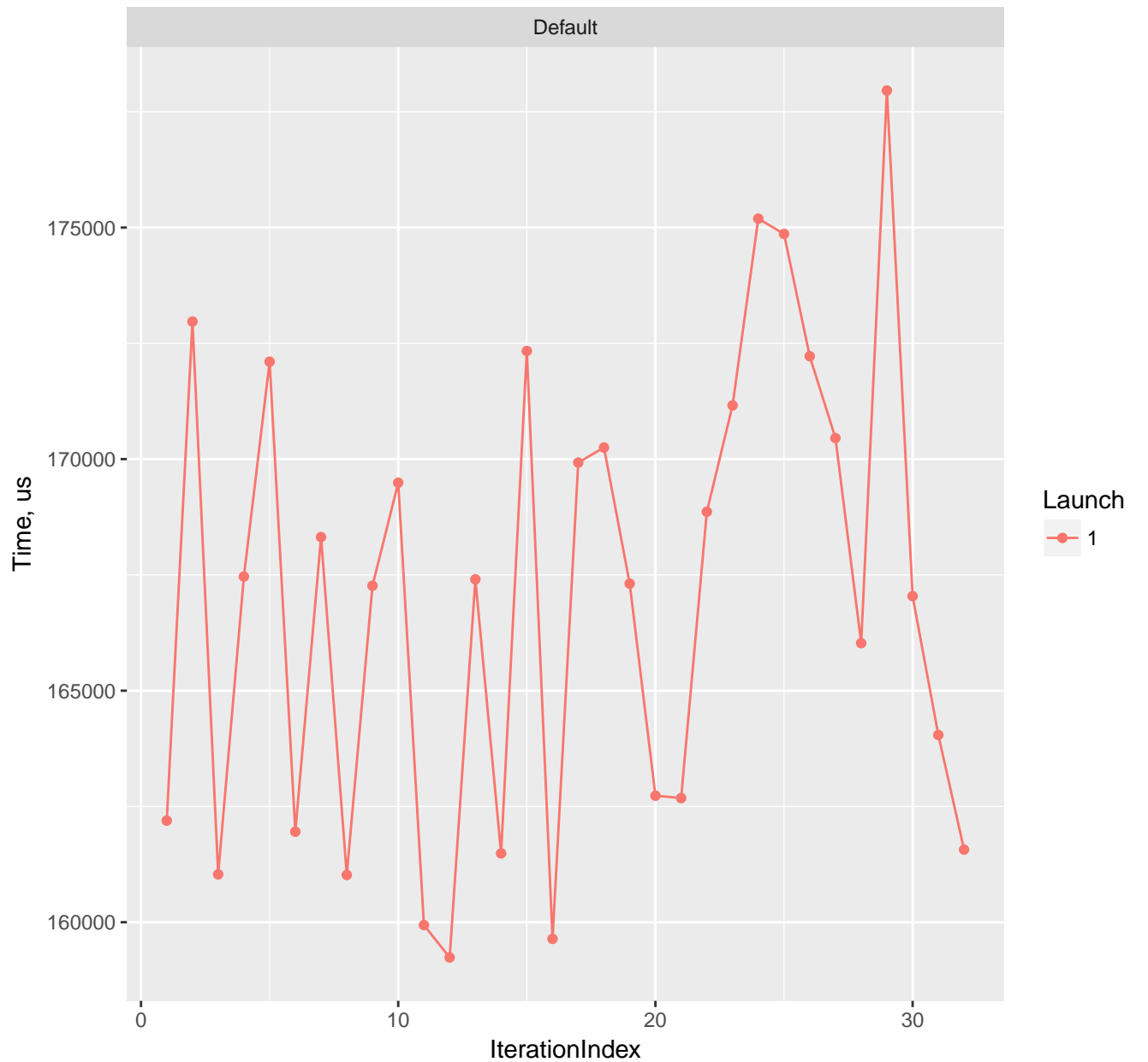


# CpsBenchmark+CpsPerformanceTests / LateAvm3 / Default



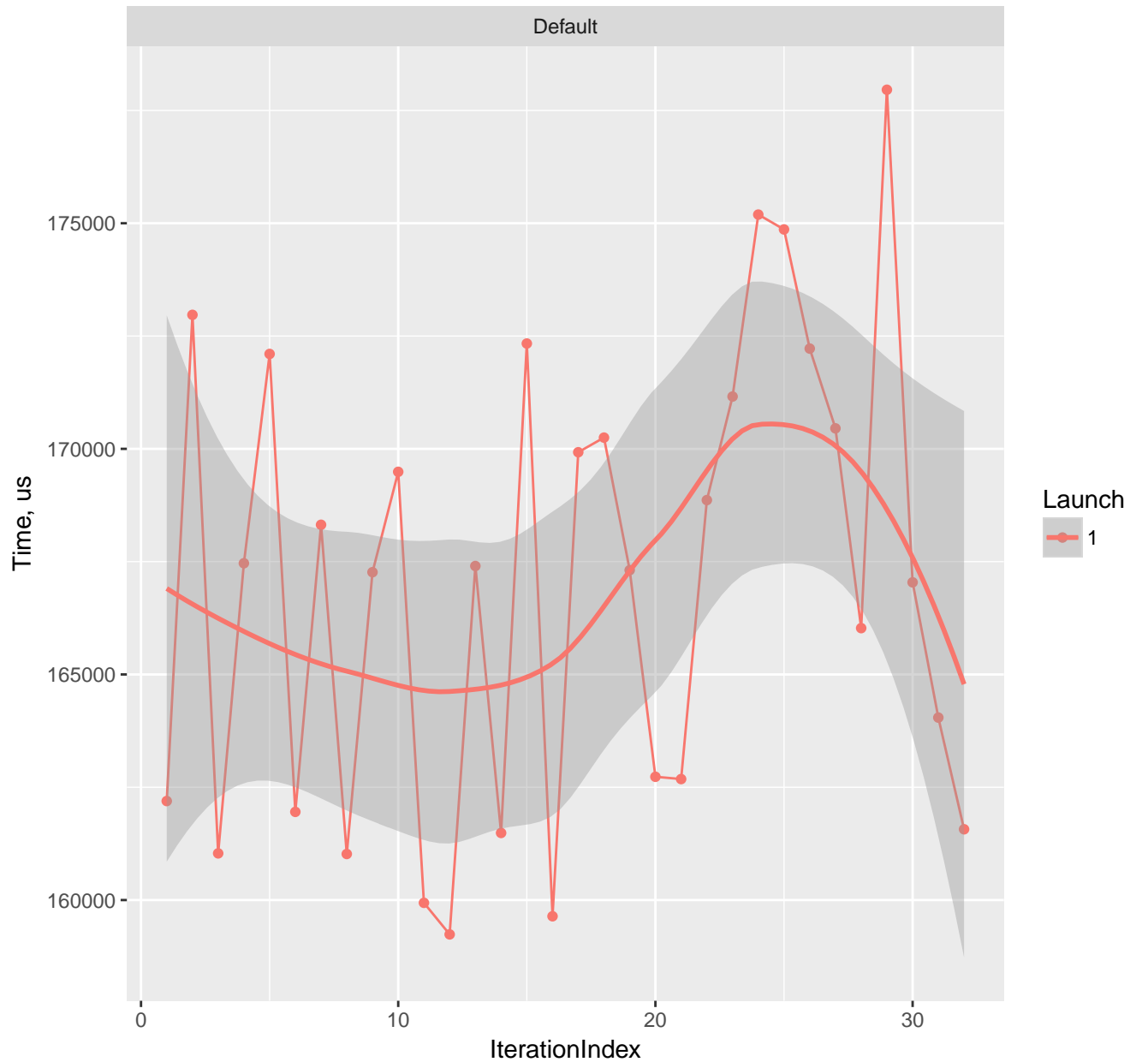


# CpsBenchmark+CpsPerformanceTests / LateAvm3

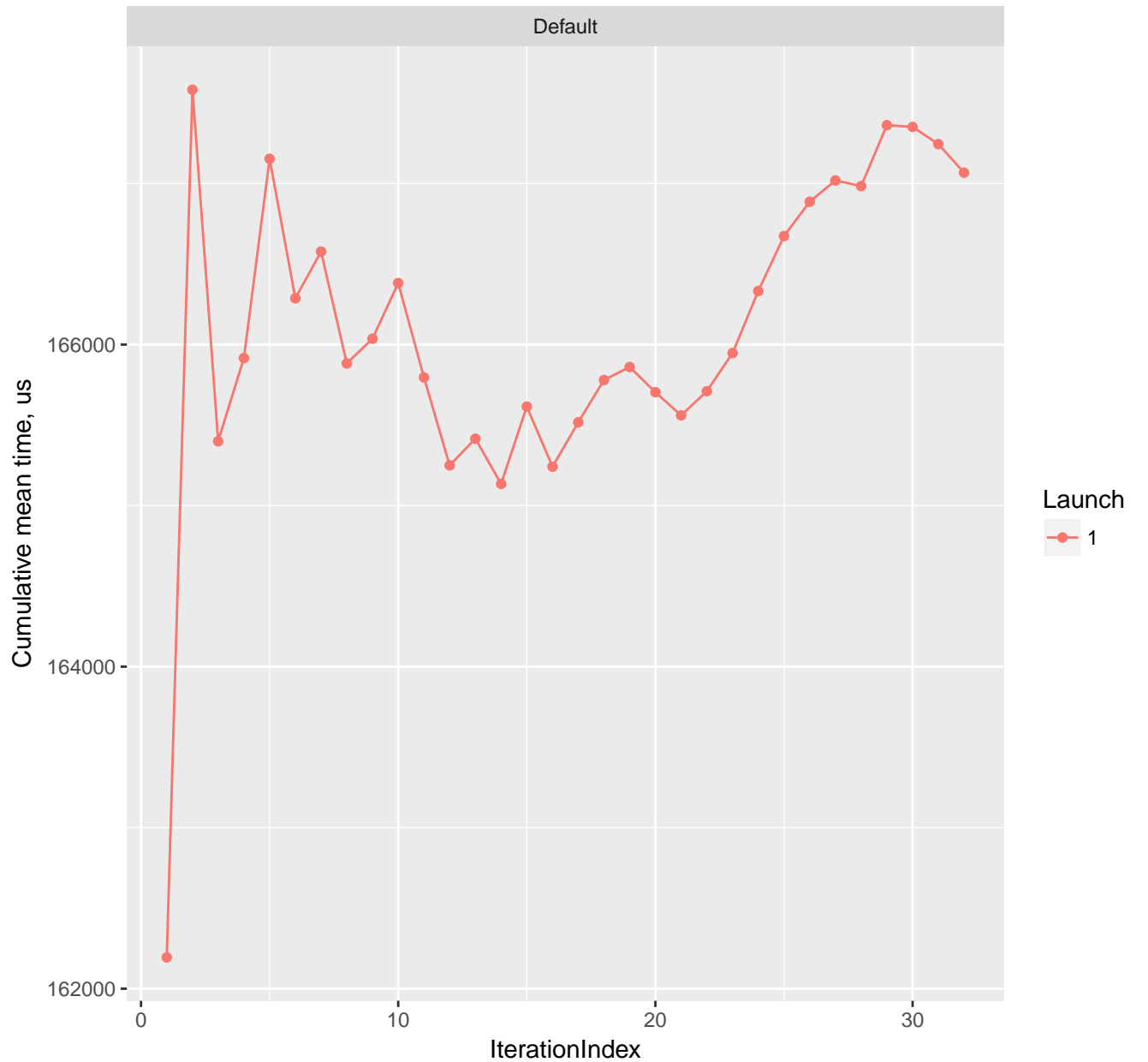




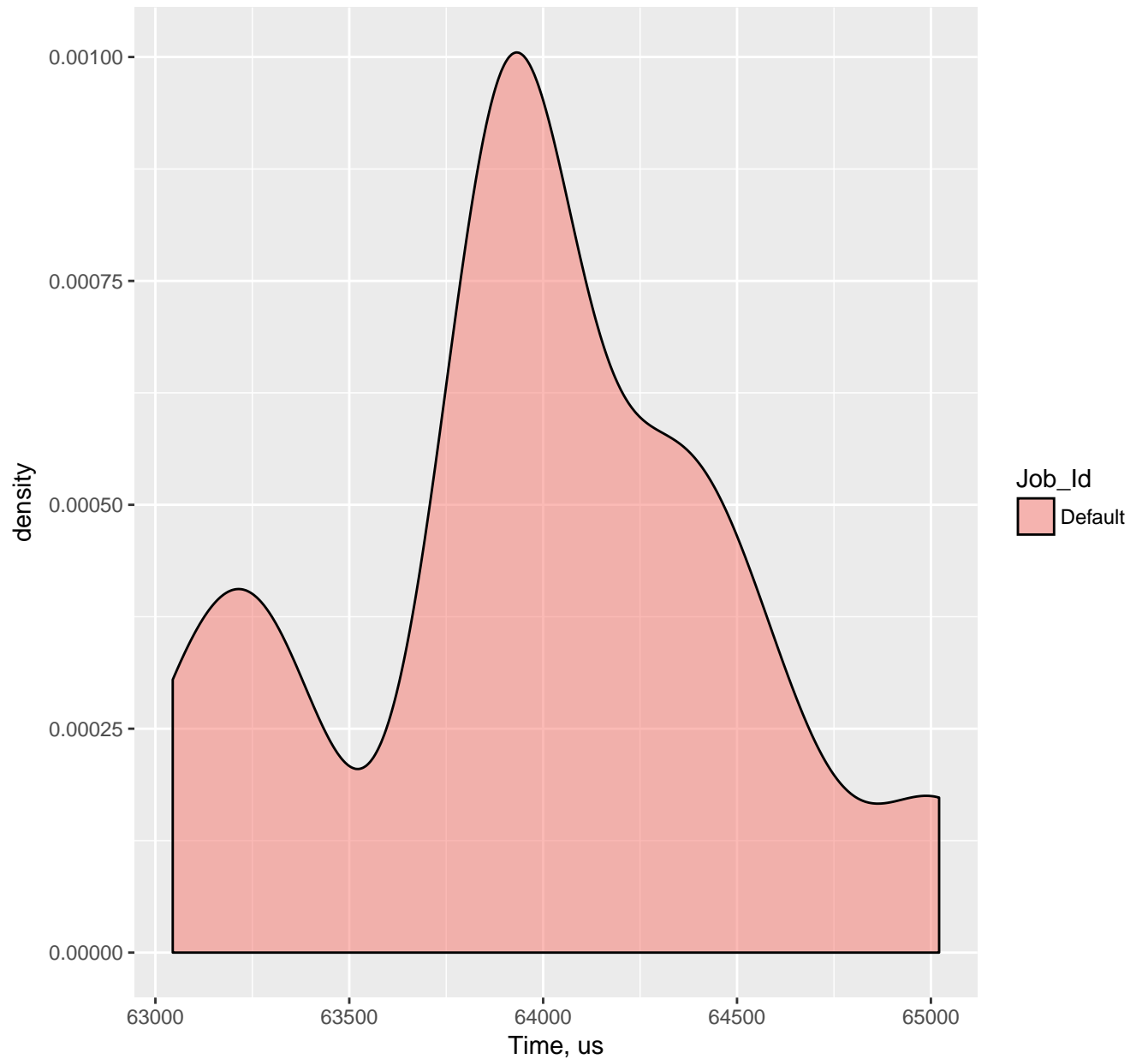
# CpsBenchmark+CpsPerformanceTests / LateAvm3



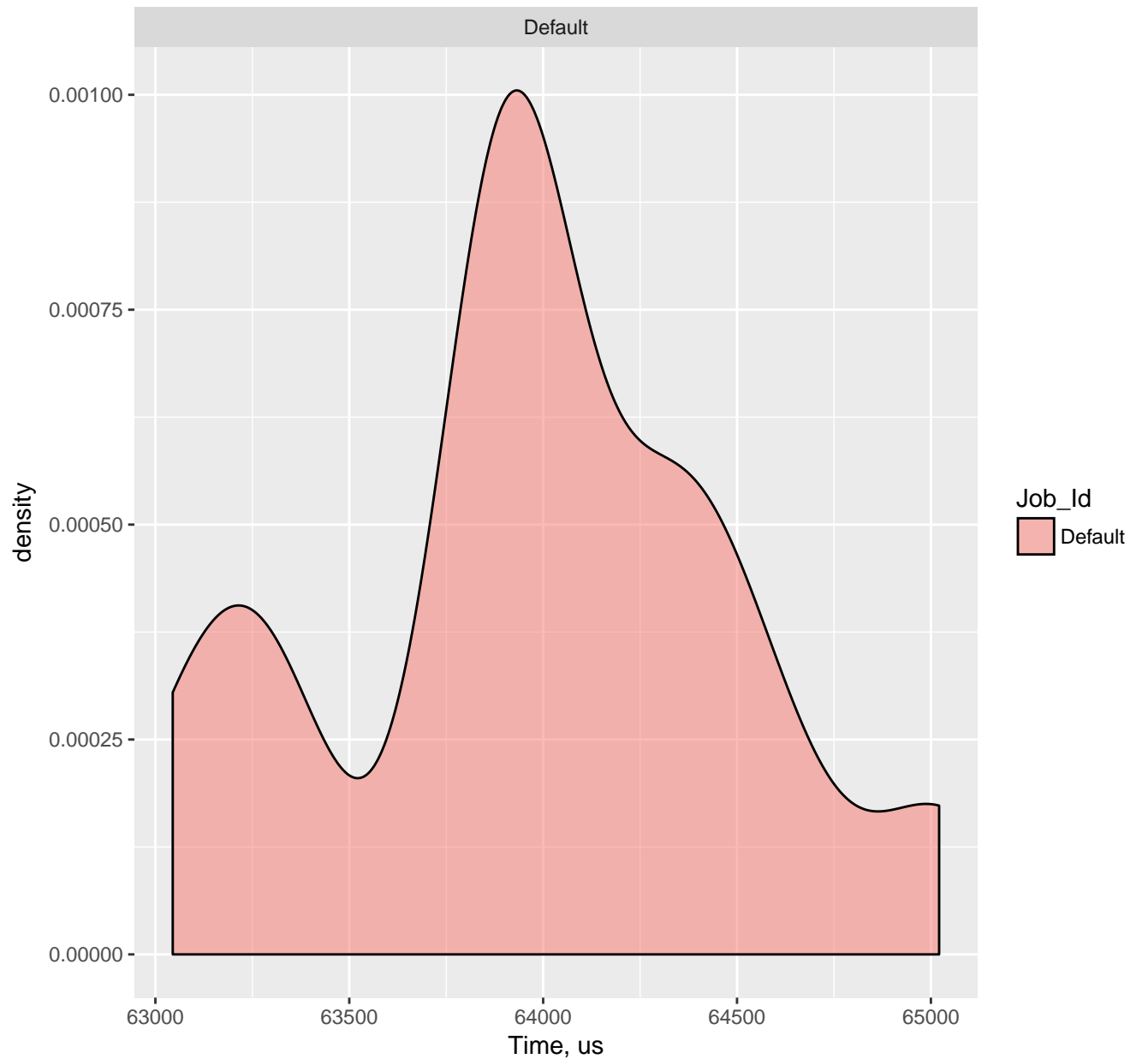
# CpsBenchmark+CpsPerformanceTests / LateAvm3

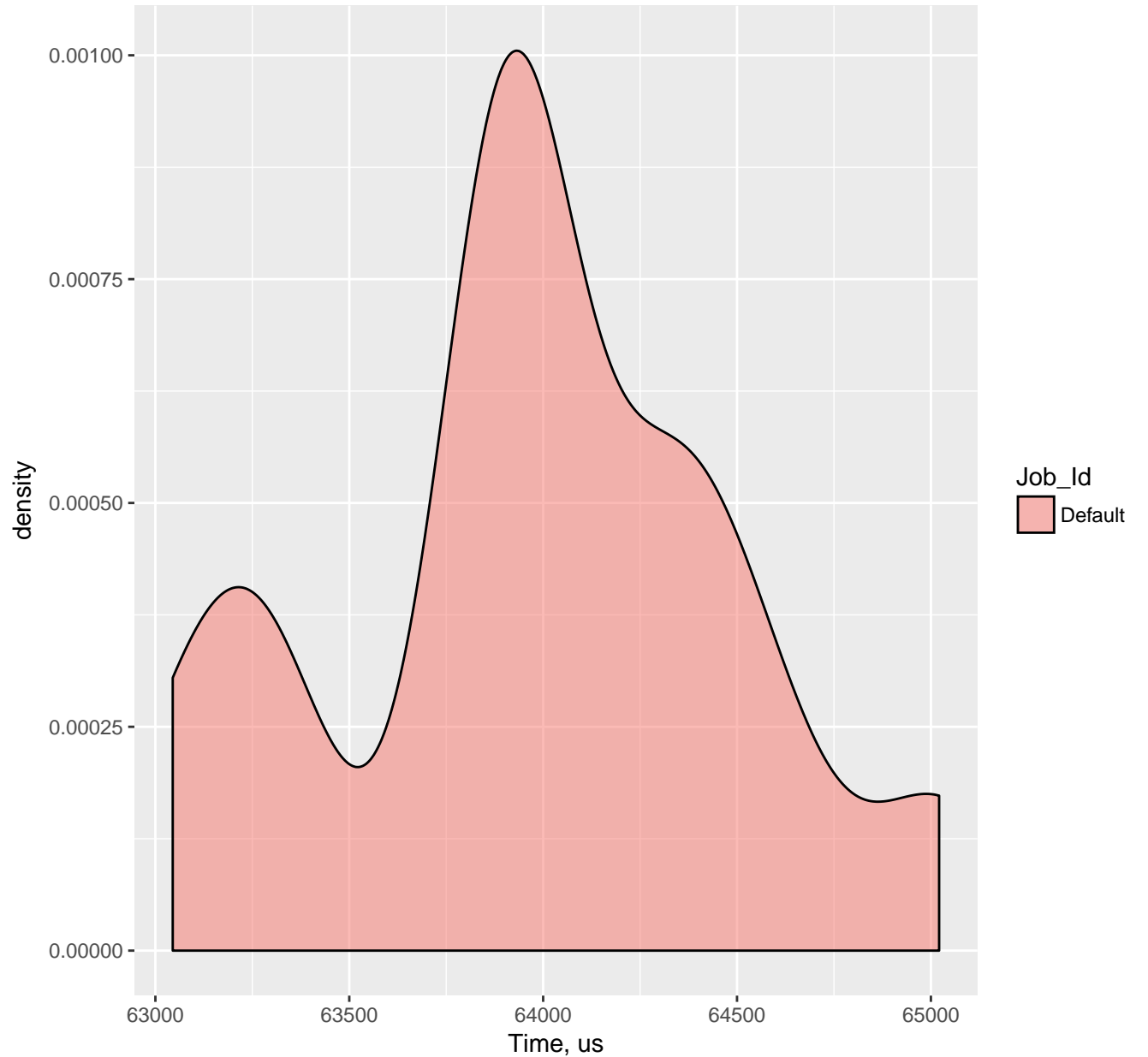


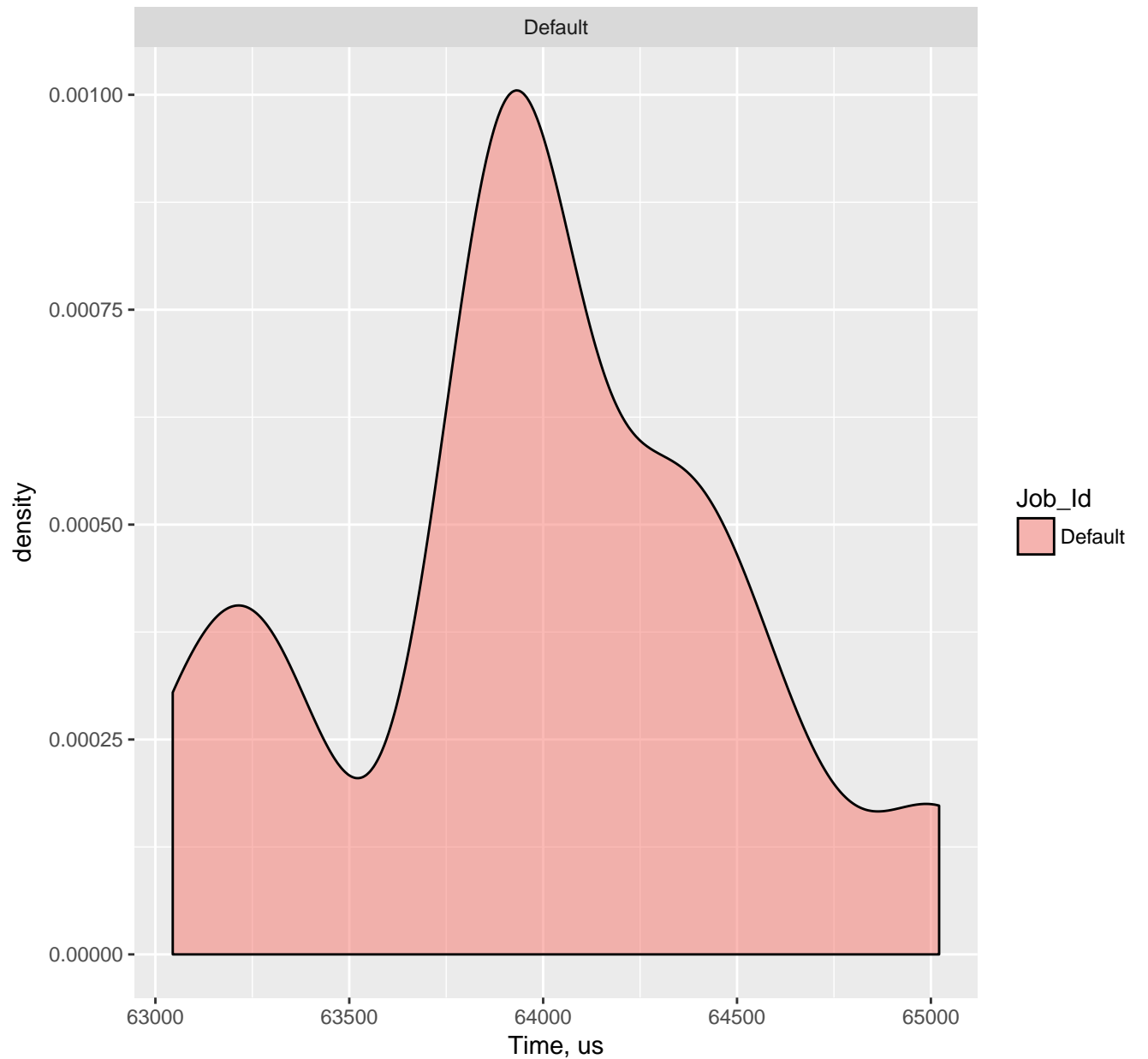
# CpsBenchmark+CpsPerformanceTests / NotFound



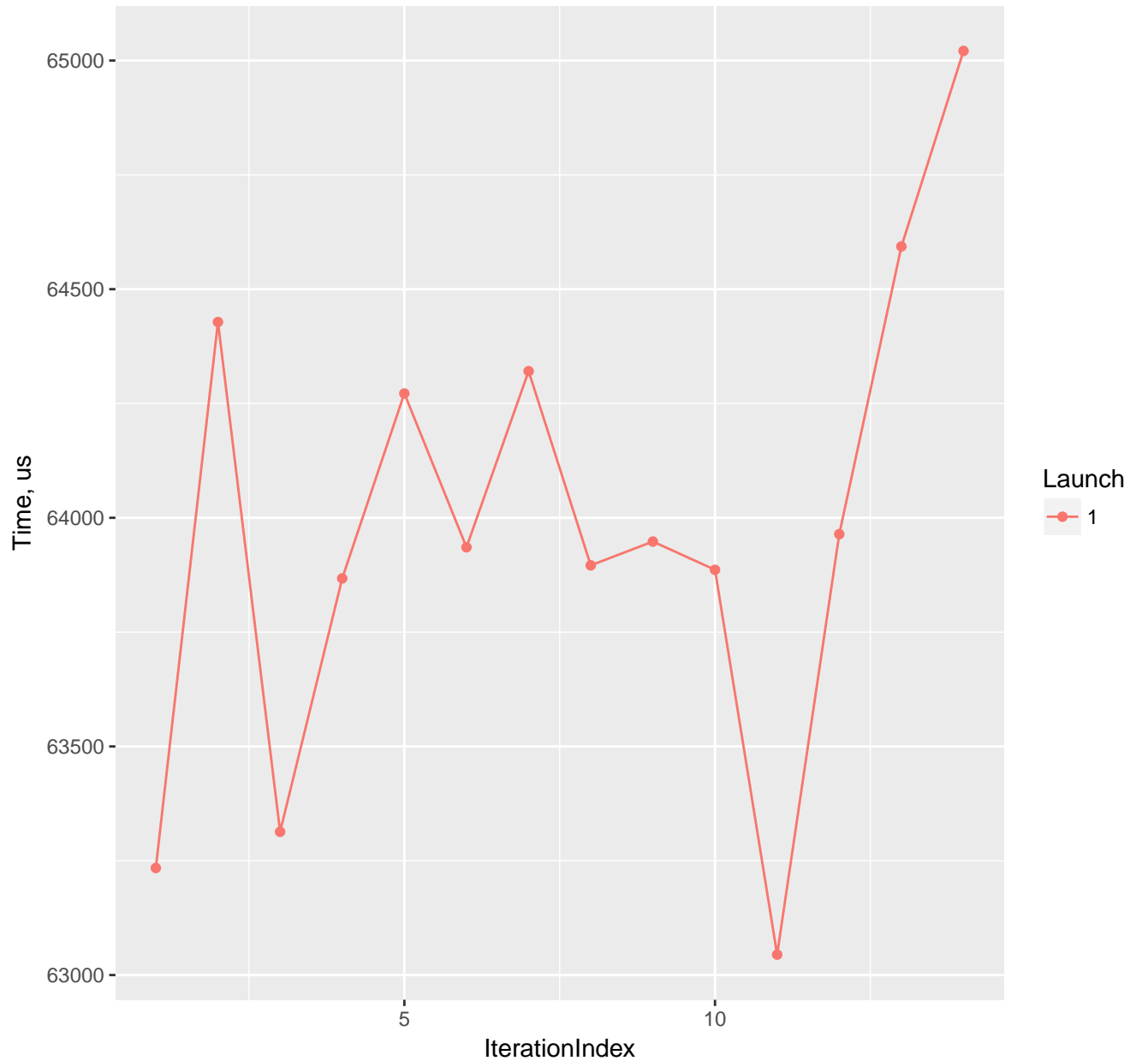
# CpsBenchmark+CpsPerformanceTests / NotFound



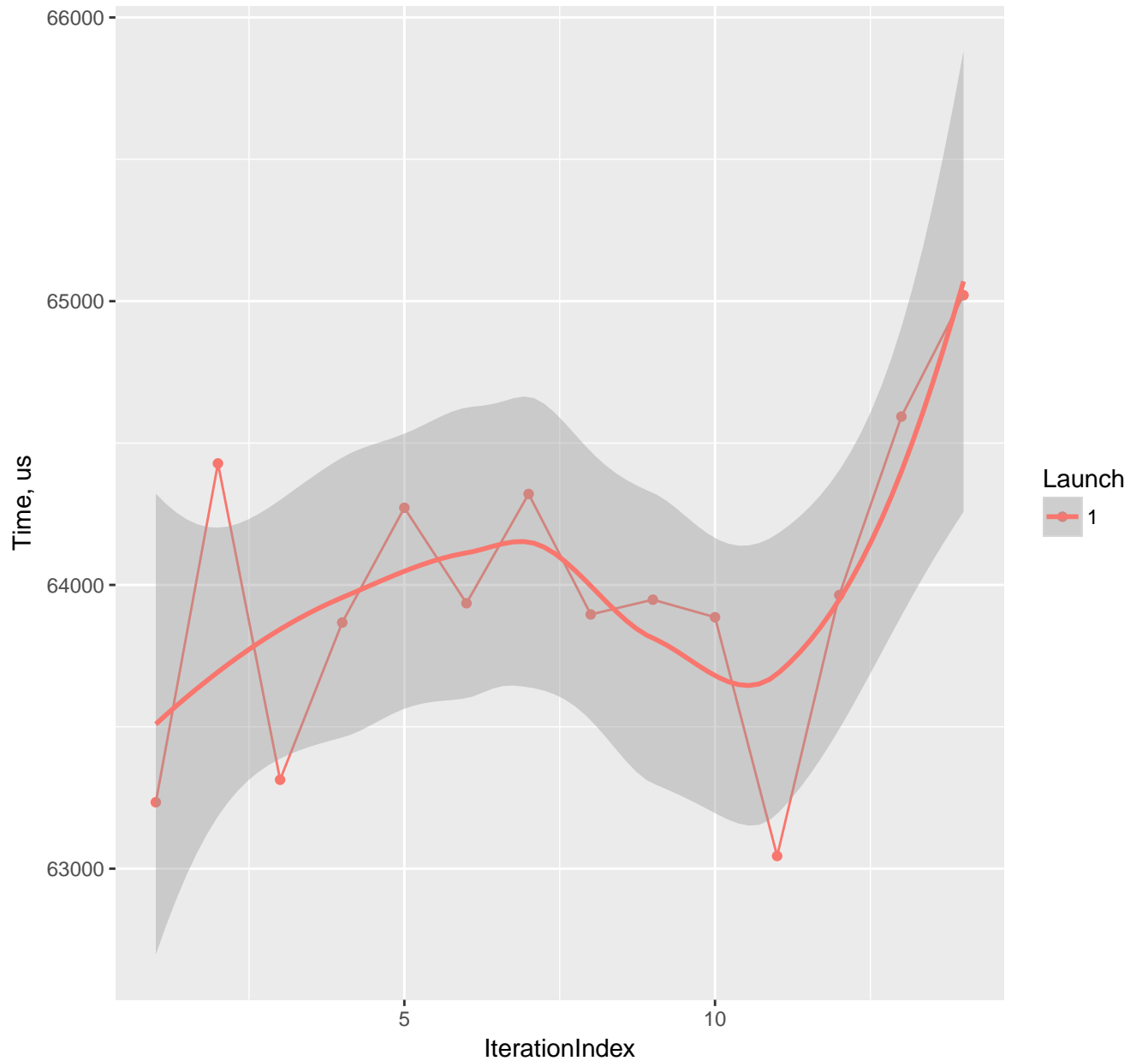




# CpsBenchmark+CpsPerformanceTests / NotFound / Default

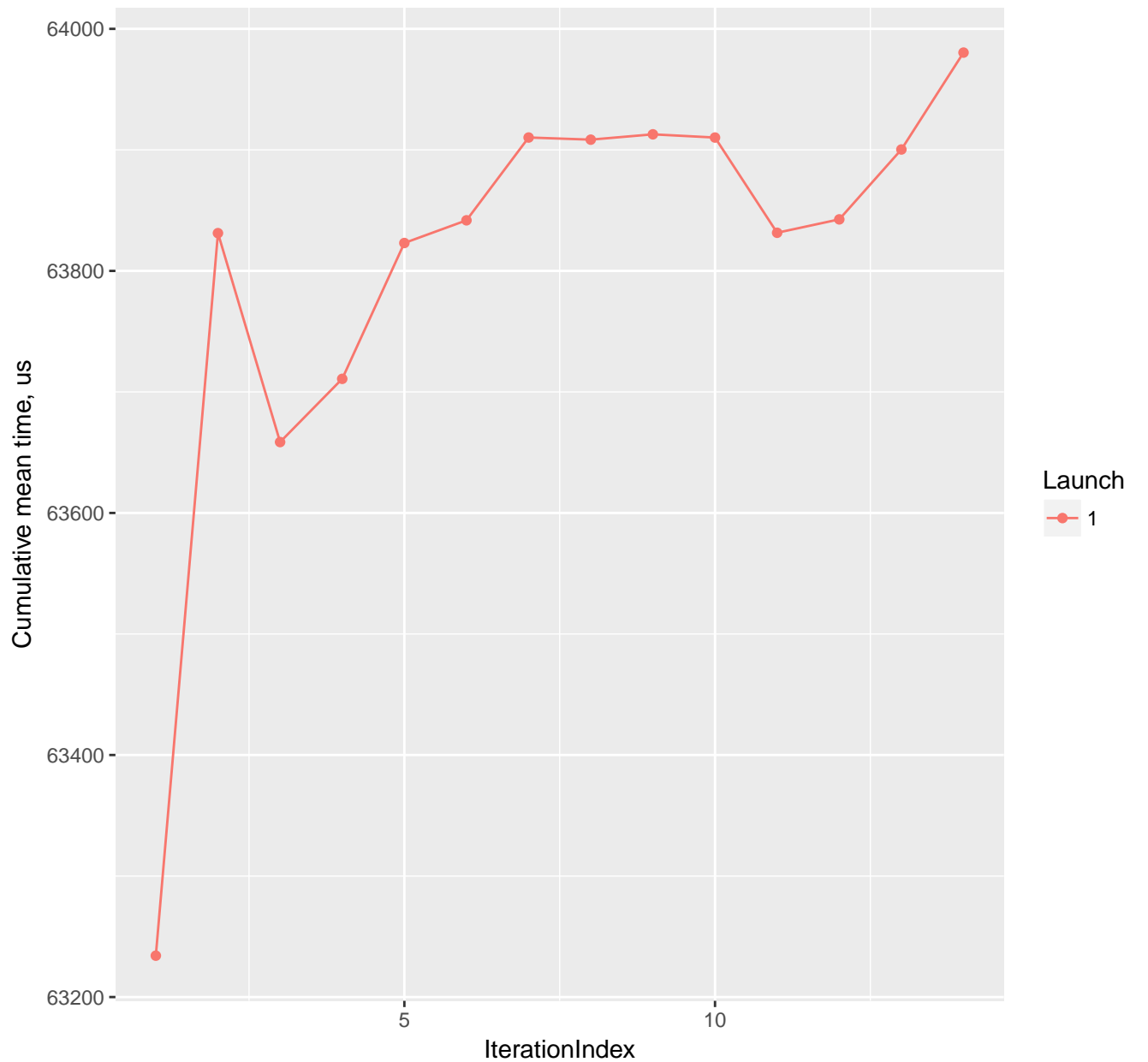


# CpsBenchmark+CpsPerformanceTests / NotFound / Default

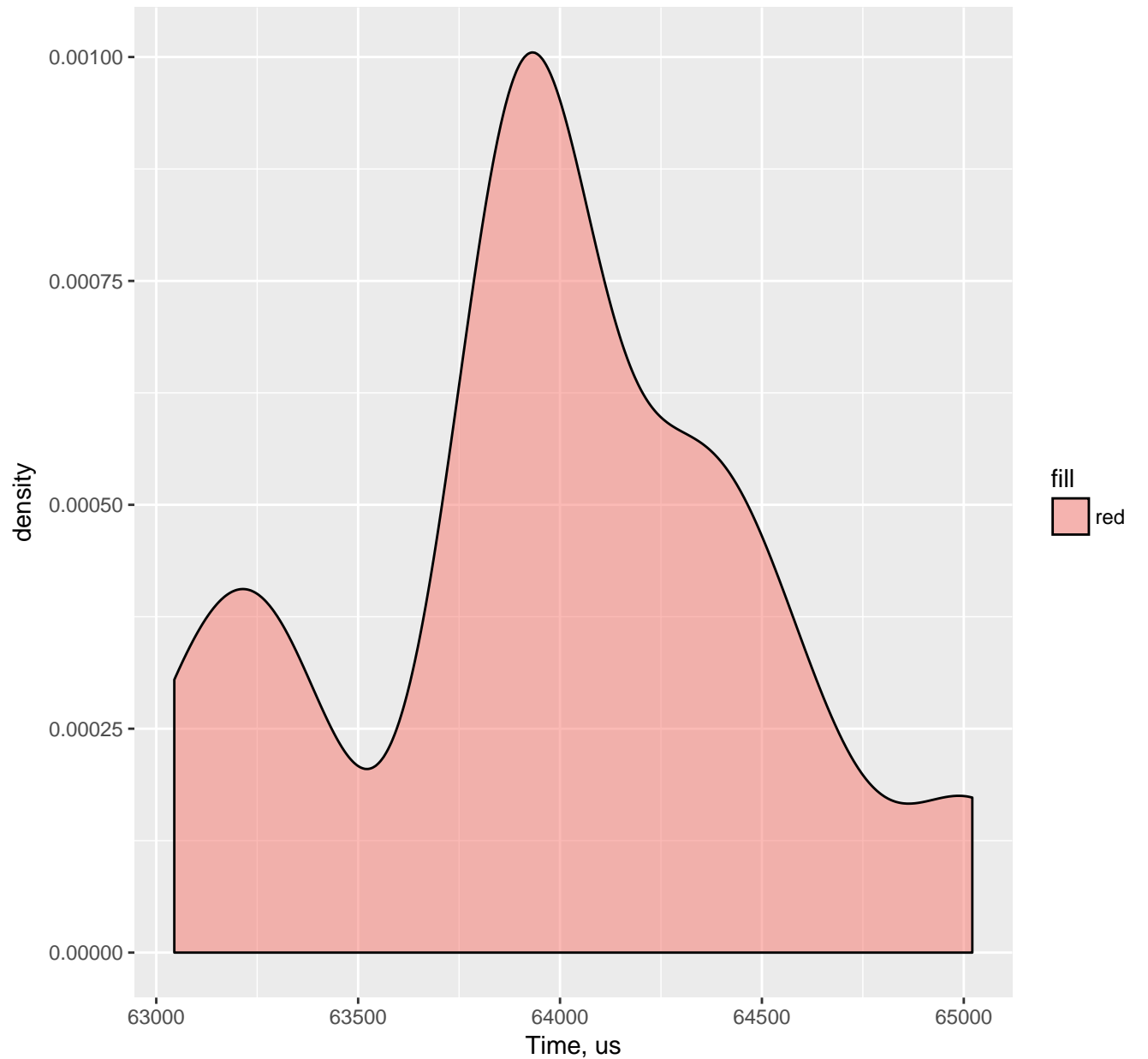




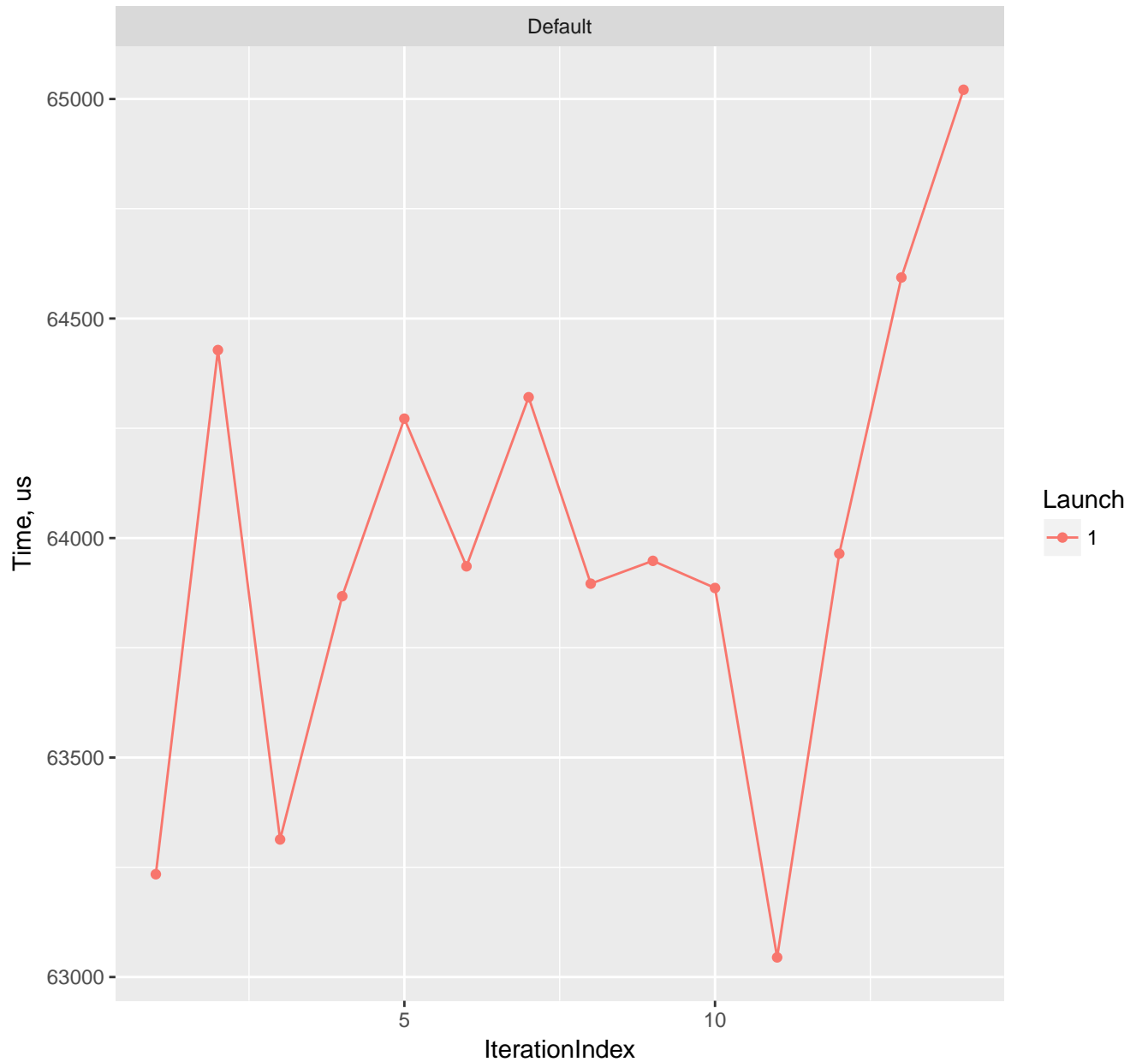
# CpsBenchmark+CpsPerformanceTests / NotFound / Default



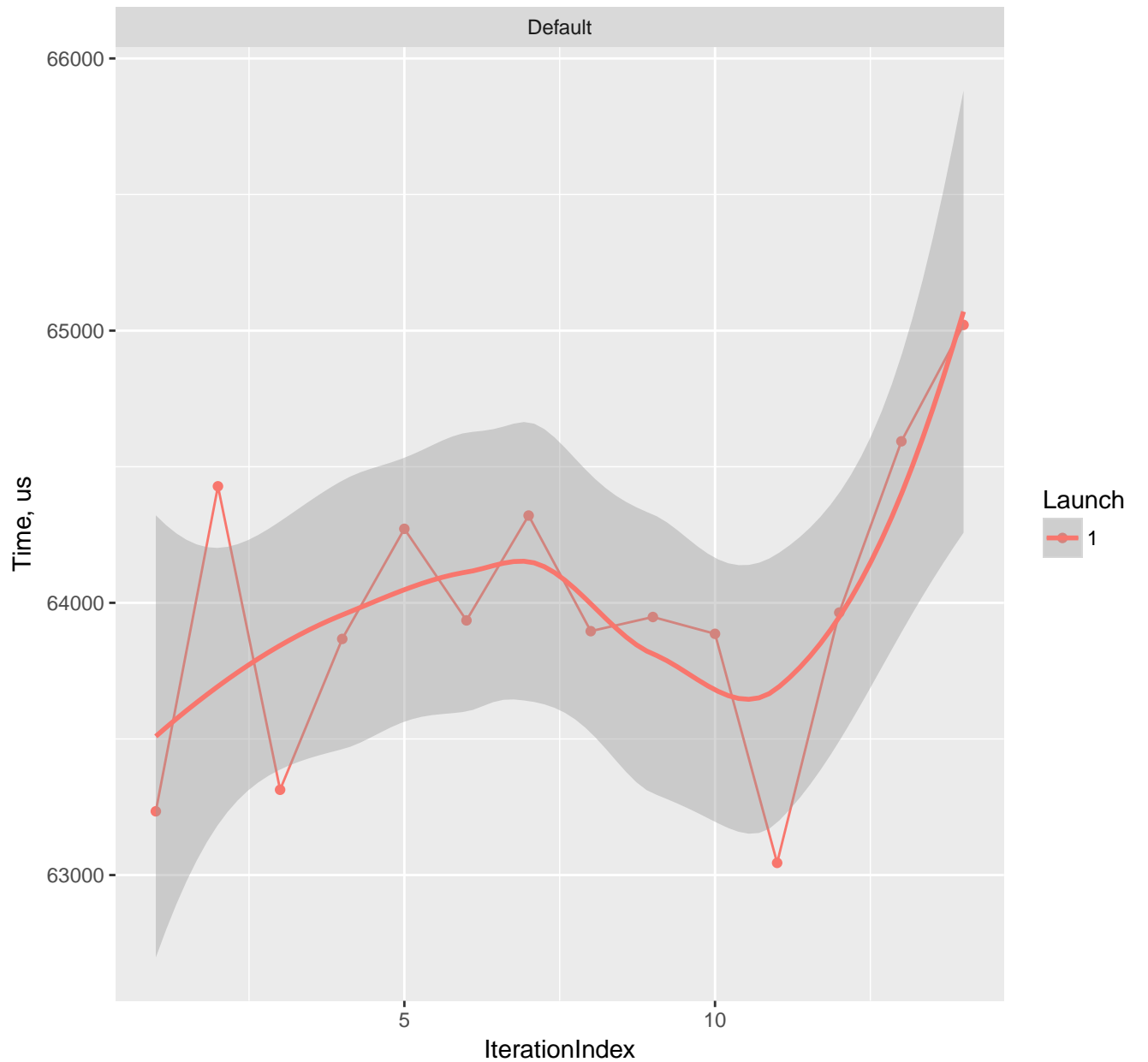
# CpsBenchmark+CpsPerformanceTests / NotFound / Default



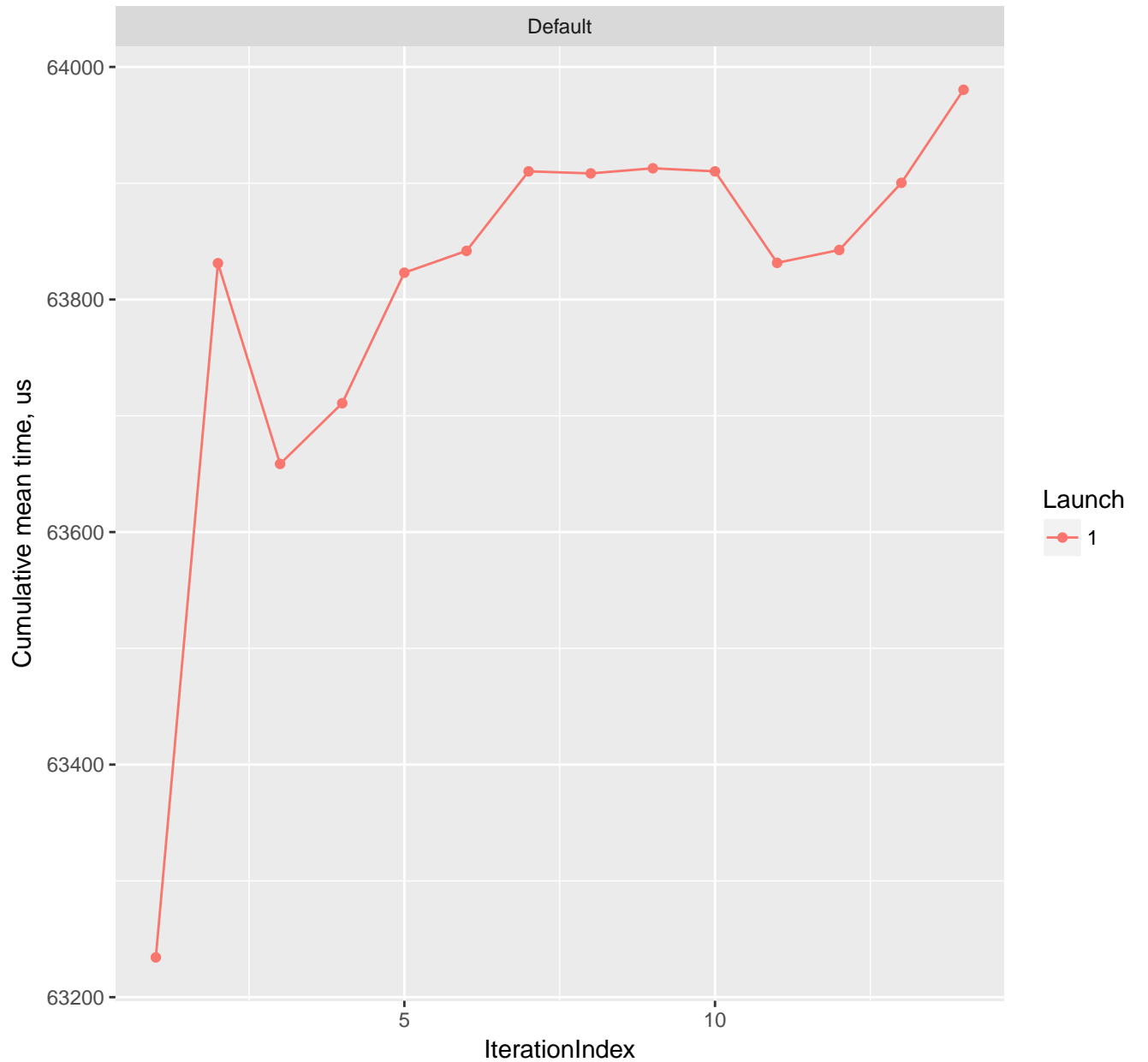
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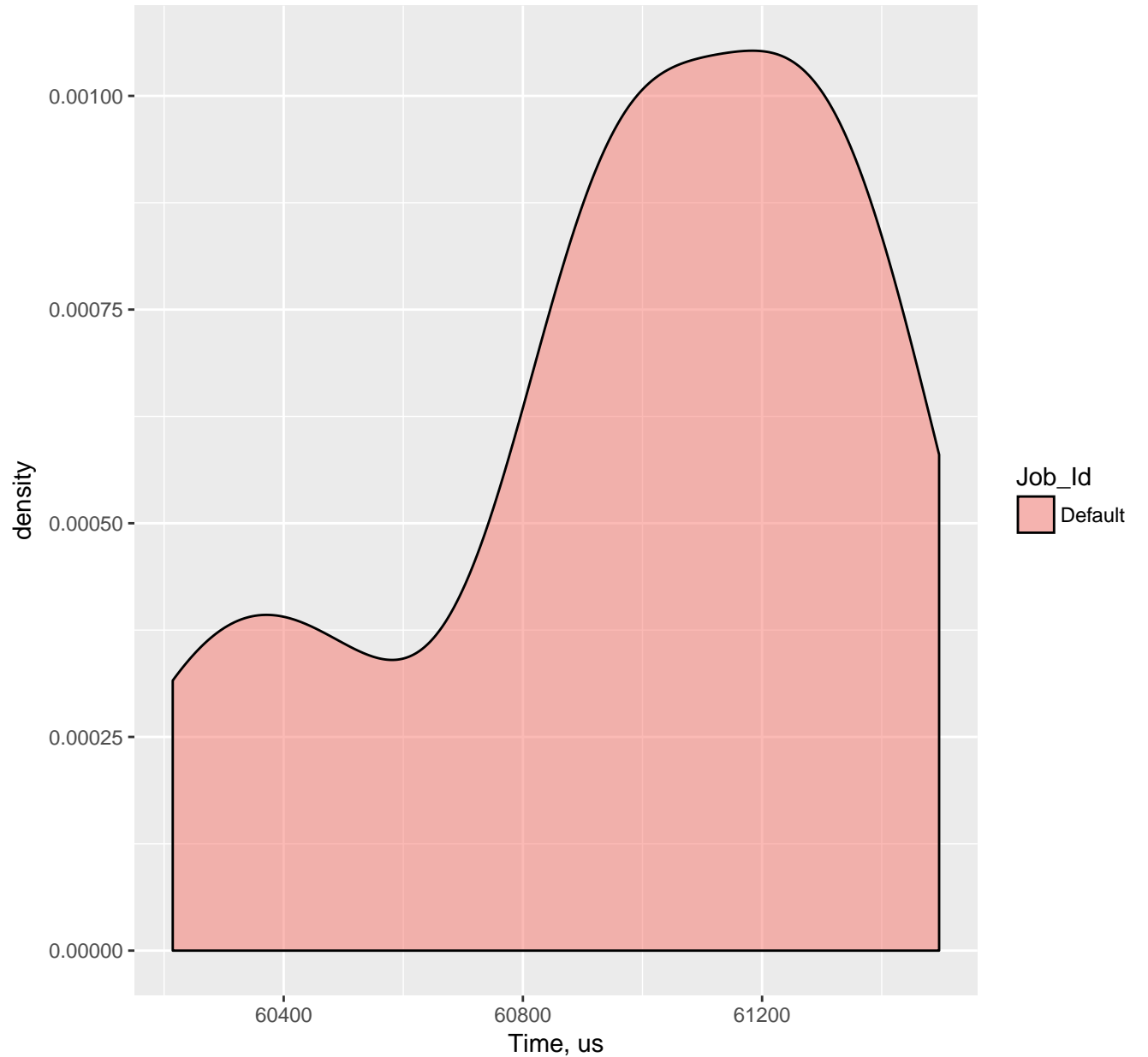
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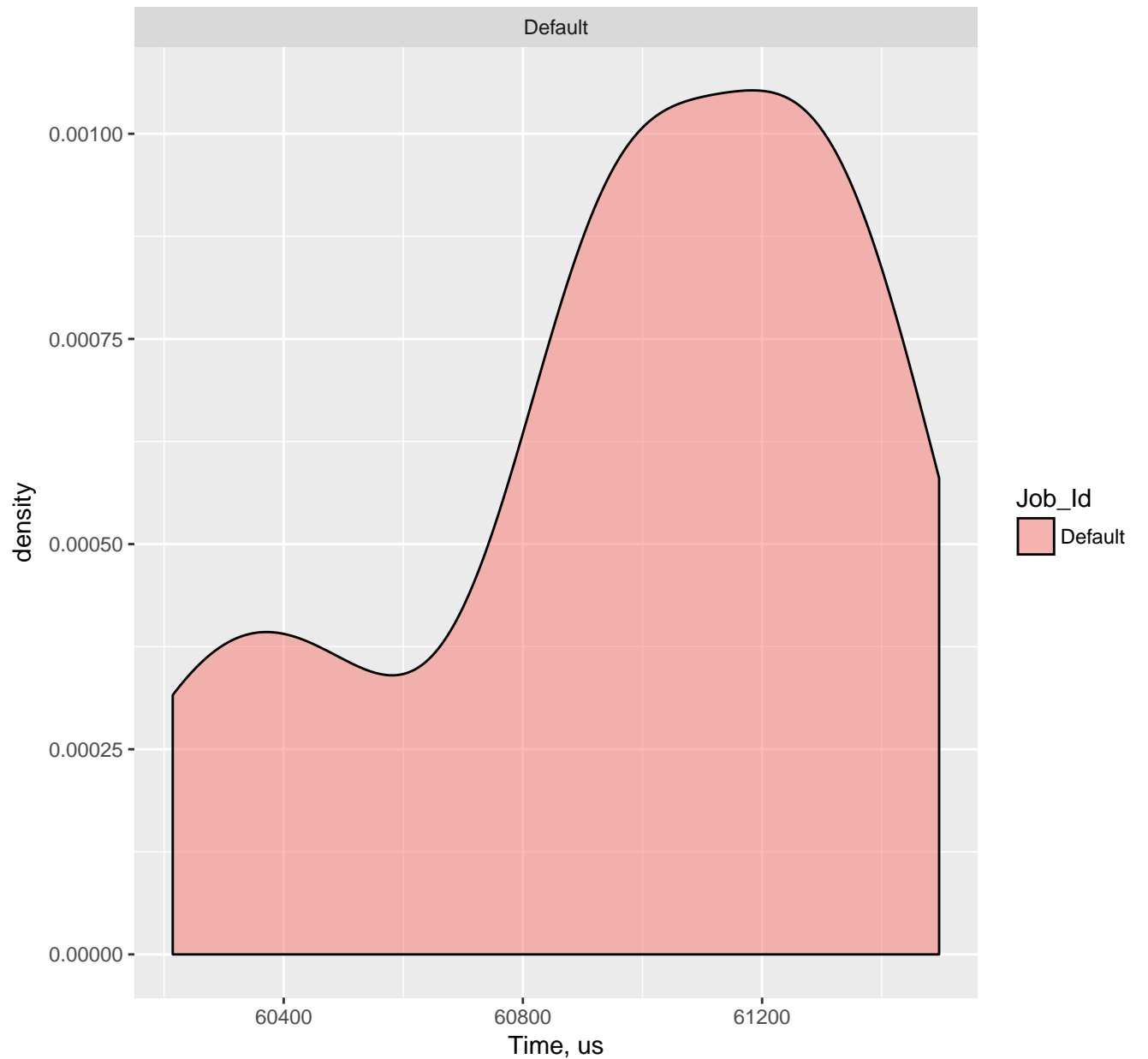
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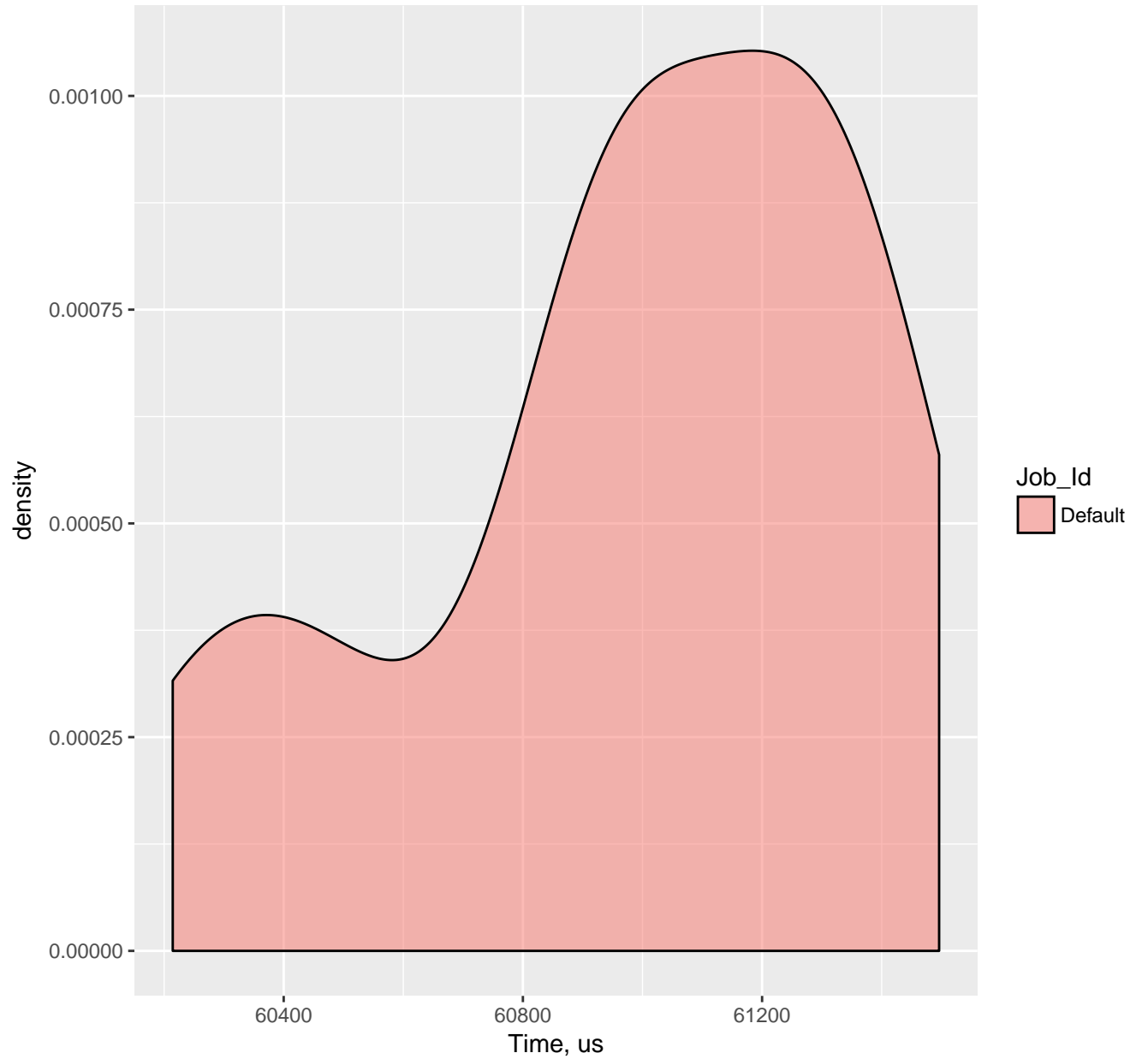


# CpsBenchmark+CpsPerformanceTests / NotFoundCPS



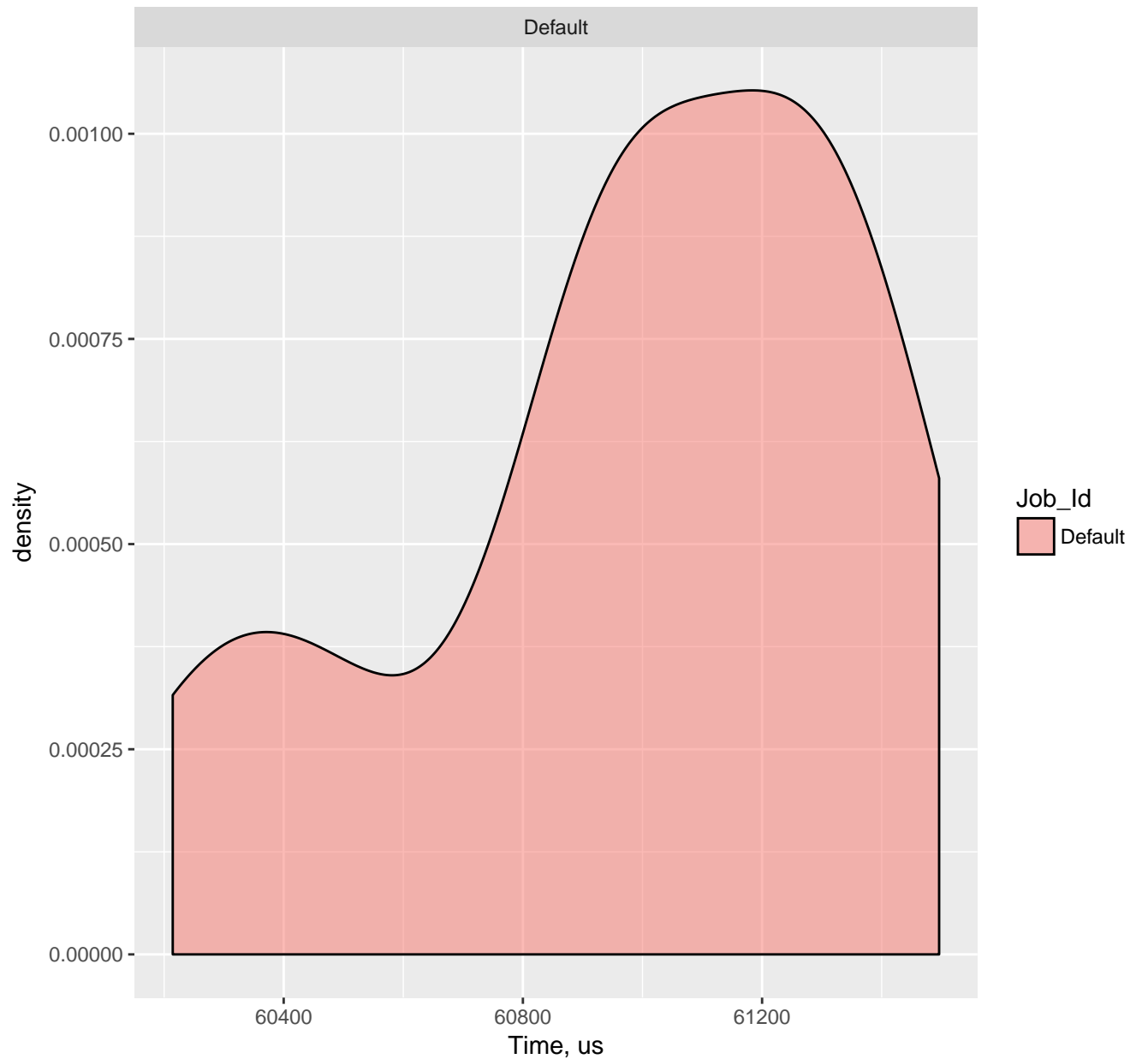
# CpsBenchmark+CpsPerformanceTests / NotFoundCPS

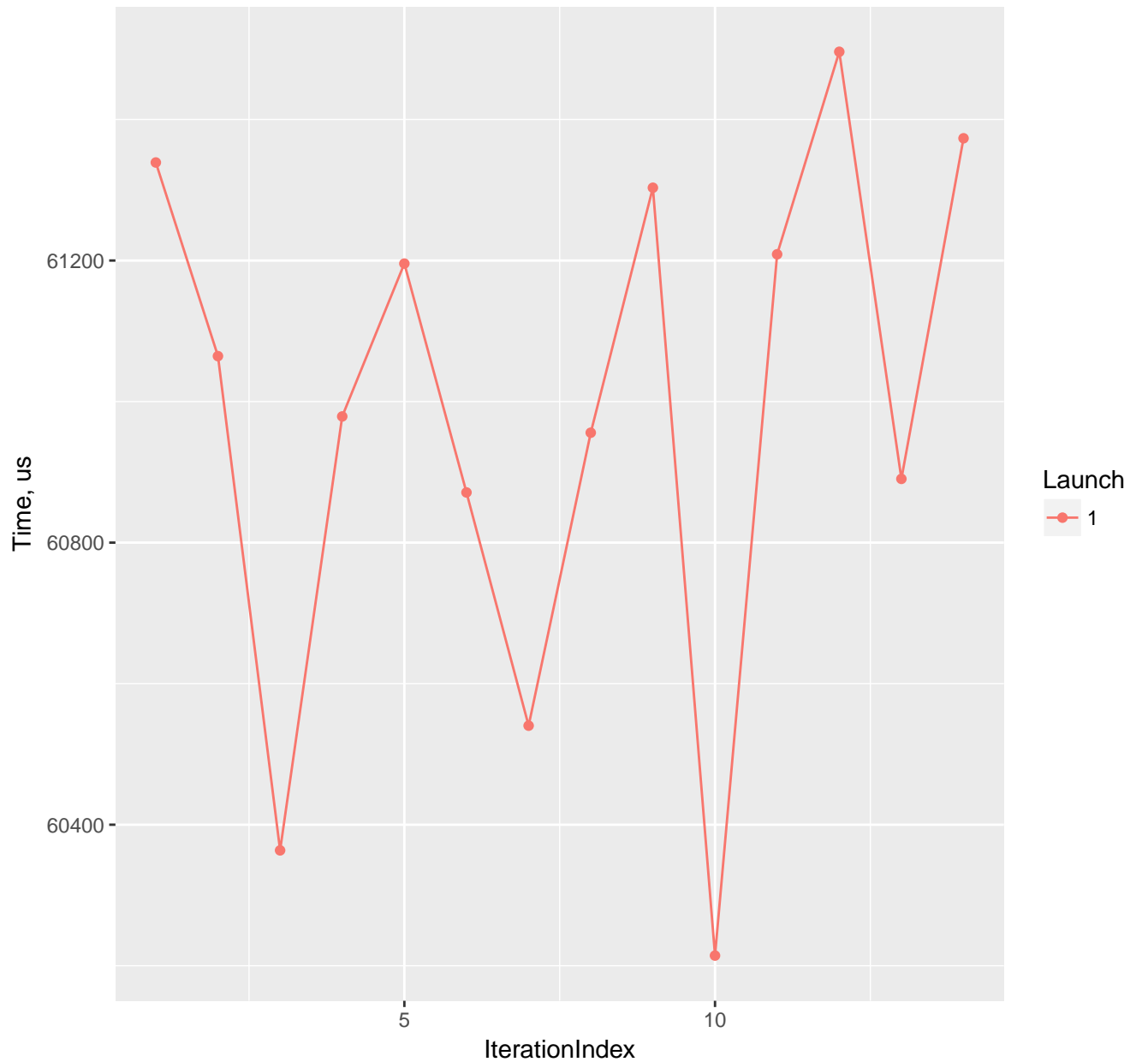


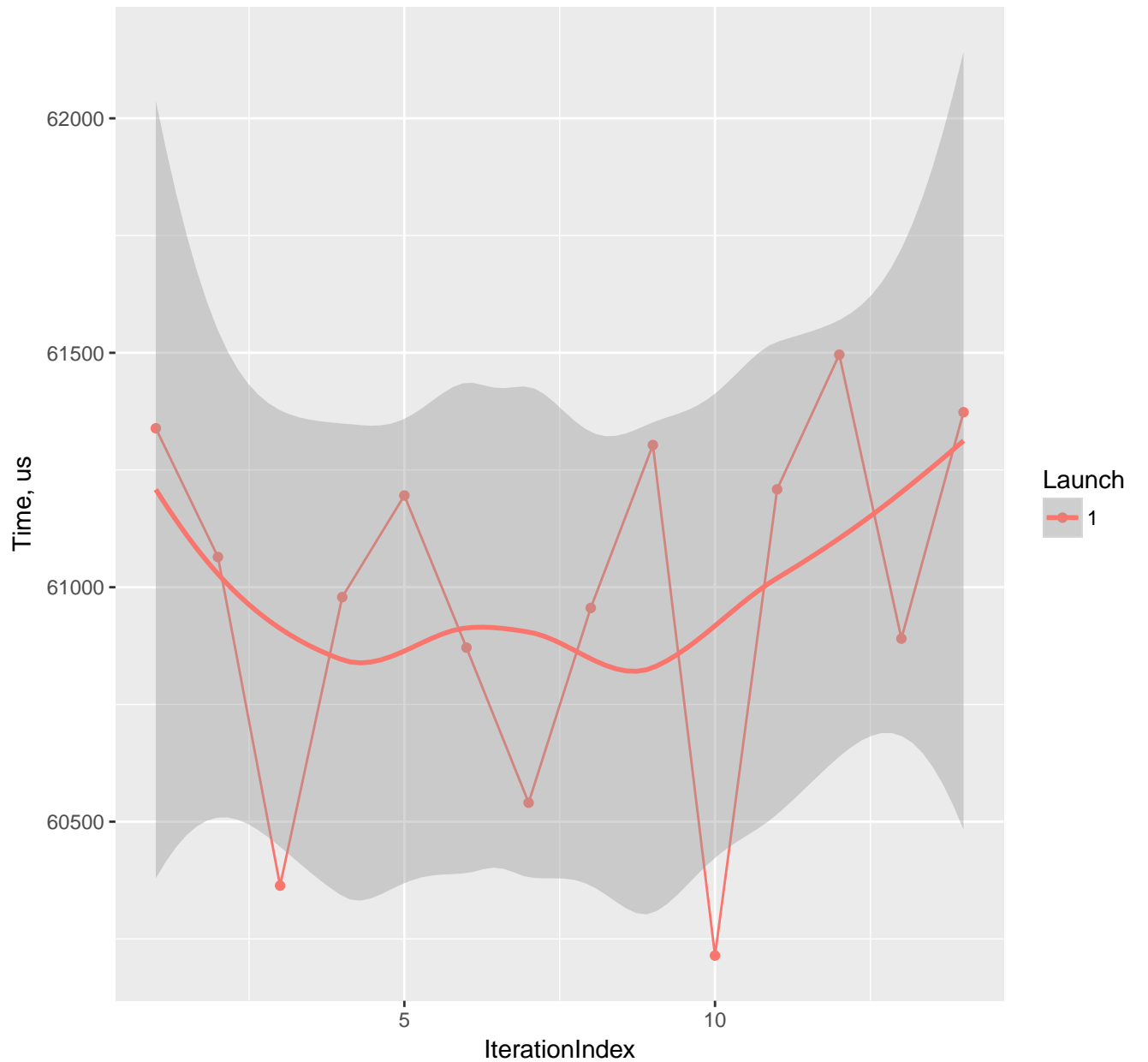


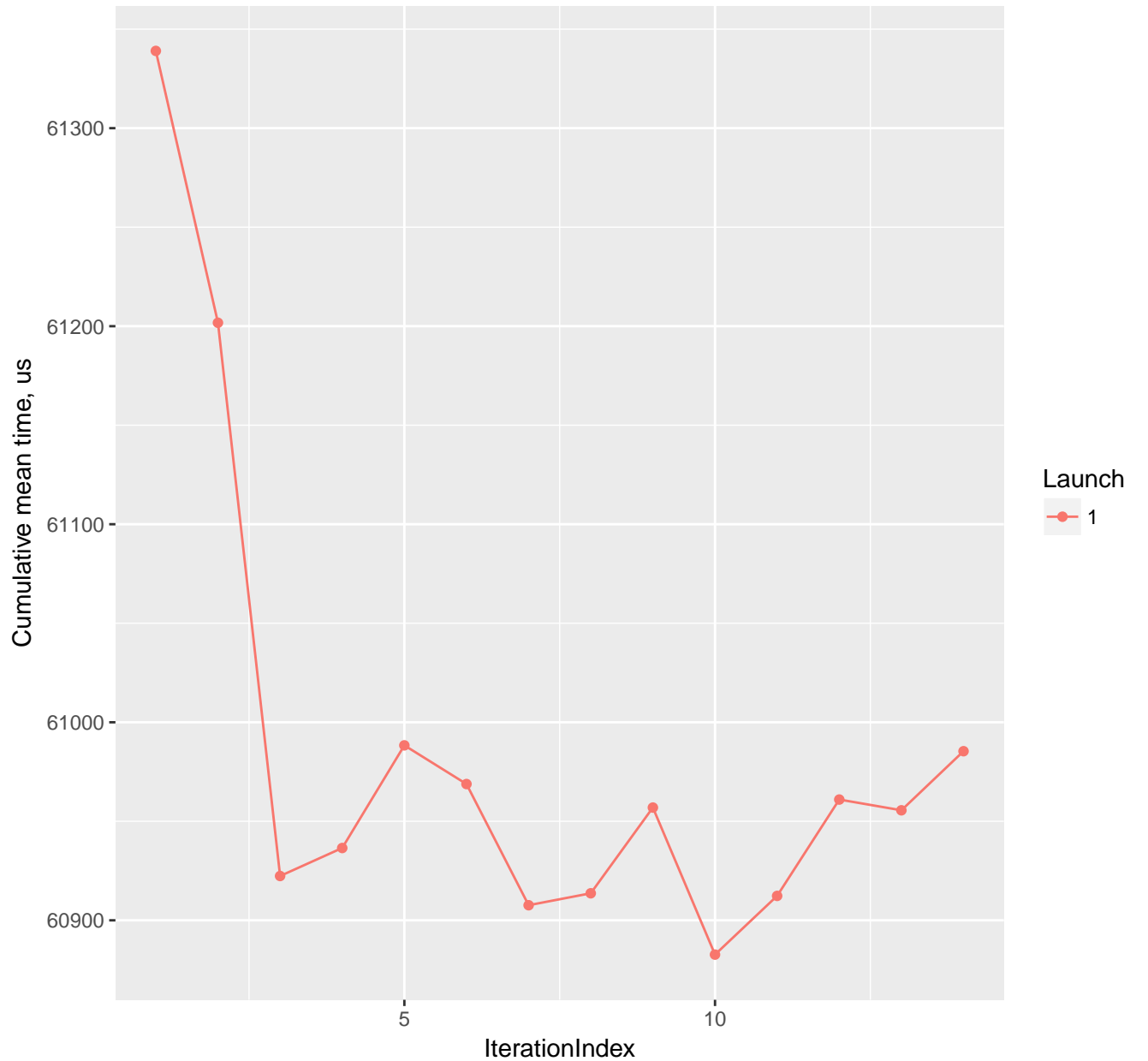


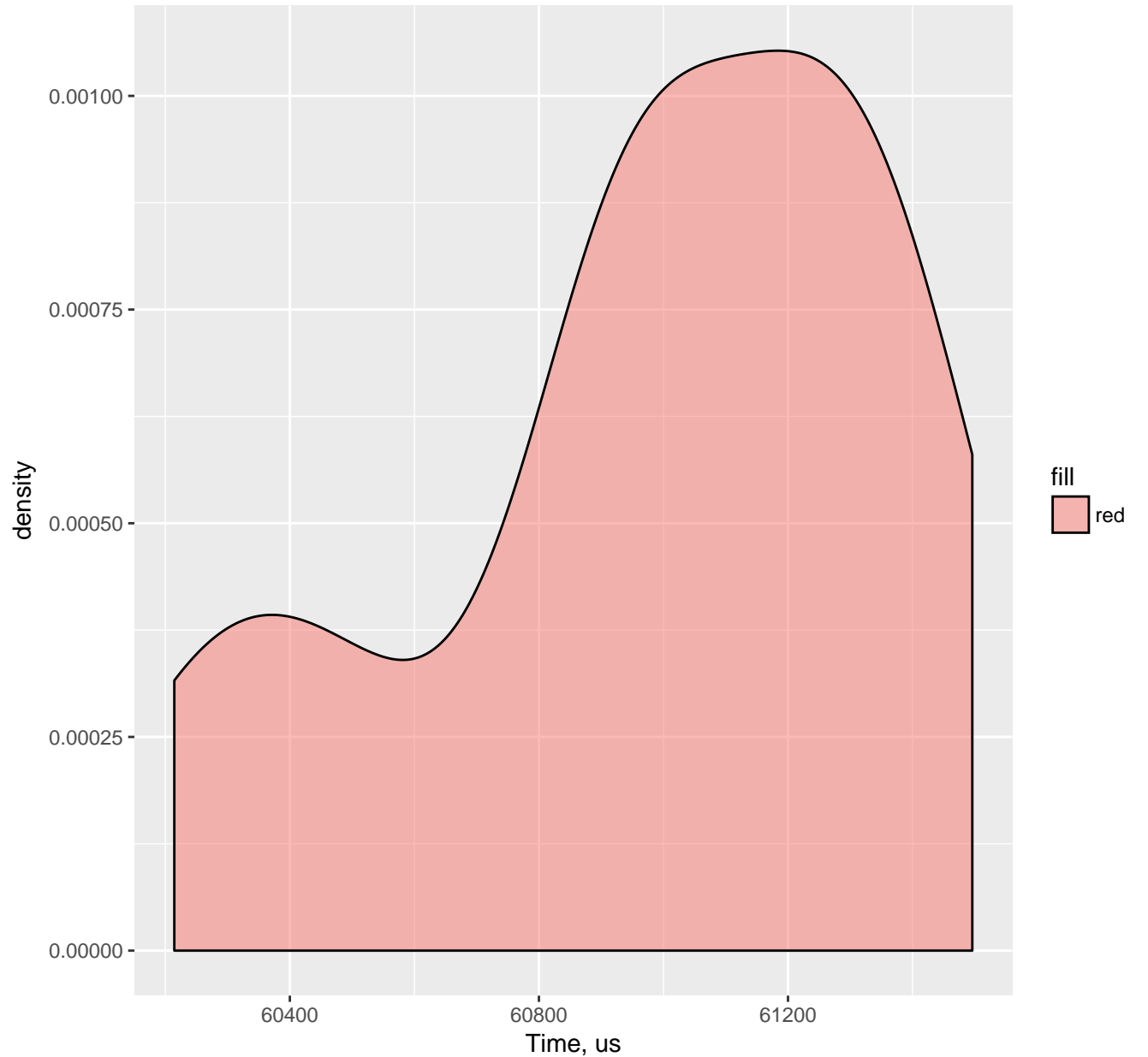
# CpsBenchmark+CpsPerformanceTests / NotFoundCPS /



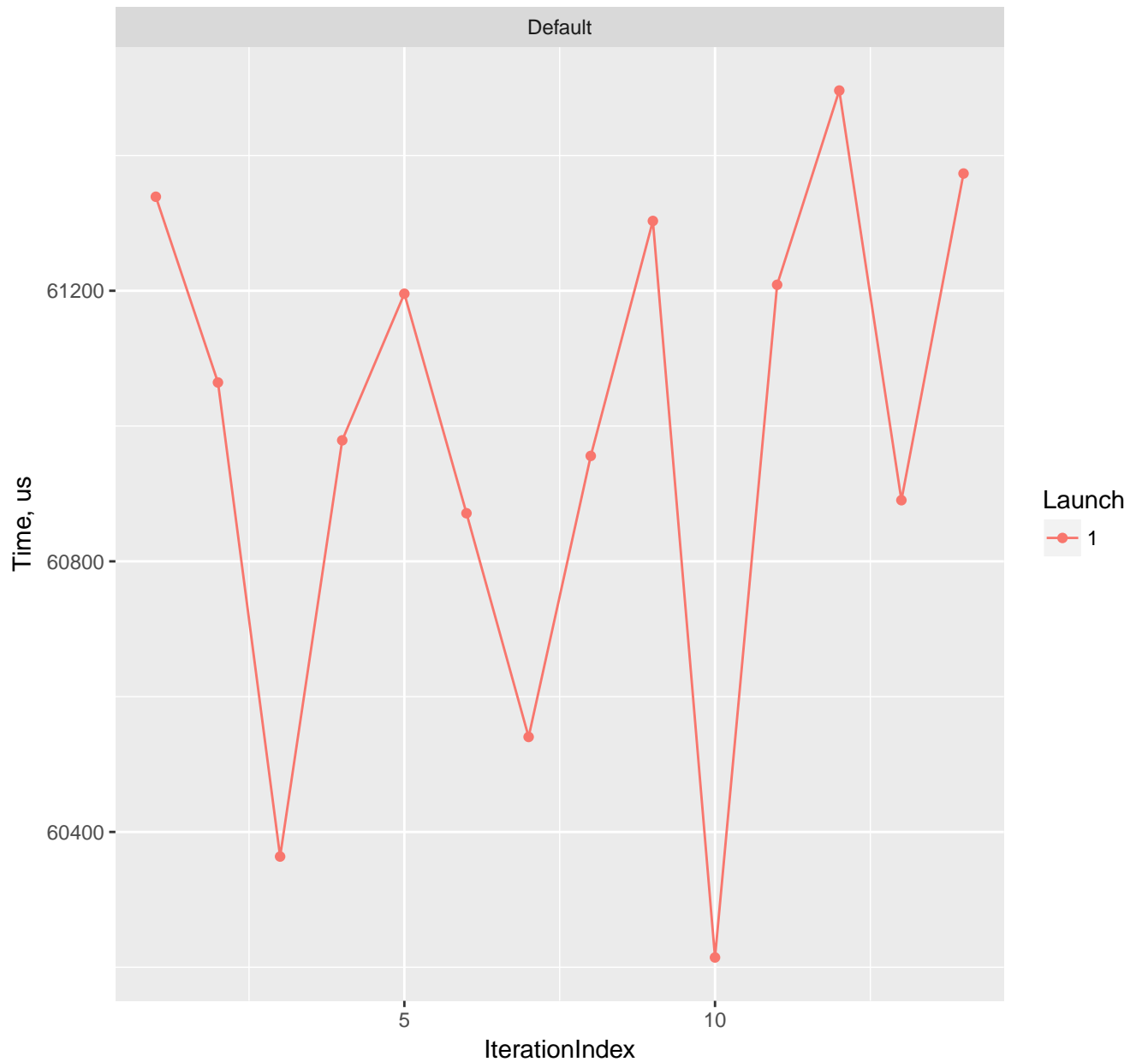




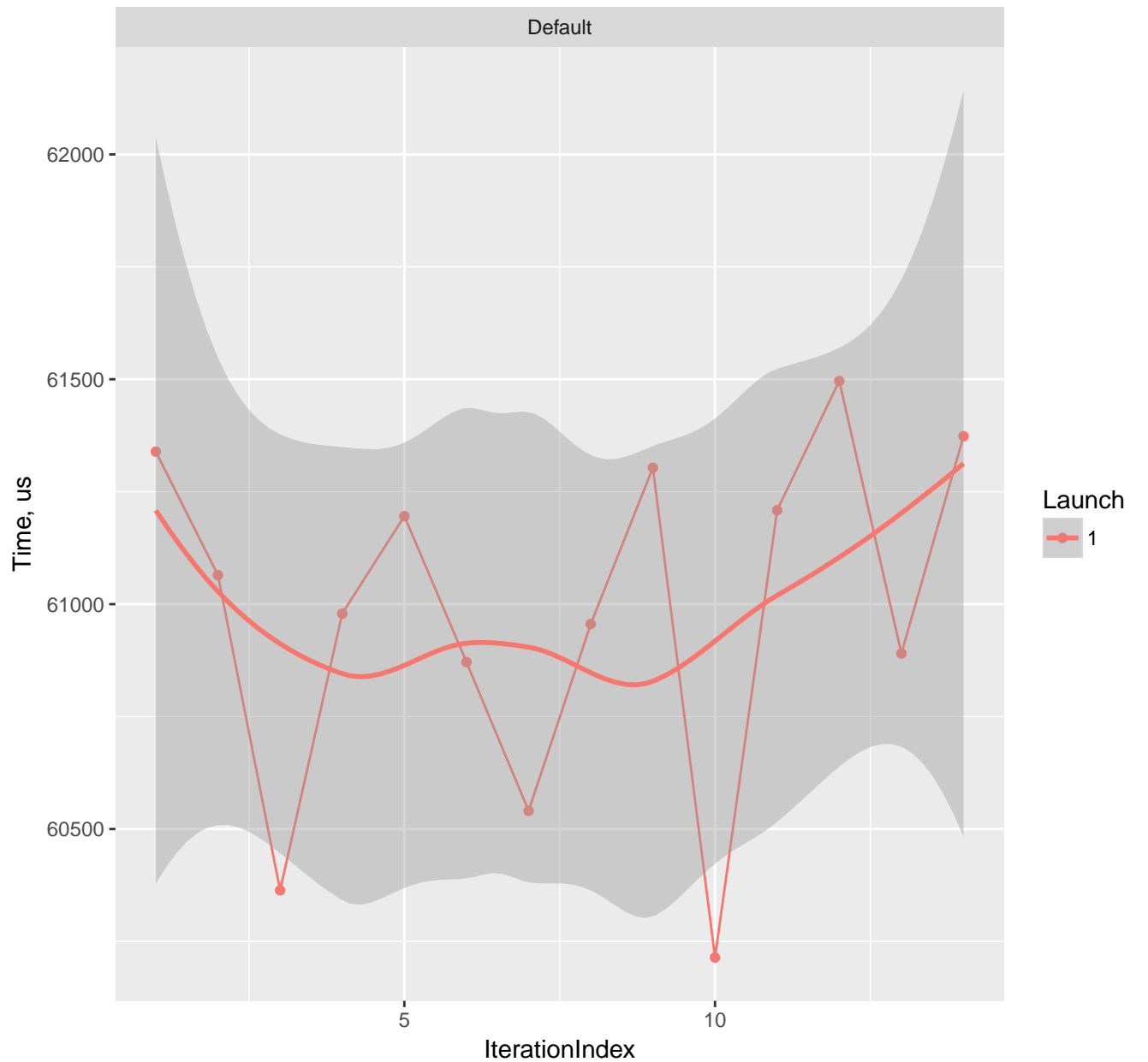




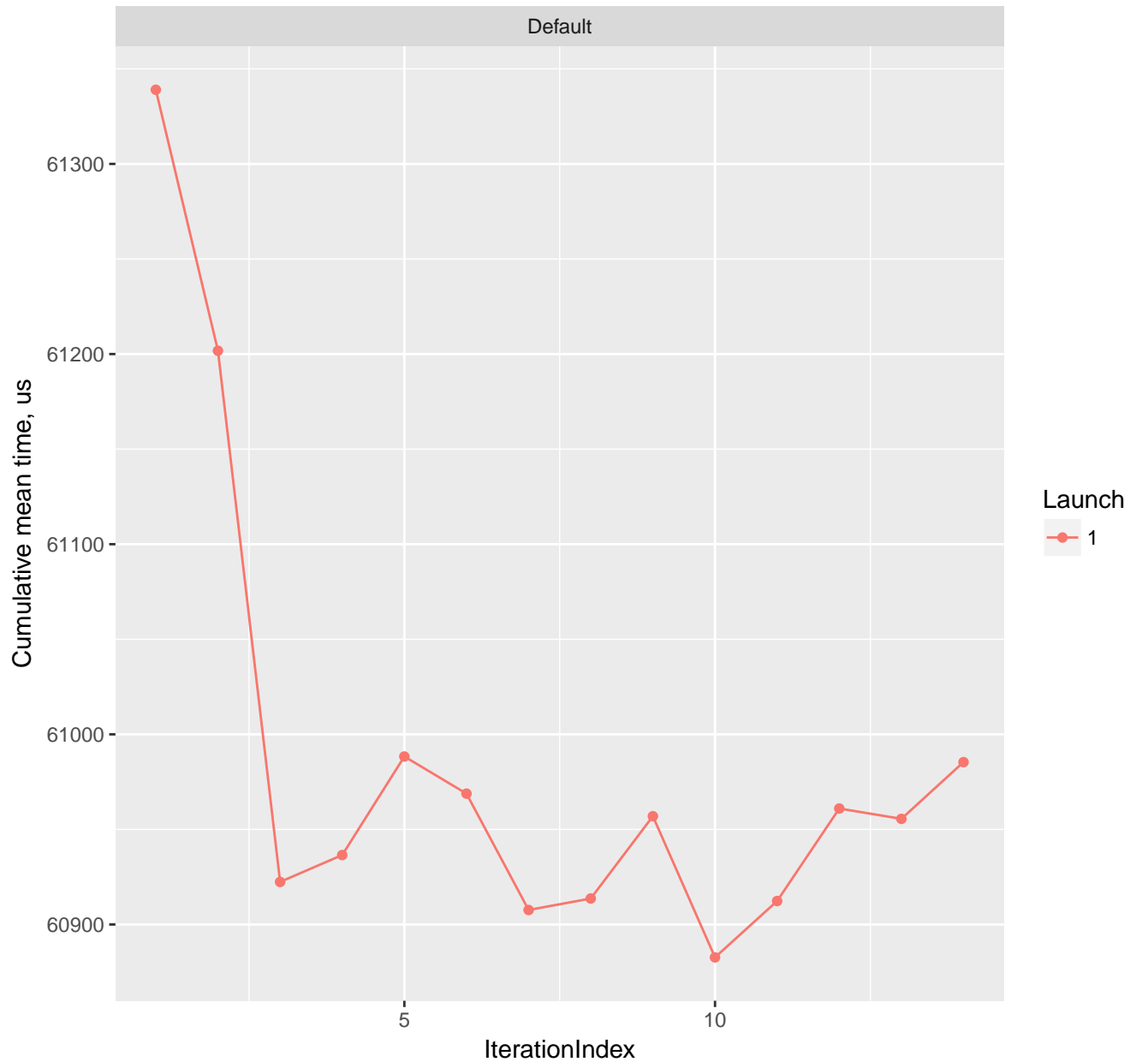
# CpsBenchmark+CpsPerformanceTests / NotFoundCPS



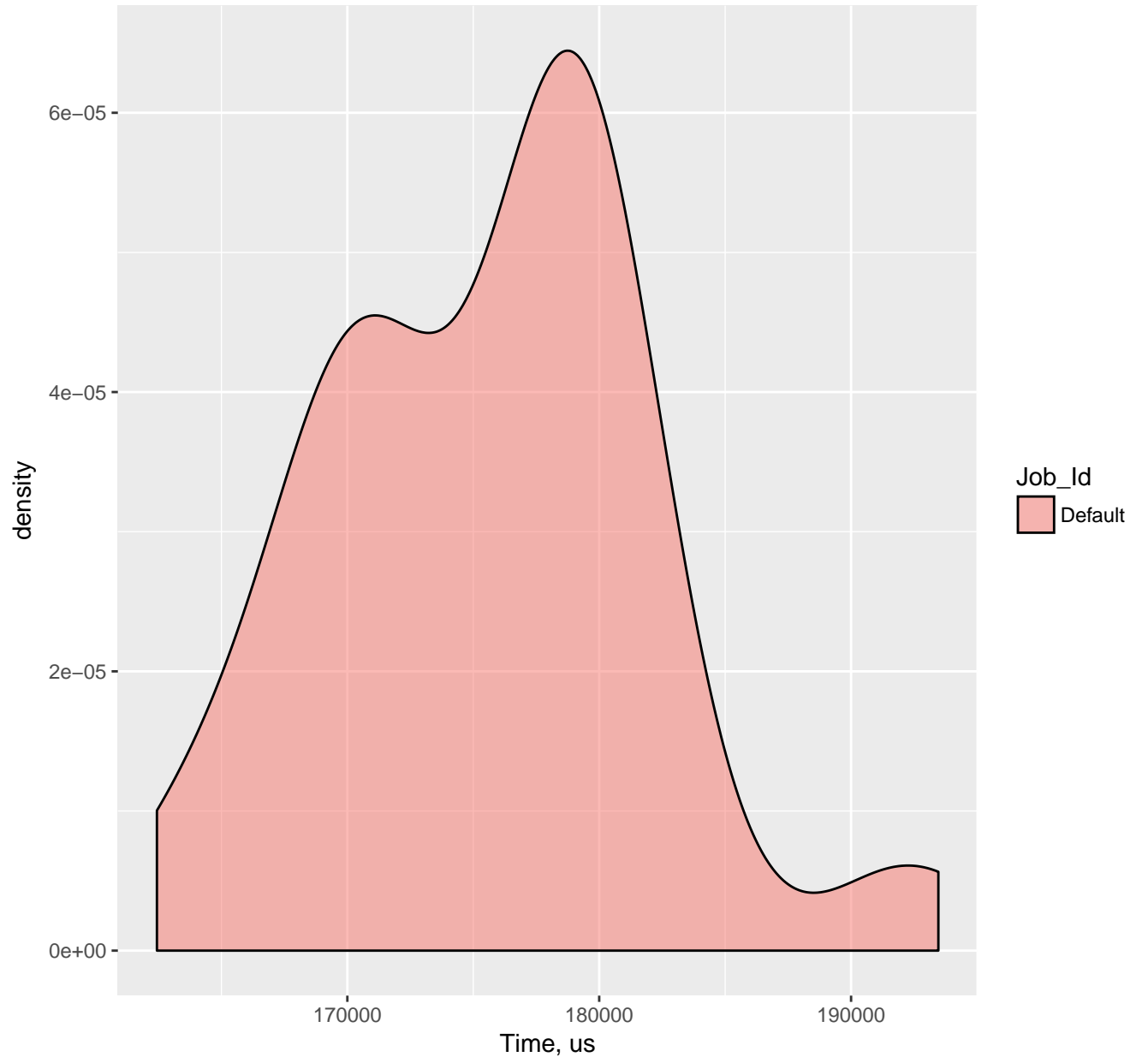
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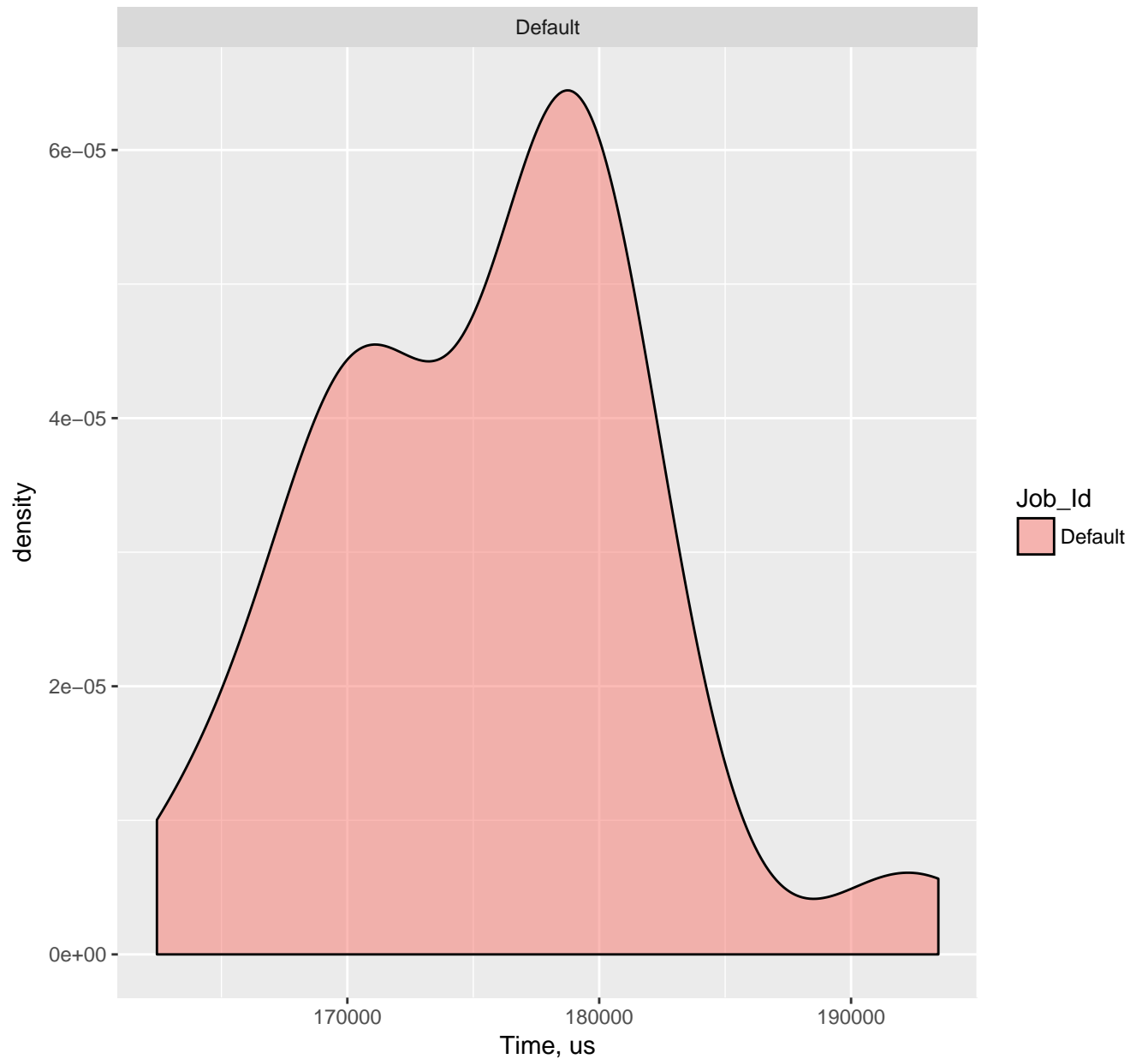
# CpsBenchmark+CpsPerformanceTests / NotFoundCPS

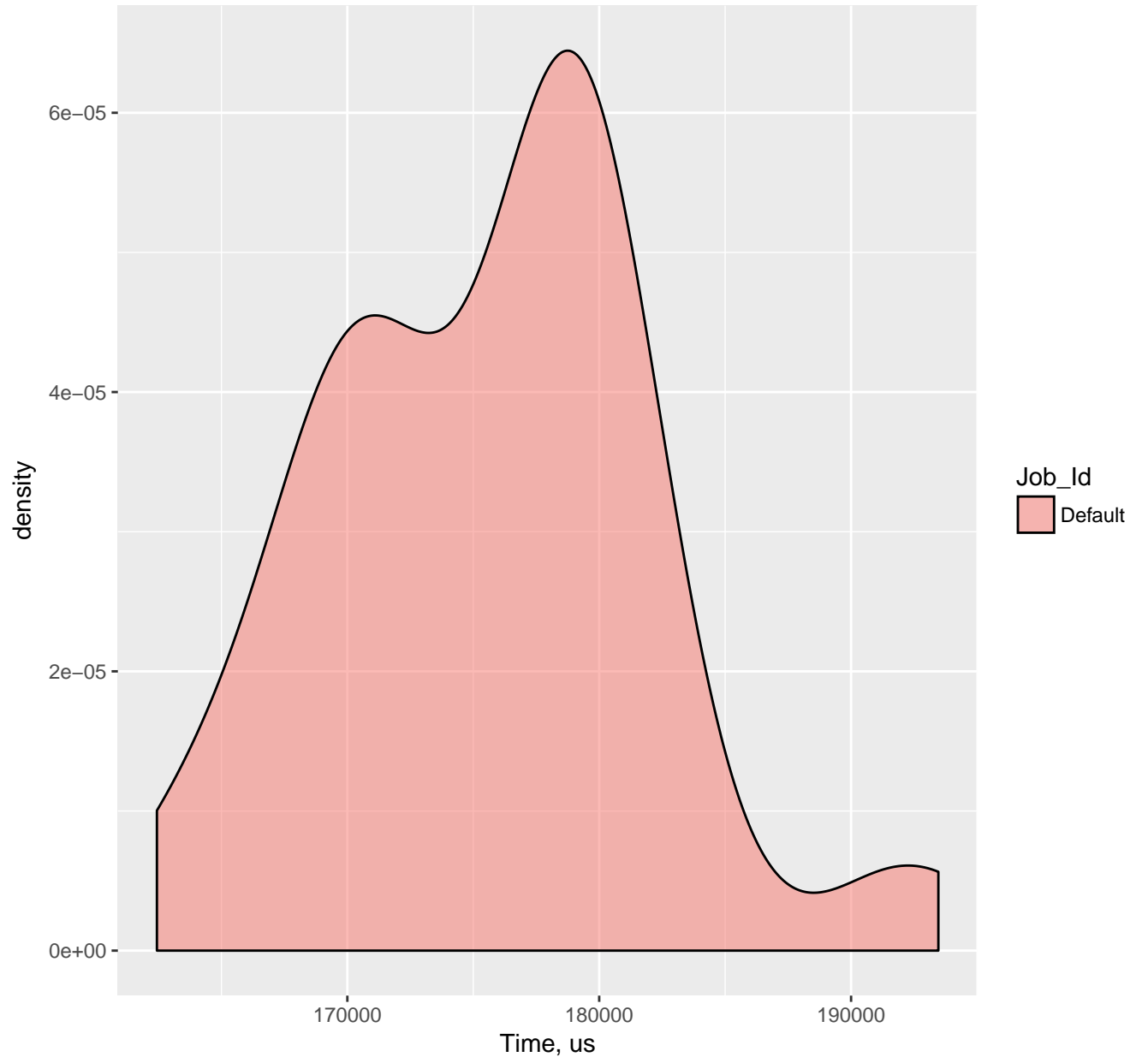


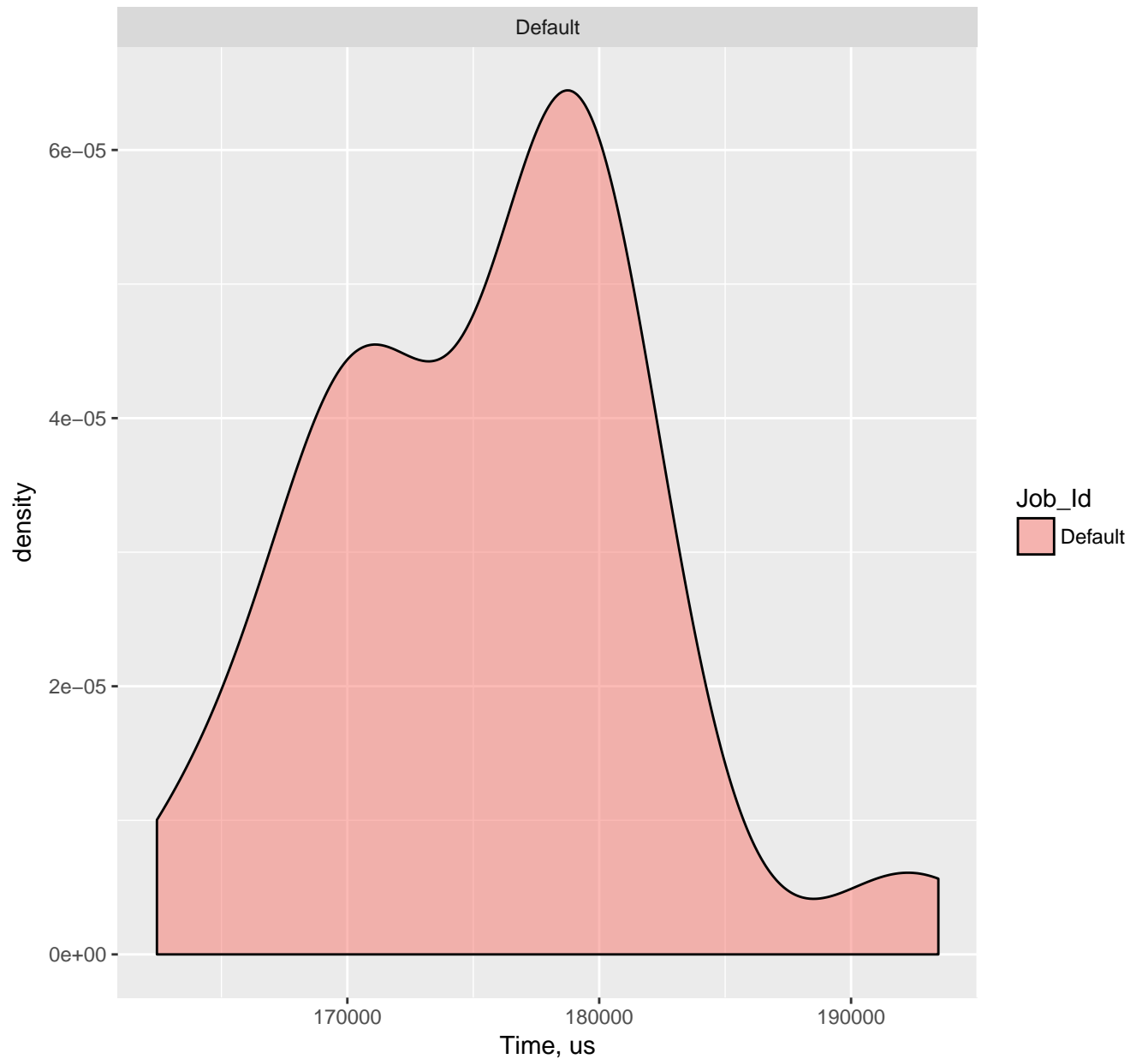


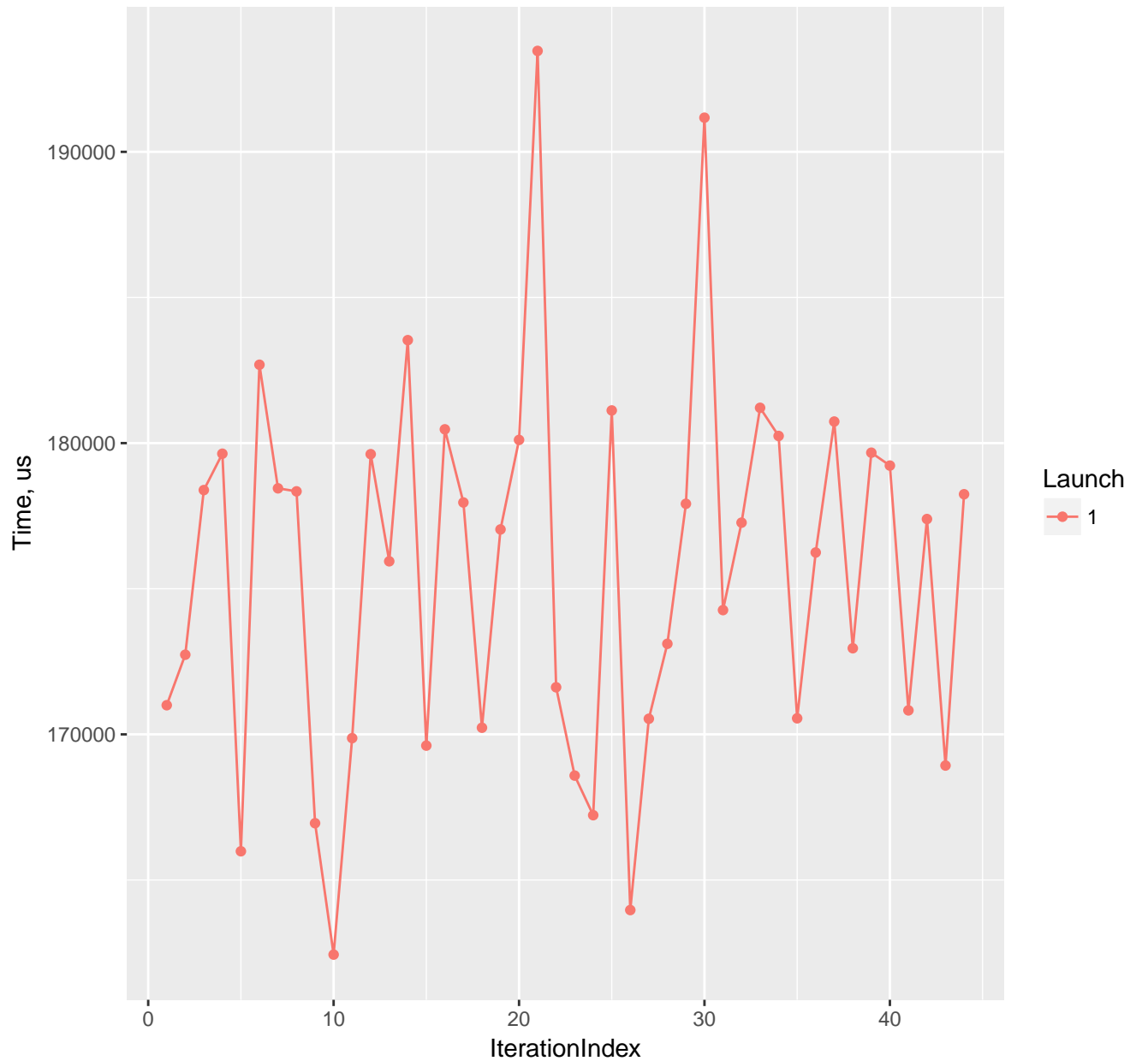


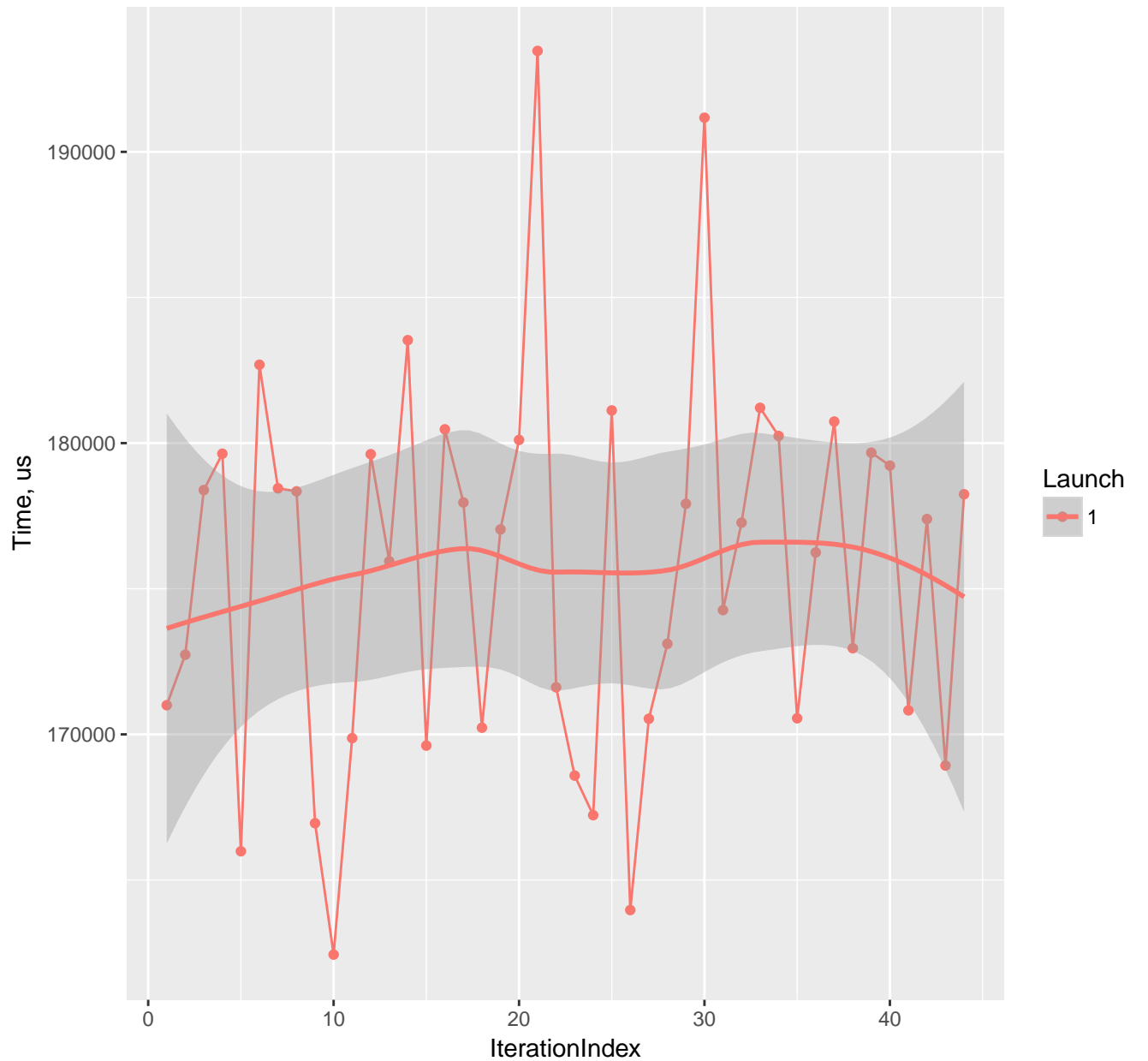
# CpsBenchmark+CpsPerformanceTests / NotFoundAvm2



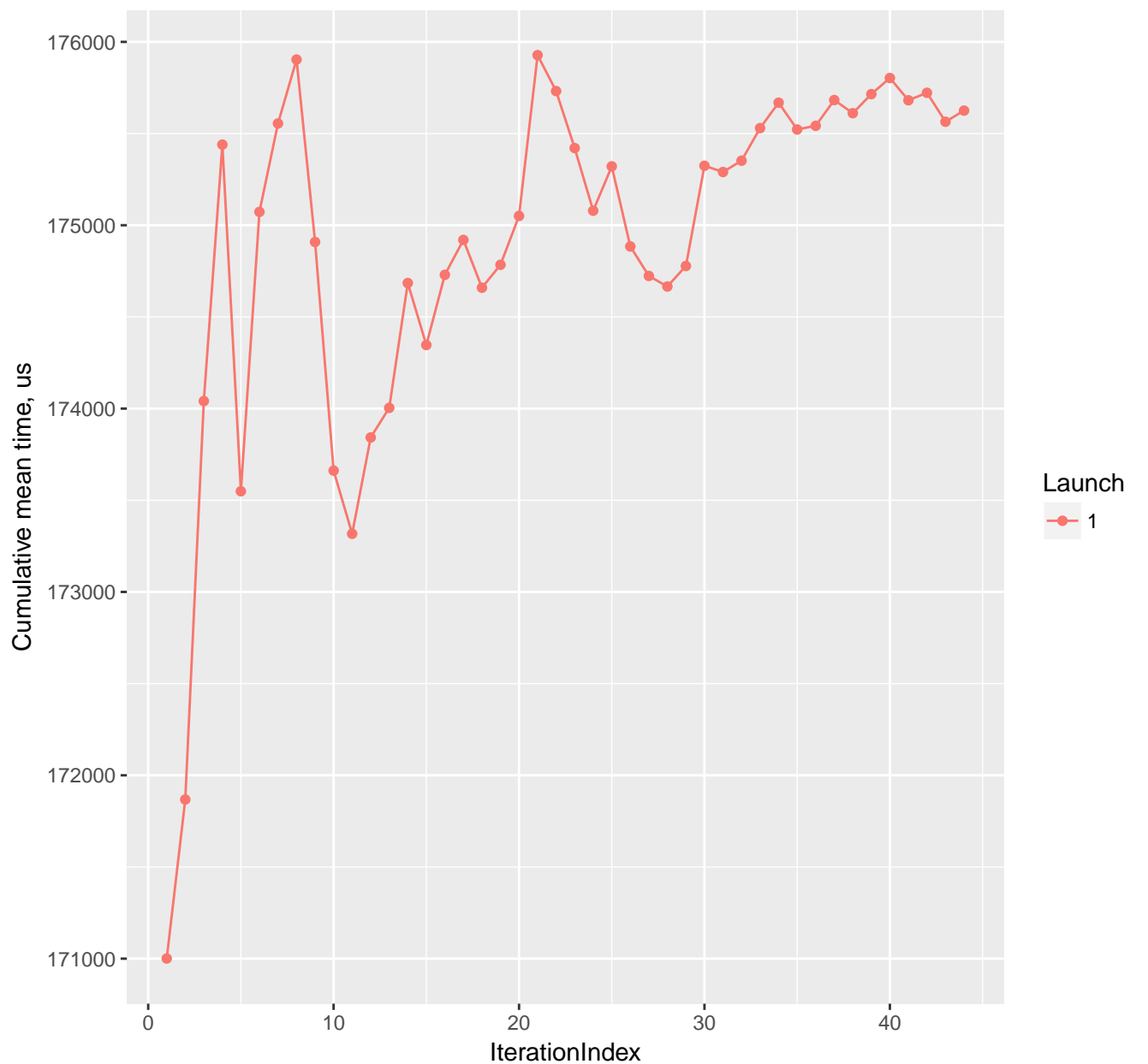


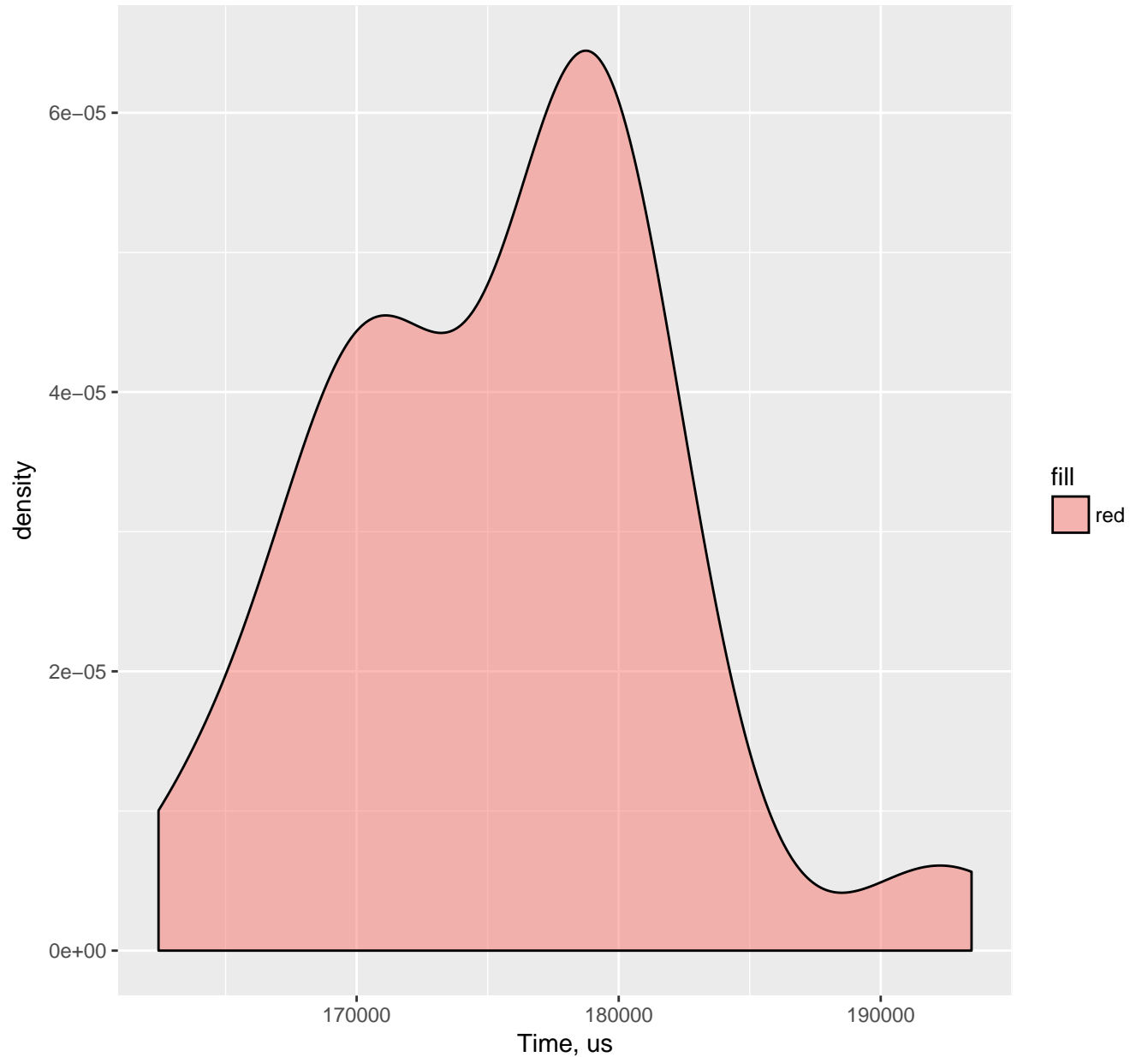






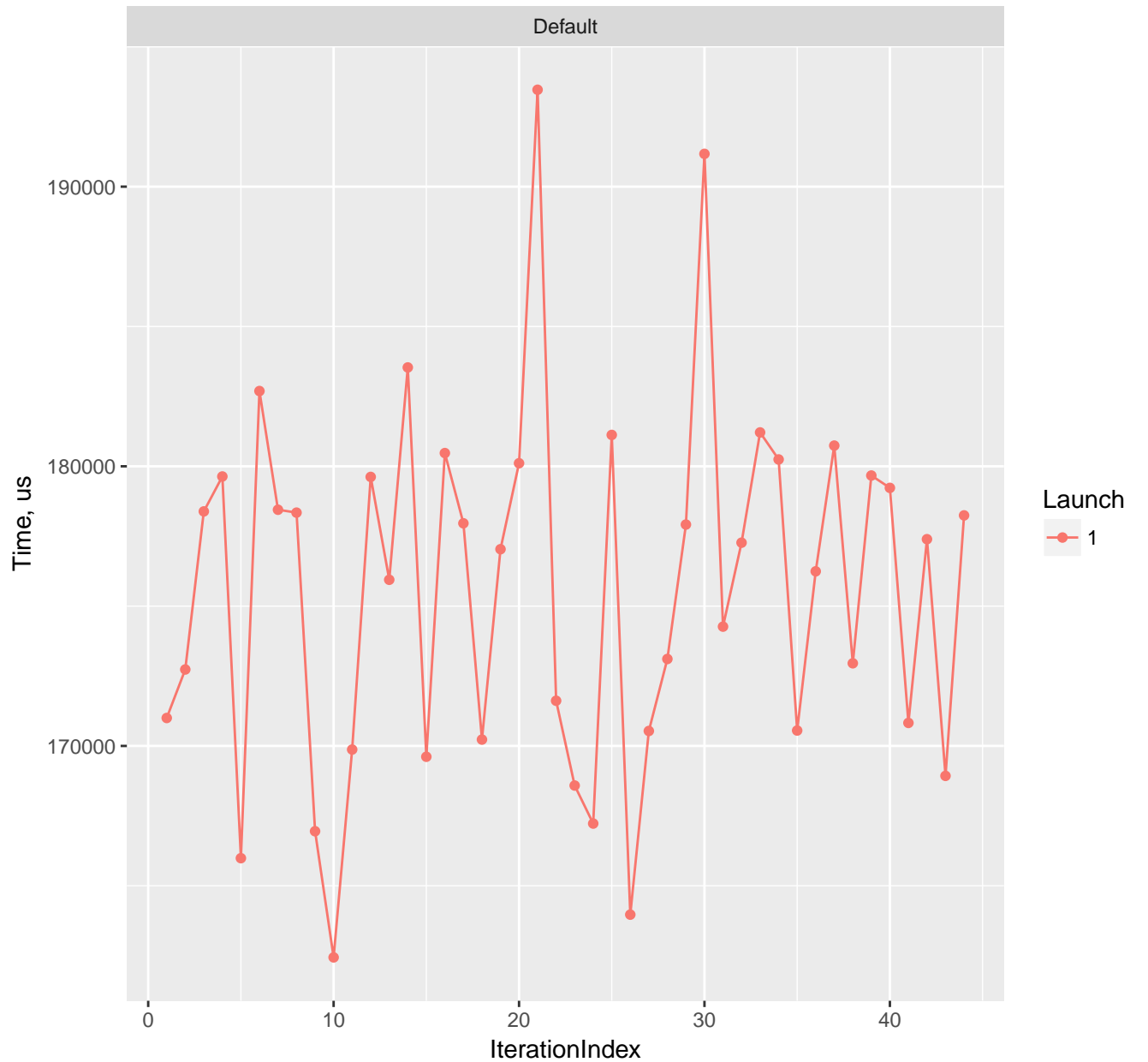
# CpsBenchmark+CpsPerformanceTests / NotFoundAvm2 / Default



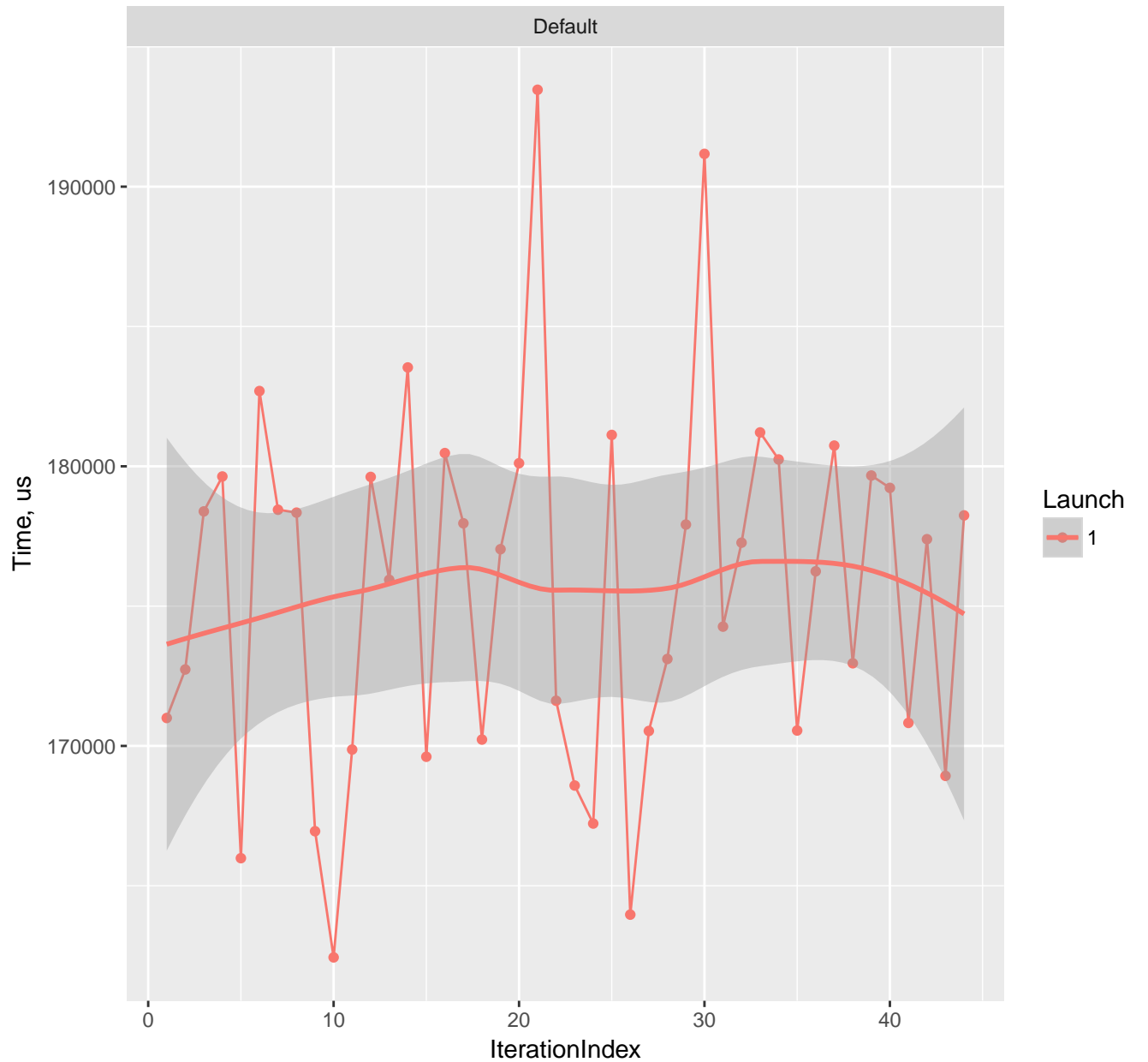




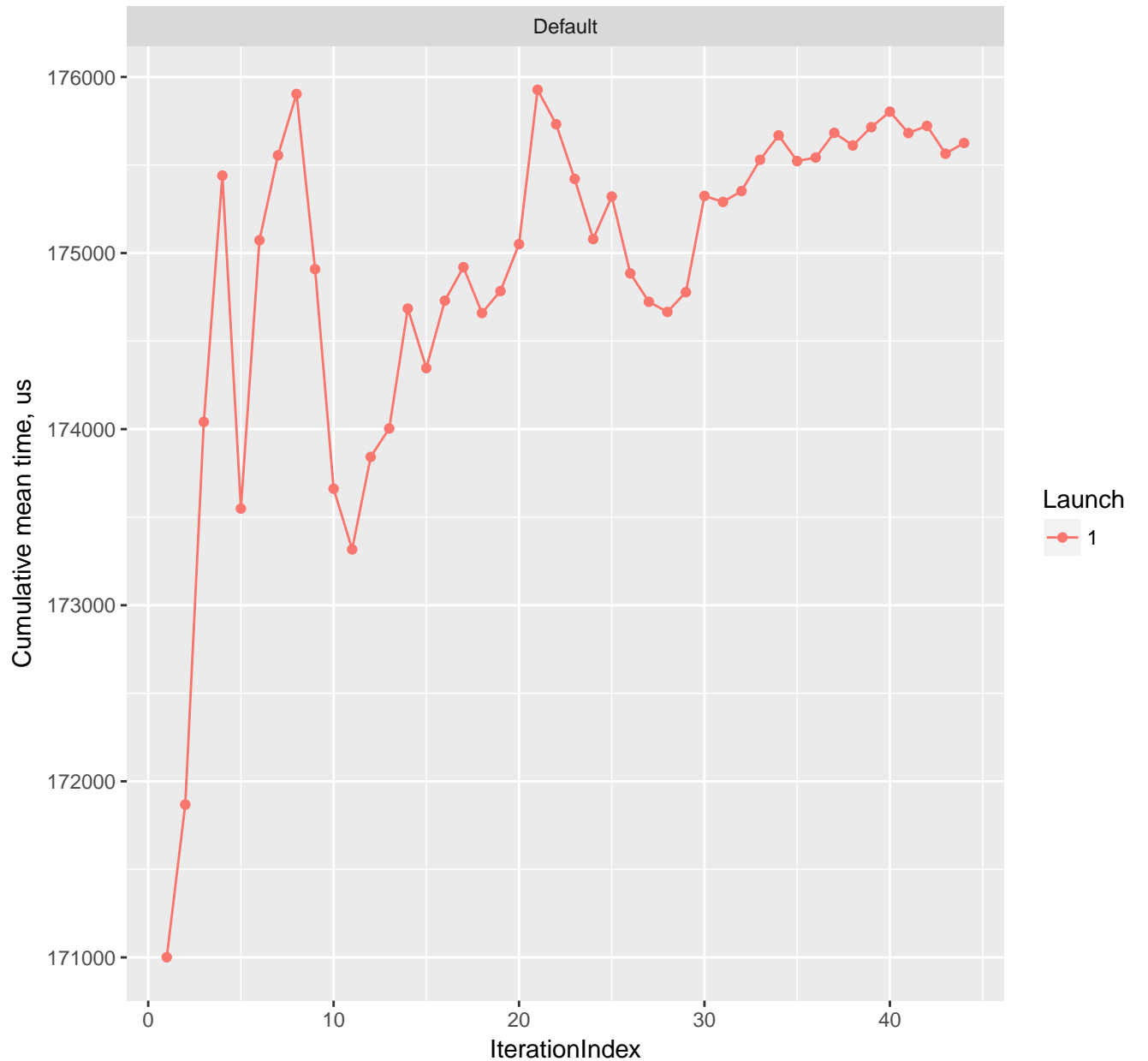
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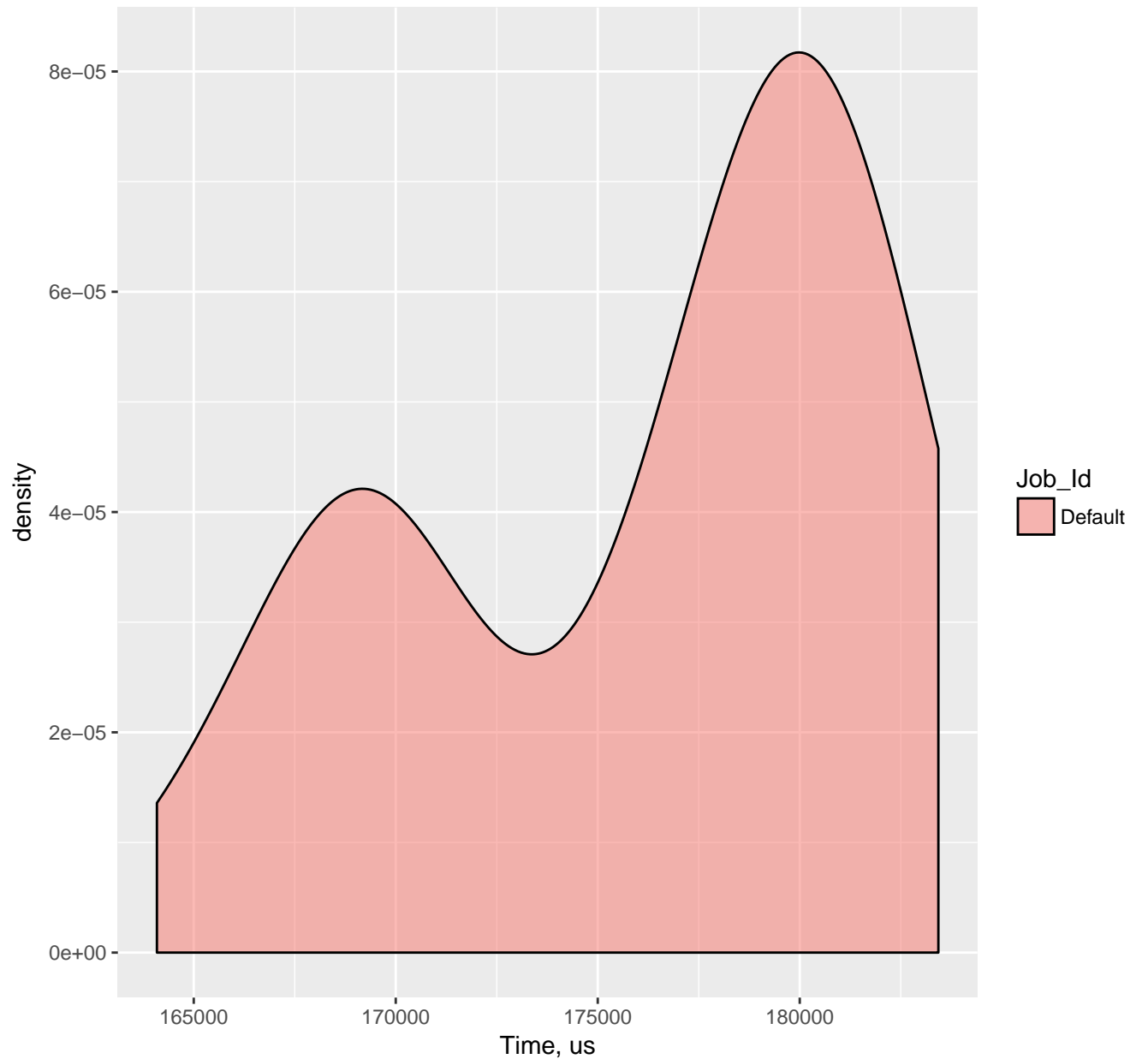
# CpsBenchmark+CpsPerformanceTests / NotFoundAvm2



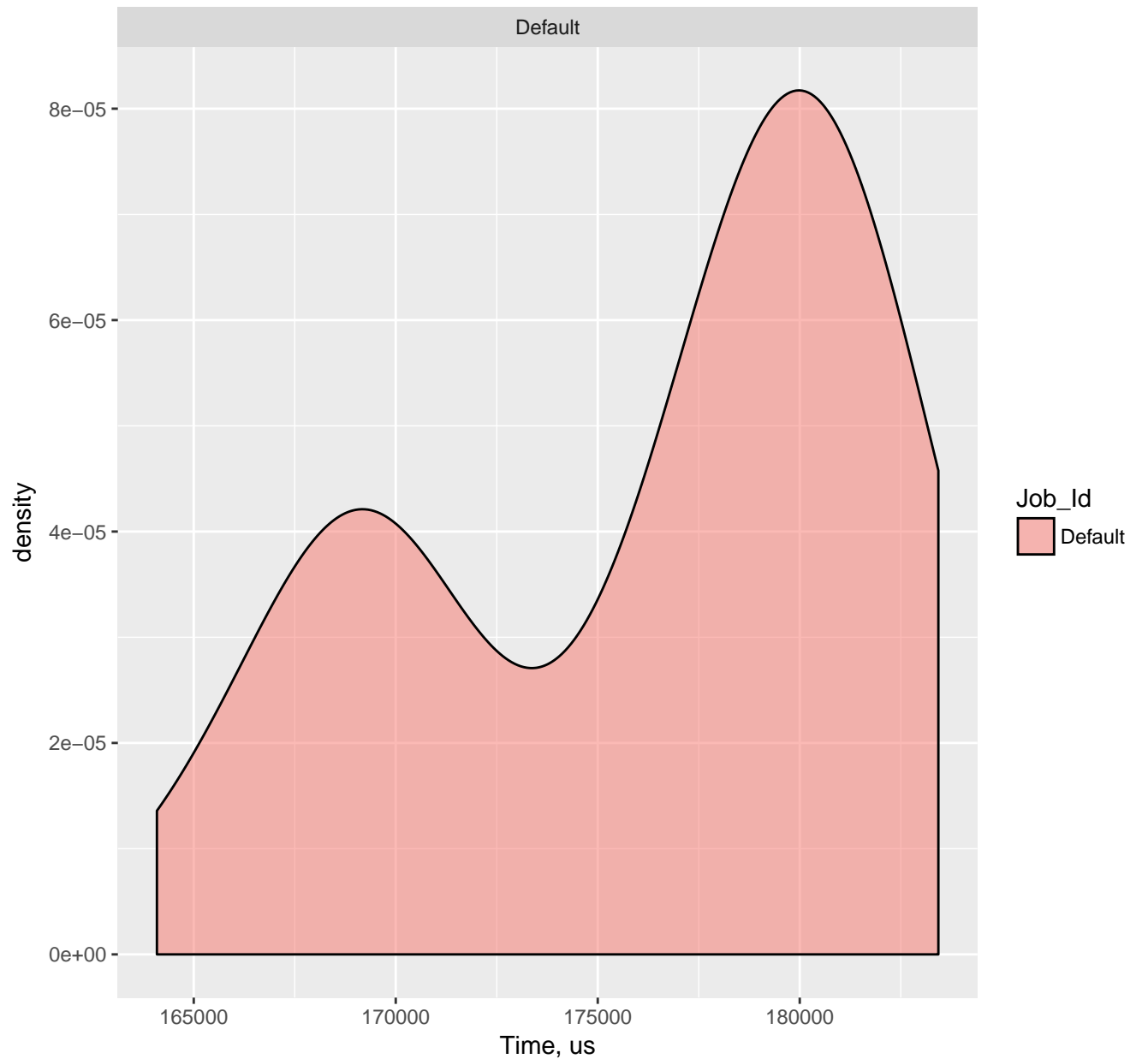
# CpsBenchmark+CpsPerformanceTests / NotFoundAvm2

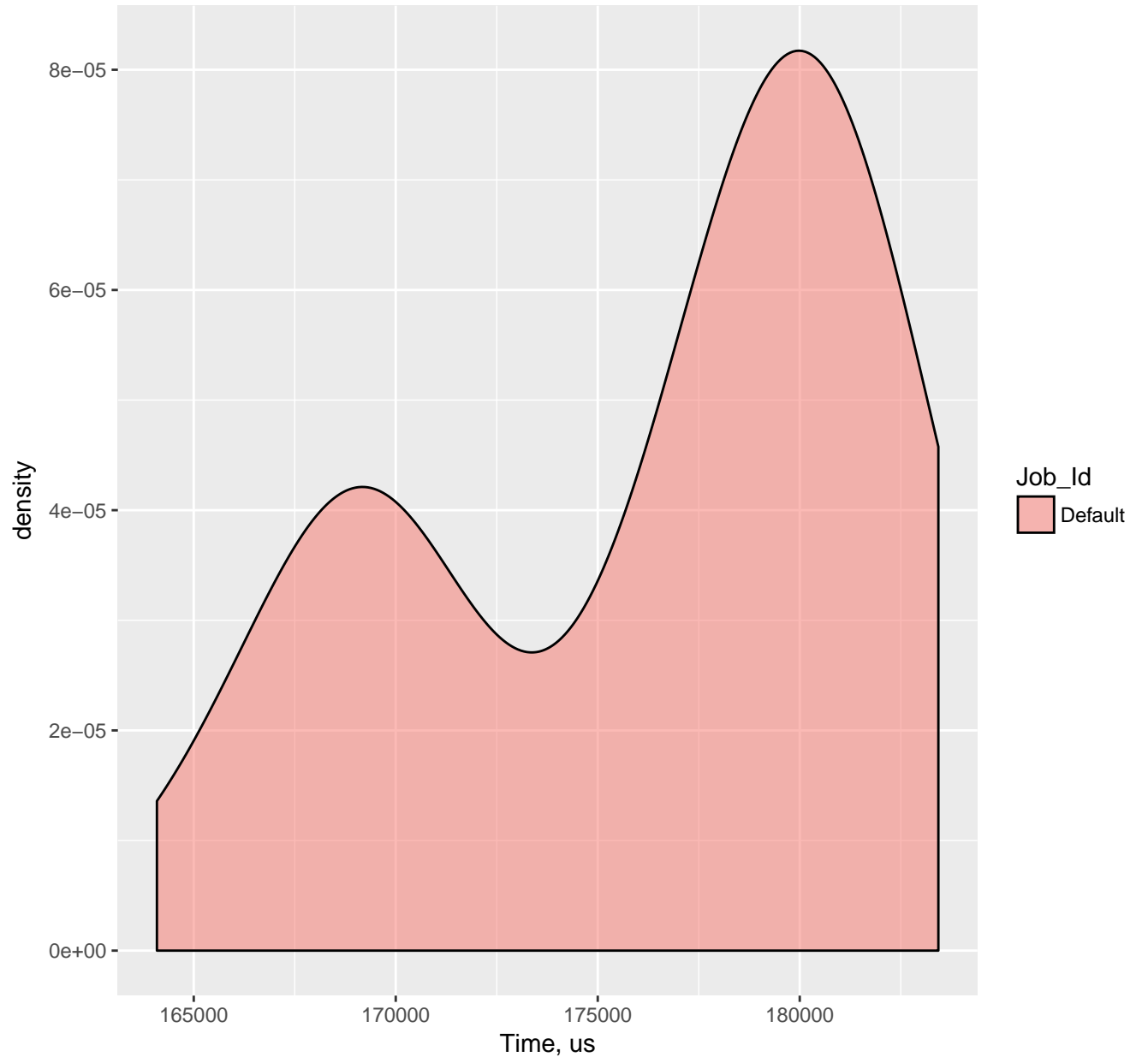


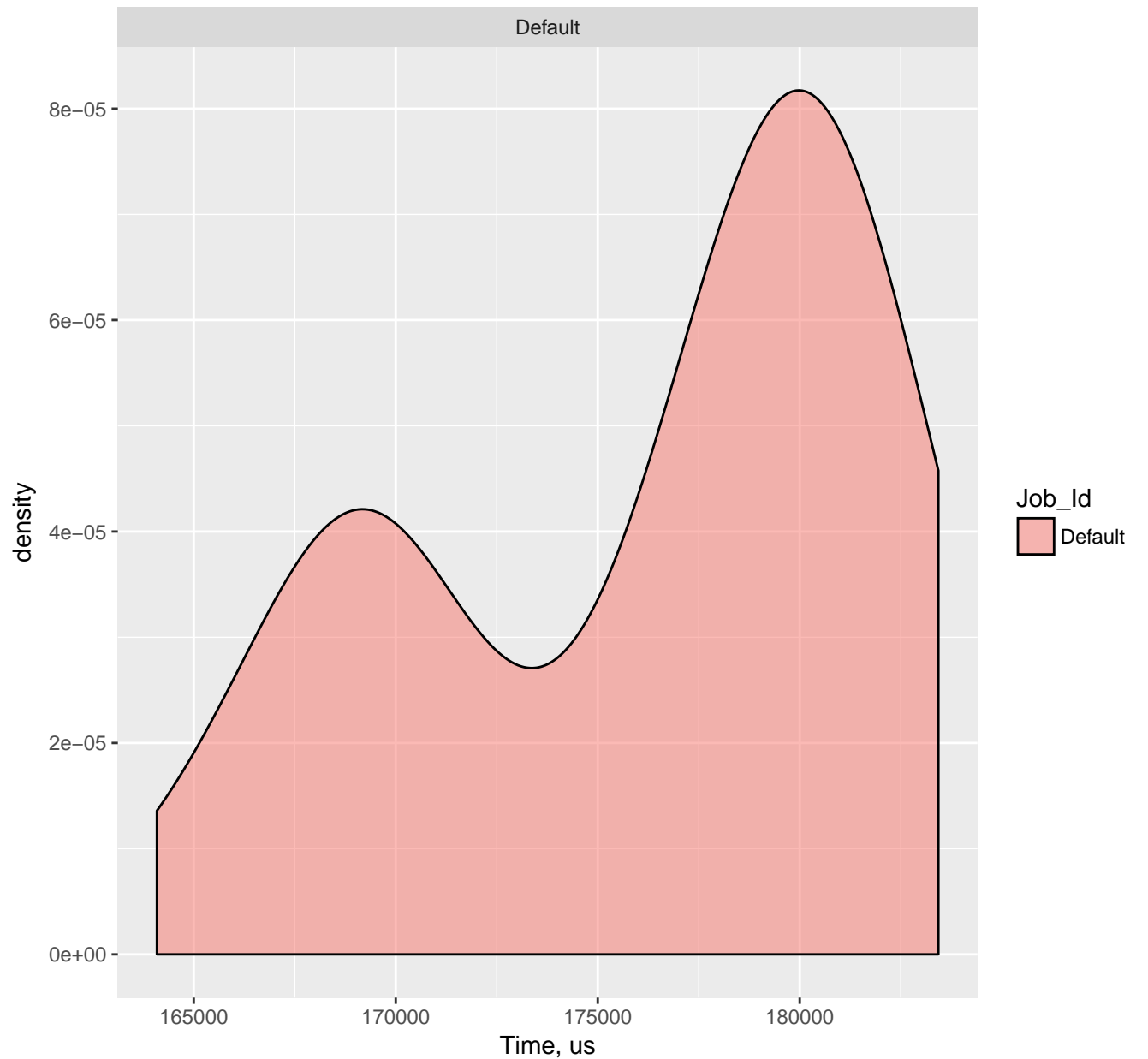
# CpsBenchmark+CpsPerformanceTests / NotFoundAvm3

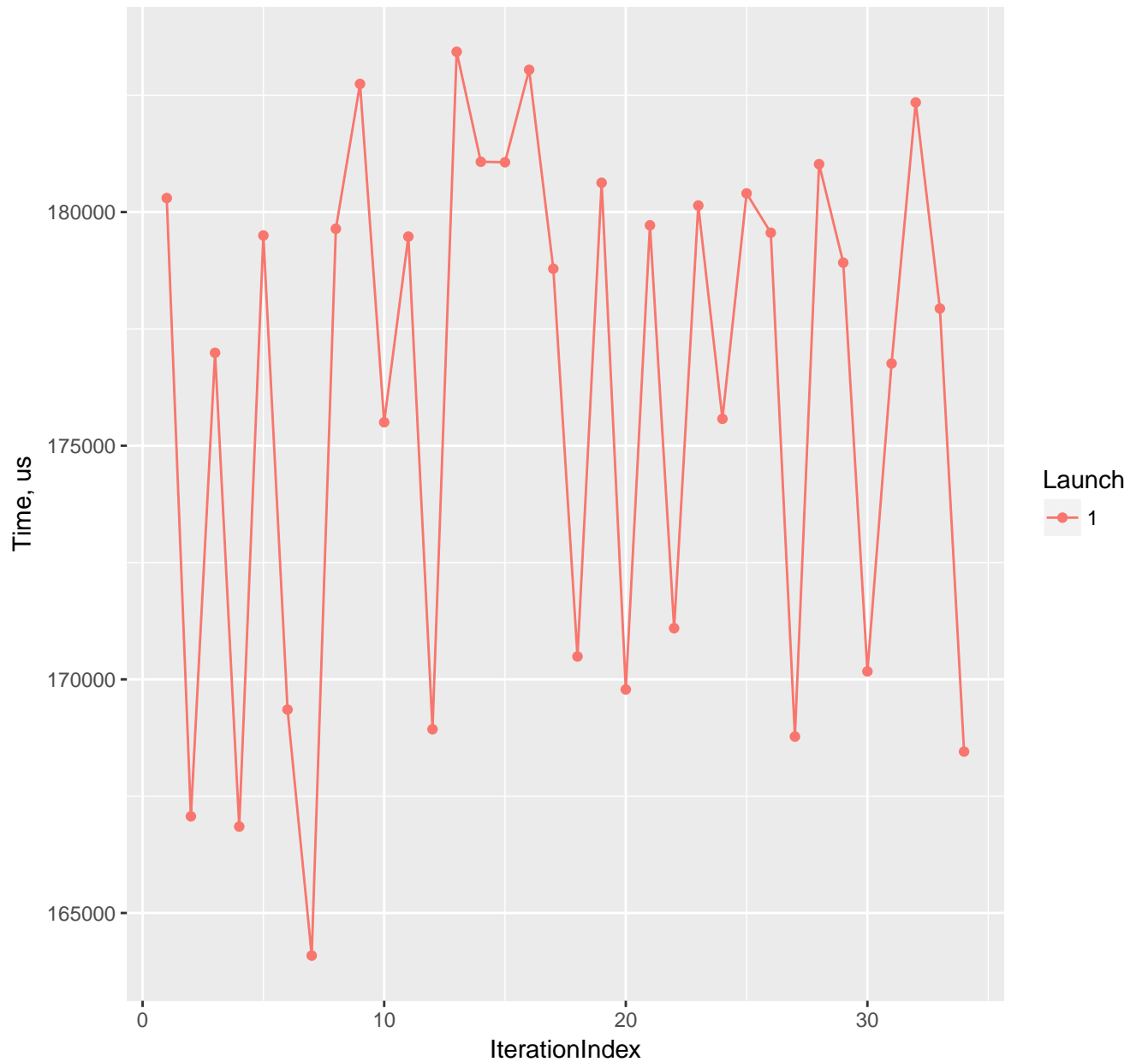


# CpsBenchmark+CpsPerformanceTests / NotFoundAvm3



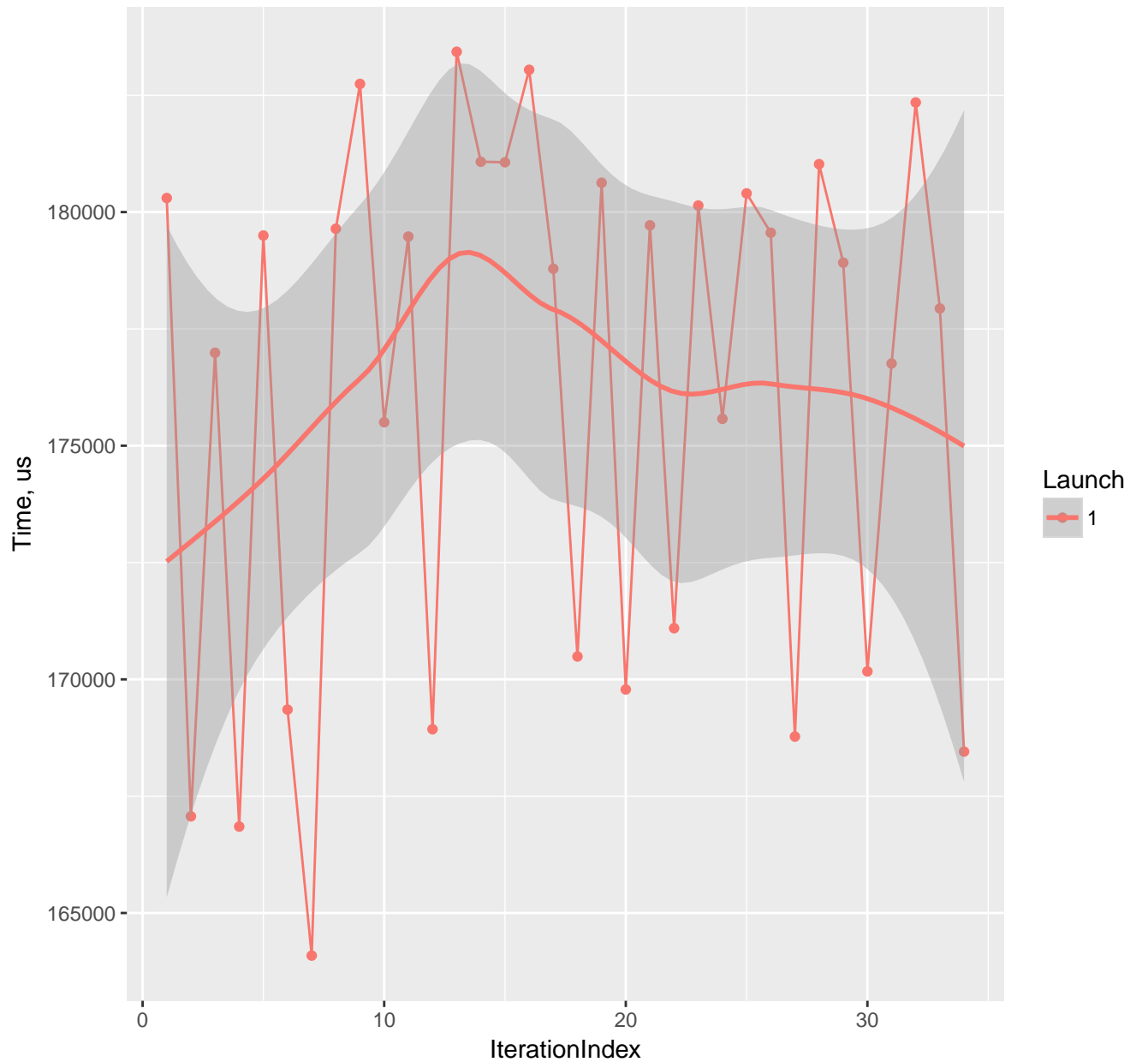


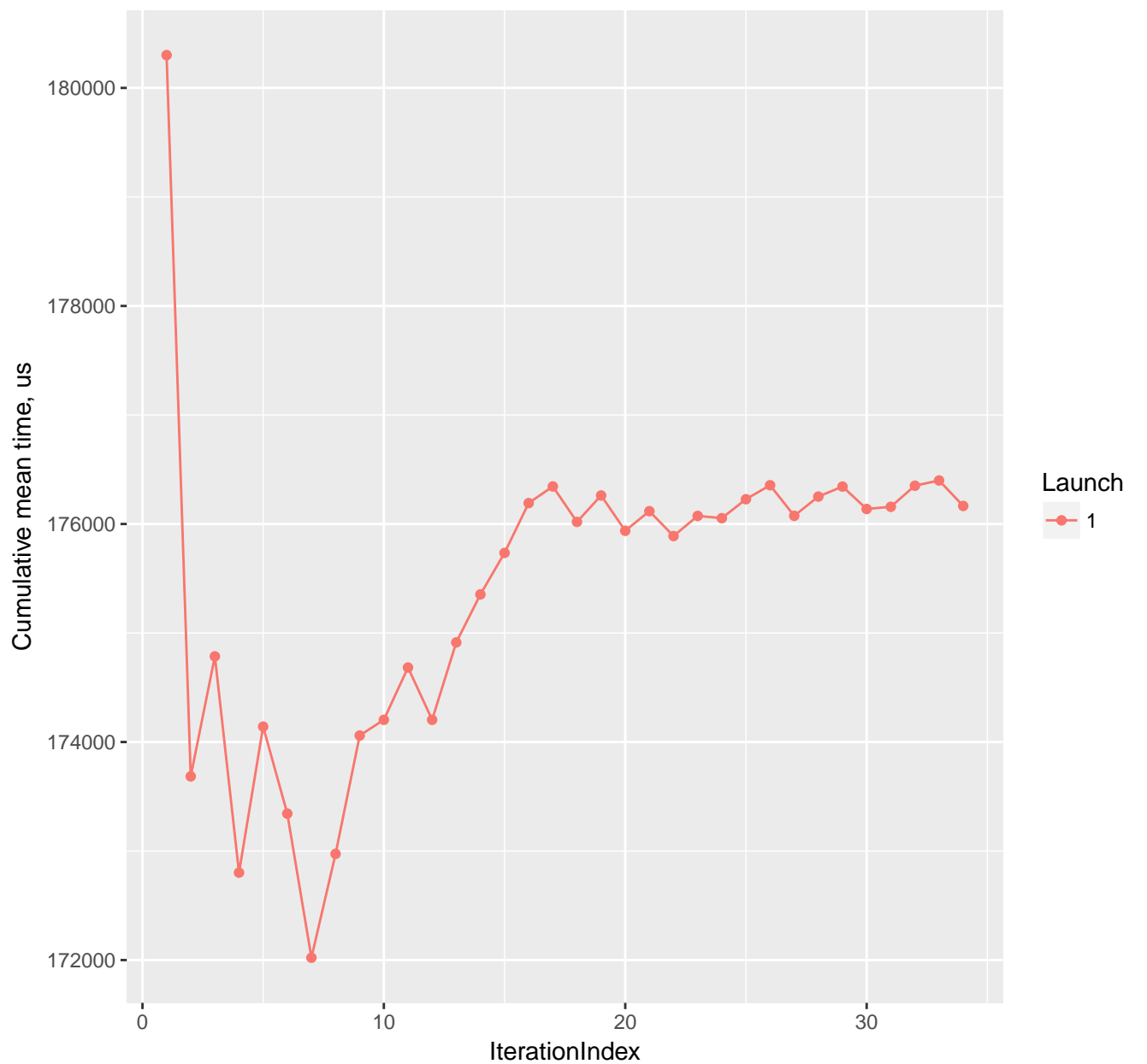


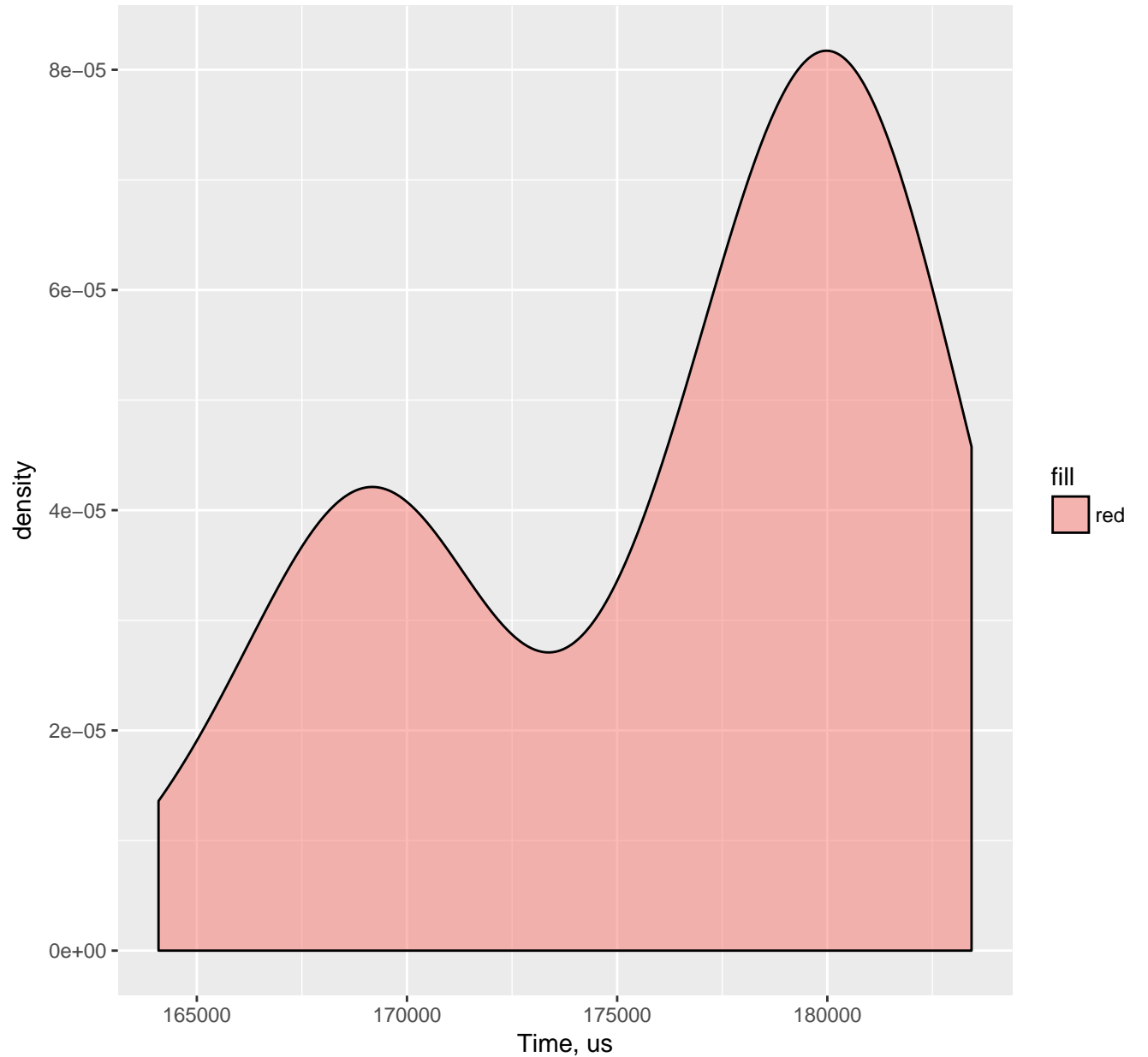




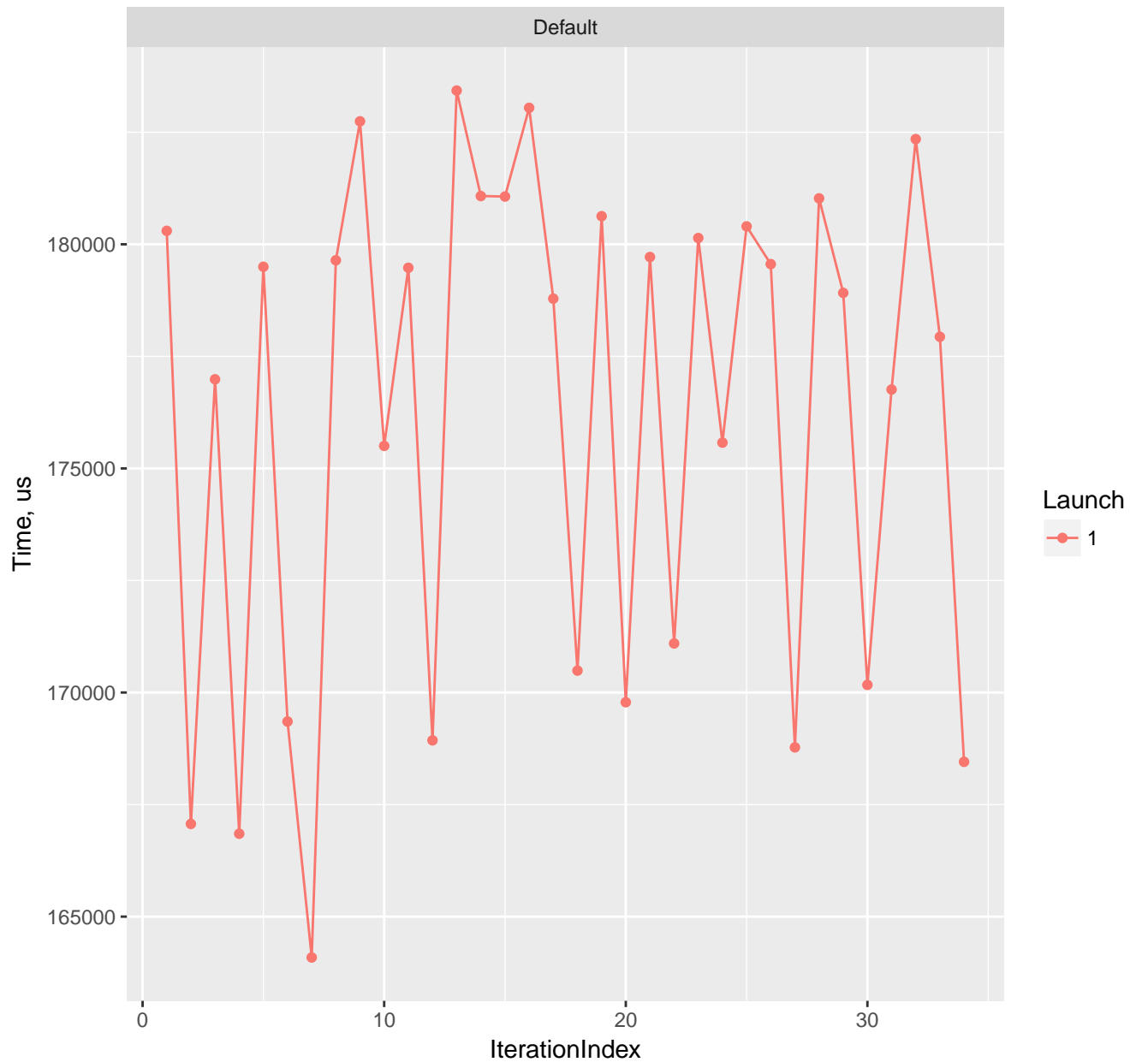
# CpsBenchmark+CpsPerformanceTests / NotFoundAvm3 / Default



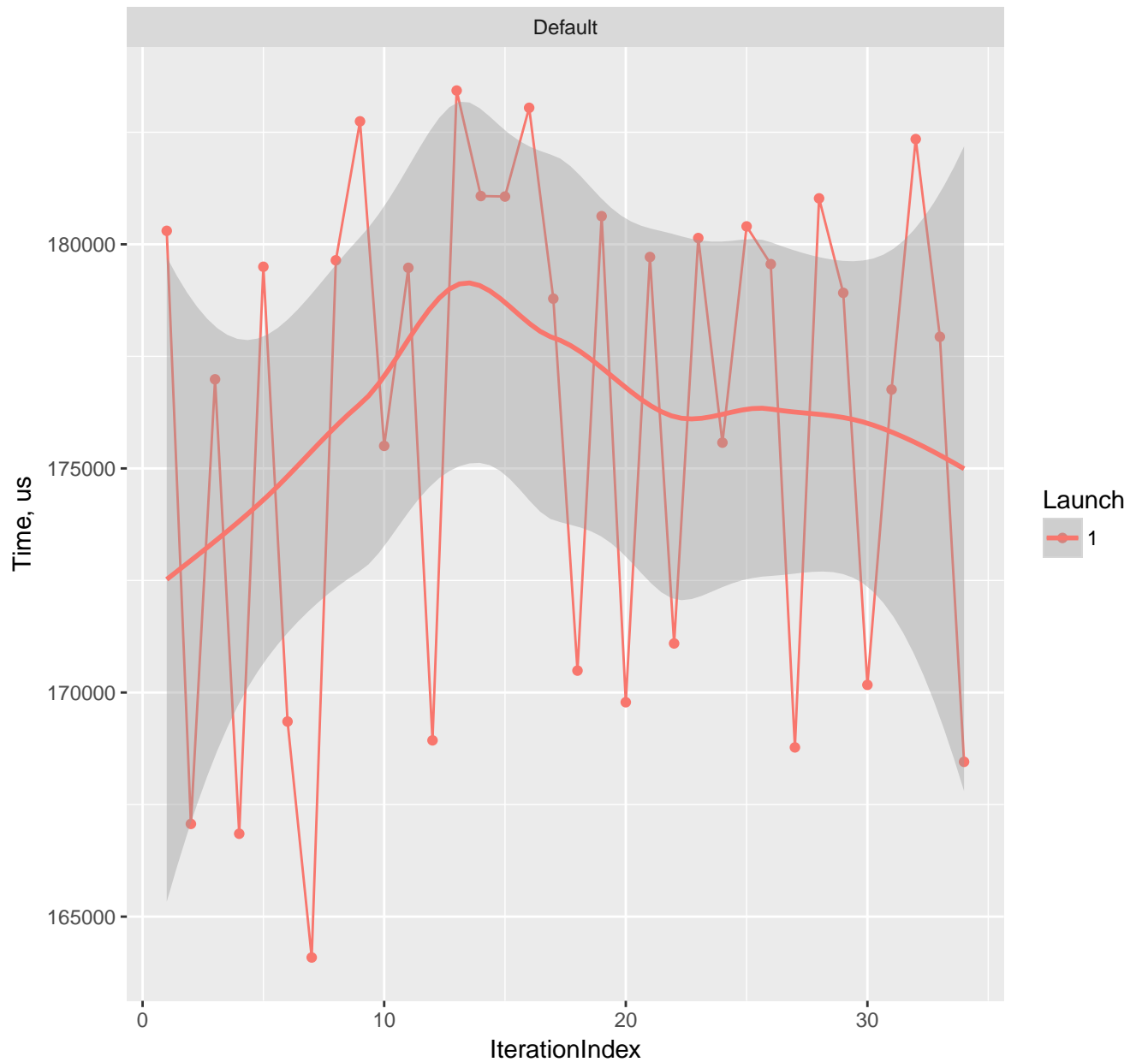




# CpsBenchmark+CpsPerformanceTests / NotFoundAvm3



# CpsBenchmark+CpsPerformanceTests / NotFoundAvm3



# CpsBenchmark+CpsPerformanceTests / NotFoundAvm3

