FIGURE 1 - System Architecture Overview Sheet 1 of 7

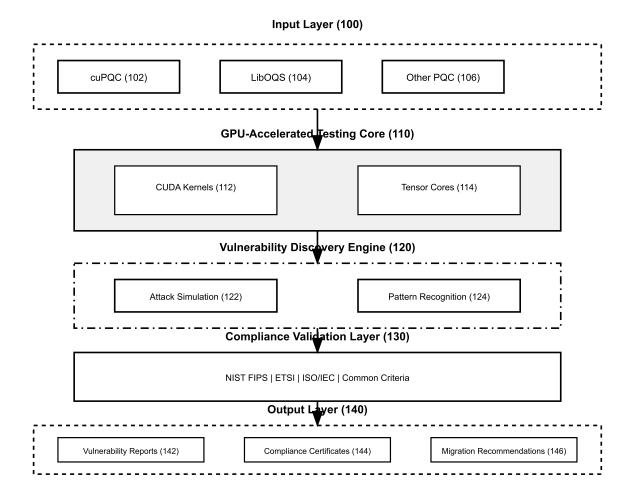


FIGURE 2 - CUDA Kernel Architecture Sheet 2 of 7

Thread Block Organization (200)

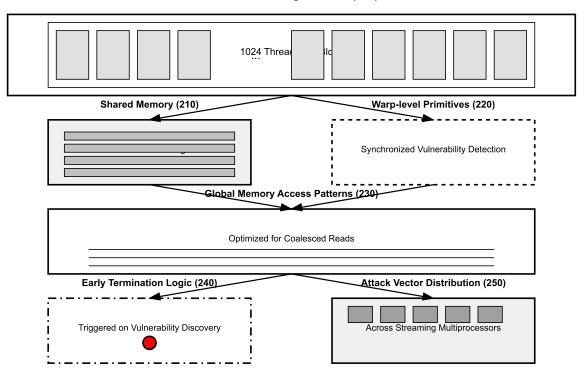


FIGURE 3 - Tensor Core Optimization Sheet 3 of 7

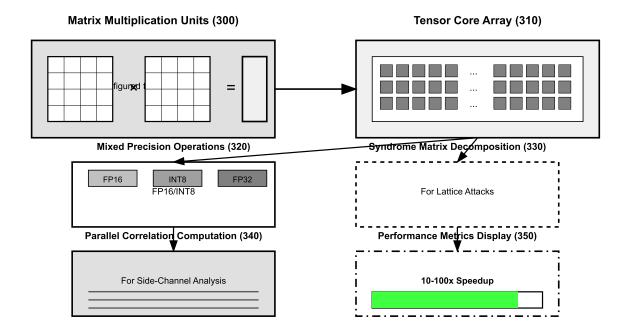


FIGURE 4 - Vulnerability Discovery Data Flow Sheet 4 of 7

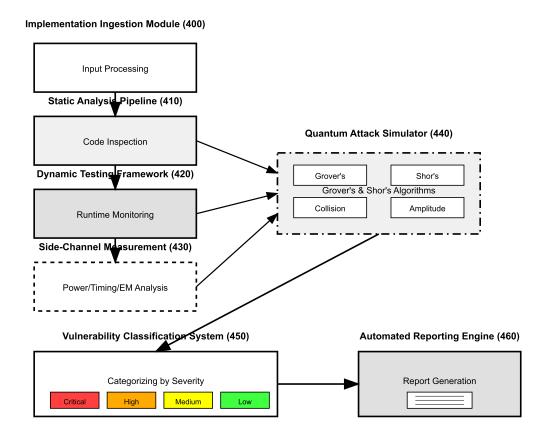


FIGURE 5 - Multi-Standard Compliance Pipeline Sheet 5 of 7

Standard Parsers (500)

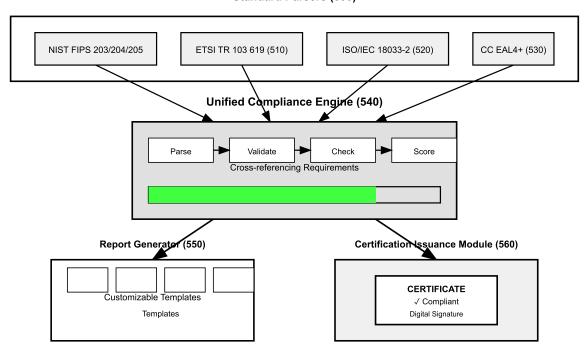


FIGURE 6 - Migration Recommendation Algorithm Sheet 6 of 7

Input Parameters (600)

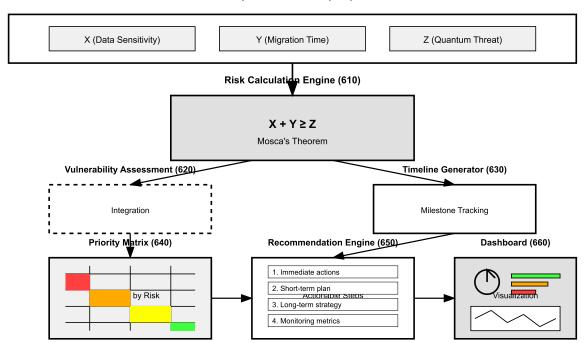


FIGURE 7 - Implementation Layer Separation Sheet 7 of 7

Implementation Layer (700)

