

FIGURE 1 - System Architecture Overview
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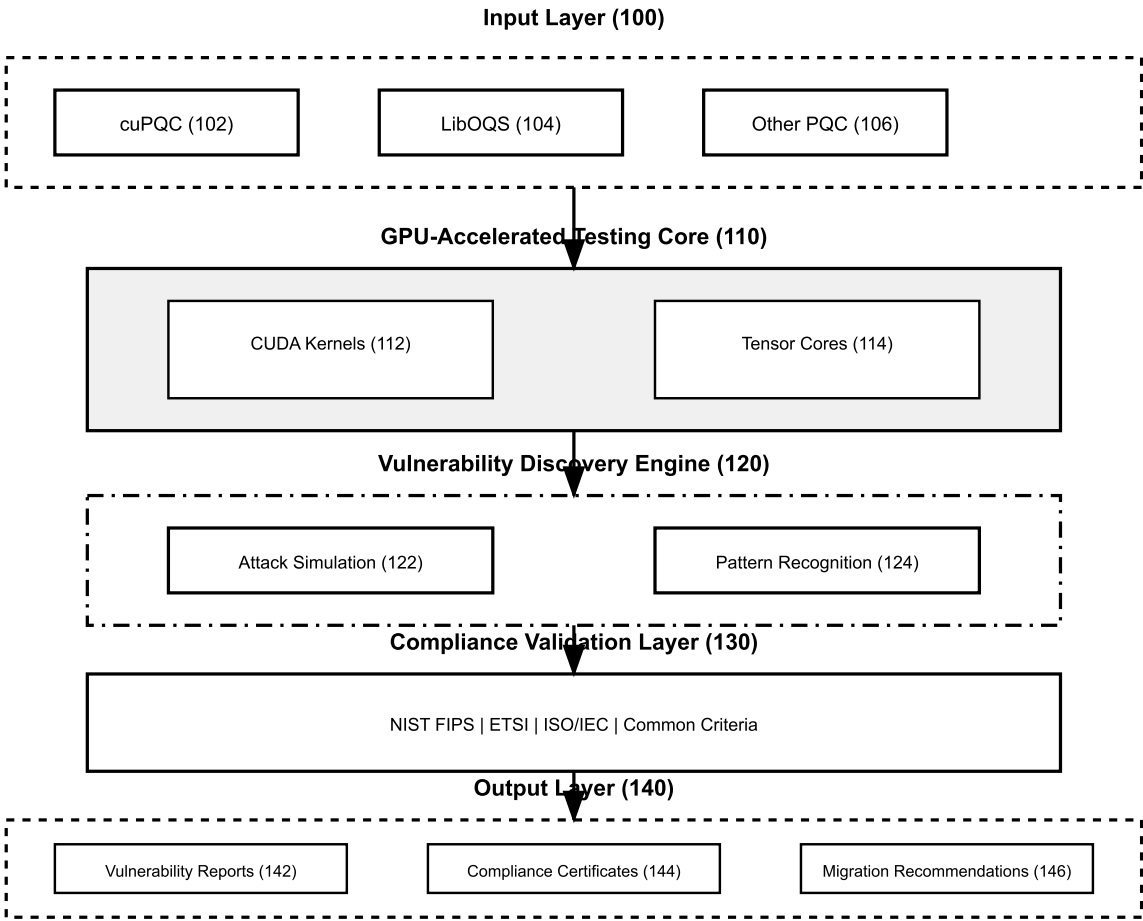


FIGURE 2 - CUDA Kernel Architecture
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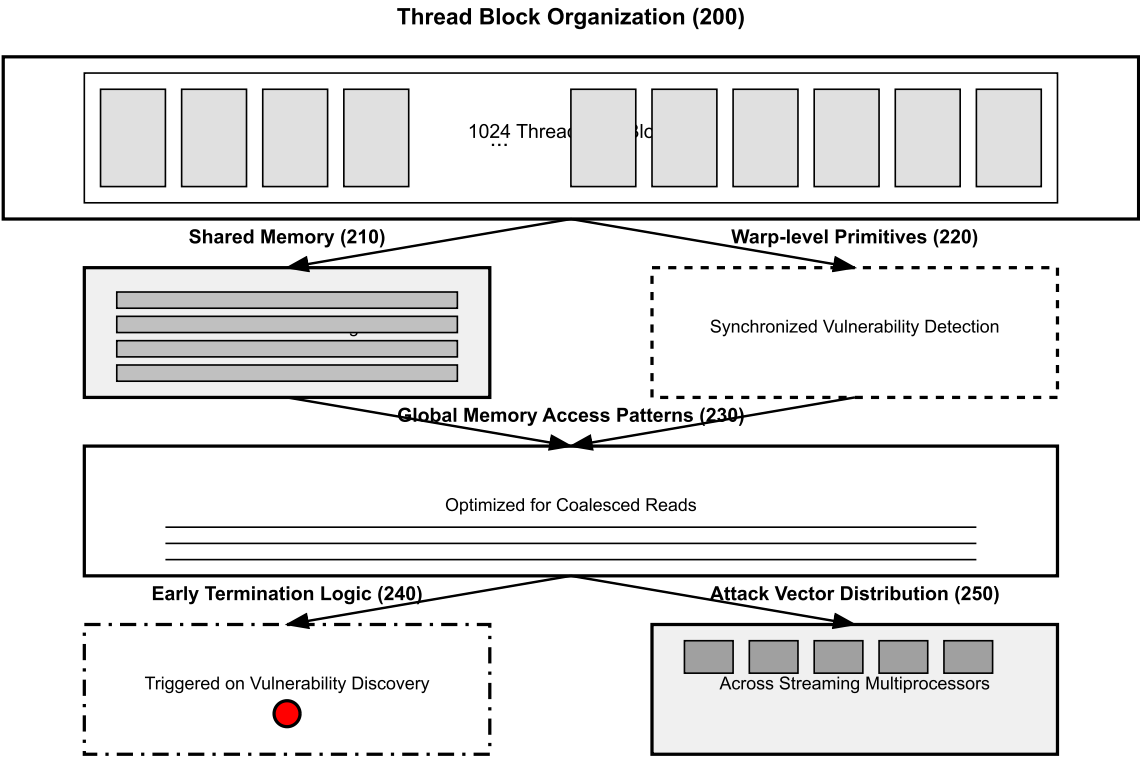


FIGURE 3 - Tensor Core Optimization
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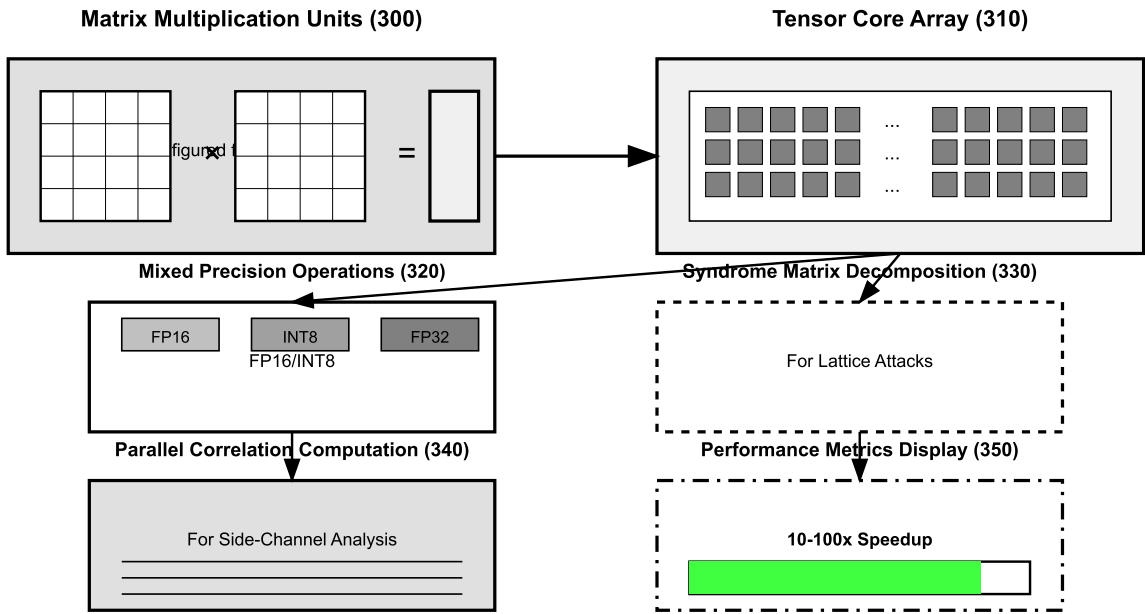


FIGURE 4 - Vulnerability Discovery Data Flow
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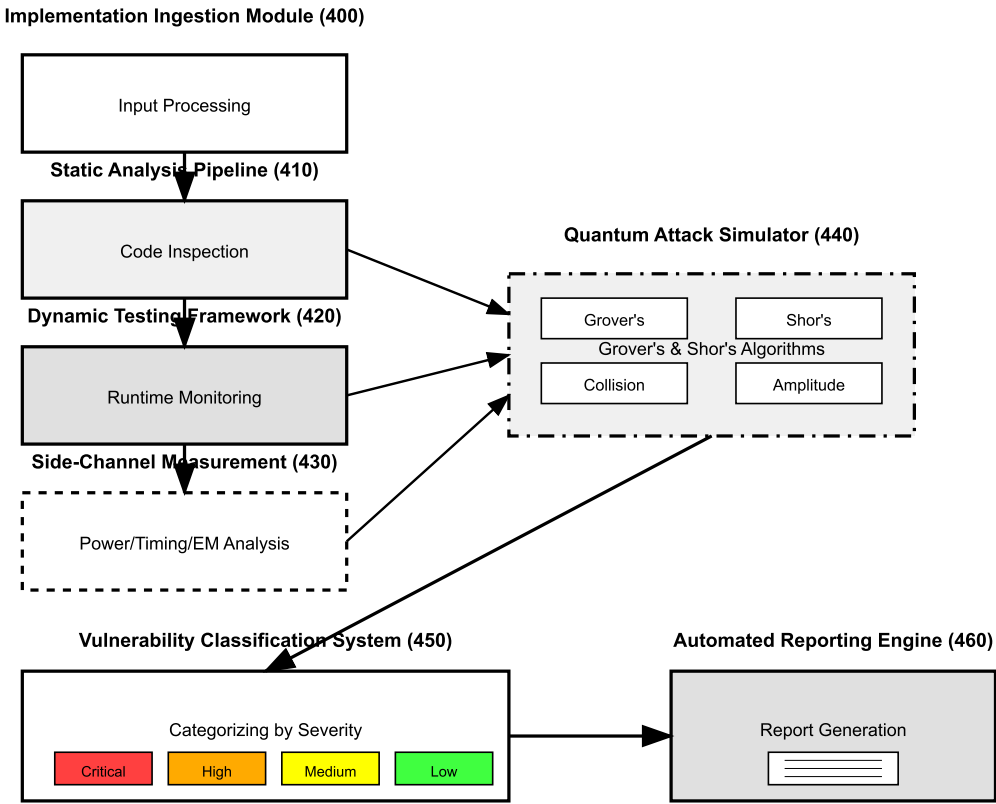


FIGURE 5 - Multi-Standard Compliance Pipeline
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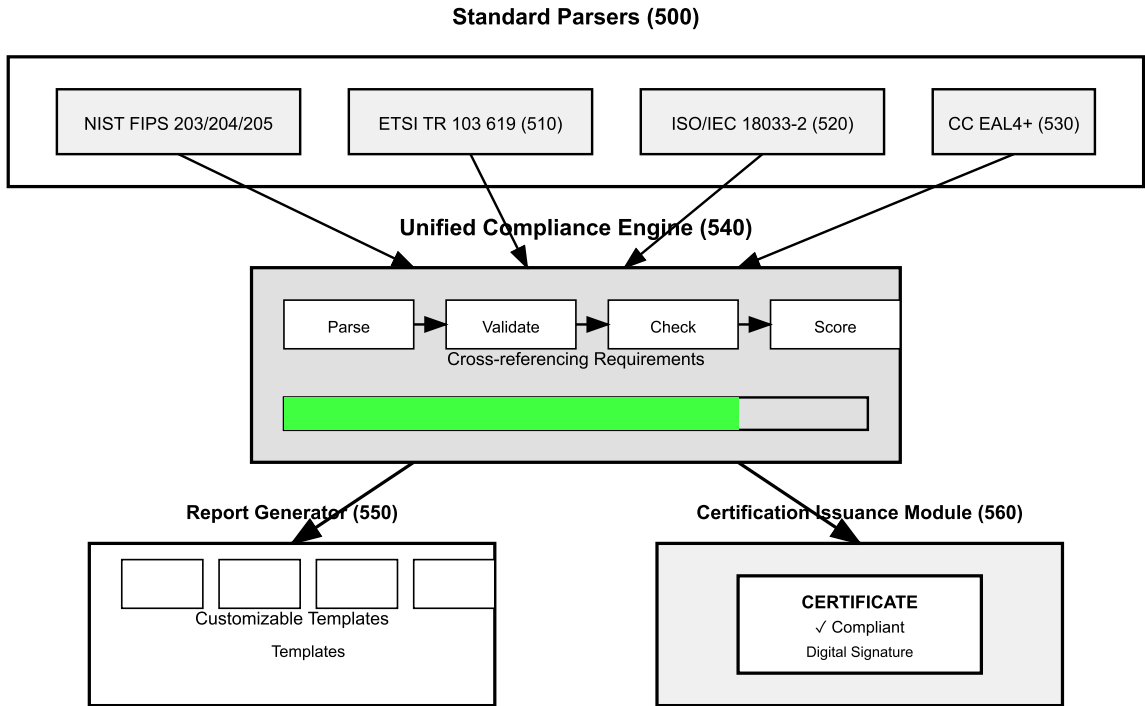


FIGURE 6 - Migration Recommendation Algorithm
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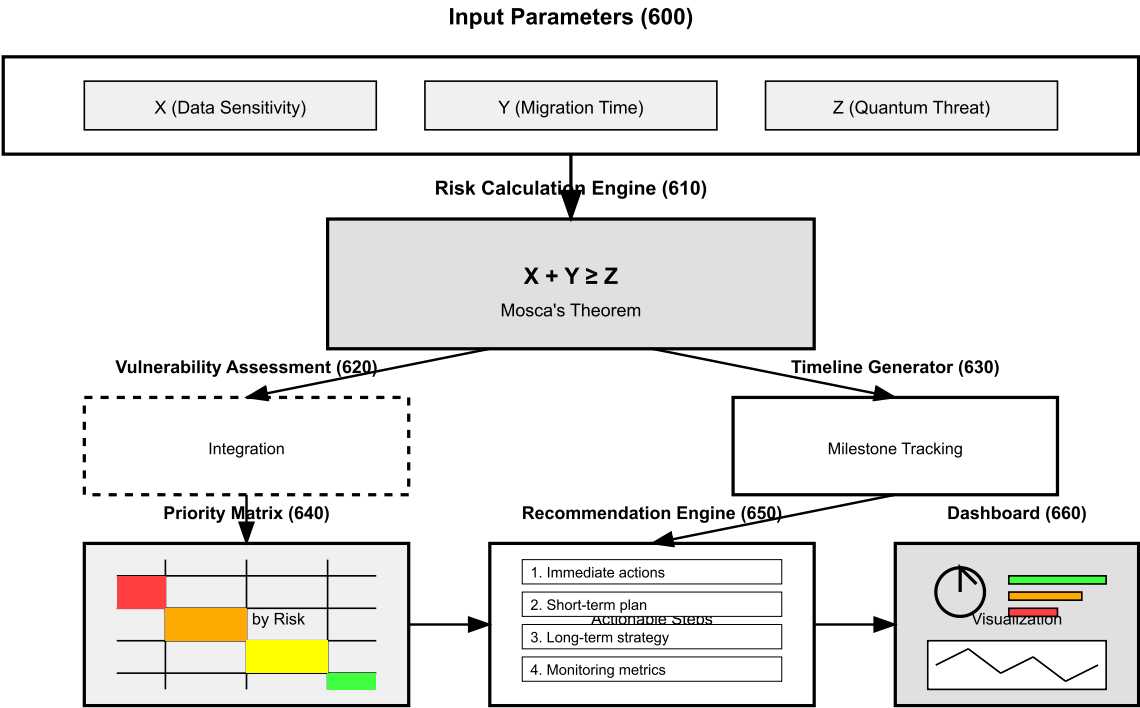


FIGURE 7 - Implementation Layer Separation
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