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FIG. 4 - POWER-OPTIMIZED MEMORY HIERARCHY

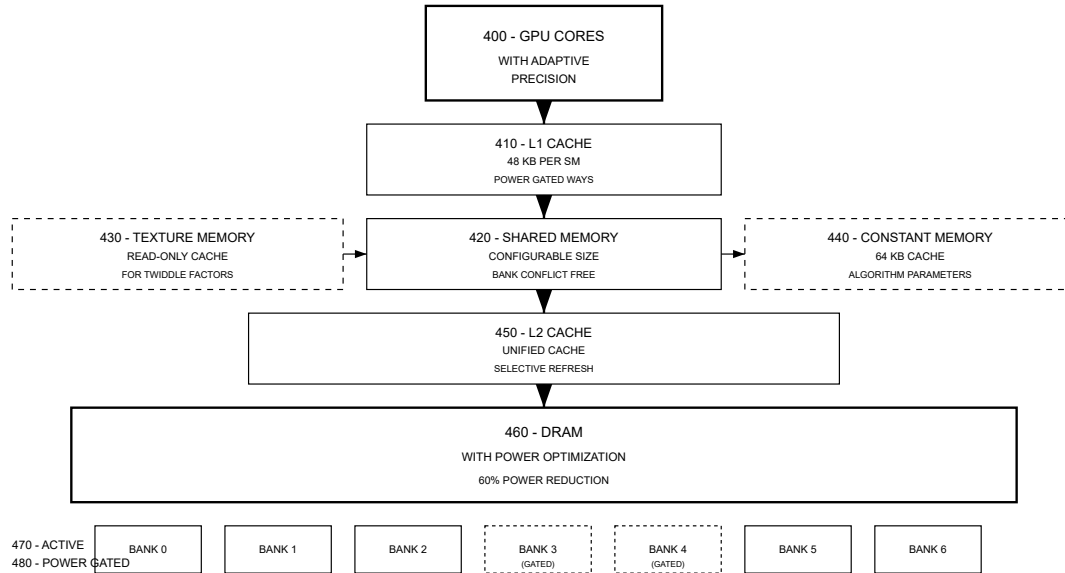


FIG. 5 - POWER EFFICIENCY PERFORMANCE METRICS

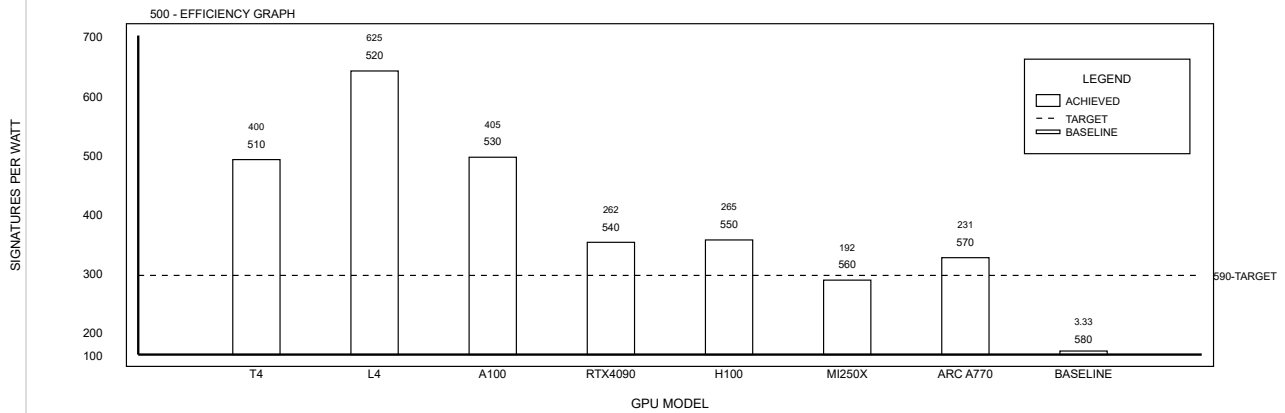


FIG. 6 - DYNAMIC VOLTAGE/FREQUENCY SCALING OPERATING POINTS

