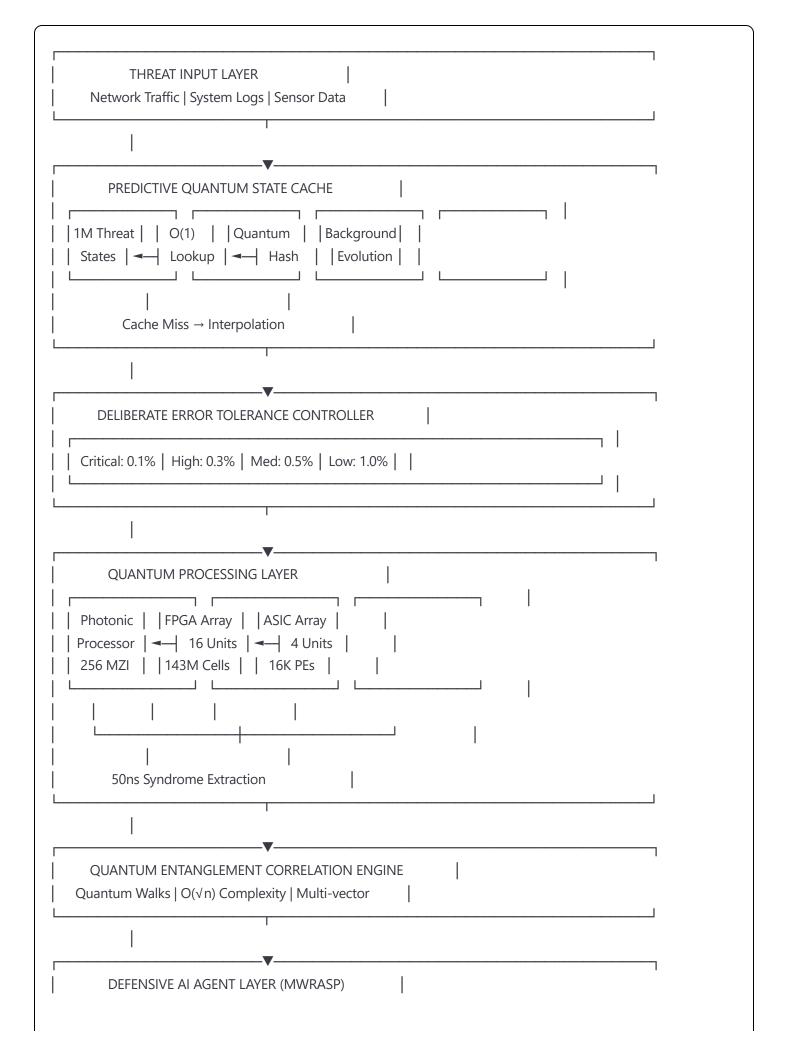
INFORMAL DRAWINGS FOR PROVISIONAL PATENT APPLICATION

Docket No.: RUTHERFORD-016-PROV

Title: DELIBERATE ERROR TOLERANCE ARCHITECTURE (DETA)

FIGURE 1: SYSTEM ARCHITECTURE

Hierarchical Processing Layers



Autonomous Response | Sub-10ms Mitigation

FIGURE 2: ERROR RATE VS LATENCY TRADE-OFF CURVE

Optimal Operating Zone

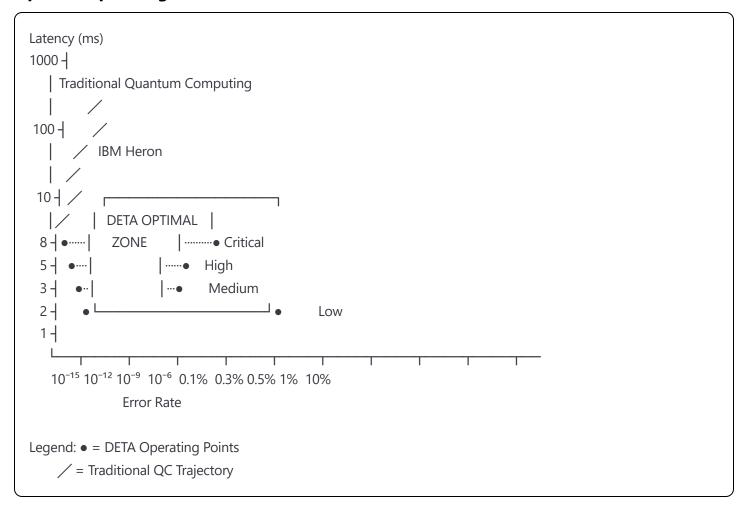


FIGURE 3: PREDICTIVE QUANTUM STATE CACHE ARCHITECTURE

Cache Structure and Interpolation Engine

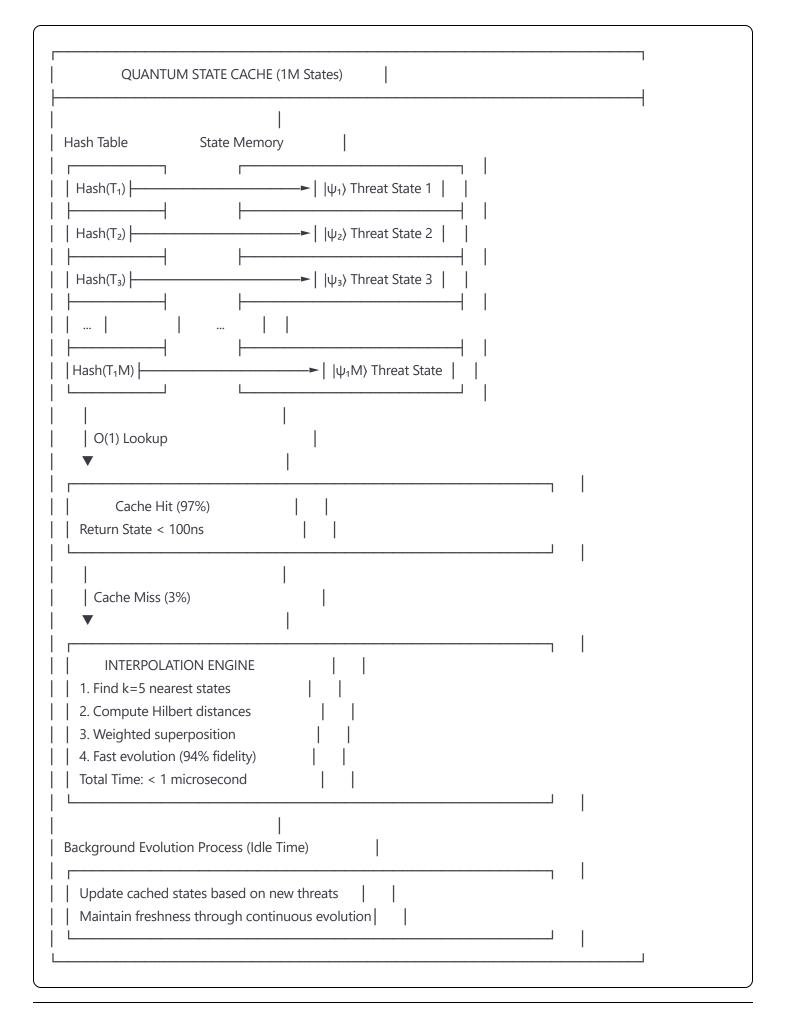
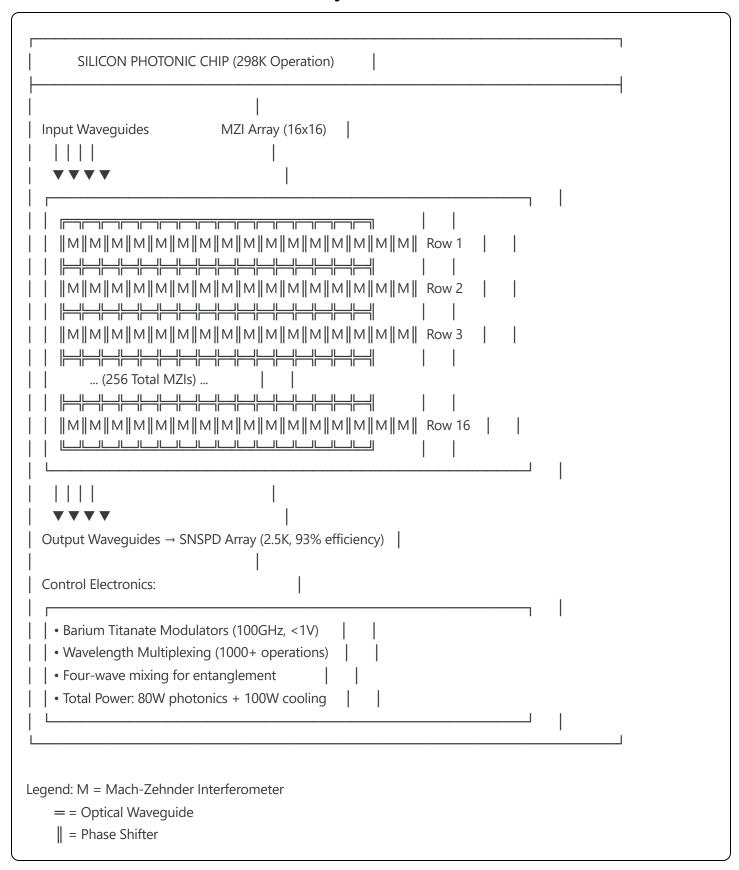


FIGURE 4: ROOM-TEMPERATURE PHOTONIC PROCESSOR LAYOUT

256 Mach-Zehnder Interferometer Array



DETA vs Current State-of-the-Art

```
DETA
                   IBM
                           Google
                                    IonQ
          System
                   Heron
                            Willow
                                     Forte
              <10ms | >100ms | >100ms | >500ms |
 Latency
 Error Rate
              0.1-1% | 0.5%
                              <0.1%
                                        0.02%
              <1kW
                        25kW
                                  30kW | 15kW |
 Power
 Temperature |
                         15mK | 20mK | 77K |
                298K
 Throughput | 10M/sec | 10K/sec | 5K/sec | 1K/sec |
                                        | 100µs |
 Syndrome
                50ns
                        1-10μs | 63μs
 Deployment | Standard | Quantum | Quantum | Quantum |
                  | Facility | Facility | Facility |
          Rack
Performance Improvement Factors:
Speed:
           100-1000x
           25-30x
Power:
Deployment: Immediate vs Years
 Cost:
          100x lower
Threat Detection Accuracy:
 DETA:
           99.5% @ 10ms
 Classical: 95% @ 100ms
Traditional QC: 99.99% @ 1000ms
Key Insight: 99.5% accuracy in 10ms provides superior
real-world protection compared to 99.99% in 1000ms
```

DRAWING NOTES FOR USPTO

- 1. These are informal drawings suitable for provisional patent application
- 2. Formal drawings will be prepared for non-provisional filing

3. All drawings are original work created for this invention

4. No copyrighted material has been incorporated

5. Drawings illustrate the key technical innovations claimed

Prepared by: Brian James Rutherford

Date: [Current Date]

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