

FIGURE 4
ROOM-TEMPERATURE PHOTONIC PROCESSOR LAYOUT

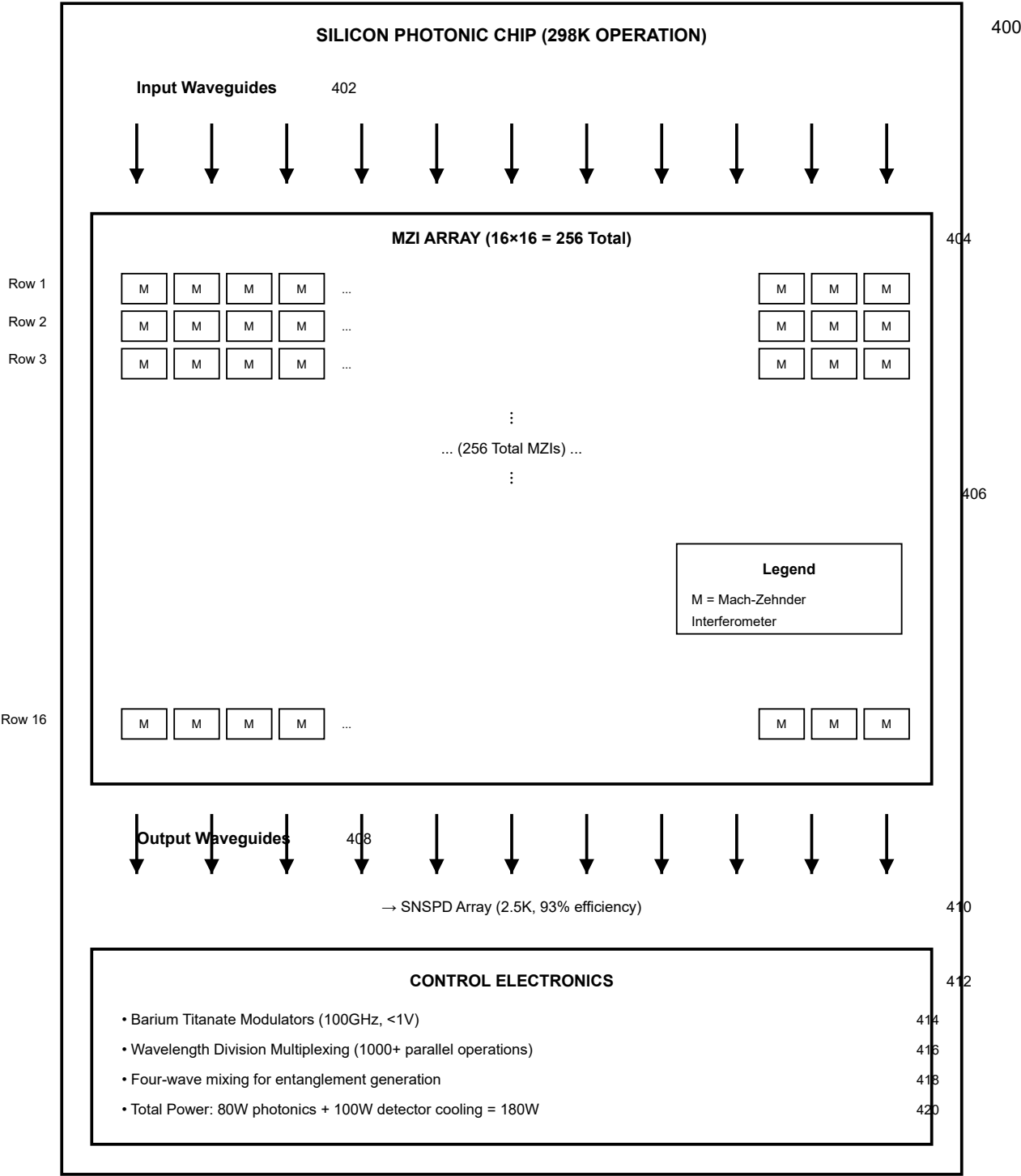


FIG. 4