

Brauer diagrams, modular operads, & a graphical nerve theorem for circuit algebras

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I acknowledge the traditional custodians of the land on which I work: the Wattamattageal clan of the Darug nation. I pay my respects to Elders past, present and future.

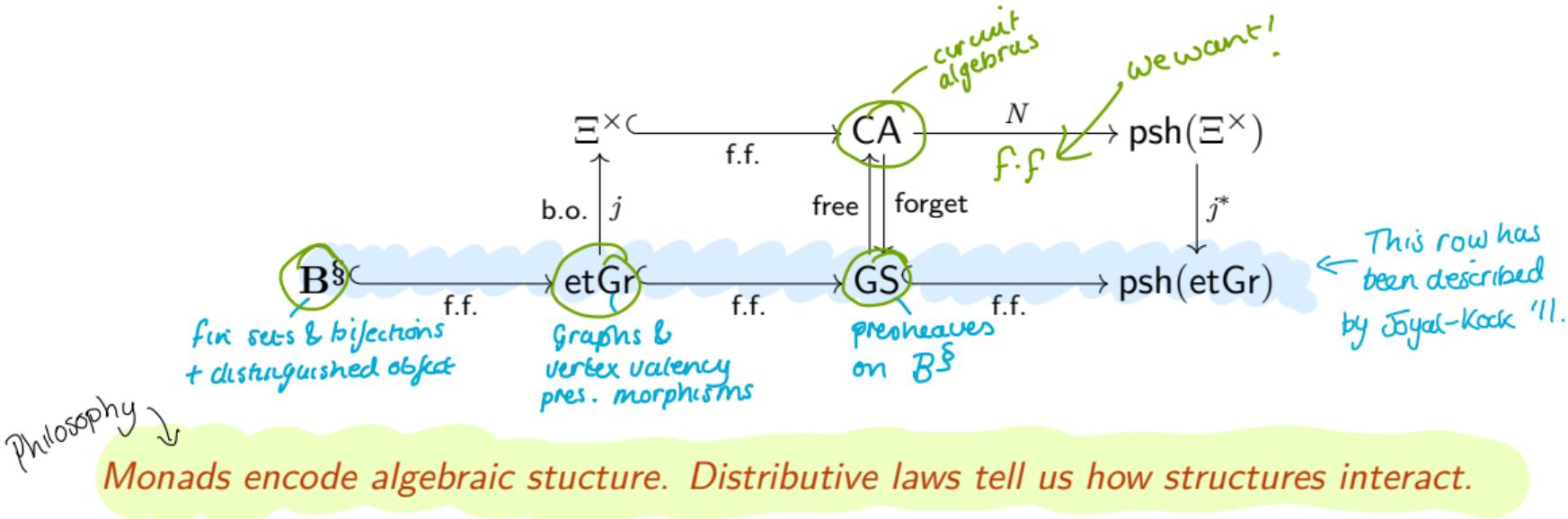
Aims

- Construct a monad for circuit algebras and prove a nerve theorem,
- describe relationships with other structures
- including a nice example of ‘exceptional loops’

This talk is based on my paper of the same name draft on www.sophieraynor.org, and my paper *Graphical combinatorics and a distributive law for modular operads* (arXiv:1911.05914) to appear *Adv Math..*

Method

Use abstract nerve theory and (iterated) distributive laws



1. Introduction

2. Brauer diagrams

3. Circuit algebras

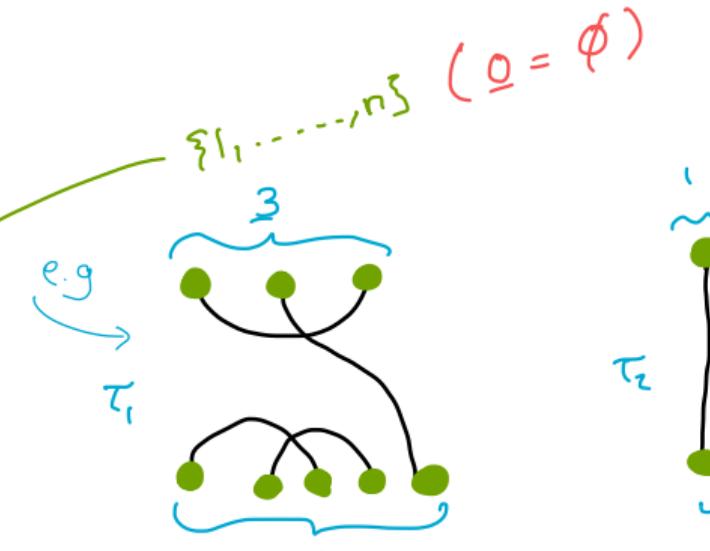
4. The monad

Category BD of (monochrome) Brauer diagrams.

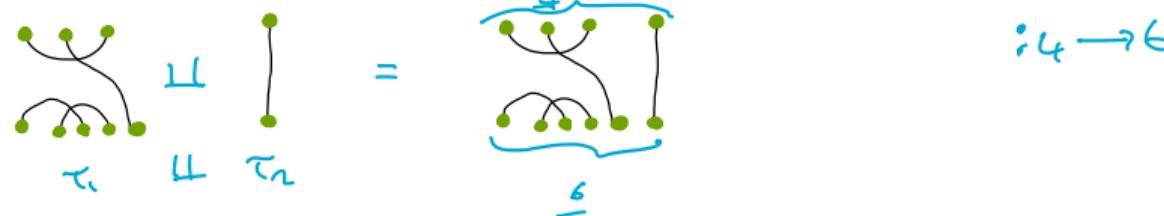
Objects: $n \in \mathbb{N}$,

Morphisms: $f: m \rightarrow n$, $f = (\tau, \mathfrak{k})$ where

- τ is a **perfect matching** on $m \amalg n$
- $\mathfrak{k} \in \mathbb{N}$.

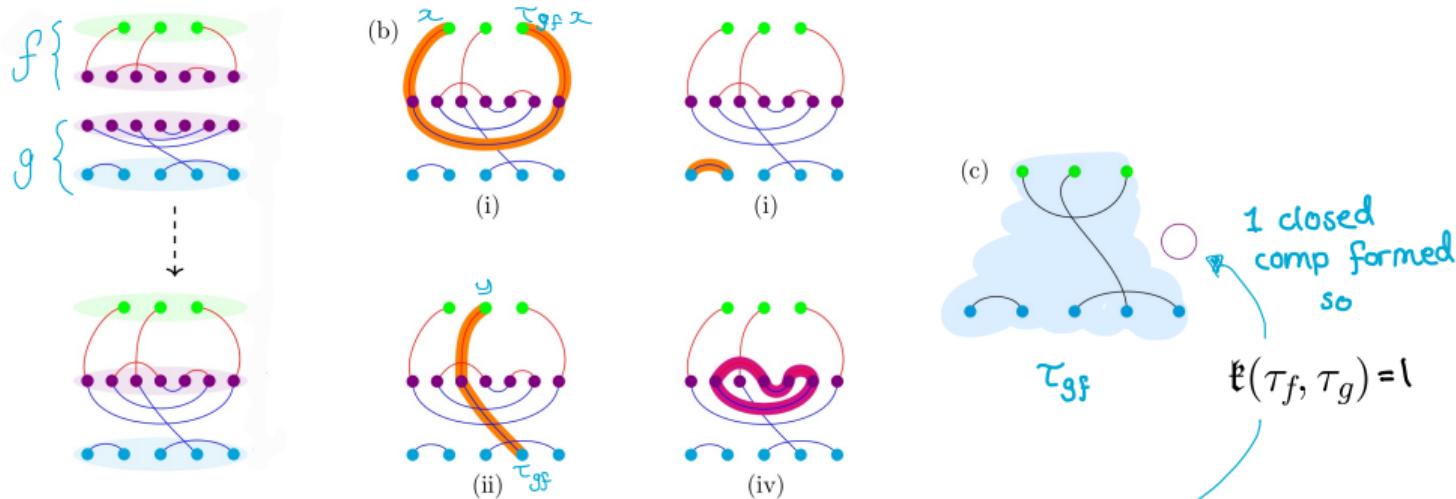


Horizontal (monoidal) composition: $(\tau_1, \mathfrak{k}_1) \oplus (\tau_2, \mathfrak{k}_2) = (\tau_1 \sqcup \tau_2, \mathfrak{k}_1 + \mathfrak{k}_2)$



Composition in BD

Vertical (categorical) composition: $f = (\tau_f, \mathfrak{k}^f) \in \text{BD}(k, m)$, $g = (\tau_g, \mathfrak{k}^g) \in \text{BD}(m, n)$



$$gf = (\tau_{gf}, \mathfrak{k}^{gf}) \in \text{BD}(k, n), \text{ where } \mathfrak{k}^{gf} = \mathfrak{k}^g + \mathfrak{k}^f + \mathfrak{k}(\tau_f, \tau_g).$$

BD - duality

pair $(*, \circ)$ of the
The unique matching $*$ on $\mathbf{1} \sqcup \mathbf{1} \cong \mathbf{2}$ describes morphisms
and $o \in \mathbb{N}$

$$\begin{array}{l|l} | = id : 1 \rightarrow 1 & , \quad \cup : 2 \rightarrow o , \quad \cap : o \rightarrow 2 . \end{array}$$

Together with , these generate BD under \oplus and \circ . (Lehrer-Zheng '15)

$$\begin{array}{c} \cup \\ = \\ \cap \end{array}$$

So BD is a **wheeled PROP**.

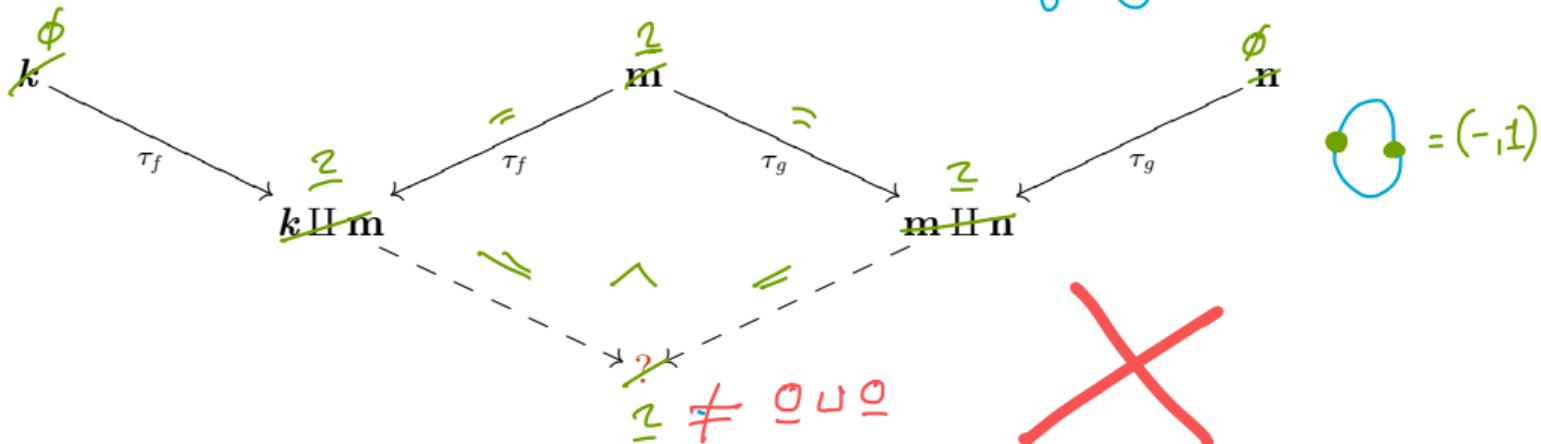
Pushouts of cospans

Brauer diagrams are described by **cospans**

$$m \xrightarrow{\tau|_m} m \amalg n \xleftarrow{\tau|_n} n.$$

But composition *in BD* is not by pushouts of cospans.

e.g. $f = \cap : 0 \rightarrow 2$
 $g = \cup : 2 \rightarrow 0$



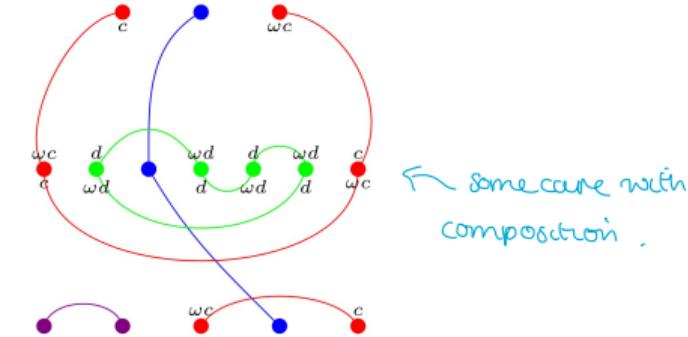
Coloured Brauer diagrams

We can colour morphisms by involutive sets (\mathfrak{C}, ω) :

$$\begin{array}{ccc}
 m \amalg n & \xrightarrow{\overset{\circ}{\lambda}} & \mathfrak{C} \\
 \tau \cong \swarrow \quad \downarrow \cong \omega & & \\
 m \amalg n & \xrightarrow{\overset{\circ}{\lambda}} & \mathfrak{C} \\
 \downarrow & \swarrow & \downarrow \\
 f = (\tau, k) & \xrightarrow{\pi_0(f)} & \widetilde{\mathfrak{C}}
 \end{array}$$

orbits of ω in \mathfrak{C}

$k + \# \text{orb}(i) =$



Example: $\mathfrak{C} = \{+, -\}$, $\omega : + \leftrightarrow -$ describes oriented Brauer diagrams.

With a little care, we get category $\text{BD}^{(\mathfrak{C}, \omega)}$ of (\mathfrak{C}, ω) -coloured Brauer diagrams.

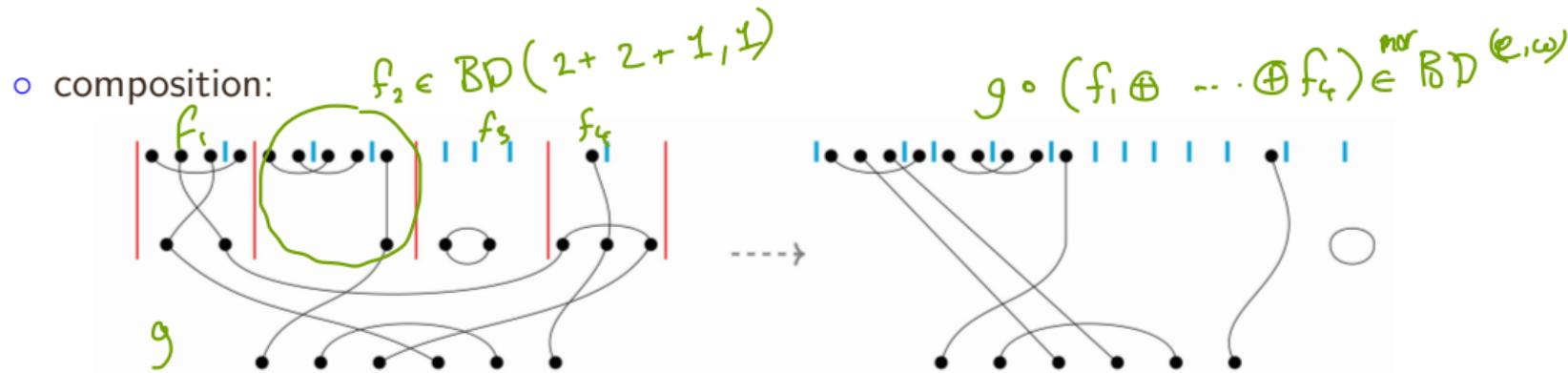
Operads of wiring diagrams

(list \mathcal{C})-coloured

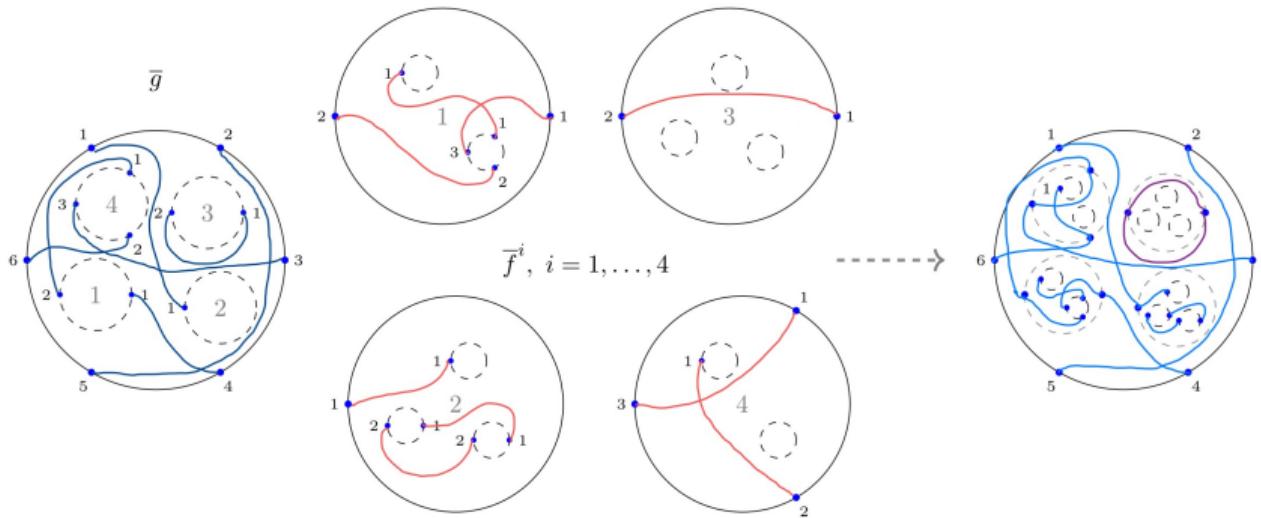
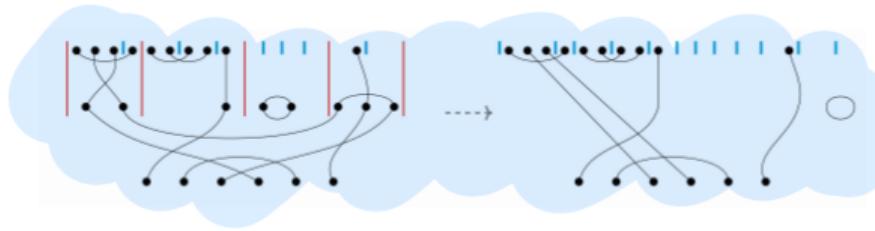
Operad $WD^{(\mathcal{C}, \omega)}$ of (\mathcal{C}, ω) -coloured wiring diagrams is the operad underlying $BD^{(\mathcal{C}, \omega)}$:

- for all lists of lists $(c_1, \dots, c_k; d)$, where $\underline{c}_i = (c_{i1}, \dots, c_{im})$ $c_j \in \mathcal{C}_{1 \leq j \leq m}$,

$$WD^{(\mathcal{C}, \omega)}(c_1, \dots, c_k; d) = BD^{(\mathcal{C}, \omega)}(c_1 \oplus \dots \oplus c_k, d),$$



... but circuit algebras?



Circuit algebras

A (\mathfrak{C}, ω) -coloured circuit algebra (in a symmetric monoidal category (V, \otimes, I)) is an algebra for $WD^{(\mathfrak{C}, \omega)}$:

- a coloured collection $\mathcal{A} = (\mathcal{A}(c))_{c \in \text{list}\mathfrak{C}}$ in V ,
- for all lists of lists $(c_1, \dots, c_k; d)$, and all $(f, \lambda) \in BD^{(\mathfrak{C}, \omega)}(c_1 \oplus \dots \oplus c_k; d)$,

$$\begin{aligned} & \mathcal{A}(f, \lambda): \mathcal{A}(c_1) \otimes \mathcal{A}(c_k) \rightarrow \mathcal{A}(d) \\ + \text{axioms.} & V(\overset{\nwarrow}{\mathcal{A}(c_1) \otimes \dots \otimes \mathcal{A}(c_k)}, \mathcal{A}(d)) \end{aligned}$$

We'll assume
that $V = \text{Sets}$

(\mathfrak{C}, ω) -coloured circuit algebras in (V, \otimes, I) are equivalent to lax symmetric monoidal functors $BD^{(\mathfrak{C}, \omega)} \rightarrow (V, \otimes, I)$.

Some properties of circuit algebras

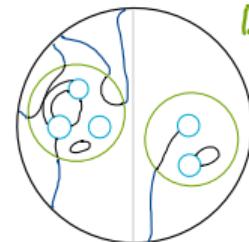
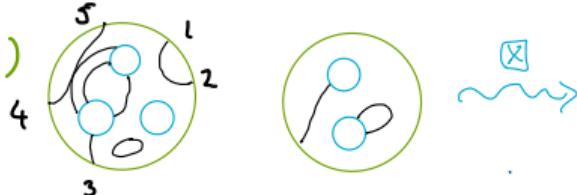
$\mathcal{A} = (\mathcal{A}(c))_{c \in \text{listC}}$ is a circuit algebra. Then,

NOTE: In fact this sketch is more complicated than necessary.

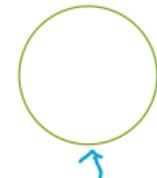
It would have been sufficient to draw the purple diagrams highlighted below

- $(\mathcal{A}(c))_c$ is a graded monoid with product \boxtimes

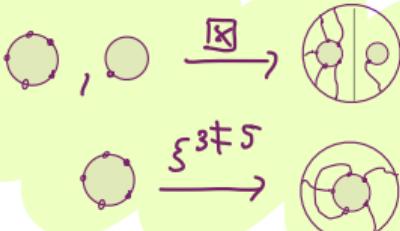
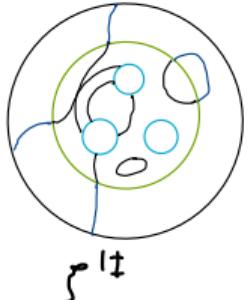
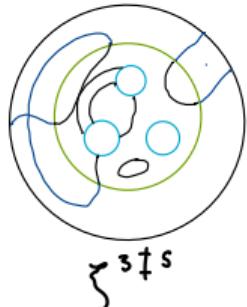
for $c, d \in \text{listC}$,
 $\underline{id}_{cd} \in BD(c, \omega)(cd, cd)$
 \underline{id}_{cd} describes element of
 $WD^{(c, \omega)}(cd, cd)$



$$\boxtimes_{cd} = \lambda(\underline{id}_{cd}): \mathcal{A}(c) \otimes \mathcal{A}(d) \rightarrow \mathcal{A}(cd)$$

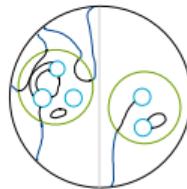


- \mathcal{A} has a contraction operation ζ induced by $U \in BD(2, 0)$ (and permutations)

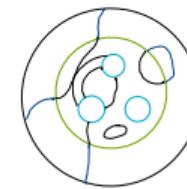


Some properties of circuit algebras

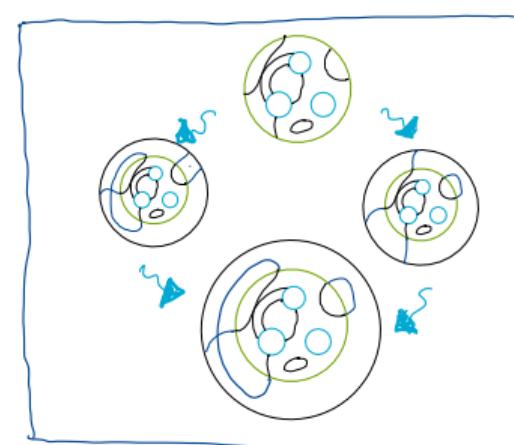
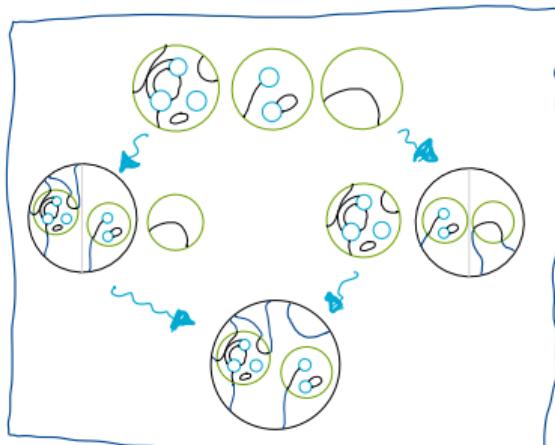
$\mathcal{A} = (\mathcal{A}(c))_{c \in \text{list}\mathfrak{C}}$ is a circuit algebra.



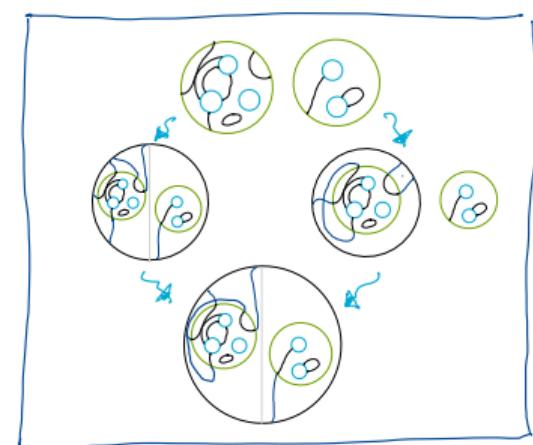
(c1) \otimes is associative



(c2) ζ commutes



(c3) \otimes and ζ commute



Downward Brauer diagrams

The category $\text{BD}_\downarrow \subset \text{BD}$ of downward Brauer diagrams is generated, under \oplus and \circ , by



morphisms given
by matchings alone

Algebras \mathcal{D} for underlying operad also satisfy (c1)-(c3) and are called ‘non-unital’ circuit algebras.

- pushouts of cospans describe composition in BD_\downarrow ,
- Kock has described a monad for non-unital circuit algebras. (Kock, ‘18)
- symmetric strict monoidal functors $\text{BD}_\downarrow \rightarrow \text{Vect}_{\mathbb{k}}$ are equivalent to representations of $O(\infty)$. (Sam-Snowden, ‘15)

\mathbb{k} has char. 0

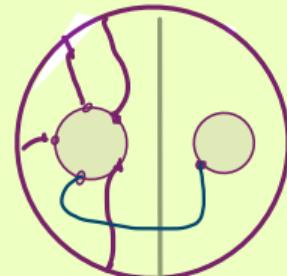
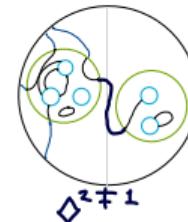
Modular operads

A gain, diagram presented in
the talk should have been
simpler

A circuit algebra $\mathcal{A} = (\mathcal{A}(c))_{c \in \text{list}\mathfrak{C}}$ in (V, \otimes, I) has a **multiplication** \diamond

$$\text{eg } \mathcal{L} = \Sigma + \mathfrak{S}$$

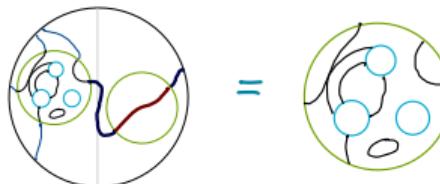
$$\mathcal{A}(m) \otimes \mathcal{A}(n) \rightarrow \mathcal{A}(m+n-2)$$



such that $(\mathcal{A}, \diamond, \zeta)$ is a **non-unital modular operad**.

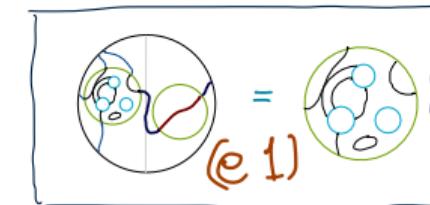
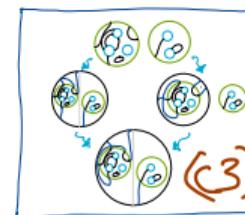
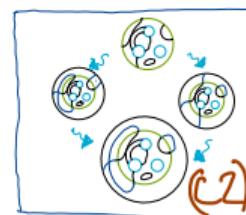
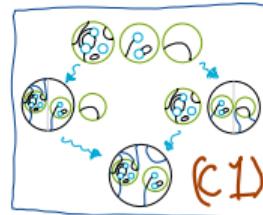
The multiplication \diamond has a 2-sided unit induced by $\cap \in BD(0, 2)$:

(e1) for all $c \in \mathfrak{C}$, the V -morphism $\epsilon_c \stackrel{\text{def}}{=} \mathcal{A}(\cap_c): I \rightarrow (c, \omega c)$ satisfies



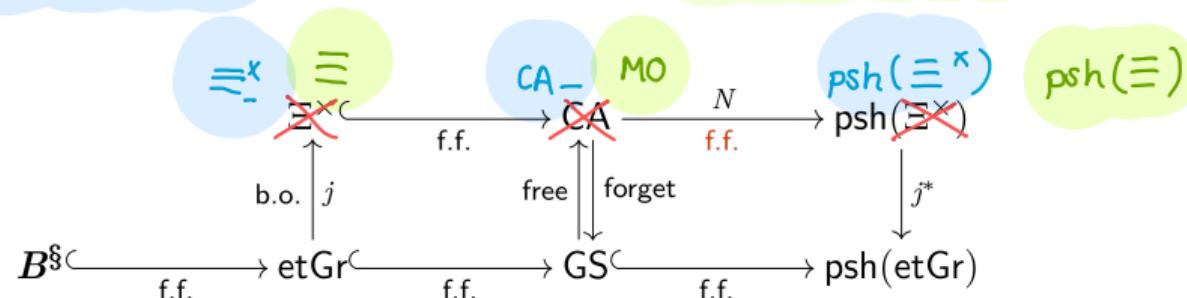
Taking stock: what we know so far

Circuit algebras are coloured collections $(\mathcal{A}(c))_{c \in \mathfrak{C}}$ with \boxtimes , ζ , and ϵ such that



\boxtimes , ζ , (c1)-(c3):
non-unital circuit algebras.
(Kock, 18), monad \mathbb{T}^\times on GS.

ζ , $\diamond = \zeta \boxtimes$, ϵ , (m1)-(m4), (e1): modular
operads.
(R, 18), monad $\mathbb{D}\mathbb{T}$ on GS.



Graphical species (Joyal-Kock, '11)

Describe (\mathfrak{C}, ω) -coloured collections $(S_c)_c$ as presheaves on a category B^\S :

- groupoid B of **finite sets and bijections** is full subcategory,
- distinguished object \S with $B^\S(\S, \S) = \{id, \tau\}$, $\tau^2 = id$,
- For each X , and $x \in X$, morphisms $ch_x, ch_x \circ \tau : \S \rightarrow X$ (ch_x 'chooses' x).



GS is category of presheaves or **graphical species** $S: B^\S \text{op} \rightarrow \text{Set}$.

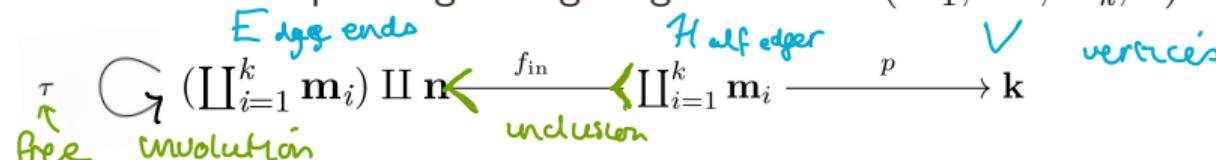
$$S_\S = e \qquad S(\tau) = \omega$$

Graphs

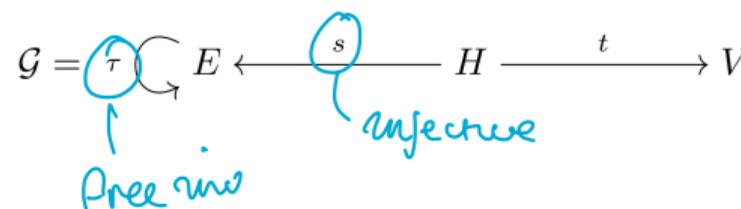
Let $(\tau, 0) \in \text{BD}(m_1 + \cdots + m_k, n)$:

$$\coprod_{i=1}^k \mathbf{m}_i \xrightarrow{\tau} (\coprod_{i=1}^k \mathbf{m}_i) \amalg \mathbf{n} \xleftarrow{\tau} \mathbf{n}.$$

So we can rewrite the corresponding wiring diagram in $WD(\mathbf{m}_1, \dots, \mathbf{m}_k; \mathbf{n})$:



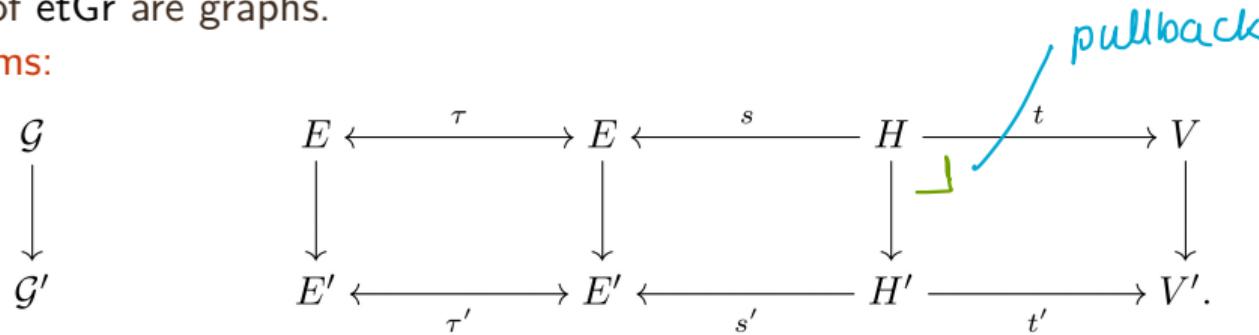
(Joyal-Kock, '11.) A diagram of finite sets of this form is a **graph**.



Graphs and graphical species (Joyal-Kock,'11)

Objects of etGr are graphs.

Morphisms:



$$\mathbf{B}^{\S} \hookrightarrow \text{etGr}$$

$$\xi \mapsto | \quad \text{given by } \{\!\{1, 2\}\!\} \leftarrow \emptyset \rightarrow \emptyset$$

$$X \mapsto \text{graph} \quad \text{given by } X \cup X \xleftarrow{\text{loop}} X \xrightarrow{*}$$

$$\text{etGr} \hookrightarrow \text{GS}$$

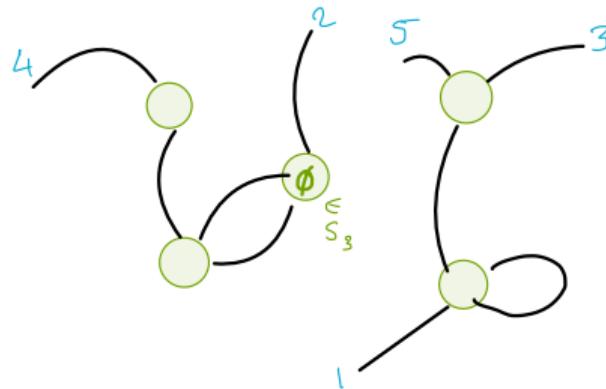
$$g \mapsto \text{etGr}(-, g).$$

Kock's monad \mathbb{T}^\times for non-unital circuit algebras

S is a graphical species. Build a species $T^\times S$ of formal combinations of elements of S :

$$T^\times S(\S) = S_\S = (\mathfrak{C}, \omega).$$

Take equivalence classes of graphs \mathcal{G} , with $\rho: \partial\mathcal{G} \xrightarrow{\cong} X$, decorated by S :



$T^\times S_X$ is colimit over graph isomorphisms that fix
 $X \cong \partial\mathcal{G}$.

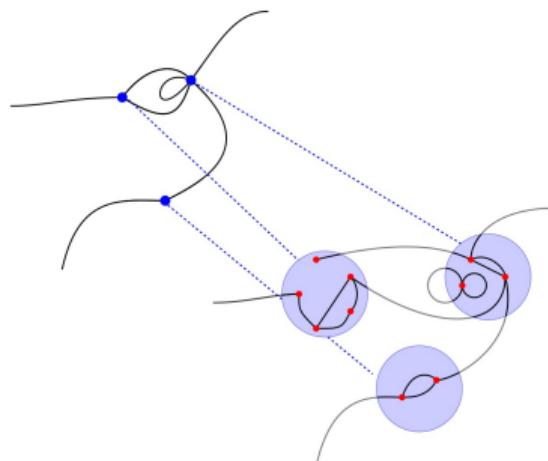
Monad \mathbb{T}^\times for (non-unital) circuit algebras (Kock, '18)

Monadic unit:

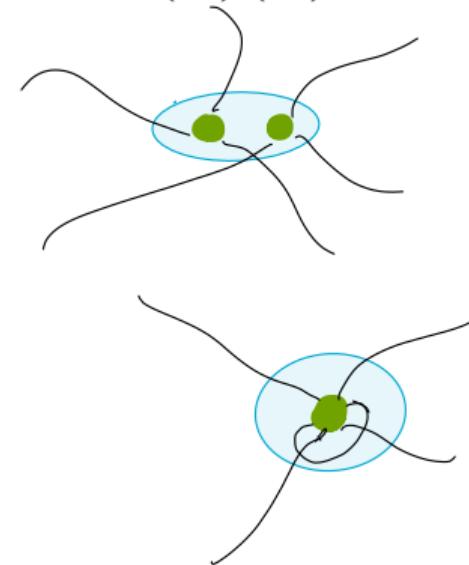
$$\mathcal{S}, \ni \emptyset \longmapsto$$



Monadic multiplication:

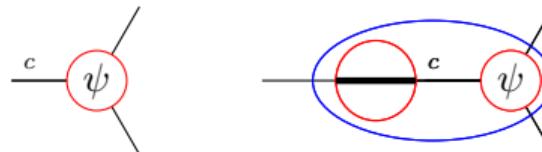


Algebras satisfy (c1)-(c3).



A monad for circuit algebras?

What about ϵ ?



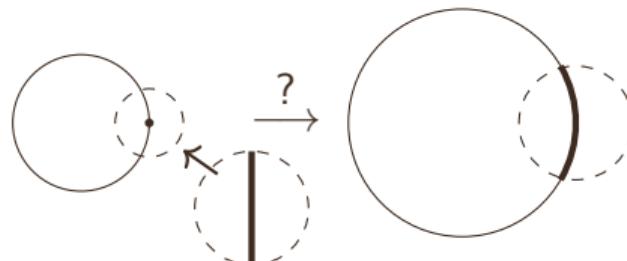
Recall the pesky pushout!



In fact we get

$$\begin{array}{c|ccccc} a & \xrightarrow{\quad} & a & \xrightarrow{\text{colimit}} & a \\ b & \xrightarrow{\text{id}} & b & \xrightarrow{\quad} & b \end{array}$$

But...



But we still need to take a colimit .

$$\begin{array}{c|ccccc} a & \xrightarrow{\quad} & a \\ b & \xrightarrow{\text{id}} & b & \xrightarrow{\quad} & b \end{array}$$

The monad for modular operads (R, '18, '21)

For modular operads, the problem can be solved by constructing the composite monad $\mathbb{D}\mathbb{T}$ on GS , where

- \mathbb{T} is the **monad for contraction and multiplication** defined exactly as \mathbb{T}^\times but restricted to connected graphs,
- $\mathbb{D} = (D, \mu^{\mathbb{D}}, \eta^{\mathbb{D}})$ is the **monad that governs the multiplicative units** ϵ .

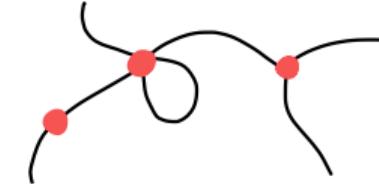
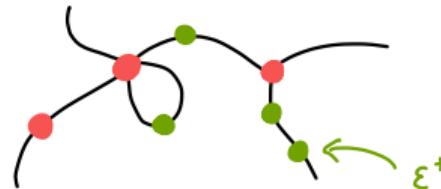
$D : \text{GS} \rightarrow \text{GS}$ **adjoins** elements

- ϵ_c^+ to S_2 , for all $c \in \mathfrak{C}$,
- $o_{\tilde{c}}^+$ to S_0 , for each orbit \tilde{c} of ω in \mathfrak{C} .

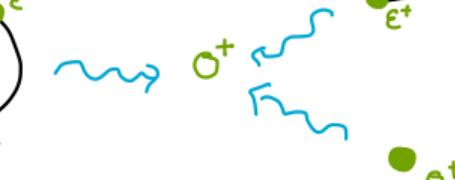
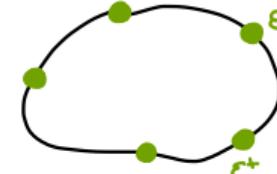
Combining the monads: strategy I

Distributive law for modular operads: $\lambda_{DT} : TD \Rightarrow DT$

If \mathcal{G} has vertices decorated by S , delete any vertices decorated by ϵ^+ :



Otherwise



Strategy I: Try to modify λ_{DT} for T^\times and \mathbb{D} .

But

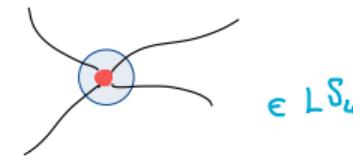


A solution: \mathbb{T}^\times is also a composite!

$$\mathbb{T}^\times = \mathbb{L}\mathbb{T}$$

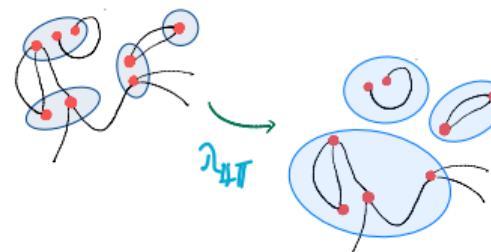
non-unital mod op-

where $\mathbb{L} = (L, \mu^{\mathbb{L}}, \eta^{\mathbb{L}})$ is the free graded monoid monad on GS.



Distributive law

$\lambda_{\mathbb{LT}}$: $TL \Rightarrow LT$ takes a connected graph whose vertices are decorated by tuples of elements of S and returns a tuple of connected graphs decorated by S .



The monad LDT for circuit algebras.

By (Cheng, '11), composite LDT exists if there is a distributive law $\lambda_{\text{LD}}: DB \Rightarrow BD$, and the Yang-Baxter equations hold:

$$\begin{array}{ccccc}
 & & DTL & \xrightarrow{D\lambda_{TL}} & DLT \\
 TDL & \nearrow \lambda_{TD} L & & & \searrow \lambda_{DL} T \\
 & & TLD & \xrightarrow{\lambda_{TL} D} & LTD \\
 & \searrow T\lambda_{DL} & & & \nearrow L\lambda_{TD}
 \end{array}$$

Proposition (R, 2021)

There is, and they do!



The monad LDT for circuit algebras.

Proposition (R, '21)

Algebras for LDT satisfy (c1)-(c3) and (e1).

proof follows easily from what we already know.

Theorem (R, '21)

The category CA of Set valued circuit algebras is equivalent to the Eilenberg-Moore category of algebras for LDT .

Need to check that wiring diagrams define suitable morphisms.

- part coming from matching τ uses graph descr. of wiring diagrams.
- part coming from closed components uses dist. law for $D \otimes T$.

The nerve

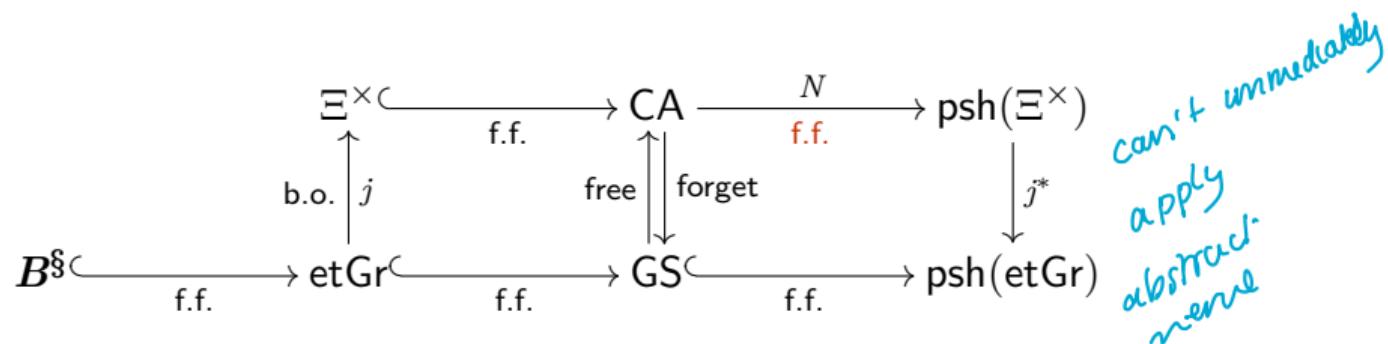
Theorem (R, '21)

There is a full, dense subcategory Ξ^\times of CA whose objects are **graphs**.

The essential image of the induced fully faithful nerve $N: \text{CA} \rightarrow \text{psh}(\Xi^\times)$ is characterised by **Segal presheaves**.

Proof sketch:

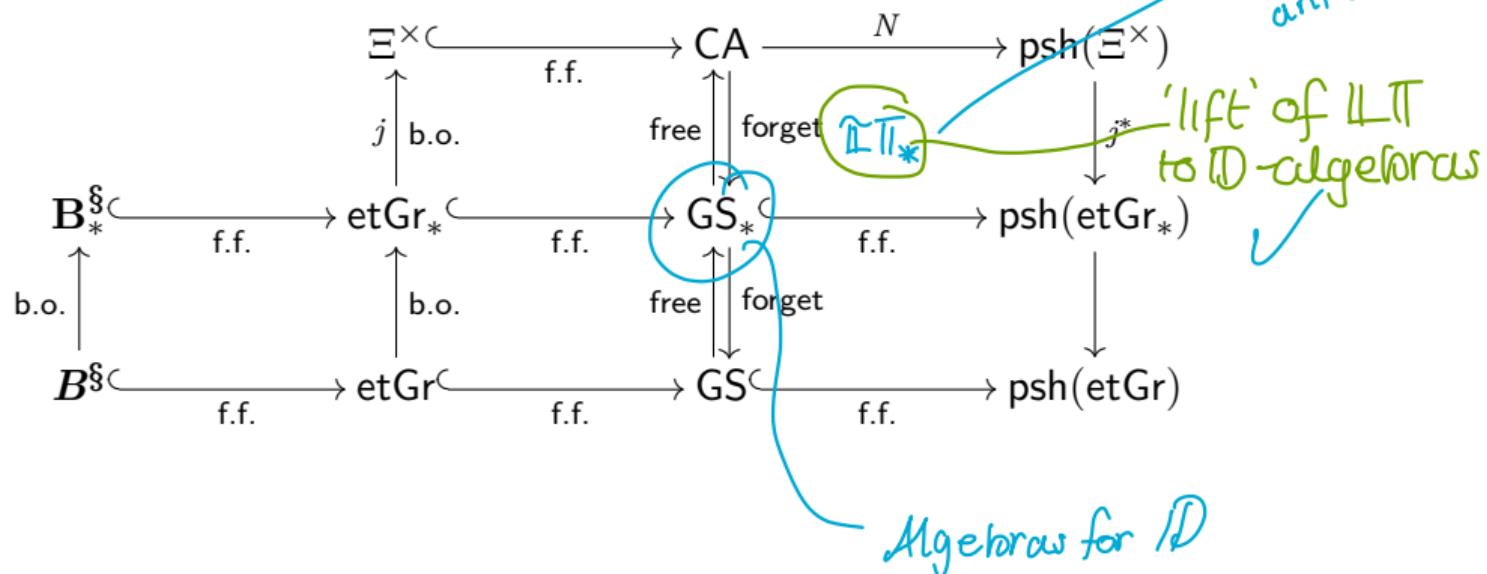
We want



But \mathbb{LDT} does not '*have arities*'.

The nerve

Use the distributive law and abstract nerve theory (Berger-Mellies-Weber, '12)



Comments and further work

- Another example of distributive laws working well in combination with abstract nerve machinery.
- Models of **weak modular operads** in (Hackney-Robertson-Yau, '20) and (R,'21) don't generalise to circuit algebras. However, there is a nice model (current work, together with Robertson).
- Finite dimensional representations of $O(\infty)$ and $GL(\infty)$ admit description as unital circuit algebras. Together with R. Street, we are working to understand these relationships more fully, and for general circuit algebras.
- Weakening the axioms e.g. braiding.

Thank
you!