ECE492/592 – Operating Systems Design: Project #4

Due date: Design document by November 3; implementation by November 14, 2020

Note: You can perform this assignment in teams of two.

Objectives

- To understand Xinu's memory management.
- To understand how to implement a virtual memory system. This includes understanding in details interactions between the hardware and the software.
- To understand how interrupt handling works (including the related hardware and software interactions).

Overview

The goal of this project is to implement virtual memory management and demand paging in Xinu. Refer to the chapters of the Intel Manual (available in Moodle) related to Paging and Interrupt Handling. In particular, as you read the Intel Manual, focus on the following: (i) registers used to support virtual memory and paging, (ii) PDE and PTE format, (iii) interrupt handling support.

Specifically, you will find the information you need in the **Intel Architecture Software Developer's Manual Volume 3: System Programming Guide**:

- Memory management:
 - Memory management registers Section 2.4
 - o Invalidating TLBs Section 2.6.4
 - o Paging and Virtual Memory Section 3.6
 - o Translation Lookaside Buffers Section 3.7
- Interrupt handling: Chapter 5

In addition, see attached slides **xinu_memory_mng.pdf** for an overview of Xinu's memory management.

Please read carefully the whole project description below before starting.

System call implementation

Your solution must implement the system calls listed below.

```
1. pid32 vcreate (void *funcaddr, uint32 ssize, pri16 priority, char *name,
    uint32 nargs, ...)
```

This system call creates a "user" process with a virtual heap. The process's heap must be private and exist in the process's own virtual memory space. Your implementation should use paging and a **4KB page size**. All processes can still use the getmem system call to allocate *shared* heap space.

<u>Note</u>: While "system" processes created with Xinu's <u>create</u> function should not have a private virtual heap, enabling virtual memory and paging might require you to modify the <u>create</u> function as well.

2. char* vmalloc (uint32 nbytes)

This function allocates the desired amount of memory (in bytes) off a process's virtual heap space, and returns SYSERR if the allocation fails.

ECE492 students: Virtual heap space should be allocated using the <u>next-fit policy</u> from lower to higher addresses. For simplicity, you can assume that the memory allocations performed by a process never exceed the size of the virtual address space.

ECE592 students: Virtual heap space should be allocated using the <u>first-fit policy</u> from lower to higher addresses. Note that this can cause external fragmentation of the virtual space.

```
3. syscall vfree (char* ptr, uint32 nbytes)
```

This function frees heap space (previously allocated with vmalloc). vfree returns OK in case of success, and SYSERR in case of failure.

ECE492 students: For simplicity, your implementation can assume that *vfree* is always invoked correctly (i.e., on pages that had been allocated previously using a *vmalloc*).

ECE592 students: Your implementation should handle also the case where *vfree* is invoked incorrectly (i.e., on pages that had not been allocated before). In case of failure, none of the pages involved should be freed.

Note: *vfree* releases only heap space, and not page tables. Page tables are released only when a process is terminated.

Additional requirements

1. Debugging

For debugging purposes, provide the following utility functions (which will be used in the test cases):

- uint32 free ffs pages() number of free frames in the FFS space (see below)
- uint32 allocated_virtual_pages (pid32 pid) number of virtual pages allocated by a particular process (including pages allocated but not mapped to physical memory space)
- uint32 used ffs frames (pid32 pid) number of FFS frames in use by a given process

2. Handling of segmentation faults

In case of segmentation fault your code should:

- Print the process triggering the fault as follows:

```
P<pid>:: SEGMENTATION_FAULT
```

- Kill the faulting process and continue execution

Your code does not need to handle protection faults.

3. Memory initialization

The original Xinu memory initialization does not allow the whole 4GB address space to be available for paging. Replace the original meminit.c and i386.c files in the system folder with the ones attached, which fix the problem.

Important hint: After paging is enabled, all memory accesses are through virtual addresses. Therefore, you need to map the static segments (TEXT, DATA, etc.) into the virtual memory space of each process.

4. Free Frame Space (FFS)

The FFS is the physical memory space where processes map their virtual heap space. FFS must be released when no longer required (in other words, heap frames should be released upon heap deallocation). The total amount of FSS frames available is determined by macro MAX_FSS_SIZE defined in paging.h. MAX_FSS_SIZE is not a per-process limitation – it indicates the total amount of FSS available to all processes. When a user process maps a FFS frame to a virtual page, that FFS frame should not be visible to other user processes (i.e., user processes can map to their virtual address space only the FFS frames they use).

5. Page directory and Page Tables

A total of MAX PT SIZE frames can be reserved for page directory and page tables.

ECE492 students: Physical memory used by 2nd-level page tables must be released when a process terminates.

ECE592 students: Physical memory used by page directory and 2nd-level page tables must be released when a process terminates. The area of memory where page directories and page tables are stored cannot be accessed by user processes (i.e., this area must not be mapped to user processes' virtual address space).

6. Heap allocation

You must use a **lazy allocation policy** for heap allocation. That is, the physical space should be reserved not at allocation time, but when the virtual page is first accessed. Accesses to pages that have not been previously allocated must cause a segmentation fault.

7. Constant definitions in paging.h

Your code should work independent of the constant definitions in paging. h with the following exceptions:

- The page size won't be modified (you can assume a 4KB page size).
- MAX PT SIZE and MAX FFS SIZE will always be set to a multiple of 1024 frames.

Simplifying assumptions

1. You can assume that the operating system performs allocations and triggers faults at a page granularity.

For example, assuming 8-byte pages, the following allocation operations:

```
char *ptr1 = vmalloc(16);
char *ptr2 = vmalloc(15);
```

will be treated the same way. In other words, the OS will not keep track of the number of bytes within a page that have been effectively allocated. In addition, the OS won't trigger a segmentation fault if the code accesses ptr2[15] even if the allocation was just for 15 bytes.

2. Similarly, you can assume that allocation operations **are aligned to the beginning of a page**. For example, you can assume that the following allocations:

```
char *ptr1 = vmalloc(16);
char *ptr2 = vmalloc(15);
```

will go to two different pages.

Hints

- 1. Paging should be enabled at the end of system initialization. A good place to enable it is the end of the sysinit() function in initialize.c
- 2. x86 PDE and PTE have bits that are reserved for software use. You can use those bits as you please in your implementation.
- 3. Interrupt handling Study the code in clkinit.c, clkdisp.S and clkhandler.c to find out how to install an interrupt service routine (ISR) in Xinu. Implement the page fault handler in the following two files: pagefault_handler_disp.S and pagefault_handler.c. As indicated in the Intel Manual, a page fault triggers interrupt 14. Thus, you need to install your page fault handler to interrupt 14. Intel processors automatically push an error code on the stack when an ISR is called, so you need to pop out the error code off the stack within your Page Fault Handler.
- 4. For convenience, we have added a control reg.c and paging.h that you can extend.
- 5. Use the same page table for all system processes.
- 6. Even if Xinu does not have a clear difference between user mode and system mode, you should aim at a design that conceptually distinguished "user mode" and "kernel mode". To this end, assume that, when a user process issues a system call, that call is executed in kernel mode. On the other hand, the rest of the user process's code (that is, the code within the function executed by the process) is executed in user mode.

7. Test your code "incrementally".

- A. Start by testing the code with paging enabled but without invoking the three system calls above. This will allow verifying that the page directories and page tables have been setup correctly. If this is not the case, the system will hang on startup. Add a function to dump the content of page directories and page tables in a format that is easy for you to read and interpret.
- B. When (A) works, test user process creation and termination. Start with a single process.
- C. Add calls to vmalloc and vfree from one process.
- D. Add heap accesses (to trigger lazy allocation).
- E. Add more processes, so to test context switch between user processes.

Submissions instructions

- 1. **Design Document:** You need to submit a document covering the various aspects of your design **by November 3**. We attach a draft indicating the aspects that your document should cover. Submit your design document in Moodle by November 3.
- 2. **Report:** Your report should be brief, and:
 - describe any changes in design over your original design document (no need to document the aspects you had already discussed in your design document);
 - briefly explain what works and what not in your implementation.
- 3. <u>Test cases:</u> For this project, you do **not** need to submit your test cases. We will test your implementations using different test cases (i.e., different main.c files). Therefore, do not implement any essential functionality (other than your test cases) in the main.c file. Also, turn off debugging output before submitting your code.
- 4. Go to the xinu/compile directory and invoke make clean.
- 5. As for previous project, create a xinu/tmp folder and **copy** all the files you have modified/created into it (the tmp folder should have the same directory structure as the xinu folder).
- 6. Go to the parent folder of the xinu folder. Compress the whole xinu directory into a tgz file.

```
tar czf xinu project4.tgz xinu
```

7. Submit report and code through Moodle by November 14.