|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  | Inputs | | | Output |
| Cases | Clock | LoadAdd | Reset | CMAdd |
| A |  | 0/1 | 1 | All 0’s |
| B | ↑ | 0 | 0 | CMAdd + 1 |
| C | ↑ | 1 | 0 | Address |

In the CAR, the LoadAdd signal controls whether the output is loaded from the address signal or incremented from the previous output. This is done by the inputs of the JK Flip Flops being connected to a series of AND gates with each bit of the output as inputs.

When the Reset signal = 1, all bits are reset to 0.