### Phase Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Min Green	5	20	5	12	5	20	5	12	0	0	0	0	0	0	0	0
Veh Ext	2.0	6.0	2.0	2.0	2.0	6.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max Green 1	20	40	15	30	15	40	15	30	0	0	0	15	40	0	0	0
Max Green 2	70	70	70	70	70	70	70	70	0	0	0	0	0	0	0	0
Max Green 3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Yellow	3.2	3.6	3.2	3.6	3.2	3.6	3.2	3.6	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Red Clr	1.0	1.5	1.5	1.0	1.0	1.5	1.5	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Adv Flash	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Bike MG	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Walk	0	7	0	7	0	7	0	7	0	0	0	0	0	0	0	0
Ped Clr	0	28	0	20	0	28	0	20	0	0	0	0	0	0	0	0
Walk2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Early Wlk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Delay Wlk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max Initial	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Gap	2.0	2.5	2.0	2.0	2.0	2.5	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Reduce After	5	10	5	10	5	10	5	10	0	0	0	0	0	0	0	0
TTReduce	5	5	5	5	5	5	5	5	0	0	0	0	0	0	0	0
CS Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CS Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red Revert	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Neg Ped	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
AP Disc	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Pmt Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Pmt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Pmt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Return Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

### Phase Options

Phases				1-	-8					9-	16		
Min Recalls		2		4		6		8					
Max Recalls			3	4			7	8					П
Ped Recalls													П
Soft Recall													П
Dual Entry		2		4		6		8					П
Red Rest													П
Walk Rest													П
Walk Expand													П
Ped Recycle		2		4		6		8					П
Sim Ped Term													
PC Thru Clr													П
Guar Passage													П
No Simult Gap													П
Yel Lock													
Red Lock	1		3		5		7						П
PhaseNext Lock		2		4		6		8					
No Term Call	1	2	3	4	5	6	7	8					
Cond Serv													П
CS Enable													П
Cond Reserve													П
Reserve													П
Veh Omit													П
Ped Omit													П
Perm Phase													
Protect Calls													
Protect Calls 2													
Flash Entry													
Flash Exit													П
Flash Exit Yel													
Flash Exit Red													П
Ped Scramble													П
No Min Yel													
No Min Red Rev													
Max Scramble Walk													П
Flash Yellow													П
Flash FYA								П					П
CNA 1								П					П
CNA 2													П
	_								•				

#### **Phase Startup Options**

2/15/2019 8:21:49 AM

Startup Flash	0					M	lod	е		•	Ye	->	Re	d
Startup All Red	5					Υe	olle	W	C	0.0				
Phases			1-8						9-	16				
Startup Phases	2			6										
Startup Yellow	2			6										
Startup Red														
Startup No Walk														
Startup Next		3			7									
Startup Yel Fls														
Startup FYA														
No Veh Call														
No Ped Call														

#### **Phase Startup Timing**

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Start Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Start Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Start Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Unit

Red Revert 5.0 Ped Protect Yes AdvFls in Flash No

#### Ring Sequence / Conflicting Phases

2/15/2019 8:21:49 AM

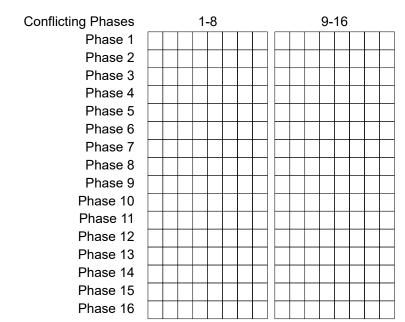
Ringgroup 1

Ring 1 Ring 2 

Ringgroup 2

#### **Custom Sequences**

ucrices																	
Seq 1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	



FYA/FRA

FYA	4	2	2	Λ	E	e	7	o								
Prot Phs	0	0	3 0	0	5 0	6	7	8								
Opp Thru	0	0	0	0	0	0	0	0								
Start Phs	0	0	0	0	0	0	0	0								
Opp Ped	0	0	0	0	0	0	0	0								
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0								
Min FYA	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0								
Skip Prot Red	Disabled	Disabled	Disabled		Disabled	Disabled		Disabled								
Head Mode	FYA 1	FYA 1	FYA 1	FYA 1	FYA 1	FYA 1	FYA 1	FYA 1								
				Pe	ed Hawk	1										
Veh Phase	0															
Ped Phase	Ped Hawk 1  0 0 0 0.0 Dark Signal Yes 0.0 Flash Carryover 0.0  Normal															
Flash Yel	0.0	Dai	rk Signal	Ye	S											
Flash Delay	0.0	Flash C	arryover	0.0												
Green Mode	Norn	nal														
				Pe	d Hawk	2										
Veh Phase					a nawk	_										
Ped Phase		0 0 0.0 Dark Signal Yes 0.0 Flash Carryover 0.0														
		Day	ulc Ciava al	Va												
Flash Yel			-		S											
Flash Delay	0.0		arryover	0.0												
Green Mode	Norn	nal														
				Pe	ed Hawk	3										
Veh Phase	0															
Ped Phase	0															
Flash Yel	0.0	Dai	rk Signal	Ye	s											
Flash Delay	0.0	Flash C	arryover	0.0												
Green Mode	Norn	nal														
				Б-		4										
				PE	ed Hawk	4										
Veh Phase	0															
Ped Phase	0															
Flash Yel	0.0	Dai	rk Signal	Ye	S											
Flash Delay	0.0	Flash C	arryover	0.0												
Green Mode	Norn	nal														
	•															

### Coordination Options

Sync Time	00:00	RTC Set Time	00:00
Transition Mode	Best 2	Ped Adjust	None
Trans Short %	20	Trans Long %	35
Offset Reference	Lead FO	Short Cycles	0
Dual Entry	Normal	Overlap F/O	Disabled
Master Sync Mode	RTC	Master Sync Length	0
Adapt Thresh	0	Adapt Step	0
External Plan Max	0		
Hardwire No Match	Sched	Hardwire Sync Fail	0
Override Omit/Recall	No		
Phases	1-8	9-16	
No Trans Recall			
Trans Ped Recall			
Trans Phases			

#### Hardwire Plans

Plan 1 0 0 Hardwire	9
Plan 2 0 0 Hardwire	9
Plan 3 0 0 Hardwire	Э
Plan 4 0 0 Hardwire	)
Plan 5 0 0 Hardwire	9
Plan 6 0 Hardwire	9
Plan 7 0 0 Hardwire	9
Plan 8 0 0 Hardwire	9
Plan 9 0 0 Hardwire	9
Plan 10 0 Hardwire	9
Plan 11 0 0 Hardwire	)
Plan 12 0 0 Hardwire	)
Plan 13 0 0 Hardwire	)
Plan 14 0 0 Hardwire	)
Plan 15 0 0 Hardwire	)
Plan 16 0 0 Hardwire	9
Plan 17 0 0 Hardwire	9
Plan 18 0 0 Hardwire	9
Plan 19 0 0 Hardwire	9
Plan 20 0 Hardwire	)
Plan 21 0 0 Hardwire	)
Plan 22 0 Hardwire	)
Plan 23 0 Hardwire	9
Plan 24 0 0 Hardwire	9
Plan 25 0 Hardwire	9
Plan 26 0 Hardwire	Э
Plan 27 0 0 Hardwire	9
Plan 28 0 Hardwire	9
Plan 29 0 Hardwire	9
Plan 30 0 Hardwire	9
Plan 31 0 0 Hardwire	9
Plan 32 0 0 Hardwire	9

Soft Interconnect

Mode		Sla	ve		Rer	no	te	Int	Νı	um	be	er	0
Yield Delay	0												
Yield Duration	0												
Permissive	0												
Local Hold Limit	0												
Phases			1-8					9-	16				
Local Control Phases													
Local Hold Phases													
Local Perm Phases													
Local Call Phases													
Remote Perm Phases													
Remote Hold Phases													

#### Coordination Pattern 1

Cycle	120	Ring	group	1 - C	Offset 1	17	7	Offse	t 2	0	Offs	et 3	0						
		Ring	group	2 - C	Offset 1	0		Offse	t 2	0	Offs	et 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	25	41	20	34	20	46	20	34	0	0	0	0	0	0	0	0	7		
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Perm Min Green	8	12	8	12	8	12	8	12	0	0	0	0	0	0	0	0	1		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	]		
Permissive Mode	S	ing Ba	and		Max	Mode	Э	Ma	ax 2		W	alk R	est		Yield				
Ped Permissive		Yield	t																
Permissive Limit	15	]		 	Perm 2	2 Star	t C	)			Perr	m 2 E	nd	0					
Alt Sequence 2	4	<u>,</u>			TOD	) Link	0	7											
Phases/Overlaps		1-	-8			9-16	3		Tı	rans N	/lode		Defa	ult					
Coord Phases	2		6							Offse	t Ref		Defa	ult	=				
No Extend													Disab		=				
Float Enable									Auap	otive N	loue	L	Jisab	ieu					
Veh = Ped Perm										Diag	ahla Du	.i _ wi#. /			1 1				
Walk Rest											able Pr	•						 	 
Ped Recall									Pro	gress	ion Ph	ases							
Cond Ped Call										Pric	ority Alt	t Seq							
Olap Ped Recall										Rese	erve Ex	ktend		ÌÌ					
Ped Recycle																			
Min Recall																			
Max Recall		4		8															
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit																			
FYA Omit				$\perp$		$\perp \perp$		$\perp$											
Adapt Phases			Ш																
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	_		
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Recovery Min Green	0	0	I 0	0	0	0	0	I 0 _	0	0	l n T	0	0	0	0	0			

Coordination Pattern 1

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

#### Coordination Pattern 2

Cycle	120	Ring	group	1-0	ffset 1	41		Offse	t 2	0	Offs	et 3	0								
		Ring	group	2 - 0	ffset 1	0		Offse	t 2	0	Offs	et 3	0								
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16					
Splits	16	50	20	34	16	50	20	34	0	0	0	0	0	0	0	0	]				
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				
Perm Min Green	8	12	8	12	8	12	8	12	0	0	0	0	0	0	0	0	1				
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				
Permissive Mode	S	ing Ba	and		Max	Mode	•	Ма	x 2		W	/alk R	est	,	Yield						
Ped Permissive		Yield	t						Perm 2 End 0												
Permissive Limit	15			F	Perm :	2 Start	t C	)	Perm 2 End 0												
Alt Sequence 2	4				TOD	Link	0														
															_						
Phases/Overlaps		1-				9-16	<u> </u>		Tı	rans M	/lode		Defa	ult							
Coord Phases	2		6							Offset	t Ref		Defa	ult							
No Extend									Adar	otive N	/lode		Disab	led	=						
Float Enable																					
Veh = Ped Perm										Disa	ble P	riority									
Walk Rest	$\vdash$							$\sqcup$	Pro	gressi		•		++					$\top$	$\neg$	$\neg$
Ped Recall Cond Ped Call									1 10	_				++-				Ш	Ш		
											rity Al										
Olap Ped Recall Ped Recycle	$\vdash$							+		Rese	rve E	xtend									
Min Recall																					
Max Recall		4		8																	
Cond Serv																					
Reservice								+													
Veh Omit																					
Ped Omit																					
Olap Omit								+													
Perm Reserve								+													
Perm 1 Phases				7				+													
Max Inhibit				-				$\Box$													
FYA Omit	-			++				$\forall$													
Adapt Phases			$\dashv$	+			++	+													
ority Timing-Phase	1	2	3	·	5	6	7	8	9	10	11	12	13	14	15	16					
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				
covery Min Green	0	0	0	0	0	0	n	0	0	0	n	0	0	0	0	0	1				

#### Coordination Pattern 2

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

#### Coordination Pattern 3

Cycle	120	Ring	group	1 - O	ffset 1	4	1	Offse	t 2	0	Offs	et 3 [	0						
		Ring	group	2 <b>-</b> O	offset 1	0		Offse	t 2	0	Offs	et 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	25	41	20	34	20	46	20	34	0	0	0	0	0	0	0	0			
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Perm Min Green	8	12	8	12	8	12	8	12	0	0	0	0	0	0	0	0			
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Permissive Mode	S	ing Ba	and		Max	Mode	Э	Ma	ax 2		W	alk Re	est		Yield				
Ped Permissive		Yield	ł																
Permissive Limit	15			-	Perm :	2 Star	t (	0			Perr	m 2 E	nd	0					
Alt Sequence 2	4				TOD	) Link	0												
Phases/Overlaps		1-	8			9-16	6		T	rans N	/lode		Defau	ult					
Coord Phases	2		6							Offse			Defau		$\exists$				
No Extend								+							=				
Float Enable								+	Adap	otive N	/lode	L	)isabl	ed					
Veh = Ped Perm																			
Walk Rest										Disa	able Pr	iority							
Ped Recall									Pro	gress	ion Ph	ases							
Cond Ped Call										Pric	rity Alt	t Seq	Πİ		Ì				
Olap Ped Recall											rve Ex								
Ped Recycle										11030	1 VC L/	(lenu							
Min Recall																			
Max Recall		4		8															
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit																			
FYA Omit																			
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Recovery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			

#### Coordination Pattern 3

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		•			•											•

#### Coordination Pattern 11

Cycle	90	Ring	group	1 - 0	ffset 1	I 31	1	Offse	t 2	0	Offs	et 3 [	0						
		Ring	group	2 - 0	offset 1	0		Offse	t 2	0	Offs	et 3 [	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	13	32	15	30	13	32	17	28	0	0	0	0	0	0	0	0	ı		
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
Perm Min Green	8	20	8	15	8	20	8	15	0	0	0	0	0	0	0	0	ı		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
		•		•	•		•	•	1	-					•				
Permissive Mode	Si	ing Ba	and		Max	Mode	Э	Ma	ax 2		W	alk Re	est		Yield				
Ped Permissive		Yield	d																
Permissive Limit	0			F	Perm :	2 Star	t (	)			Peri	m 2 E	nd	0					
Alt Sequence					TOE	) Link	0												
Phases/Overlaps		1-	8			9-16	6		T	rans N	/lode		Defau	ult					
Coord Phases	2		6							Offse	t Ref	I	ead l	=O	=				
No Extend															=				
Float Enable									Adap	otive N	/loae	L	Disabl	ea					
Veh = Ped Perm										Б.	5								
Walk Rest										Disa	able Pr	riority							
Ped Recall									Pro	gress	ion Ph	ases							
Cond Ped Call										Pric	rity Alt	t Seq							
Olap Ped Recall											rve Ex	-							
Ped Recycle										11030	,1 VC L/	\toriu							
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit				$\top$		$\top$													
FYA Omit				$\top$															
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
Recovery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		

Coordination Pattern 11

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		•	•	•	•		•	•			•	•	•	•		•

#### Coordination Pattern 12

Cycle	110	Ring	group	1-0	ffset 1	I 11		Offse	t 2	0	Offs	et 3	0						
		Ring	group	2 - 0	ffset 1	0		Offse	t 2	0	Offs	et 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	13	54	13	30	17	50	15	28	0	0	0	0	0	0	0	0			
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Perm Min Green	8	20	8	15	8	20	12	15	0	0	0	0	0	0	0	0	1		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Permissive Mode	S	ing Ba	and		Max	Mode		Ма	x 2		W	alk R	est	,	Yield				
Ped Permissive		Yield	ł																
Permissive Limit	0			F	Perm 2	2 Start	: C	)			Per	m 2 E	nd	0					
Alt Sequence					TOD	) Link	0	7					_						
Phases/Overlaps		1-	8			9-16	i		Ti	rans N	/lode		Defa	ult					
Coord Phases	2		6							Offset	t Ref		_ead l	FO					
No Extend										otive N			Disab		_				
Float Enable									Aua	JUVE IV	loue		Jisab	icu					
Veh = Ped Perm										Dica	ıble P	riority							
Walk Rest									_					44		$\perp$			
Ped Recall									Pro	gress	ion Pr	nases							$\perp \perp$
Cond Ped Call										Pric	rity Al	t Seq							
Olap Ped Recall										Rese	rve E	xtend							
Ped Recycle																			
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit				$\perp \!\!\! \perp \!\!\! \mid$		$\perp \perp$													
FYA Omit			$\perp \downarrow \downarrow$	Ш		$\perp \perp$		Ш											
Adapt Phases																			
ority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	-		
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
covery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	<u> </u>	0	0			

Coordination Pattern 12

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

#### Coordination Pattern 13

Cycle	120	Ring	group	1 - 0	ffset 1	41		Offse	t 2	0	Offs	et 3	0						
		Ring	group	2 - 0	ffset 1	0		Offse	t 2	0	Offs	et 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	22	53	15	30	13	62	15	30	0	0	0	0	0	0	0	0			
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Perm Min Green	8	20	8	15	8	20	8	15	0	0	0	0	0	0	0	0	1		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Permissive Mode	S	ing Ba	and		Max	Mode		Ма	x 2		W	alk R	est	,	Yield				
Ped Permissive		Yield	t																
Permissive Limit	0	]		F	Perm 2	2 Start	: (	)			Per	m 2 E	nd	0					
Alt Sequence					TOD	) Link	0												
<u> </u>																			
Phases/Overlaps		1-	.8			9-16	;		T	rans N	/lode		Defa	ult					
Coord Phases	2		6							Offset	t Ref		_ead I	FO	Ħ				
No Extend										otive N			Disabl	ed	=				
Float Enable									/ way	ouve iv	louc		Jioabi	-					
Veh = Ped Perm										Disa	ıble P	riority		1 1					
Walk Rest									D					++		$\blacksquare$		 	 _
Ped Recall									Pro	gress			Щ	<u> </u>				Ш	_
Cond Ped Call						$\perp$				Pric	rity Al	t Seq							
Olap Ped Recall										Rese	rve E	xtend							
Ped Recycle																			
Min Recall																			
Max Recall						$\perp$													
Cond Serv				+		+													
Reservice				+															
Veh Omit Ped Omit				++		++													
Olap Omit				+		++													
Perm Reserve				+															
Perm 1 Phases				7															
Max Inhibit				-															
FYA Omit			+	++		++		+											
Adapt Phases			$\dashv$	+			+	+											
	1							 	0	10	44	40	40	4.4	1 E	16			
ority Timing-Phase Priority Min Green	0	2	3	4	5	6	7	8	9	10	11	12 0	13	14	15	16	1		
covery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-		

Coordination Pattern 13

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

#### Coordination Pattern 14

Cycle	120	Ring	group	1 - 0	ffset 1	I 4		Offse	t 2	0	Offs	et 3 [	0						
		Ring	group	2 - 0	offset 1	0		Offse	t 2	0	Offs	et 3 [	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	22	53	15	30	13	62	15	30	0	0	0	0	0	0	0	0	ı		
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
Perm Min Green	8	20	8	15	8	20	8	15	0	0	0	0	0	0	0	0	ı		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
			•	•	•	•	•	•	•	•					•	•			
Permissive Mode	Si	ng Ba	and		Max	Mode	e	Ma	ax 2		W	alk Re	est		Yield				
Ped Permissive		Yield																	
Permissive Limit	0			F	Perm :	2 Star	t (	)			Peri	m 2 E	nd	0					
Alt Sequence					TOE	) Link	0												
Phases/Overlaps		1-	8			9-16	3		Ti	rans N	/lode		Defau	ult					
Coord Phases	2		6							Offse	t Ref	I	ead l	<del>-</del> 0	=				
No Extend													Disabl		=				
Float Enable									Aua	otive N	loue	L	Jisabi	eu					
Veh = Ped Perm										D:	.LL. D.	.::4							
Walk Rest											able Pr	•	Щ					 	
Ped Recall									Pro	gress	ion Ph	ases							
Cond Ped Call										Pric	rity Alt	t Seq							
Olap Ped Recall										Rese	rve Ex	rtend							
Ped Recycle											,, , o _,								
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit																			
FYA Omit																			
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		
Recovery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı		

Coordination Pattern 14

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

#### Coordination Pattern 15

Cycle	120	Ring	group	1 - O	ffset 1	1 41	1	Offse	t 2 🗍	0	Offs	et 3	0	]					
•		Ring	group	2 - 0	ffset ′	0		Offse	t 2	0	Offs	et 3	0	]					
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	16	50	20	34	16	50	20	34	0	0	0	0	0	0	0	0	7		
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7		
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7		
Perm Min Green	8	20	8	15	8	20	8	15	0	0	0	0	0	0	0	0	7		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7		
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7		
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7		
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7		
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Permissive Mode	S	ing Ba	and		Max	Mode	Э	Ма	x 2		W	alk R	est		Yield				
Ped Permissive		Yield	d																
Permissive Limit	0			ı	Perm	2 Star	t C	)			Perr	m 2 E	ind	0					
Alt Sequence 2	4				TOE	) Link	0												
Phases/Overlaps		1-	-8			9-16	3		Ti	rans N	/lode		Defa	ult					
Coord Phases	2		6							Offse	t Ref		ead	FΩ					
No Extend																			
Float Enable									Adap	otive N	loge	L	Disab	iea					
Veh = Ped Perm										ς.							1		
Walk Rest										Disa	able Pr	iority							
Ped Recall									Pro	gress	ion Ph	ases							
Cond Ped Call										Pric	rity Alt	t Seq							
Olap Ped Recall										Rese	rve Ex	rtend					, 		
Ped Recycle										11000	,, vo L	(toria					I		
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit																			
FYA Omit																			
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7		
Pocovory Min Groon	Λ	Λ	Λ	Λ	Λ	Λ	Λ	Λ	Λ	Λ	^	Λ	Λ	Λ	Λ	Λ	-		

Coordination Pattern 15

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

#### Coordination Pattern 16

Cycle	120	Ring	group	1 - O	ffset 1	I 4		Offse	t 2	0	Offs	et 3	0	]					
		Ring	group	2 - 0	ffset 1	1 0		Offse	t 2	0	Offs	et 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	20	46	20	34	20	46	20	34	0	0	0	0	0	0	0	0			
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Perm Min Green	8	20	8	15	8	20	8	15	0	0	0	0	0	0	0	0			
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
													_				_		
Permissive Mode	S	ing Ba			Max	Mode	9	Ма	x 2		W	alk R	est		Yield				
Ped Permissive		Yield	<u> </u>																
Permissive Limit	0			I	Perm	2 Star	t C	)			Perr	n 2 E	ind	0					
Alt Sequence 2	4				TOE	) Link	0												
Phases/Overlaps		1-	.8			9-10	3		Tı	rans N	/lode		Defa	ult					
Coord Phases	2		6							Offse	t Ref		_ead	FΩ					
No Extend													Disab						
Float Enable									Aua	otive N	loue	L	Jisab	ieu					
Veh = Ped Perm										D:	.l. D.						1		
Walk Rest											ible Pr	•	Щ				]	 	 
Ped Recall									Pro	gress	ion Ph	ases							
Cond Ped Call										Pric	rity Alf	t Seq							
Olap Ped Recall										Rese	rve Ex	rtend					]		
Ped Recycle												(torra					]		
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit																			
FYA Omit																			
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Pacayany Min Groon	Λ	Λ	0	Λ	0	Λ	Λ	0	0	Λ	0	0	^	Λ	Λ	Λ	1		

Coordination Pattern 16

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		•	•	•	•		•	•			•	•	•	•		•

#### Coordination Pattern 17

Cycle	100	Ring	group	1 - O	ffset '	1 10	)	Offse	t 2 [	0	Offs	et 3	0						
		Ring	group	2 - 0	ffset '	1 0		Offse	t 2	0	Offs	et 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	20	34	17	29	18	36	17	29	0	0	0	0	0	0	0	0	7		
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Perm Min Green	8	20	8	15	8	20	8	15	0	0	0	0	0	0	0	0	7		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	7		
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
																	_		
Permissive Mode	S	ing Ba			Max	Mode	e	Ма	x 2		W	alk R	est		Yield				
Ped Permissive		Yield	t										_						
Permissive Limit	0			ı	Perm	2 Star	t (	)			Perr	n 2 E	nd	0					
Alt Sequence					TOE	) Link	0												
Phases/Overlaps		1-	-8			9-16	6		Tı	rans N	/lode		Defa	ult					
Coord Phases	2		6							Offse	t Ref	ı	_ead	FΩ	=				
No Extend													Disab		=				
Float Enable									Auap	otive N	loue	L	Jisab	ieu					
Veh = Ped Perm										Diag	hia De	.i _ mi4. <i>r</i>			1 1	1	1		
Walk Rest											able Pr	•	Щ	44			]	 	 
Ped Recall									Pro	gress	ion Ph	ases							
Cond Ped Call										Pric	rity Alt	Seq							
Olap Ped Recall										Rese	rve Ex	tend					]		
Ped Recycle																	J		
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit																			
FYA Omit				Щ															
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	_		
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Pacayony Min Groon	Λ	Λ	Λ	Λ	Λ	Λ	Λ	Λ.	Λ	Λ	0	^	Λ	Λ	Λ	Λ	1		

Coordination Pattern 17

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

#### Coordination Pattern 18

Cycle	120	Ring	group	1 - 0	offset 1	I 4	1	Offse	t 2	0	Offs	et 3	0						
		Ring	group	2 - 0	offset 1	0		Offse	t 2	0	Offs	et 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	25	41	20	34	20	46	20	34	0	0	0	0	0	0	0	0			
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Perm Min Green	8	20	8	15	8	20	8	15	0	0	0	0	0	0	0	0			
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Permissive Mode	Si	ing Ba	and		Max	Mode	е 🗀	Ма	x 2		W	alk Re	est [		Yield				
Ped Permissive		Yield																	
Permissive Limit	0			 	Perm :	2 Star	t 🗆	)			Peri	n 2 E	nd	0					
Alt Sequence 2	4				TOE	) Link	0												
Phases/Overlaps		1-	8			9-10	3		Ti	rans N	/lode		Defau	ult					
Coord Phases	2		6							Offset			_ead I						
No Extend																			
Float Enable									Adap	otive N	loae	L	Disabl	ea					
Veh = Ped Perm										ъ.		,							
Walk Rest										Disa	ıble Pr	iority					 		
Ped Recall									Pro	gress	ion Ph	ases					$\perp$		
Cond Ped Call										Pric	rity Alt	t Seq							
Olap Ped Recall										Rese	rve Ex	ctend			i i				
Ped Recycle																			
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit																			
FYA Omit			$\perp \downarrow \downarrow$			$\perp \perp$	$\perp \perp$												
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Recovery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			

Coordination Pattern 18

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

#### Coordination Pattern 19

Cycle	120	Ring	group	1 - 0	ffset 1	41		Offse	t 2	0	Offs	et 3	0						
		Ring	group	2 - 0	ffset 1	0		Offse	t 2	0	Offs	set 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	25	41	20	34	20	46	20	34	0	0	0	0	0	0	0	0	]		
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Perm Min Green	8	20	8	15	8	20	8	15	0	0	0	0	0	0	0	0	1		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Permissive Mode	S	ing Ba	and		Max	Mode		Ма	x 2		W	alk R	est	,	Yield				
Ped Permissive		Yield	t																
Permissive Limit	0	]		ŀ	Perm 2	2 Start	: C	)			Per	m 2 E	nd	0					
Alt Sequence 2	4				TOD	) Link	0												
															_				
Phases/Overlaps		1-				9-16	i		Tı	rans N	/lode		Defa	ult					
Coord Phases	2		6							Offset	t Ref	l	_ead I	FO					
No Extend									Adar	otive N	/lode		Disabl	ed					
Float Enable				Ш					, ,,,,,,										
Veh = Ped Perm				$\perp \!\!\!\perp \!\!\! \perp \!\!\! \mid$						Disa	hle P	riority		$\Box$					
Walk Rest									Dua			•		++					
Ped Recall									Pio	gress				$\perp \perp$		$\perp$			$\perp \perp \perp$
Cond Ped Call										Pric	rity Al	t Seq							
Olap Ped Recall										Rese	rve E	xtend							
Ped Recycle																			
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve				_															
Perm 1 Phases				7															
Max Inhibit			+					$\perp$											
FYA Omit			+	4		$\perp \perp$		$\perp$											
Adapt Phases																			
ority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	1		
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-		
covery Min Green	0	l 0	1 ()	0	1 0	1 0 1	()	1 0	l 0	0	ı ()	l 0	l 0	ı ()	( )	Ω	1		

Coordination Pattern 19

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

		Preempt 3	(Configuration)	2/15/2019 8:21:4	9 AM
Enabled	Yes	Dwell Mode	Normal	Output Mode All	
Output2 Mode	All	Fail Action	Preempt Off	Exit Mode Normal	
Override Flash	No	Change Phasenext	Yes		
Enable Phases Preempt Inputs	1-8	9-16	LRV Disable LRV Dwell Flash LRV Omit LRV No Ye	Delay	0
		Preempt 3 (Timir	ng/Phases/Overlaps)		
Phases/Overland	1-8	9-16			
Phases/Overlaps Omit Olap Grn Clr	1-0	9-10	Start Green	0 Start Walk	0
Phs EWIk to Grn					
TClr 1 Veh Phases				Start Ped Clr	0
TClr 1 Ped Phases			l Track Clear 1	0 Track Clear 2	0
TClr 1 Olap			Track Clear i	0 Hack Clear 2	<u> </u>
TClr 1 Olap Ped			TC1 Extend	TC1 Max	0
TClr 2 Veh Phases			F " P 10		
TClr 2 Ped Phases			Exit Ped Cli	0 Exit Yellow 0	.0
TClr 2 Olap			Exit Red	0.0	
TClr 2 Olap Ped					
Init Dwell Phases			Min Dwel	Min Duration	0
<b>Dwell Veh Phases</b>	2 5		Dwell Extend		
<b>Dwell Ped Phases</b>			Dwell Exterio	2	
Dwell Olap			Max Dwel	Max Call	85
Dwell Olap Ped					
Exit Veh Phases			Reserve Inh Same	0	
Exit Ped Phases			Reserve Inh Al	0	
Exit Olap			Neselve IIIII Al	0	
Exit Olap Ped			Delay	0	
Zero Phase Walk					
Zero Phase Ped Clr			Phases/Overlaps	1-8 9-16	
Zero Phase Green			TClr 1 FR Olap		
Zero Olap Walk			TClr 2 FR Olap		
Zero Olap Ped Clr			Dwell FR Olap		
Zero Olap Green			TClr 1 FYA		
Dwell-Phase Red			TCIr 2 FYA		
Dwell-Phase Red Flash			Dwell FYA		
Dwell-Phase Yel Flash					
Dwell-Olap Red Flash					
Dwell-Olap Yel Flash					
Dwell-Ped Dark					
Dwell-Olap Ped Dark					

		Preempt 4	(Configuration)	2/15/2019 8:21:49 AM
Enabled	Yes	Dwell Mode	Normal	Output Mode All
Output2 Mode	All	Fail Action	Preempt Off	Exit Mode Normal
Override Flash	No	Change Phasenext	Yes	
Enable Phases Preempt Inputs	1-8	9-16	LRV Disable LRV Dwell Flash LRV Omit	1-8 Max 0 Delay 0
			LRV No Yel	
		Preempt 4 (Timir	ng/Phases/Overlaps)	
Phases/Overlaps	1-8	9-16		
Omit Olap Grn Clr			Start Green	0 Start Walk 0
Phs EWlk to Grn			_	Chart Dad Ola O
TClr 1 Veh Phases			-	Start Ped Clr 0
TClr 1 Ped Phases			Track Clear 1	0 Track Clear 2 0
TClr 1 Olap				
TClr 1 Olap Ped			TC1 Extend	0 TC1 Max 0
TClr 2 Veh Phases			Exit Ped Clr	0 Exit Yellow 0.0
TClr 2 Ped Phases			LXILT Ed Oil	C Exit reliew 0.0
TClr 2 Olap			Exit Red	0.0
TClr 2 Olap Ped				
Init Dwell Phases			Min Dwell	10 Min Duration 0
Dwell Veh Phases	1 6		Dwell Extend	2
Dwell Ped Phases			DWCII EXIONG	
Dwell Olap			Max Dwell	0 Max Call 85
Dwell Olap Ped				
Exit Veh Phases			Reserve Inh Same	0
Exit Ped Phases			Reserve Inh All	0
Exit Olap			TROSCIVO IIIITA	
Exit Olap Ped			Delay	0
Zero Phase Walk			Diameter (Occupies	1.0
Zero Phase Ped Clr			Phases/Overlaps	1-8 9-16
Zero Phase Green			TClr 1 FR Olap	
Zero Olap Walk			TClr 2 FR Olap	
Zero Olap Ped Clr			Dwell FR Olap	
Zero Olap Green			TCIr 1 FYA	
Dwell-Phase Red			TClr 2 FYA	
Dwell-Phase Red Flash			Dwell FYA	
Dwell-Phase Yel Flash				
Dwell-Olap Red Flash				
Dwell-Olap Yel Flash				
Dwell-Ped Dark				
Dwell-Olap Ped Dark				

		Preempt 5	(Configuration)	2/15/2019 8:21:49 AM
Enabled	Yes	Dwell Mode	Normal	Output Mode All
Output2 Mode	All	Fail Action	Preempt Off	Exit Mode Normal
Override Flash	No	Change Phasenext	Yes	
Enable Phases Preempt Inputs	1-8	9-16	LRV Disable LRV Dwell Flash LRV Omit LRV No Yel	1-8 Max 0  Delay 0
		Preempt 5 (Timir	ng/Phases/Overlaps)	
Phases/Overlaps	1-8	9-16		
Omit Olap Grn Clr			Start Green	0 Start Walk 0
Phs EWIk to Grn				Start Ped Clr 0
TClr 1 Veh Phases				
TClr 1 Ped Phases			Track Clear 1	0 Track Clear 2 0
TClr 1 Olap TClr 1 Olap Ped			TC1 Extend	0 TC1 Max 0
TClr 2 Veh Phases				o rormax o
TClr 2 Ped Phases			Exit Ped Clr	0 Exit Yellow 0.0
TClr 2 Olap			Exit Red	0.0
TClr 2 Olap Ped			Exit Neu	0.0
Init Dwell Phases			Min Dwell	10 Min Duration 0
Dwell Veh Phases	4	7	5	
Dwell Ped Phases			Dwell Extend	2
Dwell Olap			Max Dwell	0 Max Call 85
Dwell Olap Ped				
Exit Veh Phases			Reserve Inh Same	0
Exit Ped Phases			Reserve Inh All	0
Exit Olap			Neseive IIII All	0
Exit Olap Ped			Delay	0
Zero Phase Walk			Discourse (Occordence)	1.0
Zero Phase Ped Clr			Phases/Overlaps	1-8 9-16
Zero Phase Green			TClr 1 FR Olap	
Zero Olap Walk			TCIr 2 FR Olap  Dwell FR Olap	
Zero Olap Ped Clr			TClr 1 FYA	
Zero Olap Green			TCIr 2 FYA	
Dwell-Phase Red Dwell-Phase Red Flash			Dwell FYA	
Dwell-Phase Ked Flash			] DWOILLIA [	
Dwell-Olap Red Flash				
Dwell-Olap Yel Flash				
Dwell-Ped Dark				
Dwell-Olan Ped Dark				

		Preempt 6	(Configuration)	2/15/2019 8:21:49 AM
Enabled	Yes	Dwell Mode	Normal	Output Mode All
Output2 Mode	All	Fail Action	Preempt Off	Exit Mode Normal
Override Flash	No	Change Phasenext	Yes	
Enable Phases Preempt Inputs	1-8	9-16	LRV Disable LRV Dwell Flash LRV Omit LRV No Yel	1-8 Max 0 Delay 0
Preempt 6 (Timing/Phases/Overlaps)				
Phases/Overlaps	1-8	9-16		
Omit Olap Grn Clr			Start Green	0 Start Walk 0
Phs EWlk to Grn				Start Ped Clr 0
TClr 1 Veh Phases				Start Ped Clr 0
TClr 1 Ped Phases			Track Clear 1	0 Track Clear 2 0
TClr 1 Olap				
TClr 1 Olap Ped			TC1 Extend	0 TC1 Max 0
TClr 2 Veh Phases			Exit Ped Clr	0 Exit Yellow 0.0
TClr 2 Ped Phases				
TClr 2 Olap			Exit Red	0.0
TClr 2 Olap Ped			Min Dwell	10 Min Duration 0
Init Dwell Phases			Willi Dwell	10 Willi Duration 0
Dwell Veh Phases	3	8	Dwell Extend	2
Dwell Ped Phases				
Dwell Olan Red			Max Dwell	0 Max Call 85
Dwell Olap Ped Exit Veh Phases			Reserve Inh Same	0
Exit Veri Priases Exit Ped Phases			Treserve iiii saine	
Exit Olap			Reserve Inh All	0
Exit Olap Ped			Dalay	
Zero Phase Walk			Delay	0
Zero Phase Ped Clr			Phases/Overlaps	1-8 9-16
Zero Phase Green			TClr 1 FR Olap	
Zero Olap Walk			TClr 2 FR Olap	
Zero Olap Ped Clr			Dwell FR Olap	
Zero Olap Green			TCIr 1 FYA	
Dwell-Phase Red			TCIr 2 FYA	
Dwell-Phase Red Flash			Dwell FYA	
Dwell-Phase Yel Flash			-	
Dwell-Olap Red Flash				
Dwell-Olap Yel Flash				
Dwell-Ped Dark				
Dwell-Olan Ped Dark			1	

		Preempt 8	(Configuration)	2/15/2019 8:21:49 AM
Enabled	Yes	Dwell Mode	Normal	Output Mode All
Output2 Mode	All	Fail Action	Preempt Off	Exit Mode Normal
Override Flash	No	Change Phasenext	Yes	
Enable Phases Preempt Inputs	1-8	9-16	LRV Disable LRV Dwell Flash LRV Omit LRV No Yel	1-8 Max 0 Delay 0
		Preempt 8 (Timir	ng/Phases/Overlaps)	
Phases/Overlaps	1-8	9-16		
Omit Olap Grn Clr			Start Green	0 Start Walk 0
Phs EWIk to Grn				Start Ped Clr 0
TClr 1 Veh Phases TClr 1 Ped Phases				
TCIr 1 Ped Phases			Track Clear 1	0 Track Clear 2 0
TClr 1 Olap Ped			TC1 Extend	0 TC1 Max 0
TClr 2 Veh Phases			Exit Ped Clr	C Svit Vollow 0.0
TClr 2 Ped Phases			EXIL PEG CII	0 Exit Yellow 0.0
TClr 2 Olap			Exit Red	0.0
TClr 2 Olap Ped			Min Dwall	10 Min Duration 0
Init Dwell Phases			Min Dwell	10 Min Duration 0
Dwell Veh Phases Dwell Ped Phases	4 4	8	Dwell Extend	0
Dwell Ped Fliases  Dwell Olap	4		May Dwall	May Call 0
Dwell Olap Ped			Max Dwell	15 Max Call 0
Exit Veh Phases			Reserve Inh Same	0
Exit Ped Phases			December Inde All	
Exit Olap			Reserve Inh All	0
Exit Olap Ped			Delay	0
Zero Phase Walk			Dhaga (Overland	1.0
Zero Phase Ped Clr			Phases/Overlaps TClr 1 FR Olap	1-8 9-16
Zero Phase Green			TClr 2 FR Olap	
Zero Olap Walk Zero Olap Ped Clr			Dwell FR Olap	
Zero Olap Fed Cli Zero Olap Green			TClr 1 FYA	
Dwell-Phase Red			TCIr 2 FYA	
Dwell-Phase Red Flash			Dwell FYA	
Dwell-Phase Yel Flash				
Dwell-Olap Red Flash				
Dwell-Olap Yel Flash				
Dwell-Ped Dark				
Dwell-Olan Ped Dark			†	

## TOD Pattern Events

2/15/2019 8:21:49 AM

	Time			D	OV	Ν				Н	olic	day	s		Mode	Pattern	Offset
Event 1	00:00	S	М	Т	W	Т	F	S							Free	0	0
Event 2	05:30		М	Т	W	Т	F								Sched	2	0
Event 3	08:30		М	Т	W	Т	F								Sched	1	0
Event 4	11:00		М	Т	W	Т	F								Sched	3	0
Event 5	15:30		М	Т	W	Т	F								Sched	3	0
Event 6	18:30	S	М	Т	W	Т	F	S							Sched	1	0
Event 7	22:00	S	М	Т	W	Т	F	S							Free	0	0
Event 8	08:30	S						S							Sched	1	0
Event 9	00:00														Sched	0	0
Event 10	00:00														Sched	0	0
Event 11	00:00														Sched	0	0
Event 12	00:00														Sched	0	0
Event 13	00:00														Sched	0	0
Event 14	00:00														Sched	0	0
Event 15	00:00														Sched	0	0
Event 16	00:00														Sched	0	0
Event 17	00:00														Sched	0	0
Event 18	00:00														Sched	0	0
Event 19	00:00														Sched	0	0
Event 20	00:00														Sched	0	0
Event 21	00:00														Sched	0	0
Event 22	00:00														Sched	0	0
Event 23	00:00														Sched	0	0
Event 24	00:00														Sched	0	0
Event 25	00:00														Sched	0	0
Event 26	00:00														Sched	0	0
Event 27	00:00														Sched	0	0
Event 28	00:00														Sched	0	0
Event 29	00:00														Sched	0	0
Event 30	00:00														Sched	0	0
Event 31	00:00														Sched	0	0
Event 32	00:00														Sched	0	0

## 332/336 Outputs (Connector C1S)

2/15/2019 8:21:49 AM

	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8	Pin 9
Output	DntWlk	Walk	VehRed	VehYel	VehGrn	VehRed	VehYel	VehGrn
Index	4	4	4	4	4	3	3	3
	Pin 10	Pin 11	Pin 12	Pin 13	Pin 15	Pin 16	Pin 17	Pin 18
Output	DntWlk	Walk	VehRed	VehYel	VehGrn	VehRed	VehYel	VehGrn
Index	2	2	2	2	2	1	1	1
	Pin 19	Pin 20	Pin 21	Pin 22	Pin 23	Pin 24	Pin 25	Pin 26
Output	DntWlk	Walk	VehRed	VehYel	VehGrn	VehRed	VehYel	VehGrn
Index	8	8	8	8	8	7	7	7
	Pin 27	Pin 28	Pin 29	Pin 30	Pin 31	Pin 32	Pin 33	Pin 34
Output	DntWlk	Walk	VehRed	VehYel	VehGrn	VehRed	VehYel	VehGrn
Index	6	6	6	6	6	5	5	5
	Pin 35	Pin 36	Pin 37	Pin 38	Pin 83	Pin 84	Pin 85	Pin 86
Output	PedClr	PedClr	PedClr	PedClr	VehRed	VehRed	LRVRed	LRVYel
Index	0	0	0	0	0	0	2	2
	Pin 87	Pin 88	Pin 89	Pin 90	Pin 91	Pin 93	Pin 94	Pin 95
Output	LRVGrn	LRVRed	LRVYel	LRVGrn	VehRed	VehRed	OlpRed	OlpYel
Index	2	6	6	6	0	0	10	10
	Pin 96	Pin 97	Pin 98	Pin 99	Pin 100	Pin 101	Pin 102	Pin 103
Output	OlpGrn	OlpRed	OlpYel	TrnWrn	VehRed	VehRed	VehRed	VehRed
Index	10	0	9	2	0	0	0	0

## 332/336 Outputs (Connector C11S)

	Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8
Output	VehRed							
Index	0	0	0	0	0	0	0	0

## 332/336 Inputs (Connector C1S)

2/15/2019 8:21:49 AM

	Pin 39	Pin 40	Pin 41	Pin 42	Pin 43	Pin 44	Pin 45	Pin 46
Input	VehDet	VehDet	VehDet	VehDet	VehDet	VehDet	VehDet	VehDet
Index	2	16	8	22	3	17	9	23
Ĺ	Pin 47	Pin 48	Pin 49	Pin 50	Pin 51	Pin 52	Pin 53	Pin 54
Input	VehDet	VehDet	VehDet	VehDet	Preempt	Preempt	GenIn	GenIn
Index	6	20	12	26	1	2	5	4
·	Pin 55	Pin 56	Pin 57	Pin 58	Pin 59	Pin 60	Pin 61	Pin 62
Input	VehDet	VehDet	VehDet	VehDet	GenIn	GenIn	GenIn	GenIn
Index	15	1	21	7	3	7	2	6
	Pin 63	Pin 64	Pin 65	Pin 66	Pin 67	Pin 68	Pin 69	Pin 70
Input	VehDet	VehDet	VehDet	VehDet	PedDet	PedDet	PedDet	PedDet
Index	5	19	11	25	2	6	4	8
L	Pin 71	Pin 72	Pin 73	Pin 74	Pin 75	Pin 76	Pin 77	Pin 78
Input	Preempt	Preempt	Preempt	Preempt	GenIn	VehDet	VehDet	VehDet
Index	3	4	5	6	1	4	18	10
	Pin 79	Pin 80	Pin 81	Pin 82				
Input	VehDet	GenIn	LocFlash	StopTm				
Index	24	8	5	5				

## 332/336 Inputs (Connector C11S)

0

Index

0

0

0

	Pin 10	Pin 11	Pin 12	Pin 13	Pin 15	Pin 16	Pin 17	Pin 18
Input	None							
Index	0	0	0	0	0	0	0	0
L	Pin 19	Pin 20	Pin 21	Pin 22	Pin 23	Pin 24	Pin 25	Pin 26
Input	None							
Index	0	0	0	0	0	0	0	0
L	Pin 27	Pin 28	Pin 29	Pin 30				
Input	None	None	None	None				

## Cabinet / MMU Configuration

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Cabinet Type         332/336         MMU Channel Ignore           MMU Disable         No         Det BIU 1-No Fail Call           Det BIU 2-No Fail Call         Alt LS Flash           Alt LS Flash         Alt Phase Flash           Alt Overlap Flash         Alt LRV Flash           CMU Channel Ignore         1-8         9-16           CMU Channel Ignore         1-8         9-16           Det IASM1-Det Diag         17-24         17-24           Det IASM2-Det Diag         17-24         17-24	_			1-8	9-16
Det BIU 2-No Fail Call  Alt LS Flash Alt Phase Flash Alt Overlap Flash Alt LRV Flash  CMU Channel Ignore  1-8 9-16  CMU Channel Ignore  17-24 25-32  17-24  17-24  17-24  17-24  17-24  17-24  17-24  17-24  17-24	Cabinet Type	332/336	MMU Channel Ignore		
Alt LS Flash Alt Phase Flash Alt Overlap Flash Alt LRV Flash  CMU Channel Ignore  1-8 9-16  CMU Channel Ignore  17-24 25-32  1-8 9-16  Det IASM1-Det Diag  17-24 17-24 17-24 17-24 17-24 17-24 17-24 17-24	MMU Disable	No	Det BIU 1-No Fail Call		
Alt Phase Flash Alt Overlap Flash Alt LRV Flash  CMU Channel Ignore  1-8 9-16  CMU Channel Ignore  17-24 25-32  1-8 9-16  Det IASM1-Det Diag  17-24  17-24  17-24  17-24  17-24			Det BIU 2-No Fail Call		
Alt Overlap Flash Alt LRV Flash  1-8 9-16  CMU Channel Ignore  17-24 25-32  1-8 9-16  Det IASM1-Det Diag  17-24  17-24  17-24  17-24  17-24  17-24			Alt LS Flash		
Alt LRV Flash  1-8 9-16  CMU Channel Ignore  17-24 25-32  1-8 9-16  Det IASM2-Det Diag  1-8 9-16  Det IASM2-Det Diag			Alt Phase Flash		
1-8 9-16  CMU Channel Ignore  17-24 25-32  1-8 9-16  Det IASM1-Det Diag  17-24  17-24  17-24  17-24  17-24  17-24  17-24			Alt Overlap Flash		
17-24 25-32  1-8 9-16  Det IASM1-Det Diag  17-24  17-24  17-24  17-24  17-24  17-24  1-8 9-16  Det IASM2-Det Diag			Alt LRV Flash		
Det IASM2-Det Diag 9-16				17-24	25-32
			Det IASM2-Det Diag	1-8	9-16

### Phase / Overlap Outputs

	Phase	Overlap
1	Normal	Normal
2	Normal	Normal
3	Normal	Normal
4	Normal	Normal
5	Normal	Normal
6	Normal	Normal
7	Normal	Normal
8	Normal	Normal
9	Normal	Normal
10	Normal	Normal
11	Normal	Normal
12	Normal	Normal
13	Normal	Normal
14	Normal	Normal
15	Normal	Normal
16	Normal	Normal

## LRV Outputs

	LRV
1	3 Head
2	3 Head
3	3 Head
4	3 Head
5	3 Head
6	3 Head
7	3 Head
8	3 Head

## I/O Logic Channels

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	Func1	ldx	Oper	Func2	ldx	Out1	ldx	Out2	ldx	Dly	Ext	Trig	Fls
Chan 1	GenIn	1	And(Not2)	GenOut	51	TChkin	2	None	0	0	0	No	No
Chan 2	GenIn	2	And(Not2)	GenOut	52	TChkin	2	None	0	0	0	No	No
Chan 3	GenIn	3	And	GenOut	53	TChkout	2	None	0	0	5	No	No
Chan 4	GenIn	4	And	GenOut	54	TChkout	0	None	0	0	0	No	No
Chan 5	GenIn	5	And(Not2)	GenOut	55	TChkin	0	None	0	0	0	No	No
Chan 6	GenIn	6	And(Not2)	GenOut	56	TChkin	6	None	0	0	0	No	No
Chan 7	GenIn	7	And	GenOut	57	TChkout	6	None	0	0	5	No	No
Chan 8	GenIn	8	And	GenOut	58	TChkout	0	None	0	0	0	No	No
Chan 9	PhsChk	4	Or	PhsChk	8	None	0	None	0	0	0	No	No
Chan 10	Preempt	3	Nor	Preempt	4	None	0	None	0	0	0	No	No
Chan 11	GenIn	1	And	GenOut	51	TChkout	0	None	0	0	0	No	No
Chan 12	GenIn	2	And	GenOut	52	TChkout	6	None	0	0	5	No	No
Chan 13	GenIn	3	And(Not2)	GenOut	53	TChkin	6	None	0	0	0	No	No
Chan 14	GenIn	4	And(Not2)	GenOut	54	TChkin	6	None	0	0	0	No	No
Chan 15	GenIn	5	And	GenOut	55	TChkout	0	None	0	0	0	No	No
Chan 16	GenIn	6	And	GenOut	56	TChkout	2	None	0	0	5	No	No
Chan 17	GenIn	7	And(Not2)	GenOut	57	TChkin	2	None	0	0	0	No	No
Chan 18	GenIn	8	And(Not2)	GenOut	58	TChkin	2	None	0	0	0	No	No
Chan 19	GenOut	9	And	GenOut	10	Preempt	8	None	0	180	0	No	No
Chan 20	PhsNxt	1	Or	PhsNxt	5	LRV Omi	t 2	LRV Omi	t 6	0	0	No	No
Chan 21	PhsNxt	4	Or	PhsNxt	8	LRV Omi	t 2	LRV Omi	t 6	0	0	No	No
Chan 22	PhsNxt	3	Or	PhsNxt	7	LRV Omi	t 2	LRV Omi	t 6	0	0	No	No
Chan 23	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 24	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 25	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 26	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 27	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 28	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 29	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 30	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 31	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 32	None	0	Or	None	0	None	0	None	0	0	0	No	No

## I/O Logic Channels

2/15/2019 8:21:49 AM

	Func1	ldx	Oper	Func2	ldx	Out1	ldx	Out2	ldx	Dly	Ext	Trig	Fls
Chan 33	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 34	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 35	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 36	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 37	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 38	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 39	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 40	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 41	GenIn	1	Or	None	0	None	0	None	0	0	120	No	No
Chan 42	GenIn	2	And(Not2)	GenIn	3	None	0	None	0	0	120	No	No
Chan 43	GenIn	3	And(Not2)	GenIn	2	None	0	None	0	0	120	No	No
Chan 44	GenIn	4	Or	None	0	None	0	None	0	0	120	No	No
Chan 45	GenIn	5	Or	None	0	None	0	None	0	0	120	No	No
Chan 46	GenIn	6	And(Not2)	GenIn	7	None	0	None	0	0	120	No	No
Chan 47	GenIn	7	And(Not2)	GenIn	6	None	0	None	0	0	120	No	No
Chan 48	GenIn	8	Or	None	0	None	0	None	0	0	120	No	No
Chan 49	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 50	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 51	GenOut	42	Or	GenOut	52	None	0	None	0	0	0	No	No
Chan 52	GenOut	43	Or	GenOut	44	None	0	None	0	0	0	No	No
Chan 53	GenOut	41	Or	GenOut	42	None	0	None	0	0	0	No	No
Chan 54	GenOut	43	Or	GenOut	53	None	0	None	0	0	0	No	No
Chan 55	GenOut	46	Or	GenOut	56	None	0	None	0	0	0	No	No
Chan 56	GenOut	47	Or	GenOut	48	None	0	None	0	0	0	No	No
Chan 57	GenOut	45	Or	GenOut	46	None	0	None	0	0	0	No	No
Chan 58	GenOut	47	Or	GenOut	57	None	0	None	0	0	0	No	No
Chan 59	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 60	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 61	GenOut	53	Or	GenOut	56	None	0	None	0	0	0	No	No
Chan 62	GenOut	52	Or	GenOut	57	None	0	None	0	0	0	No	No
Chan 63	LRVGrn	2	And(Not2)	GenOut	61	TChkout	2	None	0	0	0	No	No
Chan 64	LRVGrn	6	And(Not2)	GenOut	62	TChkout	6	None	0	0	0	No	No

	Vehicle Detector 1	2/15/2019 8:21:49 AM
Delay Mode Fail Mode Delay 2	0.0     Extend     0.0     Carryover     0.0     Queue Limit     0       No Disc     Added     Disabled     System     Disabled       None     Max Pres     0     No Act     0     Erratic     0       0.0	ed Fail Time 0
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	1-8 9-16	
	Vehicle Detector 2	
Delay	0.0 Extend 0.0 Carryover 0.0 Queue Limit 0	
Mode	No Disc Added Disabled System Disable	ed
Fail Mode	None Max Pres 0 No Act 0 Erratic 0	Fail Time 0
Delay 2	0.0	
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	1-8 9-16	

		Vehicle	Detector 3		2/15/2019 8:21:49 AM
Delay Mode Fail Mode Delay 2	No Disc  None		Queue Lim abled Sys No Act 0		fail Time 0
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	1-8	9-16	Detector 4		
Dalay	O O Fritar			4 0	
Delay Mode	0.0 Exter		0.0 Queue Lim		od .
Fail Mode	None	Max Pres 0	No Act 0	Erratic 0	Fail Time 0
Delay 2	0.0				
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases	1-8	9-16			

Bike Call Phases

	Vehicle Detector 5	2/15/2019 8:21:49 AM
Delay Mode Fail Mode Delay 2	0.0     Extend     0.0     Carryover     0.0     Queue Limit     0       No Disc     Added     Disabled     System     Disabled       None     Max Pres     0     No Act     0     Erratic     0       0.0	ed Fail Time 0
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	1-8 9-16	
	Vehicle Detector 6	
Delay	0.0 Extend 0.0 Carryover 0.0 Queue Limit 0	
Mode	No Disc Added Disabled System Enable	d
Fail Mode	None Max Pres 0 No Act 0 Erratic 0	Fail Time 0
Delay 2	0.0	
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	1-8 9-16	

AM

			Vehicl	e Detect	or 7			2/15/2019	8:21:49
Delay Mode	0.0 Exte	end 0.0	Carryov	er 0.0	Queue L	imit 0	Disable	4	
Wiode	NO DISC	Adde	su D	isabieu		ysterri	Disable	J	
Fail Mode	None	Max Pres	0	No Act	0	Erratic	0	Fail Time	0
Delay 2	0.0	-							
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	3 3 3	9	0-16						
			Vehicl	e Detect	or 8				
Delay	0.0 Exte	end 0.0	Carryov	er 0.0	Queue L	imit 0			
Mode	No Disc	Adde	ed D	isabled	S	ystem	Disable	b	
Fail Mode	None	Max Pres	0	No Act	0	Erratic	0	Fail Time	0
Delay 2	0.0	1							
Phases	1-8	9	)-16						
Call Phases Yellow Lock Phases Red Lock Phases	4								

**Extend Phases** 

XSwitch Phases Bike Call Phases 4

	Vehicle Detector 9	2/15/2019 8:21:49 AM
Delay Mode Fail Mode Delay 2	0.0     Extend     0.0     Carryover     0.0     Queue Limit     0       No Disc     Added     Disabled     System     Disabled       None     Max Pres     0     No Act     0     Erratic     0       0.0	ed Fail Time 0
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	1-8 9-16	
	Vehicle Detector 10	
Delay	0.0 Extend 0.0 Carryover 0.0 Queue Limit 0	
Mode	No Disc Added Disabled System Disable	ed
Fail Mode	None Max Pres 0 No Act 0 Erratic 0	Fail Time 0
Delay 2	0.0	
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	1-8 9-16	

#### 2/15/2019 8:21:49 AM Vehicle Detector 11 0.0 Queue Limit Delay 0.0 Extend Carryover 0.0 Mode No Disc Added Disabled System Disabled Fail Mode Max Pres 0 Fail Time None 0 No Act 0 Erratic 0 Delay 2 **Phases** 1-8 9-16 Call Phases 4 Yellow Lock Phases Red Lock Phases **Extend Phases** 4 XSwitch Phases Bike Call Phases Vehicle Detector 12 Delay 0.0 Extend 0.0 Carryover 0.0 Queue Limit Mode No Disc Disabled System Enabled Added Fail Mode None Max Pres 0 No Act 0 Erratic 0 Fail Time 0 Delay 2 0.0 Phases 1-8 9-16 Call Phases 4 Yellow Lock Phases

Red Lock Phases Extend Phases

XSwitch Phases Bike Call Phases 4

#### Vehicle Detector 15 2/15/2019 8:21:49 AM Delay 0.0 Extend 0.0 Carryover 0.0 Queue Limit Mode No Disc Added Disabled System Disabled Fail Mode None Max Pres 0 No Act 0 Erratic 0 Fail Time 0 Delay 2 1-8 9-16 Phases 5 Call Phases Yellow Lock Phases Red Lock Phases **Extend Phases** 5

### Vehicle Detector 16

Delay	0.0 Exte	nd 0.0	Carry	over 0.0	Queue	Limit 0			
Mode	No Disc	Adde	ed	Disabled		System	Disable	ed	
Fail Mode	None	Max Pres	0	No Act	0	Erratic	0	Fail Time	0
Delay 2	0.0								

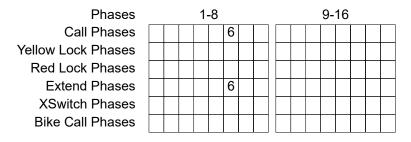
Phases		1-	-8					9-	16		
Call Phases				6							
Yellow Lock Phases											
Red Lock Phases											
Extend Phases				6							
XSwitch Phases											
Bike Call Phases											

XSwitch Phases Bike Call Phases

Vehicle Detector 17

2/15/2019 8:21:49 AM

#### Delay 0.0 Extend 0.0 Carryover 0.0 Queue Limit Mode No Disc Added Disabled System Disabled Fail Mode None Max Pres 0 No Act 0 Erratic 0 Fail Time 0 Delay 2



### Vehicle Detector 18

Delay	0.0 Exte	nd 0.0 Car	ryover 0.0	Queue Limit 0	
Mode	No Disc	Added	Disabled	System [	Disabled
Fail Mode	None	Max Pres 0	No Act	0 Errat	c 0 Fail Time 0
Delay 2	0.0				

Phases		1	-8					9-	16		
Filases	 	١.	-0					<b>9</b> -	10		
Call Phases				6							
Yellow Lock Phases											
Red Lock Phases											
Extend Phases				6							
XSwitch Phases											
Bike Call Phases											

		Vehicle Detector 19	2/15/2019 8:21:49 AM
Delay	0.0 Exte	nd 0.0 Carryover 0.0 Queue Limit 0	
Mode	No Disc	Added Disabled System Disable	b
Fail Mode	None	Max Pres 0 No Act 0 Erratic 0	Fail Time 0
Delay 2	0.0		
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	1-8	9-16	
		Vehicle Detector 20	
Delay	0.0 Exte	nd 0.0 Carryover 0.0 Queue Limit 0	
Mode	No Disc	Added Disabled System Enabled	Ė
Fail Mode	None	Max Pres 0 No Act 0 Erratic 0	Fail Time 0
Delay 2	0.0		
Phases Call Phases Yellow Lock Phases Red Lock Phases	1-8	9-16	

**Extend Phases** 

XSwitch Phases Bike Call Phases 6

#### Vehicle Detector 21 2/15/2019 8:21:49 AM 0.0 0.0 Carryover 0.0 Queue Limit Delay Extend Mode No Disc Added Disabled System Disabled Fail Mode None Max Pres 0 No Act 0 Fail Time 0 Erratic 0 Delay 2 1-8 Phases 9-16 Call Phases Yellow Lock Phases Red Lock Phases **Extend Phases** XSwitch Phases Bike Call Phases Vehicle Detector 22 Delay 0.0 Extend 0.0 Carryover 0.0 Queue Limit

Disabled

No Act

0

System

Erratic

Disabled

Fail Time

0

0

Phases		1.	-8					9-	16		
Call Phases					8						
Yellow Lock Phases											
Red Lock Phases											
Extend Phases					8						
XSwitch Phases											
Bike Call Phases											

No Disc

None

Added

0

Max Pres

Mode

Fail Mode

Delay 2

0.0

		Vehicle Detector 23									
Delay	0.0 Exter		ahla d								
Mode	No Disc	Added Disabled System Dis	abled								
Fail Mode	None	Max Pres 0 No Act 0 Erratic 0	Fail Time 0								
Delay 2	0.0										
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	1-8	9-16 8 8 Vehicle Detector 24									
Delay	0.0 Exter	nd 0.0 Carryover 0.0 Queue Limit 0									
Mode	No Disc	Added Disabled System Dis	abled								
Fail Mode	None	Max Pres 0 No Act 0 Erratic 0	Fail Time 0								
Delay 2	0.0										
Phases Call Phases Yellow Lock Phases Red Lock Phases	1-8	9-16									
Extend Phases		8									

XSwitch Phases Bike Call Phases

#### 2/15/2019 8:21:49 AM Vehicle Detector 25 0.0 Queue Limit Delay 0.0 Extend Carryover 0.0 Mode No Disc Added Disabled System Enabled Fail Mode Max Pres 0 Fail Time None 0 No Act 0 Erratic 0 Delay 2 Phases 1-8 9-16 Call Phases 8 Yellow Lock Phases Red Lock Phases **Extend Phases** 8 XSwitch Phases Bike Call Phases Vehicle Detector 26 Delay 0.0 Extend 0.0 Carryover 0.0 Queue Limit Mode No Disc Disabled System Disabled Added Fail Mode None Max Pres 0 No Act 0 Erratic 0 Fail Time 0 Delay 2 0.0 Phases 1-8 9-16 Call Phases 8

Yellow Lock Phases Red Lock Phases Extend Phases

> XSwitch Phases Bike Call Phases

8

		Pedestrian Detect	tor 2	2/15/201	9 8:21:49 AM
No Act	0 Max Pres 0	Erratic 0	Fail Mode	None	
Phases/Overlaps	1-8	9-16			
Call Ped Phases	2				
Call Ped Olaps					
Call Phases					
Locked Call Phases					
Ped Entry Phases					
Olap Ped Entry Phases					
Ped Cascade Phases Call Walk2					
Call Walk2					
		Pedestrian Detect	tor 4		
No Act	0 Max Pres 0	Erratic 0	Fail Mode	None	
Phases/Overlaps	1-8	9-16			
Call Ped Phases	4				
Call Ped Olaps					
Call Phases					
Locked Call Phases					
Ped Entry Phases					
Olap Ped Entry Phases Ped Cascade Phases					
Call Walk2					
Call Walk2					
		Pedestrian Detect	tor 6		
No Act	0 Max Pres 0	Erratic 0	Fail Mode	None	
Phases/Overlaps	1-8	9-16			
Call Ped Phases	6				
Call Ped Olaps					
Call Phases					
Locked Call Phases					
Ped Entry Phases					
Olap Ped Entry Phases					
Ped Cascade Phases					
Call Walk2					
		Pedestrian Detect	tor 8		
No Act	0 Max Pres 0	Erratic 0	Fail Mode	None	
Phases/Overlaps	1-8	9-16			
Call Ped Phases	8				
Call Ped Olaps					
Call Phases					
Locked Call Phases					
Ped Entry Phases					
Olap Ped Entry Phases Ped Cascade Phases					
Call Walk2					
Call Walk2					

		Transit Detector 2	2/15/2019 8:21:49 AM
Delay Delay2 Checkout Limit Checkin Fail Diag Enable Call LRVs Checkout Dets Low Priority PE Call Phases	1.0 Extend 0.0  0.0 Warning Ext 6.0  90 Checkout Mode  0 Checkout Fail  No  1-8  2   1-8  9-10	Travel Time 0 Travel Slack 0  Normal  0 CO Fail Cnt 0	Recall Lmt 0
Call Phases			
		Transit Detector 6	
Delay Delay2	1.0         Extend         0.0           0.0         Warning Ext         6.0	Travel Time 0 Travel Slack 0	
Checkout Limit	90 Checkout Mode	Normal	
Checkin Fail	0 Checkout Fail	0 CO Fail Cnt 0	Recall Lmt 0
Diag Enable	No		
Call LRVs Checkout Dets	1-8		
	1-8 9-10	6	
Low Priority PE			
Call Phases			

Adaptive Priority - General/Local Detectors

2/15/2019 8:21:49 AM

Local Detector Slack	0
Remote Detector Slack	0
Local Adjust Threshold	0
Remote Adjust Threshold	0

Detector	1	2	3	4	5	6	7	8
Step (Base)	0	0	0	0	0	0	0	0
Max (Base)	0	0	0	0	0	0	0	0
Step (Alt 1)	0	0	0	0	0	0	0	0
Max (Alt 1)	0	0	0	0	0	0	0	0
Step (Alt 2)	0	0	0	0	0	0	0	0
Max (Alt 2)	0	0	0	0	0	0	0	0
Step (Alt 3)	0	0	0	0	0	0	0	0
Max (Alt 3)	0	0	0	0	0	0	0	0

## **Estimated Delay**

2/15/2019 8:21:49 AM

Transit				
Disable				
Rem Phs				
Loc Int				
Loc TT				
RM1 Int				

1	2	3	4	5	6	7	8
No	No	No	No	No	No	No	No
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0

## Remote Transit Detectors (Transit/LRV 2)

1	2	3	4
7	6	5	0
2	2	2	0
15	30	60	0
Raw Chkout	Raw Chkout	Raw Chkout	Presence
0	0	0	0
0	0	0	0
0	0	0	0
0	0	0	0

		Transit/LRV Startup/Options	2/15/2019 8:21:49 AM
No Startup Call	LRV 1-8	Warn Flash Rate 1 Hz	Rsrv Inh Mode Seconds
		Transit/LRV Phase 2	
Phases Parents No Call Queue Jmp Phs	1-8	9-16	
Min Green Adv Call Call Mode	5.0 Yellow 0 Adv Green Locked 1-8	6.0 Red 6.0  0 Extend 0.0  Warning Mode G+YCall	Adv Warning 20
Train Coming Queue Delay Fail Green	0.0 Queue TT	0 Queue Mode Serve Fail Warn Off	
		Transit Priority 2	
Phases Priority Phases Coord Veh Omit Coord Ped Omit Free Veh Omit Free Ped Omit Queue Clearance	1-8	9-16 Coord Pri Mode Coord Extend Free Pri Mode Free Recovery Mode Free Extend Free Hold Priority Adv Queue Clearance Coord Ext Pmt Free Ext Pmt Reserve Inhibit Same Reserve Inhibit All	Early/Extend  20  Early/Extend  Normal  20  20  0  0  0  0  0  0
		Free Priority Timing	
Phases Min Priority Green	1 2 3 4 5 8 12 8		
Alt Sequence Rsv Extend	2 4		

## Transit/LRV Phase 6

2/15/2019 8:21:49 AM

Phases Parents No Call Queue Jmp Phs	1-8 9-16
Min Green Adv Call Call Mode Train Coming Queue Delay Fail Green	5.0         Yellow         6.0         Red         6.0           0         Adv Green         0         Extend         0.0           Locked         Warning Mode         G+YCall         Adv Warning         20           1-8         2         6         0         Serve           0         Queue TT         0         Queue Mode         Serve           0         Fail Warn         Off
	Transit Priority 6
Phases Priority Phases Coord Veh Omit Coord Ped Omit Free Veh Omit Free Ped Omit Queue Clearance	1-8 9-16 Coord Pri Mode Early/Extend Coord Extend 20 Free Pri Mode Early/Extend Free Recovery Mode Normal Free Extend 20 Free Hold 20  Priority 0 Adv Queue Clearance 0 Coord Ext Pmt 0 Free Ext Pmt 0 Reserve Inhibit Same 0 Reserve Inhibit All 0
	Free Priority Timing
Phases Min Priority Green	1     2     3     4     5     6     7     8     9     10     11     12     13     14     15     16       8     12     8     12     8     12     0     0     0     0     0     0     0     0
Alt Sequence Rsv Extend	

		Control / Config	2/15/2019 8:21:49 AM
Pattern Mode Manual Pattern	Sched  0 Manual Offset	0	
Stop Time Input	Enabled		
Aux Switch	StopTm	5	
DLS Mode	D4	Time Zone Pac (UTC-8)	GPS Thresh 0
Password Timeout	5	0.40	
Maint Phs Recalls Maint Ped Recalls	1-8	9-16	
		Serial 1 Port Configuration	
Broadcast Plan/Sync	Disabled	Broadcast Time 00:00	
Serial Rebroadcast	Disabled	Response None	
Broadcast Plan/Sync	Disabled	Serial 2 Port Configuration  Broadcast Time 00:00	
		Ethernet Port Configuration	
Broadcast Plan/Sync	Disabled	Broadcast Time 00:00	
Serial Rebroadcast	Disabled		
		Peer Configuration	
Peer 1	4		
Peer 2	5		
Peer 3	6		
Peer 4	7		
Peer 5	8		
Peer 6	9		
Peer 7	10		
	<del></del>		

Peer 8 219

		Restricted Data	2/15/2019 8:21:49 AN
		(Serial Ports)	
Serial Port 1	4		
Baud Rate	38400 8N1	RTS On 0	RTS Off 0
Serial Port 2	1		
Baud Rate	38400 8N1	RTS On 0	RTS Off 0
		(Ethernet)	
IP Address Netmask Broadcast Address Gateway Gateway 2 Gateway 3 Gateway 4	10. 7. 111. 40 255. 255. 255. 0 10. 7. 111. 255 10. 7. 111. 1 0. 0. 0. 0 0. 0. 0. 0		
Admin IP Admin Netmask	192. 168. 2. 235 255. 255. 255. 0	Leases 0	
Port Broadcast Port Time Port	161 Reply Mode 4141 Response	Host Time/Plan	
		(General)	
Controller Address Peer Address	1 Timeout 0  40 Timeout 10		
Remote Calls	Disabled		
Remote Preempt Remote Soft Preempt	Disabled Disabled		
Remote Priority	Disabled		
Remote MCE	Disabled MC	CE Max 0	