Phase Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Min Green	4	10	0	8	4	10	0	8	0	0	0	0	0	0	0	0
Veh Ext	3.0	5.0	0.0	4.0	3.0	5.0	0.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max Green 1	15	50	0	30	15	50	0	30	0	0	0	15	40	0	0	0
Max Green 2	20	70	0	40	20	70	0	40	0	0	0	0	0	0	0	0
Max Green 3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Yellow	3.0	3.6	0.0	3.2	3.0	3.6	0.0	3.2	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Red Clr	1.3	1.4	0.0	1.8	1.3	1.4	0.0	1.8	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Adv Flash	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Bike MG	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Walk	0	7	0	7	0	7	0	7	0	0	0	0	0	0	0	0
Ped Clr	0	10	0	26	0	10	0	26	0	0	0	0	0	0	0	0
Walk2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Early Wlk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Delay Wlk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max Initial	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Gap	1.5	3.0	0.0	2.0	1.5	3.0	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Reduce After	5	10	0	10	5	10	0	10	0	0	0	0	0	0	0	0
TTReduce	5	5	0	5	5	5	0	5	0	0	0	0	0	0	0	0
CS Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CS Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red Revert	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Neg Ped	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
AP Disc	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Pmt Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Pmt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Pmt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Return Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Options

Phases				1-	-8						9-	16		
Min Recalls		2				6								
Max Recalls														
Ped Recalls														
Soft Recall														
Dual Entry		2		4		6		8						
Red Rest														
Walk Rest														
Walk Expand														
Ped Recycle		2				6								
Sim Ped Term														
PC Thru Clr														
Guar Passage														
No Simult Gap	1		3		5		7							
Yel Lock														
Red Lock	1	2		4	5	6		8						
PhaseNext Lock	1	2		4	5	6		8						
No Term Call	1	2		4	5	6		8						
Cond Serv														
CS Enable														
Cond Reserve														
Reserve														
Veh Omit														
Ped Omit														
Perm Phase														
Protect Calls														
Protect Calls 2														
Flash Entry														
Flash Exit														
Flash Exit Yel														
Flash Exit Red														
Ped Scramble														
No Min Yel														
No Min Red Rev														
Max Scramble Walk														
Flash Yellow														
Flash FYA														
CNA 1														
CNA 2														
									_	 	-			

Phase Startup Options

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Startup Flash	C)						M	od	е		,	Ye	->	Re	d	
Startup All Red	5	5					•	Ύе	llo	W	C	0.0					
Phases			1-	8							9-	16					
Startup Phases			4			8											
Startup Yellow			4			8	Ī										
Startup Red							Ī										
Startup No Walk																	
Startup Next		2			6												
Startup Yel Fls																	
Startup FYA																	
No Veh Call																	
No Ped Call																	

Phase Startup Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Start Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Start Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Start Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Unit

Red Revert 5.0	Ped Protect	Yes	AdvFls in Flash	No	
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Ring Sequence / Conflicting Phases

2/15/2019 7:56:42 AM

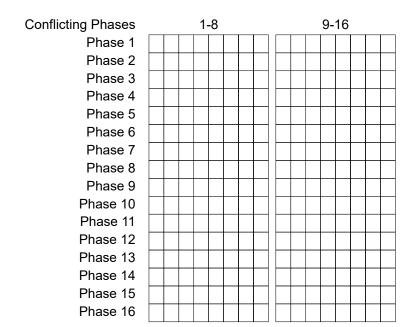
Ringgroup 1

Ring 1 Ring 2

Ringgroup 2

Custom Sequences

ucrices																	
Seq 1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Seq 8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	



FYA/FRA

FYA 1 2 3 4 5 6 7 8 Prot Phs 0 0 0 0 0 0 0 0 0 0 0 0 Opp Thru 0 0 0 0 0 0 0 0 0 0 0 0 Opp Ped 0 0 0 0 0 0 0 0 0 0 0 0 0 Opp Ped 0 0 0 0 0 0 0 0 0 0 0 0 0 0 Opp Ped 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 Opp Ped 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 Opp Ped 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0						17 (1 1 () (_, ,
Opp Thru 0<	FYA	1	2	3	4	5	6	7	8	
Start Phs	Prot Phs	0		0	0	0		0	0	
Opp Ped Delay 0 <										
Delay 0.0 0.										
Min FYA Skip Prot Red Head Mode Ped Hawk 1 Ped Hawk 1 Ped Hawk 1 Ped Hawk 2 Veh Phase Ped P										
Skip Prot Red Head Mode	_									_
Ped Hawk 1										
Ped Hawk 1 Veh Phase	•									
Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 2 Veh Phase 0 Ped Phase Ped Phase Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Signal Yes Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase Ped Phase 0 Ped Phase Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0										J
Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 2 Veh Phase 0 Ped Phase Ped Phase Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Signal Yes Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase Ped Phase 0 Ped Phase Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0										
Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Ped Hawk 2 Veh Phase 0 Ped Hawk 2 Ped Phase 0 Pes Pes Plash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Ped Hawk 3 Veh Phase 0 Pes Plash Carryover 0.0 Pes Plash Carryover 0.0 Green Mode Normal Normal Ped Hawk 4 Veh Phase 0 Ped Phase					Pe	ed Hawk	1			
Flash Yel	Veh Phase	0								
Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 2 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 3 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Flash Yel 0.0 Dark Signal Yes Flash Yel 0.0 Flash Carryover 0.0 Flash Yel 0.0 Flash Carryover 0.0	Ped Phase	0								
Ped Hawk 2 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 3 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Yel 0.0 Flash Carryover 0.0 Flash Olay 0.0 Flash Carryover 0.0	Flash Yel	0.0	Dai	rk Signal	Ye	s				
Ped Hawk 2 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 3 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Yel 0.0 Flash Carryover 0.0 Flash Yel 0.0 Flash Carryover 0.0 Flash Yel 0.0 Flash Carryover 0.0	Flash Delay	0.0	Flash C	arryover	0.0	J				
Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 3 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Yel Normal Ped Hawk 4	Green Mode	Norn	nal							
Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Normal Yes Ped Phase 0 Plash Yel Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Ped Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0					Pe	d Hawk	2			
Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 3 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Yel Normal	Vals Diseas				1 0	u Hawk	_			
Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 3 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Yel 0.0 Flash Carryover 0.0 Flash Yel 0.0 Flash Carryover 0.0 Flash Delay 0.0 Flash Carryover 0.0										
Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 3 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Yel 0.0 Flash Carryover 0.0			5	. 0						
Ped Hawk 3 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Pel O.0 Flash Carryover 0.0				_		S				
Ped Hawk 3 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0	_			arryover	0.0					
Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0	Green Mode	Norn	nal							
Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0					Pe	ed Hawk	3			
Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0	Veh Phase	0								
Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0	Ped Phase	0								
Flash Delay 0.0 Flash Carryover 0.0 Green Mode Normal Ped Hawk 4 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0	Flash Yel	0.0	Dai	rk Signal	Ye	S				
Ped Hawk 4 Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0	Flash Delay									
Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0	_	Norn								
Veh Phase 0 Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0					D-	مليمياء	1			
Ped Phase 0 Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0					Pe	ed Hawk	4			
Flash Yel 0.0 Dark Signal Yes Flash Delay 0.0 Flash Carryover 0.0										
Flash Delay 0.0 Flash Carryover 0.0										
	Flash Yel				Ye	s				
Green Mode Normal	Flash Delay			arryover	0.0					
	Green Mode	Norn	nal							

Coordination Options

Sync Time	00:00	RTC Set Time	00:00
Transition Mode	Best 2	Ped Adjust	None
Trans Short %	20	Trans Long %	35
Offset Reference	Lead FO	Short Cycles	0
Dual Entry	Normal	Overlap F/O	Disabled
Master Sync Mode	RTC	Master Sync Length	0
Adapt Thresh	0	Adapt Step	0
External Plan Max	0		
Hardwire No Match	Sched	Hardwire Sync Fail	0
Override Omit/Recall	No		
Phases	1-8	9-16	
No Trans Recall			
Trans Ped Recall			
Trans Phases			

Hardwire Plans

Hardwire	Plan Select	Pattern	Offset	Mode
Plan 1		0	0	Hardwire
Plan 2		0	0	Hardwire
Plan 3		0	0	Hardwire
Plan 4		0	0	Hardwire
Plan 5		0	0	Hardwire
Plan 6		0	0	Hardwire
Plan 7		0	0	Hardwire
Plan 8		0	0	Hardwire
Plan 9		0	0	Hardwire
Plan 10		0	0	Hardwire
Plan 11		0	0	Hardwire
Plan 12		0	0	Hardwire
Plan 13		0	0	Hardwire
Plan 14		0	0	Hardwire
Plan 15		0	0	Hardwire
Plan 16		0	0	Hardwire
Plan 17		0	0	Hardwire
Plan 18		0	0	Hardwire
Plan 19		0	0	Hardwire
Plan 20		0	0	Hardwire
Plan 21		0	0	Hardwire
Plan 22		0	0	Hardwire
Plan 23		0	0	Hardwire
Plan 24		0	0	Hardwire
Plan 25		0	0	Hardwire
Plan 26		0	0	Hardwire
Plan 27		0	0	Hardwire
Plan 28		0	0	Hardwire
Plan 29		0	0	Hardwire
Plan 30		0	0	Hardwire
Plan 31		0	0	Hardwire
Plan 32		0	0	Hardwire

Soft Interconnect

Mode		Slav	'e		Rer	no	te	Int	Ν	um	be	r	0
Yield Delay	0												
Yield Duration	0												
Permissive	0												
Local Hold Limit	0												
Phases		1	-8					9-	16				
Local Control Phases													
Local Hold Phases													
Local Perm Phases													
Local Call Phases													
Remote Perm Phases													
Remote Hold Phases													

Coordination Pattern 1

Cycle	90	Ring	group	1 - C	offset 1	50)	Offse	t 2	0	Offs	set 3	0						
		Ring	group	2 - C	ffset 1	0		Offse	t 2	0	Offs	set 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	15	37	0	38	15	37	0	38	0	0	0	0	0	0	0	0]		
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-		
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Perm Min Green	3	10	0	5	3	10	0	5	0	0	0	0	0	0	0	0	1		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
		•			•			•		'	•		•	•					
Permissive Mode	S	ing Ba	and		Max	Mode	9	Ма	x 2		W	/alk R	est	,	Yield				
Ped Permissive		Yield	d																
Permissive Limit	10			I	Perm 2	2 Star	t ()			Per	m 2 E	nd	0					
Alt Sequence 1	3				TOD	Link	0												
Phases/Overlaps		1-	.8			9-16	3		Tı	rans N	/lode		Defa	ılt					
Coord Phases	2		6							Offse			Defa						
No Extend																			
Float Enable									Adap	otive N	/lode		Disabl	ed					
Veh = Ped Perm																			
Walk Rest										Disa	able P	riority							
Ped Recall									Pro	gress	ion Ph	nases							
Cond Ped Call										Pric	rity Al	t Seq	1	3					
Olap Ped Recall											erve E	-				Ħ			
Ped Recycle										11000	,, vo	Atoria							
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7		$\perp \perp$													
Max Inhibit																			
FYA Omit			$\perp \downarrow \downarrow$	\perp		$\bot \bot$													
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	-		
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Recovery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			

Coordination Pattern 1

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
			•			•	•		•			•	•	•		

Coordination Pattern 2

Cycle	90	Ring	group	1 - O	offset 1	78	3	Offse	t 2 [0	Offs	set 3	0						
		Ring	group	2 - O	ffset 1	0		Offse	t 2	0	Offs	et 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	15	37	0	38	15	37	0	38	0	0	0	0	0	0	0	0]		
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Perm Min Green	3	10	0	5	3	10	0	5	0	0	0	0	0	0	0	0	†		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
								1									1		
Permissive Mode	S	ing Ba	and		Max	Mode	Э	Ma	x 2		W	/alk R	est	•	Yield				
Ped Permissive		Yield	ł																
Permissive Limit	10			-	Perm 2	2 Star	t ()			Per	m 2 E	nd	0					
Alt Sequence	3				TOD) Link	0												
								_											
Phases/Overlaps		1-	-8			9-16	6		T	ans N	/lode		Defa	ult					
Coord Phases	2		6							Offset	t Ref		Defa	ult					
No Extend									Adar	tive N	/lode		Disabl	ed					
Float Enable									, way	ouve iv	louc		Jioabi						
Veh = Ped Perm										Dies	ıble P	riority							
Walk Rest									_			•	+	++	$\vdash \vdash$	\blacksquare		 	_
Ped Recall									Pro	gress	ion Pr	nases							
Cond Ped Call				Ш						Pric	rity Al	t Seq	1	3					
Olap Ped Recall				Ш						Rese	rve E	xtend							
Ped Recycle																			
Min Recall				Ш															
Max Recall				Ш															
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit																			
FYA Omit			$\perp \downarrow \downarrow$	$\perp \downarrow \downarrow \mid$															
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Recovery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			

Coordination Pattern 2

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Coordination Pattern 3

Cycle	90	Ring	group	1 - C	Offset '	1 50)	Offse	et 2	0	Offs	et 3	0]					
		Ring	group	2 - C	Offset '	1 0		Offse	et 2	0	Offs	et 3	0]					
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	15	37	0	38	15	37	0	38	0	0	0	0	0	0	0	0			
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Perm Min Green	3	10	0	5	3	10	0	5	0	0	0	0	0	0	0	0			
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
								_							1				
Permissive Mode	S	ing Ba	and		Max	Mode	e	Ма	ax 2		W	alk R	est		Yield				
Ped Permissive		Yield	ł																
Permissive Limit	10				Perm	2 Star	t ()			Per	m 2 E	nd	0					
Alt Sequence 1	3				TOE) Link	0												
			_				_		_	_					_				
Phases/Overlaps		1-				9-16	3		T	rans N	/lode		Defa	ult					
Coord Phases	2		6							Offse	t Ref		Defa	ult					
No Extend									Adar	otive N	/lode		Disab	led					
Float Enable									'										
Veh = Ped Perm										Disa	able Pi	riority							
Walk Rest				\perp					D			•					 	 _	_
Ped Recall				\perp					Pro	•	ion Ph								
Cond Ped Call	Ш									Pric	ority Al	t Seq	1	3					
Olap Ped Recall	Ш									Rese	erve Ex	xtend							
Ped Recycle														-1 - 1 -	1 1				
Min Recall																			
Max Recall																			
Cond Serv				\perp															
Reservice																			
Veh Omit																			
Ped Omit	Ш																		
Olap Omit	Ш																		
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit								Ш											
FYA Omit																			
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Recovery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			

Coordination Pattern 3

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
'																

Coordination Pattern 11

Cycle	75	Ring	group	1 - O	ffset 1	25	5	Offse	t 2	0	Offs	et 3	0						
		Ring	group	2 - O	ffset 1	0		Offse	t 2	0	Offs	et 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	15	30	0	30	15	30	0	30	0	0	0	0	0	0	0	0	7		
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Perm Min Green	8	12	0	12	8	12	0	12	0	0	0	0	0	0	0	0	1		
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
Permissive Mode	S	ing Ba	and		Max	Mode	9	Ma	x 2		W	alk R	est		Yield				
Ped Permissive		Yield	t																
Permissive Limit	0			 	Perm 2	2 Star	t ()			Peri	m 2 E	nd	0					
Alt Sequence 1	3				TOD	Link	0	$\overline{}$					_						
Phases/Overlaps		1-	-8			9-16	3		Ti	rans N	/lode		Defa	ult					
Coord Phases	2		6							Offse	t Ref	L	_ead	FO					
No Extend										otive N			Disab		\exists				
Float Enable									Aua	JUVE IV	noue		Jisab	icu					
Veh = Ped Perm										Dicc	able Pr	iority							
Walk Rest									_			•		++				 	 _
Ped Recall									Pro	gress	ion Ph	ases							\perp
Cond Ped Call										Pric	rity Alt	t Seq							
Olap Ped Recall										Rese	rve Ex	ktend							
Ped Recycle																			
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice						$\perp \perp$													
Veh Omit						$\perp \perp$													
Ped Omit						\perp													
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit				$\perp \downarrow \downarrow$				\coprod											
FYA Omit				$\perp \downarrow \downarrow$															
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	_		
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Recovery Min Green	0	0	0	0	0	0	0	0	0	Ω	0	0	Λ	0	0	0			

Coordination Pattern 11

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
								•					•			

Coordination Pattern 12

Cycle	75	Ring	group	1 - 0	ffset 1	1 42	2	Offse	t 2	0	Offs	et 3 [0						
		Ring	group	2 - 0	offset 1	1 0		Offse	t 2	0	Offs	et 3	0						
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Splits	15	30	0	30	15	30	0	30	0	0	0	0	0	0	0	0			
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Perm Min Green	8	12	0	12	8	12	0	12	0	0	0	0	0	0	0	0			
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
		•	•	•	•	•	•	•	•	•	•			•	•				
Permissive Mode	Si	ing Ba	and		Max	Mode	Э	Ma	ax 2		W	alk Re	est		Yield				
Ped Permissive		Yield	ł																
Permissive Limit	0			F	Perm :	2 Star	t (0			Peri	m 2 E	nd	0					
Alt Sequence 1	3				TOE) Link	0												
Phases/Overlaps		1-	8			9-16	3		Т	rans N	/lode		Defa	ult					
Coord Phases	2		6							Offse	t Ref		ead I	FΩ	=				
No Extend															=				
Float Enable									Adap	otive N	vioae	L	Disabl	iea					
Veh = Ped Perm										ъ.		,							
Walk Rest										Disa	able Pr	riority				Ш			
Ped Recall									Pro	gress	ion Ph	ases							
Cond Ped Call										Pric	ority Al	t Seq							
Olap Ped Recall											erve Ex	-							
Ped Recycle										11000) VO L	(toria							
Min Recall																			
Max Recall																			
Cond Serv																			
Reservice																			
Veh Omit																			
Ped Omit																			
Olap Omit																			
Perm Reserve																			
Perm 1 Phases				7															
Max Inhibit				\top															
FYA Omit				\top															
Adapt Phases																			
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
Recovery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			

Coordination Pattern 12

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
							•					•				•

Coordination Pattern 13

Cycle	75	Ring	group	1 - 0	offset 1	32	2	Offse	t 2 [0	Offs	et 3	0								
		Ring	group	2 - 0	ffset 1	0		Offse	t 2	0	Offs	et 3	0								
Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16					
Splits	15	30	0	30	15	30	0	30	0	0	0	0	0	0	0	0]				
Split Ext	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
Float Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				
Perm Min Green	8	12	0	12	8	12	0	12	0	0	0	0	0	0	0	0	1				
Min Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				
Max Trans Split	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
Split 2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
PA Before	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
PA After	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
			•	•	•	•	•	•	•	•				•	•		•				
Permissive Mode	S	ing Ba	and		Max	Mode	9	Ма	x 2		W	alk R	est	,	Yield						
Ped Permissive		Yield	d																		
Permissive Limit	0]		F	Perm 2	2 Star	t C)			Per	m 2 E	nd	0							
Alt Sequence 1	3				TOD) Link	0														
Phases/Overlaps		1-				9-16	3		Tı	ans N	/lode		Defa	ult							
Coord Phases	2		6							Offset	t Ref	L	_ead I	FO							
No Extend									Adar	tive N	/lode		Disabl	ed							
Float Enable									, , , ,		.040		51000								
Veh = Ped Perm				Ш						Disa	ıble P	riority									
Walk Rest									D			•		++	\Box	+				_	_
Ped Recall							$\perp \perp$		Pro	gress			Щ		Ш		\bot	\bot	Ш	\perp	\perp
Cond Ped Call							$\perp \perp$			Pric	rity Al	t Seq									
Olap Ped Recall							$\perp \perp$			Rese	rve E	xtend									
Ped Recycle																					
Min Recall																					
Max Recall																					
Cond Serv																					
Reservice							11														
Veh Omit							11														
Ped Omit							++														
Olap Omit							++														
Perm Reserve				_			++														
Perm 1 Phases				7			++	\perp													
Max Inhibit							++	\perp													
FYA Omit				+			+	\perp													
Adapt Phases																					
Priority Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	-				
Priority Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
Recovery Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					

Coordination Pattern 13

Alternate Timing-Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Alt Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Ped Clr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Sol DW	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Min Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Veh Ext	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Max Green	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt Red Clr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Early Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt Delay Walk	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Alt CS Min	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Alt CS Max	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
'																

		Preempt 8	(Configuration)	2/15/2019 7:56:42 AM
Enabled	Yes	Dwell Mode	Normal	Output Mode All
Output2 Mode	All	Fail Action	Preempt Off	Exit Mode Normal
Override Flash	No	Change Phasenext	Yes	
Enable Phases Preempt Inputs	1-8	9-16 8	LRV Disable LRV Dwell Flash LRV Omit LRV No Yel	1-8 Max 0 Delay 0
		Preempt 8 (Timir	ng/Phases/Overlaps)	
Phases/Overlaps	1-8	9-16		
Omit Olap Grn Clr			Start Green	0 Start Walk 0
Phs EWlk to Grn				Start Ped Clr 0
TClr 1 Veh Phases				Start Fed Cil 0
TClr 1 Ped Phases			Track Clear 1	0 Track Clear 2 0
TClr 1 Olap				
TClr 1 Olap Ped			TC1 Extend	0 TC1 Max 0
TClr 2 Veh Phases			Exit Ped Clr	0 Exit Yellow 0.0
TClr 2 Ped Phases			LXII I GU OII	C Exit reliew 0.0
TClr 2 Olap			Exit Red	0.0
TClr 2 Olap Ped				
Init Dwell Phases			Min Dwell	10 Min Duration 0
Dwell Veh Phases	4	8	Dwell Extend	0
Dwell Ped Phases	4	8	Dwell Exterio	0
Dwell Olap			Max Dwell	15 Max Call 0
Dwell Olap Ped				
Exit Veh Phases			Reserve Inh Same	0
Exit Ped Phases			December Inh All	
Exit Olap			Reserve Inh All	0
Exit Olap Ped			Delay	0
Zero Phase Walk			,	
Zero Phase Ped Clr			Phases/Overlaps	1-8 9-16
Zero Phase Green			TClr 1 FR Olap	
Zero Olap Walk			TCIr 2 FR Olap	
Zero Olap Ped Clr			Dwell FR Olap	
Zero Olap Green			TClr 1 FYA	
Dwell-Phase Red			TClr 2 FYA	
Dwell-Phase Red Flash			Dwell FYA	
Dwell-Phase Yel Flash				
Dwell-Olap Red Flash				
Dwell-Olap Yel Flash				
Dwell-Ped Dark				
Dwell-Olap Ped Dark				

TOD Pattern Events

	Time			D	O۱	Ν				Нс	olida	ays		Mode	Pattern	Offset
Event 1	00:00	S	М	Т	W	Т	F	S						Sched	14	0
Event 2	05:30		М	Т	W	Т	F							Sched	2	0
Event 3	08:30		М	Т	W	Т	F							Sched	3	0
Event 4	15:30		М	Т	W	Т	F							Sched	3	0
Event 5	18:30		М	Т	W	Т	F							Sched	1	0
Event 6	22:00	S	М	Т	W	Т	F	S						Sched	14	0
Event 7	05:30	S						S						Sched	1	0
Event 8	00:00													Sched	0	0
Event 9	00:00													Sched	0	0
Event 10	00:00													Sched	0	0
Event 11	00:00													Sched	0	0
Event 12	00:00													Sched	0	0
Event 13	00:00													Sched	0	0
Event 14	00:00													Sched	0	0
Event 15	00:00													Sched	0	0
Event 16	00:00													Sched	0	0
Event 17	00:00													Sched	0	0
Event 18	00:00													Sched	0	0
Event 19	00:00													Sched	0	0
Event 20	00:00													Sched	0	0
Event 21	00:00													Sched	0	0
Event 22	00:00													Sched	0	0
Event 23	00:00													Sched	0	0
Event 24	00:00													Sched	0	0
Event 25	00:00													Sched	0	0
Event 26	00:00													Sched	0	0
Event 27	00:00													Sched	0	0
Event 28	00:00													Sched	0	0
Event 29	00:00													Sched	0	0
Event 30	00:00													Sched	0	0
Event 31	00:00													Sched	0	0
Event 32	00:00													Sched	0	0

332/336 Outputs (Connector C1S)

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	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8	Pin 9
Output	DntWlk	Walk	VehRed	VehYel	VehGrn	VehRed	VehYel	VehGrn
Index	4	4	4	4	4	3	3	3
	Pin 10	Pin 11	Pin 12	Pin 13	Pin 15	Pin 16	Pin 17	Pin 18
Output	DntWlk	Walk	VehRed	VehYel	VehGrn	VehRed	VehYel	VehGrn
Index	2	2	2	2	2	1	1	1
	Pin 19	Pin 20	Pin 21	Pin 22	Pin 23	Pin 24	Pin 25	Pin 26
Output	DntWlk	Walk	VehRed	VehYel	VehGrn	VehRed	VehYel	VehGrn
Index	8	8	8	8	8	7	7	7
	Pin 27	Pin 28	Pin 29	Pin 30	Pin 31	Pin 32	Pin 33	Pin 34
Output	DntWlk	Walk	VehRed	VehYel	VehGrn	VehRed	VehYel	VehGrn
Index	6	6	6	6	6	5	5	5
	Pin 35	Pin 36	Pin 37	Pin 38	Pin 83	Pin 84	Pin 85	Pin 86
Output	PedClr	PedClr	PedClr	PedClr	VehRed	VehRed	LRVRed	LRVYel
Index	0	0	0	0	0	0	2	2
	Pin 87	Pin 88	Pin 89	Pin 90	Pin 91	Pin 93	Pin 94	Pin 95
Output	LRVGrn	LRVRed	LRVYel	LRVGrn	VehRed	VehRed	OlpRed	OlpYel
Index	2	6	6	6	0	0	10	10
	Pin 96	Pin 97	Pin 98	Pin 99	Pin 100	Pin 101	Pin 102	Pin 103
Output	OlpGrn	OlpRed	OlpYel	TrnWrn	VehRed	VehRed	VehRed	VehRed
Index	10	0	9	2	0	0	0	0

332/336 Outputs (Connector C11S)

	Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8
Output	VehRed							
Index	0	0	0	0	0	0	0	0

332/336 Inputs (Connector C1S)

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	Pin 39	Pin 40	Pin 41	Pin 42	Pin 43	Pin 44	Pin 45	Pin 46
Input	VehDet	VehDet	VehDet	VehDet	VehDet	VehDet	VehDet	VehDet
Index	2	16	8	22	3	17	9	23
Ĺ	Pin 47	Pin 48	Pin 49	Pin 50	Pin 51	Pin 52	Pin 53	Pin 54
Input	VehDet	VehDet	VehDet	VehDet	Preempt	Preempt	GenIn	GenIn
Index	6	20	12	26	1	2	5	4
·	Pin 55	Pin 56	Pin 57	Pin 58	Pin 59	Pin 60	Pin 61	Pin 62
Input	VehDet	VehDet	VehDet	VehDet	GenIn	GenIn	GenIn	GenIn
Index	15	1	21	7	3	7	2	6
ı	Pin 63	Pin 64	Pin 65	Pin 66	Pin 67	Pin 68	Pin 69	Pin 70
Input	VehDet	VehDet	VehDet	VehDet	PedDet	PedDet	PedDet	PedDet
Index	5	19	11	25	2	6	4	8
ı	Pin 71	Pin 72	Pin 73	Pin 74	Pin 75	Pin 76	Pin 77	Pin 78
Input	Preempt	Preempt	Preempt	Preempt	GenIn	VehDet	VehDet	VehDet
Index	3	4	5	6	1	4	18	10
ı	Pin 79	Pin 80	Pin 81	Pin 82	1	1		1
Input	VehDet	GenIn	LocFlash	StopTm				
Index	24	8	5	5	1			

332/336 Inputs (Connector C11S)

	Pin 10	Pin 11	Pin 12	Pin 13	Pin 15	Pin 16	Pin 17	Pin 18
Input	None							
Index	0	0	0	0	0	0	0	0
	Pin 19	Pin 20	Pin 21	Pin 22	Pin 23	Pin 24	Pin 25	Pin 26
Input	None							
Index	0	0	0	0	0	0	0	0
L	Pin 27	Pin 28	Pin 29	Pin 30				
Input	None	None	None	None				
Index	0	0	0	0				

Cabinet / MMU Configuration

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			1-8	9-16
Cabinet Type	332/336	MMU Channel Ignore		
MMU Disable	No	Det BIU 1-No Fail Call		
		Det BIU 2-No Fail Call		
		Alt LS Flash		
		Alt Phase Flash		
		Alt Overlap Flash		
		Alt LRV Flash		
		CMU Channel Ignore Det IASM1-Det Diag	1-8 17-24 1-8 17-24	9-16 25-32 9-16
		Det IASM2-Det Diag	17-24	9-16

Phase / Overlap Outputs

	Phase	Overlap
1	Normal	Normal
2	Normal	Normal
3	Normal	Normal
4	Normal	Normal
5	Normal	Normal
6	Normal	Normal
7	Normal	Normal
8	Normal	Normal
9	Normal	Normal
10	Normal	Normal
11	Normal	Normal
12	Normal	Normal
13	Normal	Normal
14	Normal	Normal
15	Normal	Normal
16	Normal	Normal

LRV Outputs

	LRV
1	3 Head
2	3 Head
3	3 Head
4	3 Head
5	3 Head
6	3 Head
7	3 Head
8	3 Head

I/O Logic Channels

	Func1	ldx	Oper	Func2	ldx	Out1	ldx	Out2	ldx	Dly	Ext	Trig Fls
Chan 1	GenIn	1	And(Not2)	GenOut	51	TChkin	2	None	0	0	0	No No
Chan 2	GenIn	2	And(Not2)	GenOut	52	TChkin	2	None	0	0	0	No No
Chan 3	GenIn	3	And	GenOut	53	TChkout	2	None	0	0	5	No No
Chan 4	GenIn	4	And	GenOut	54	TChkout	0	None	0	0	0	No No
Chan 5	GenIn	5	And(Not2)	GenOut	55	TChkin	6	None	0	0	0	No No
Chan 6	GenIn	6	And(Not2)	GenOut	56	TChkin	6	None	0	0	0	No No
Chan 7	GenIn	7	And	GenOut	57	TChkout	6	None	0	0	5	No No
Chan 8	GenIn	8	And	GenOut	58	TChkout	0	None	0	0	0	No No
Chan 9	PhsChk	4	Or	PhsChk	8	None	0	None	0	0	0	No No
Chan 10	Preempt	3	Nor	Preempt	5	None	0	None	0	0	0	No No
Chan 11	GenIn	1	And	GenOut	51	TChkout	0	None	0	0	0	No No
Chan 12	GenIn	2	And	GenOut	52	TChkout	6	None	0	0	5	No No
Chan 13	GenIn	3	And(Not2)	GenOut	53	TChkin	6	None	0	0	0	No No
Chan 14	GenIn	4	And(Not2)	GenOut	54	TChkin	6	None	0	0	0	No No
Chan 15	GenIn	5	And	GenOut	55	TChkout	0	None	0	0	0	No No
Chan 16	GenIn	6	And	GenOut	56	TChkout	2	None	0	0	5	No No
Chan 17	GenIn	7	And(Not2)	GenOut	57	TChkin	2	None	0	0	0	No No
Chan 18	GenIn	8	And(Not2)	GenOut	58	TChkin	2	None	0	0	0	No No
Chan 19	GenOut	9	Or	None	0	Preempt	8	None	0	150	0	No No
Chan 20	PhsNxt	1	Or	PhsNxt	5	LRV Om	t 2	LRV Omi	t 6	0	0	No No
Chan 21	PhsNxt	4	Or	PhsNxt	8	LRV Om	t 2	LRV Omi	t 6	0	0	No No
Chan 22	PhsNxt	0	Or	PhsNxt	0	LRV Om	t 0	LRV Omi	t 0	0	0	No No
Chan 23	None	0	Or	None	0	None	0	None	0	0	0	No No
Chan 24	None	0	Or	None	0	None	0	None	0	0	0	No No
Chan 25	None	0	Or	None	0	None	0	None	0	0	0	No No
Chan 26	None	0	Or	None	0	None	0	None	0	0	0	No No
Chan 27	None	0	Or	None	0	None	0	None	0	0	0	No No
Chan 28	None	0	Or	None	0	None	0	None	0	0	0	No No
Chan 29	None	0	Or	None	0	None	0	None	0	0	0	No No
Chan 30	None	0	Or	None	0	None	0	None	0	0	0	No No
Chan 31	None	0	Or	None	0	None	0	None	0	0	0	No No
Chan 32	None	0	Or	None	0	None	0	None	0	0	0	No No

I/O Logic Channels

	Func1	ldx	Oper	Func2	ldx	Out1	ldx	Out2	ldx	Dly	Ext	Trig	Fls
Chan 33	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 34	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 35	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 36	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 37	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 38	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 39	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 40	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 41	GenIn	1	Or	None	0	None	0	None	0	0	120	No	No
Chan 42	GenIn		And(Not2)	GenIn	3	None	0	None	0	0	120	No	No
Chan 43	GenIn	3	And(Not2)	GenIn	2	None	0	None	0	0	120	No	No
Chan 44	GenIn	4	Or	None	0	None	0	None	0	0	120	No	No
Chan 45	GenIn	5	Or	None	0	None	0	None	0	0	120	No	No
Chan 46	GenIn	6	And(Not2)	GenIn	7	None	0	None	0	0	120	No	No
Chan 47	GenIn	7	And(Not2)	GenIn	6	None	0	None	0	0	120	No	No
Chan 48	GenIn	8	Or	None	0	None	0	None	0	0	120	No	No
Chan 49	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 50	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 51	GenOut	42	Or	GenOut	52	None	0	None	0	0	0	No	No
Chan 52	GenOut	43	Or	GenOut	44	None	0	None	0	0	0	No	No
Chan 53	GenOut	41	Or	GenOut	42	None	0	None	0	0	0	No	No
Chan 54	GenOut	43	Or	GenOut	53	None	0	None	0	0	0	No	No
Chan 55	GenOut	46	Or	GenOut	56	None	0	None	0	0	0	No	No
Chan 56	GenOut	47	Or	GenOut	48	None	0	None	0	0	0	No	No
Chan 57	GenOut	45	Or	GenOut	46	None	0	None	0	0	0	No	No
Chan 58	GenOut	47	Or	GenOut	57	None	0	None	0	0	0	No	No
Chan 59	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 60	None	0	Or	None	0	None	0	None	0	0	0	No	No
Chan 61	GenOut	53	Or	GenOut	56	None	0	None	0	0	0	No	No
Chan 62	GenOut	52	Or	GenOut	57	None	0	None	0	0	0	No	No
Chan 63	LRVGrn	2	And(Not2)	GenOut	61	TChkout	2	None	0	0	0	No	No
Chan 64	LRVGrn	6	And(Not2)	GenOut	62	TChkout	6	None	0	0	0	No	No

Vehicle Detector 1

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Delay	0.0 Ext	end 0.0 Carryover	r 0.0 Queue Limit 0
Mode	No Disc	Added Dis	sabled System Disabled
Fail Mode	None	Max Pres 0	No Act 0 Erratic 0 Fail Time 0
Delay 2	0.0		
Phases	1-8	9-16	
Call Phases	1		
Yellow Lock Phases			
Red Lock Phases			
Extend Phases	1		
XSwitch Phases			
Bike Call Phases			

Vehicle Detector 2

Delay 0.0 Extend 0.0 Carryover 0.0 Queue Limit 0									
Mode	No Disc	Added	Disabled	System [Disabled				
Fail Mode	None	Max Pres	0 No Act	0 Errat	ic 0 Fail Time 0				
Delay 2	0.0								

Phases 1-8 9-16

Call Phases
Yellow Lock Phases
Red Lock Phases
Extend Phases
XSwitch Phases
Bike Call Phases

Vehicle Detector 3

Delay	0.0 Exte	end 0.0 Carryove	r 0.0 Que	eue Limit 0	
Mode	No Disc	Added Di	sabled	System	Disabled
Fail Mode	None	Max Pres 0	No Act () Erratic	0 Fail Time 0
Delay 2	0.0				
Phases	1-8	9-16			
Call Phases	2				
Yellow Lock Phases					
Red Lock Phases					
Extend Phases	2				
XSwitch Phases					
Bike Call Phases					
		.,	5		
		Vehicle	e Detector 4		

Delay	0.0 Exte	nd 0.0 Ca	arryover 0.0	Queue Limit (
Mode	No Disc	Added [Disabled	System	Disabled	
Fail Mode	None	Max Pres (No Act	0 Errat	ic 0 Fail Time	0
Delay 2	0.0					

Phases		•	I - 8					9-	16		
Call Phases	2										
Yellow Lock Phases											
Red Lock Phases											
Extend Phases	2										
XSwitch Phases											
Bike Call Phases											
						-	-				

Vehicle Detector 5

2/15/2019 7:56:42 AM

Delay	0.0]		xten	d [0.0			rryc			Que	eue Lin			
Mode		No D	ISC			Ad	dde	d [Dis	abled		Sy	stem Disa	bled	
Fail Mode		Non	ie		Ma	k Pre	s	0)		No Act	: [)	Erratic 0	Fail Time	0
Delay 2	0.0															
		•														
Phases		1	-8				9-	16								
Call Phases	2															
Yellow Lock Phases																
Red Lock Phases																
Extend Phases	2															
XSwitch Phases																
Bike Call Phases																

Vehicle Detector 6

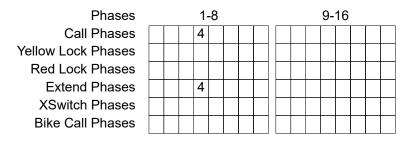
Delay	0.0 Exter	nd 0.0 C	arryover 0.0	Queue Limit	0		
Mode	No Disc	Added	Disabled	System	Enable	d	
Fail Mode	None	Max Pres	0 No Act	0 Erra	itic 0	Fail Time	0
Delay 2	0.0						

Vehicle Detector 7

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Delay	0.0 Exte	end 0.0 Carryove	er 0.0 Queue Limit 0
Mode	No Disc	Added Dis	sabled System Disabled
Fail Mode	None	Max Pres 0	No Act 0 Erratic 0 Fail Time 0
Delay 2	0.0		
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	3 3 3	9-16	

Delay	0.0 Exte	nd 0.0 Car	ryover 0.0	Queue Li	imit 0			
Mode	No Disc	Added	Disabled	S	system	Disable	ed	
Fail Mode	None	Max Pres 0	No Act	0	Erratic	0	Fail Time	0
Delay 2	0.0							



Vehicle Detector 9

2/15/2019 7:56:42 AM

0.0 Exte	end 0.0 Carryover	0.0 Queue Limit 0
No Disc	Added Disab	bled System Disabled
None	Max Pres 0	No Act 0 Erratic 0 Fail Time 0
0.0		
1-8	9-16	
4		
4		
	No Disc None 1-8	No Disc Added Disa None Max Pres 0 0.0 1-8 9-16

Vehicle Detector 10

Delay 0.0 Extend 0.0 Carryover 0.0 Queue Limit Mode No Disc Added Disabled System Disabled Fail Mode None Max Pres 0 No Act 0 Erratic 0 Fail Time 0 Delay 2 0.0

Phases 1-8 9-16

Call Phases 4

Yellow Lock Phases
Red Lock Phases
Extend Phases
XSwitch Phases
Bike Call Phases

Vehicle Detector 11

Delay	0.0 Exte	nd 0.0 Carryove	r 0.0 Q	ueue Limit 0	
Mode	No Disc	Added Dis	sabled	System I	Disabled
Fail Mode	None	Max Pres 0	No Act	0 Erratic	0 Fail Time 0
Delay 2	0.0				
Phases Call Phases Yellow Lock Phases Red Lock Phases Extend Phases XSwitch Phases Bike Call Phases	1-8	9-16			
		Vehicle	Detector ²	12	

Delay	0.0 Exte	nd 0.0 Car	ryover 0.0	Queue Limit	0			
Mode	No Disc	Added	Disabled	Syste	em	Enable	d	
Fail Mode	None	Max Pres 0	No Act	0	Erratic	0	Fail Time	0
Delav 2	0.0							

Phases		1-	-8			9-16							
Call Phases		4											
Yellow Lock Phases													
Red Lock Phases													
Extend Phases		4											
XSwitch Phases													
Bike Call Phases													

Vehicle Detector 15

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Delay	0.0	Ext	tend 0.	0	Carr	yove	0.0	Queue	Limit 0			
Mode	No	Disc		Adde	ed	Dis	abled		System	Disabled		
Fail Mode	No	one	Max	Pres	0		No Act	0	Erratic	0	Fail Time	0
Delay 2	0.0											
Phases		1-8		9	-16		_					
Call Phases		5										
Yellow Lock Phases												
Red Lock Phases												
Extend Phases		5										
XSwitch Phases												
Bike Call Phases												
							1					

Delay	0.0 Exte	nd 0.0	Carryo	over 0.0	Queue	Limit 0			
Mode	No Disc	Adde	d	Disabled		System	Disable	ed	
Fail Mode	None	Max Pres	0	No Act	0	Erratic	0	Fail Time	0
Delay 2	0.0								

Phases		1-	-8					9-	16		
Call Phases				6							
Yellow Lock Phases											
Red Lock Phases											
Extend Phases				6		Ī					
XSwitch Phases											
Bike Call Phases											

Vehicle Detector 17

2/15/2019 7:56:42 AM

Delay	0.0 Exte	nd 0.0 Carryover	or 0.0 Queue Limit 0	
Mode	No Disc	Added Dis	sabled System Disabled	
Fail Mode	None	Max Pres 0	No Act 0 Erratic 0 Fail Time 0	0
Delay 2	0.0			
Phases	1-8	9-16		
Call Phases	6		7	
Yellow Lock Phases			1	
Red Lock Phases			1	
Extend Phases	6		1	
XSwitch Phases			1	
Bike Call Phases			1	
			_	

Delay	0.0 Exte	nd 0.0	Carryo	over 0.0	Queue	Limit 0			
Mode	No Disc	Adde	d	Disabled		System	Disable	ed	
Fail Mode	None	Max Pres	0	No Act	0	Erratic	0	Fail Time	0
Delay 2	0.0								

Phases		1	8-1					9-	16		
Call Phases				6							
Yellow Lock Phases											
Red Lock Phases											
Extend Phases				6							
XSwitch Phases											
Bike Call Phases											
	 					_					

Vehicle Detector 19

2/15/2019 7:56:42 AM

Delay	0.	<u> </u>			E \	κter	nd		.0	1	,	Car	rr\/	~ \/	or	0.0	0	ueu	۰li	imit	0										
Delay	0.	U			L	(lGI	IU	U	.0			Jai	ıı y	UV	CI	0.0	Q	ucu	C LI	IIIII											
Mode		Ν	lo [Dis	С				Α	dd	ed			D	is	abled			S	yste	m		D)isat	ole	d					
Fail Mode			No	ne			M	lax	Pre	es		0				No Act		0		E	Errati	С		0		F	ail ⁻	Tim	е	0	
Delay 2	0.	0																													
Phases				1-8	3					ç	9-1	6																			
Call Phases					6								T																		
ellow Lock Phases																															
Red Lock Phases																															
Extend Phases					6																										
XSwitch Phases																															
Bike Call Phases																															
											,	Ve	hi	cle	9	Detect	or 2	20													
Delay	0.	0			Ex	ĸter	nd	0	.0		(Car	rry	οv	er	0.0	Q	ueu	e Li	imit	0										

Disabled

No Act

0

Added

0

Max Pres

System

Erratic

Enabled

0

Fail Time

0

Phases		1.	-8					9-	16		
Call Phases				6							
Yellow Lock Phases											
Red Lock Phases											
Extend Phases				6							
XSwitch Phases											
Bike Call Phases											

No Disc

None

Mode

Delay 2 0.0

Fail Mode

Vehicle Detector 21

2/15/2019 7:56:42 AM

Delay	0.0	Extend 0.0	Carryove	r 0.0 Queue	Limit 0	
Mode	No Disc	Ad	ded Dis	sabled	System Disable	d
Fail Mode	None	Max Pres	0	No Act 0	Erratic 0	Fail Time 0
Delay 2	0.0					
Phases	1-8		9-16			
Call Phases		7				
Yellow Lock Phases				-		
Red Lock Phases				-		
Extend Phases		7				
XSwitch Phases						
Bike Call Phases				1		
				_		

Delay 0.0 Exte	end 0.0 Carryov	ver 0.0 Qu	eue Limit 0	
Mode No Disc	Added [Disabled	System	Disabled
Fail Mode None	Max Pres 0	No Act	0 Erratic	0 Fail Time 0
Delay 2 0.0				

Phases		1.	-8					9-	16		
Call Phases					8						
Yellow Lock Phases											
Red Lock Phases											
Extend Phases					8						
XSwitch Phases											
Bike Call Phases											

Vehicle Detector 23

2/15/2019 7:56:42 AM

Delay	0.0	Extend	0.0 Car	ryover 0.0	Queue Li	mit 0	
Mode	No Dis	С	Added	Disabled	S	ystem Disabl	ed
Fail Mode	None	Ма	x Pres 0	No Ac	t 0	Erratic 0	Fail Time 0
Delay 2	0.0						
Phases	1-8	3	9-16				
Call Phases		8					
Yellow Lock Phases							
Red Lock Phases							
Extend Phases		8					
XSwitch Phases							
Bike Call Phases							

Delay	0.0 Exter	nd 0.0	Carryo	over 0.0	Queue	Limit 0			
Mode	No Disc	Added	b	Disabled		System	Disable	ed	
Fail Mode	None	Max Pres	0	No Act	0	Erratic	0	Fail Time	0
Delay 2	0.0								

Phases		1-8					9-	16		
Call Phases				8						
Yellow Lock Phases										
Red Lock Phases										
Extend Phases				8						
XSwitch Phases										
Bike Call Phases										
	 				_					

Vehicle Detector 25

2/15/2019 7:56:42 AM

Delay	0.0 Exte	end 0.0 Carryov	ver 0.0 Queue Limit 0	
Mode	No Disc	Added D	Disabled System Enabled	
Fail Mode	None	Max Pres 0	No Act 0 Erratic 0 Fail Time 0	
Delay 2	0.0			
Phases	1-8	9-16		
Call Phases		8		
Yellow Lock Phases				
Red Lock Phases				
Extend Phases		8		
XSwitch Phases				
Bike Call Phases				

Delay	0.0 Exte	nd 0.0	Carry	over 0.0	Queue	Limit 0			
Mode	No Disc	Adde	d	Disabled		System	Disable	ed	
Fail Mode	None	Max Pres	0	No Act	0	Erratic	0	Fail Time	0
Delay 2	0.0								

Phases		1	-8				9-	16		
Call Phases					8					
Yellow Lock Phases										
Red Lock Phases										
Extend Phases					8					
XSwitch Phases										
Bike Call Phases										
	 								•	

			Pedestri	an Detec	ctor 2	2/	/15/2019 7:56:42 AM
No Act	0	Max Pres 0	Errati	c 0	Fail Mode	None	
Phases/Overlaps		1-8	 9-16		Ĺ		_
Call Ped Phases	2						
Call Ped Olaps							
Call Phases							
Locked Call Phases							
Ped Entry Phases							
Olap Ped Entry Phases							
Ped Cascade Phases							
Call Walk2							
			Pedestri	an Detec	ctor 4		
No Act	0	Max Pres 0			Fail Mode	None	7
Phases/Overlaps		1-8	 9-16		L		_
Call Ped Phases		4					
Call Ped Olaps							
Call Phases							
Locked Call Phases							
Ped Entry Phases							
Olap Ped Entry Phases							
Ped Cascade Phases							
Call Walk2							
No Act	0	Max Pres 0		an Detec	ctor 6 Fail Mode 〔	None	٦
	U				raii iviode [None	
Phases/Overlaps		1-8	9-16	7			
Call Ped Phases Call Ped Olaps		6					
Call Phases							
Locked Call Phases							
Ped Entry Phases							
Olap Ped Entry Phases							
Ped Cascade Phases				_			
Call Walk2							
-							
			Pedestri	an Detec			_
No Act	0	Max Pres 0) Errati	c 0	Fail Mode	None	
Phases/Overlaps		1-8	9-16				
Call Ped Phases		8					
Call Ped Olaps							
Call Phases							
Locked Call Phases							
Ped Entry Phases							
Olap Ped Entry Phases			+++++				
Ped Cascade Phases		_					

Call Walk2

Transit Detector 2	2/15/2019 7:56:42 AM
1.0 Extend 0.0 Travel Time 0 0.0 Warning Ext 6.0 Travel Slack 0 90 Checkout Mode Normal 0 Checkout Fail 0 CO Fail Cnt 0 No 1-8 2 1 1 1	Recall Lmt 0
1-8 9-16	
Transit Detector 6	
1.0 Extend 0.0 Travel Time 0 0.0 Warning Ext 6.0 Travel Slack 0 90 Checkout Mode Normal 0 Checkout Fail 0 CO Fail Cnt 0 No	Recall Lmt 0
1-8 	
	1.0

Adaptive Priority - General/Local Detectors

Local Detector Slack	0
Remote Detector Slack	0
Local Adjust Threshold	0
Remote Adjust Threshold	0

Detector	1	2	3	4	5	6	7	8
Step (Base)	0	0	0	0	0	0	0	0
Max (Base)	0	0	0	0	0	0	0	0
Step (Alt 1)	0	0	0	0	0	0	0	0
Max (Alt 1)	0	0	0	0	0	0	0	0
Step (Alt 2)	0	0	0	0	0	0	0	0
Max (Alt 2)	0	0	0	0	0	0	0	0
Step (Alt 3)	0	0	0	0	0	0	0	0
Max (Alt 3)	0	0	0	0	0	0	0	0

Estimated Delay

2/15/2019 7:56:42 AM

Transit
Disable
Rem Phs
Loc Int
Loc TT
RM1 Int

	1	2	3	4	5	6	7	8
	No							
ĺ	0	0	0	0	0	0	0	0
ĺ	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0

Remote Transit Detectors (Transit/LRV 6)

Remote Detector
Intersection Number
Detector Number
Travel Time
Mode
Max
Adjust
Travel Slack
Dwell

1	2	3	4
9	10	0	0
6	6	0	0
30	45	0	0
Raw Chkout	Raw Chkout	Raw Chkout	Presence
0	0	0	0
0	0	0	0
0	0	0	0
0	0	0	0

		Transit/LRV Startup/Options	2/15/2019 7:56:42 AM
No Startup Call	LRV 1-8	Warn Flash Rate 1 Hz	Rsrv Inh Mode Seconds
		Transit/LRV Phase 2	
Phases Parents No Call Queue Jmp Phs Min Green Adv Call	1-8 2 6 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	9-16 6.0 Red 6.0 Extend 0.0	
Call Mode	Locked	0 Extend 0.0 Warning Mode G+YCall	Adv Warning 20
Train Coming Queue Delay Fail Green	1-8 2 6 0.0 Queue TT	0 Queue Mode Serve Fail Warn Off	
		Transit Priority 2	
Phases Priority Phases Coord Veh Omit Coord Ped Omit Free Veh Omit Free Ped Omit Queue Clearance	1-8	9-16 Coord Pri Mode Coord Extend Free Pri Mode Free Recovery Mode Free Extend Free Hold Priority Adv Queue Clearance Coord Ext Pmt Free Ext Pmt Reserve Inhibit Same Reserve Inhibit All	Early/Extend 20 Early/Extend Normal 20 20 0 0 0 0 0 0
		Free Priority Timing	
Phases Min Priority Green Alt Sequence	1 2 3 4 5 8 12 8 12 8		
Rsv Extend			

Transit/LRV Phase 6

Phases Parents No Call Queue Jmp Phs	1-8 9-16
Min Green Adv Call Call Mode Train Coming Queue Delay Fail Green	5.0 Yellow 6.0 Red 6.0 0 Adv Green 0 Extend 0.0 Locked Warning Mode G+YCall Adv Warning 20 1-8 2 6 0 Serve 0 Queue TT 0 Queue Mode Serve 0 Fail Warn Off
	Transit Priority 6
Phases Priority Phases Coord Veh Omit Coord Ped Omit Free Veh Omit Free Ped Omit Queue Clearance	1-8 9-16 Coord Pri Mode Early/Extend Coord Extend 20 Free Pri Mode Early/Extend Free Recovery Mode Normal Free Extend 20 Free Hold 20 Priority 0 Adv Queue Clearance Coord Ext Pmt Free Ext Pmt Reserve Inhibit Same 0 Reserve Inhibit All 0
	Free Priority Timing
Phases Min Priority Green	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 8 12 8 12 8 12 0 0 0 0 0 0 0 0
Alt Sequence Rsv Extend	

0

Control / Config 2/15/2019 7:56:42 AM Pattern Mode Sched Manual Pattern Manual Offset 0 Stop Time Input Enabled Aux Switch StopTm 5 **DLS Mode** D4 Pac (UTC-8) Time Zone **GPS Thresh Password Timeout** 1-8 9-16 Maint Phs Recalls Maint Ped Recalls **Serial 1 Port Configuration** Broadcast Plan/Sync Disabled Broadcast Time 00:00 Serial Rebroadcast Disabled Response None Serial 2 Port Configuration Broadcast Plan/Sync Disabled Broadcast Time 00:00 **Ethernet Port Configuration** Disabled Broadcast Time 00:00 Broadcast Plan/Sync Serial Rebroadcast Disabled **Peer Configuration** Peer 1 5 Peer 2 6 Peer 3 7 Peer 4 40

Peer 5

Peer 6

Peer 7

Peer 8

9

10

219

11

		Restricted Data	2/15/2019 7:56:42 AM
		(Serial Ports)	
Serial Port 1	4		
Baud Rate	38400 8N1	RTS On 0	RTS Off 0
Serial Port 2	1		
Baud Rate	38400 8N1	RTS On 0	RTS Off 0
		(Ethernet)	
IP Address Netmask	10. 7. 111. 8 255. 255. 255. 0		
Broadcast Address	10. 7. 111. 255		
Gateway	10. 7. 111. 1		
Gateway 2	0. 0. 0. 0		
Gateway 3	0. 0. 0. 0		
Gateway 4	0. 0. 0. 0		
Admin IP Admin Netmask	192. 168. 2. 235 255. 255. 255. 0	Leases 0	
Port	161 Reply Mode	Host	
Broadcast Port	4141 Response	Time/Plan	
Time Port	0		
		(General)	
Controller Address	1 Timeout 0		
Peer Address	8 Timeout 10	0	
Remote Calls	Disabled		
Remote Preempt	Disabled		
Remote Soft Preempt	Disabled		
Remote Priority	Disabled		
Remote MCE	Disabled Mo	CE Max 0	