

ELC 2137 Lab #1: Git and LaTeX Intro

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Summary









In this lab we learned how to use LaTeX and GitHub. We created our GitHub account as well as a repository. We then created our lab report on LaTeX that includes images, code, lists, tables, and section headings.

Q&A

1. Sebastian-lopez1
2. Itemize enviroment
3. $y(t) = \frac{1}{2}e^t$
4. F5

Results

| Binary | Hex | Decimal |
|--------|-----|---------|
| 0000 | 0 | 0 |
| 0010 | 2 | 2 |
| 0100 | 4 | 4 |
| 0110 | 6 | 6 |
| 1000 | 8 | 8 |
| 1010 | A | 10 |

| Name | Value | 0 ns | 10 ns | 20 ns | 30 ns | 40 ns | 50 ns | 60 ns | 70 ns |
|------------------------------------------------------------------------------------------|-------|------|-------|-------|-------|-------|-------|-------|-------|
|  a2 | 0 | | | | | | | | |
|  a1 | 0 | | | | | | | | |
|  b2 | 0 | | | | | | | | |
|  b1 | 1 | | | | | | | | |
|  mode | 0 | | | | | | | | |
|  cout | 0 | | | | | | | | |
|  s2 | 0 | | | | | | | | |
|  s1 | 1 | | | | | | | | |

Code

Listing 1: File-included Verilog code example

```
module example
  #(parameter BITS=4)
  (
    input [BITS-1:0] in0, in1,
    input sel,
    output [BITS-1:0] out
  );

  // Choose in1 or in0
  out = sel ? in1: in0;
endmodule
```
