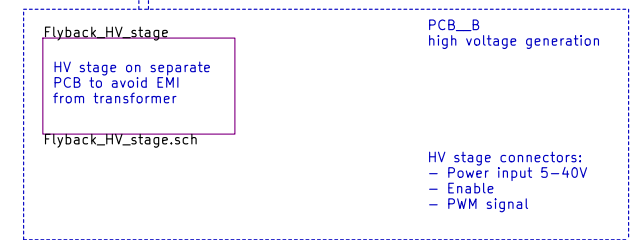
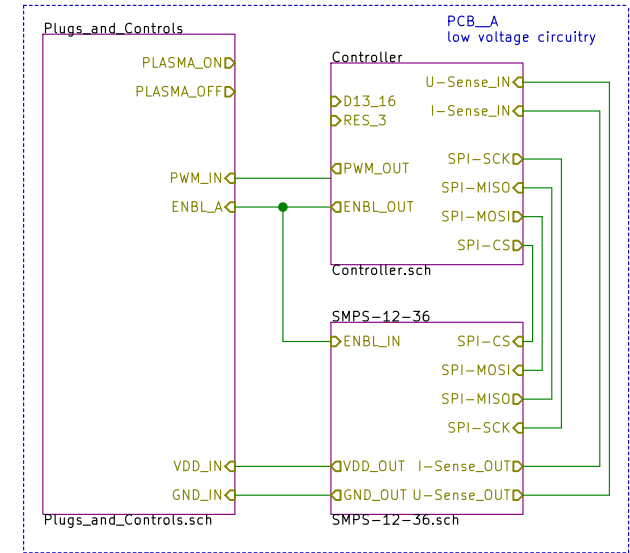
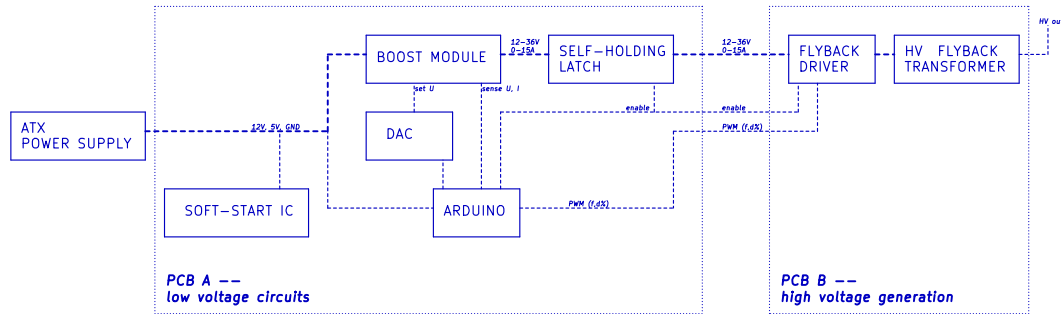


BLOCK DIAGRAM



TODO SELECT SMPS FETS
TODO SELECT SMPS SHUNT

NOTES

Boost regulator has internal Vref=1.2V. Therefore set DAC VoutB=1.2V.
DAC VoutA maps directly N=0...4095 to Uset=0...36V.
DAC CS via Arduino pin 8.

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Biotechnical faculty
University of Ljubljana



Sheet: /
File: Plasma-PSU-Eurocard-Flyback.sch

Title: Plasma-PSU-Eurocard-Flyback

Size: A4 Date: 2020-04-08

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Rev: 0.0.4

Id: 1/5

POWER CONNECTORS

J?

Moxia Mini-fit Jr. 39-28-1243

+3.3V 1 13 +3.3V
+3.3V 2 14 -12V
GND 3 15 GND
+5V 4 16 PWR_ON
GND 5 17 GND
+5V 6 18 GND
GND 7 19 GND
PWR_GOOD 8 20 GND
VSB 9 21 +5V
+12V 10 22 +5V
+12V 11 23 +5V
+3.3V 12 24 GND

Moxia 5566 24-pin ATX power connector (max 75W to SMPS)

J?

Moxia Mini-fit Jr. 39-28-1083

+12V 1 5 GND
+12V 2 6 Sense0A
+12V 3 7 GND
Sense1A 4 8 GND

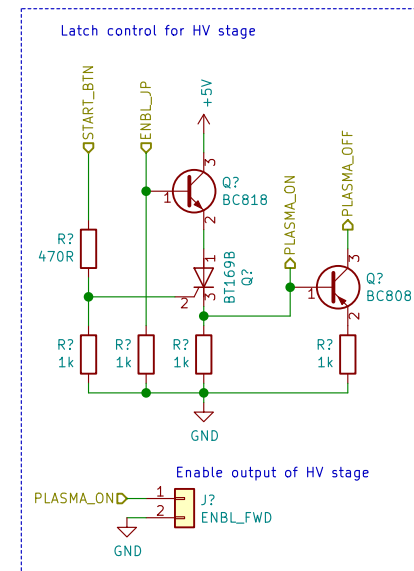
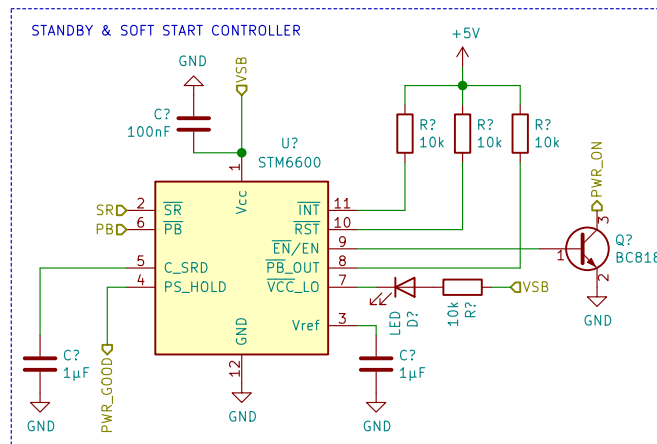
Additional PCIe 8-pin power connector for additional 150W (75W if 6-pin connected)

J?

Moxia Mini-fit Jr. 39-28-1083

+12V 1 5 GND
+12V 2 6 Sense0B
+12V 3 7 GND
Sense1B 4 8 GND

Additional PCIe 8-pin power connector for additional 150W (75W if 6-pin connected)



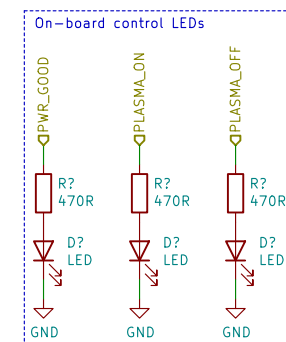
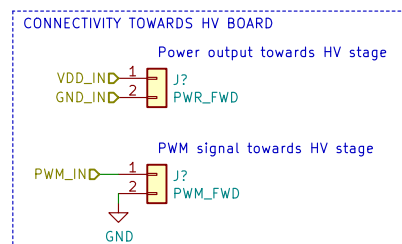
PINHEADER CONNECTIVITY

J?

Pinheader Controls

Pinheader configuration:

- 1-2 Power button
- 3-4 Reset button
- 5-6 Start button
- 7-8 Stop button
- 9-10 Interrupt jumper
- 11-12 LED Power on
- 13-14 LED Plasma on
- 15-16 LED Plasma off



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Sheet: /Plugs_and_Controls/
File: Plugs_and_Controls.sch

Title: Plasma-PSU_Eurocard_Flyback

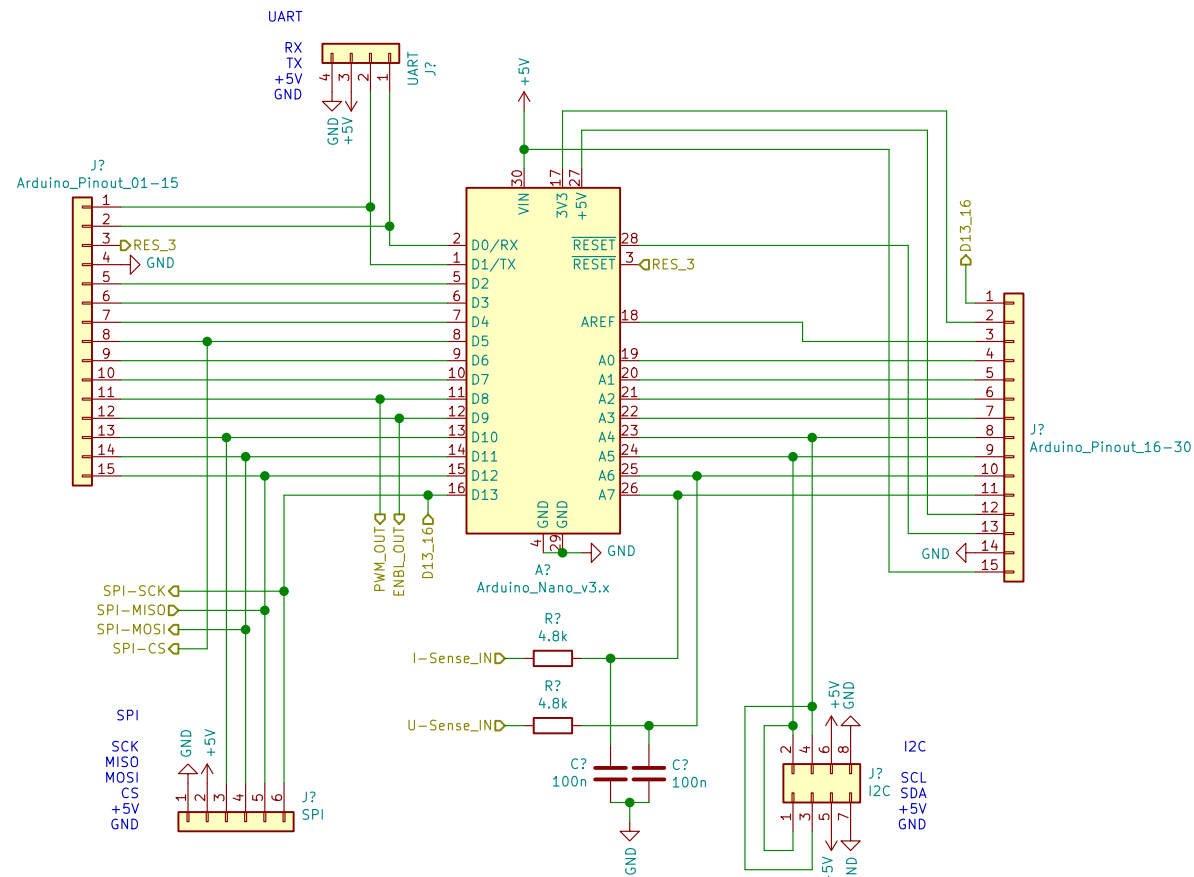
Size: A4	Date: 2020-04-08
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Size: A4	Date: 2020-04
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Configuration:
 PWM via timer1 on D8 / pin11)
 ENBL signal on D9 / pin 12
 U-CTRL via SPI with CS on pin 8
 U sensing on A6 / pin 25
 I sensing on A7 / pin 26
 UART on D0 & D1 / pin 2 & 1
 I2C signal on A4 & A5 / pin 23 & 24



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Sheet: /Controller/
 File: Controller.sch

Title: Plasma-PSU_Eurocard_Flyback

Size: A4 Date: 2020-04-08

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