

Control

The CPU architecture diagram shows a yellow 'Control' block at the top left, an orange 'Cache' block below it, and a wide orange 'DRAM' block at the bottom. To the right of the 'Control' block is a 2x2 grid of green 'ALU' blocks.

ALU

ALU

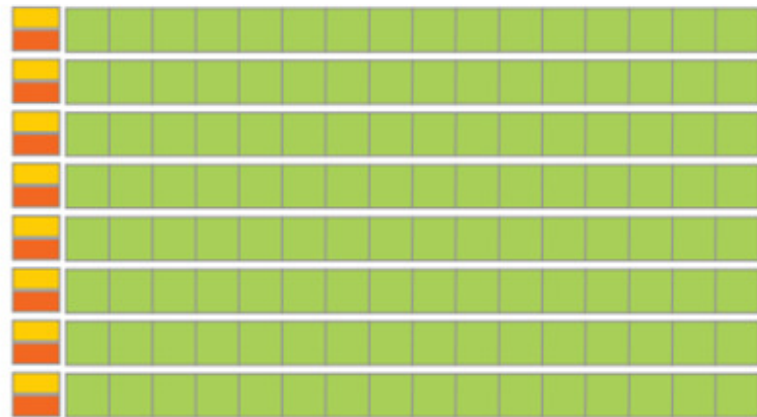
ALU

ALU

Cache

DRAM

CPU



DRAM

GPU