

# SMD 230°C High Temperature Tantalum Capacitor in Hermetic Package



### **FEATURES**

- High temperature applications
- Operational condition 230°C / 0.5U<sub>R</sub> / 1000hrs or 200°C / 0.5U<sub>R</sub> / 10.000hrs
- · Ceramic case hermetic packaging
- Large case sizes including CTC-21D provide high capacitance values
- Manufacturing and screening utilizing AVX patented Q-Process to effectively remove components that may experience excessive parametric shifts or instability in operation life

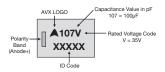
### **APPLICATIONS**

- Oil drilling
- Extreme temperature applications

For additional information on Q-process please consult the AVX technical publication "Reaching the Highest Reliability for Tantalum Capacitors" (see the link: http://www.avx.com/docs/techinfo/Qprocess.pdf)

### **MARKING**

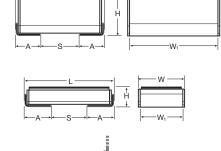
9, I CASE



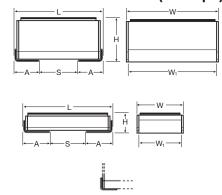
### CASE DIMENSIONS: millimeters (inches)

Code	Туре	L±0.50 (0.020)	W±0.50 (0.020)	H Max.	W <sub>1</sub> ±0.50 (0.020)	A±0.50 (0.020)	S Min.
9 (CTC-21D)	J-lead	11.50	12.50	6.15	12.50	1.90	7.00
	(L-shape)	(0.453)	(0.492)	(0.242)	(0.492)	(0.075)	(0.276)
9 (CTC-21D)	J-lead	12.10	12.50	6.50	12.00	2.00	7.20
	(flex)	(0.476)	(0.492)	(0.256)	(0.472)	(0.079)	(0.283)
9 (CTC-21D)	Undertab	11.00 ± 0.20 (0.433 ± 0.008)	12.50 ± 0.20 (0.492 ± 0.008)	5.95 (0.234)	10.50 ± 0.20 (0.413 ± 0.008)	1.50 ± 0.20 (0.059 ± 0.008)	7.80 (0.307)
I	J-lead	11.50	6.00	2.70	6.00	3.50	4.00
	(L-shape)	(0.453)	(0.236)	(0.106)	(0.236)	(0.138)	(0.157)
I	J-lead	11.90	6.00	3.00	5.50	3.60	4.20
	(flex)	(0.469)	(0.236)	(0.118)	(0.217)	(0.142)	(0.165)
I	Undertab	11.00 ± 0.20 (0.433 ± 0.008)	6.00 ± 0.20 (0.236 ± 0.008)	2.50 (0.098)	4.00 ± 0.20 (0.157 ± 0.008)	3.20 ± 0.20 (0.126 ± 0.008)	4.40 (0.173)

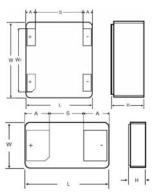
#### 'J' Lead Termination (flex)



#### 'J' Lead Termination (L-shape)



#### **Undertab Termination**





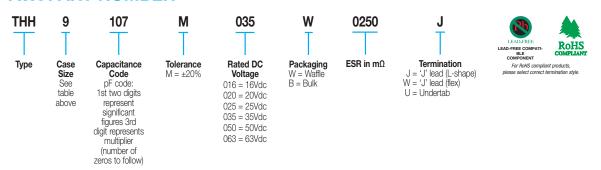
# SMD 230°C High Temperature Tantalum Capacitor in Hermetic Package

### **TECHNICAL SPECIFICATIONS**

Technical Data:	All technical data relate to an ambient temperature of +25°C									
Capacitance Range:	(	3.8 μF to	100 μF (f	or extend	led range	under de	evelopmei	nt, contact manufacturer)		
Capacitance Tolerance:	=	±20%								
Leakage Current DCL:	(	0.01CV								
Rated Voltage (V <sub>R</sub> )	≤ +85°C:	16	20	25	35	50	63			
Category Voltage (V <sub>C</sub> )	≤ +230°C:	8	10	12	17	25	31			
Temperature Range:	-	-55°C to	+230°C							
Reliability:		1% per 1	000 hours	s at 85°C	, Vr with	0.1Ω/V se	eries impe	dance, 60% confidence level		
Termination Finish:	(	Gold Plat	ing (Unde	rtab), Gol	d Plating	(J-lead L	shape), N	Nickel Plating (J-lead flex)		

### **HOW TO ORDER**

### **AVX PART NUMBER**



### CAPACITANCE AND VOLTAGE RANGE (CODE DENOTES THE CASE SIZE)

Capa	citance	Rated Voltage DC (V <sub>R</sub> ) at 175°C									
μF	Code	16V (C)	20V (D)	25V (E)	35V (V)	50V (T)	63V (J)				
3.3	335					*	*				
4.7	475					l*	*				
6.8	685				I	I					
10	106										
15	156		*	*	*						
22	226	I	*	*	*						
33	336	*	*	*			9*				
47	476		*			9*	9				
68	686					9*					
100	107			9*	9						
150	157		9*	9*	9*						
220	227	9*	9*								
330	337	9*									

#### Available Ratings

Engineering samples - please contact manufacturer

<sup>\*</sup>Codes under development – upon request, please contact manufacturer



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### **RATINGS & PART NUMBER REFERENCE**

AVX	Case Size	Capacitance	Rated	Rated Temperature (°C)	Category Voltage (V)	DCL Max. (μΑ)	DF Max. (%)	ESR Max. @ 100kHz (mΩ)	MSL	100kHz RMS Current (A)			
Part No.		΄ (μ <b>F</b> )	Voltage (V)							25°C	85°C	230°C	
16 Volt @ 85°C													
THHI226M016W0500#		22	16	175	8	3.6	8	500	1	0.81	0.73	0.73	
THHI476M016W0500#		47	16	175	8	7.5	8	500	1	0.81	0.73	0.73	
	35 Volt @ 85°C												
THHI685M035W0500#		6.8	35	175	17	2.4	8	500	1	0.81	0.73	0.73	
THHI106M035W0500#		10	35	175	17	3.5	8	500	1	0.81	0.73	0.73	
THH9107M035W0250#	9	100	35	175	17	35	8	250	1	1.26	1.13	1.13	
	50 Volt @ 85°C												
THHI685M050W0500#		6.8	50	175	25	3.4	8	500	1	0.81	0.73	0.73	
	63 Volt @ 85°C												
THH9476M063W0250#	9	47	63	175	31	29.6	8	250	1	1.26	1.13	1.13	

All technical data relates to an ambient temperature of +25°C. Capacitance and DF are measured at 120Hz, 0.5V RMS with a maximum DC bias of 2.2 volts.

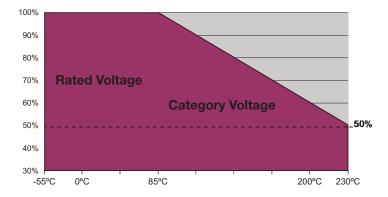
DCL is measured at rated voltage after 5 minutes.

ESR change post 1000hrs allowed up to 3 times catalog limit.

Moisture Sensitivity Level (MSL) is defined according to J-STD-020.

### **TEMPERATURE VOLTAGE DERATING**

THH 230°C Voltage vs Temperature Rating for 1000 hrs service life





### SMD 230°C High Temperature Tantalum Capacitor in Hermetic Package

### **QUALIFICATION TABLE**

TEST	THH 230°C hermetic series (Temperature range -55°C to +230°C)												
ILSI		Condition				С	haract	eristic	S				
		Visual examination  no visible damage											
	Determine category v	DCL			1.25 x initial limit								
Endurance	leaving mi	ΔC/C		,	within ±20%	6 of initia	al value						
	supply imp	DF			1.5 x initial	limit							
			ESR		;	3 x initial lir	nit						
		Visual ex	aminatio	n i	no visible d	amage							
		Determine after application of 0.5U <sub>R</sub> for 10000+48/-0 hours at 200°C temperature and					1.25 x initia	l limit					
Endurance	then leaving	ΔC/C		,	within ±20%	6 of initia	al value						
	Power sup	DF			1.5 x initial	limit							
			ESR		;	3 x initial lir	nit						
			Visual ex	aminatio	n i	no visible damage							
		DCL		i	initial limit								
Storage Life	2	ΔC/C		,	within ±5% of initial value								
			DF	DF			initial limit						
			ESR	1.25 x initial limit									
						Visual examination  no visible damage							
Biased	Determine	DCL		i	initial limit								
Humidity	85% relati	ive humidity and rated vo	ΔC/C			within ±10% of initial value							
Tullilally	recovery n	nin. 2 hours at room temp	DF	DF initial limit									
			ESR	ESR 1.25 x initial limit									
	Step	Temperature°C	Duration (min)		+20°C	-55°	C +22°C	+85°C	+125°C	+175°C	+200°C	+230°C	+22°C
	1 2	+22 -55	15 15	- DCL	IL*	n/a				n/a	n/a	n/a	IL*
Temperature	3 4	+22 +85	15 15		IL"	n/a	IL"	10 x IL*	12.5 x IL*		n/a	n/a	IL"
Stability	5	+05 +125	15	ΔC/C	n/a	+0/-20	0% ±5%	+20/-0%	+30/-0%	+30/-0%	+30/-0%	+30/-0%	±5%
	6	+175 +200	15 15	- DF	IL*	1.5 x	IL* IL*	1.5 x IL*	2 x IL*	2 x IL*	2 x IL*	2 x IL*	IL*
	8	+230	15										
	9	+22	15	ESR	1.25 x IL*	1.25 x	IL* 1.25 x IL*	1.25 x IL*	1.25 x IL*	1.25 x IL*	1.25 x IL*	1.25 x IL*	1.25 x IL
	Test tempe	Visual examination			no visible damage								
	Surge volta				ŭ								
Surge	Series pro	DCL			initial limit								
Voltage	Discharge Number of	ΔC/C			within ±20% of initial value								
	Cycle dura	DF		i	initial limit								
	5 min 30 sec discharge					-	1.25 x initial limit						

\*Initial Limit