Take Data&Delayed

Take Data&Dalayed

Valid Word

Valid Word

EXP\_EMIFA\_D12

[GPIO0\_12]

EXP\_EMIFA\_D13

[GPIO0\_13]

Ser.Data Out

Ser.Data Out

UART

RX/TX/GND

EXP\_EMIFA\_D9

[GPIO0\_9]

EXP\_EMIFA\_D11

[GPIO0\_11]

SOMI

SCS

CLK

SIMO

NC

EXP\_EMIFA\_D14

[GPIO0\_14]

EXP\_EMIFA\_D15

[GPIO0\_15]

EXP\_EMIFA\_D10

[GPIO0\_10]

EXP\_EMIFA\_D8

[GPIO0\_8]

Manchester1

Decoder

AND

Dec. Shift CLK

Dec. Shift CLK

AND

Dec. CLK

APWM2-T\_ACLKR1

2.4MHz

2.4MHz

ENC CLK

APWM2-T\_ACLKR1

ENC CLK

OMAPL

S

Manchester1

Encoder

ENC ENA

Ser. Data In

Enc Shift-CLK

Send Data

Manchester2

Decoder

Manchester2

Encoder

SPI1

OMAPL