100 ←

1100 ← −4 → 4 bits
→ 1er bit = 1

01100 → 12 − 5 bits
− 1er bit = 0

6-bits:

−10 : 101010
10 : 001010

0: 000000
−0: 100000

complemento de 1 (4-bits)

0000 → 1111

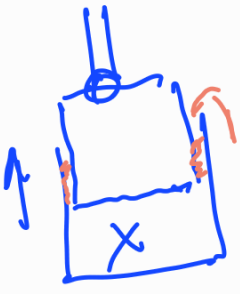
2 representaciones para 0.

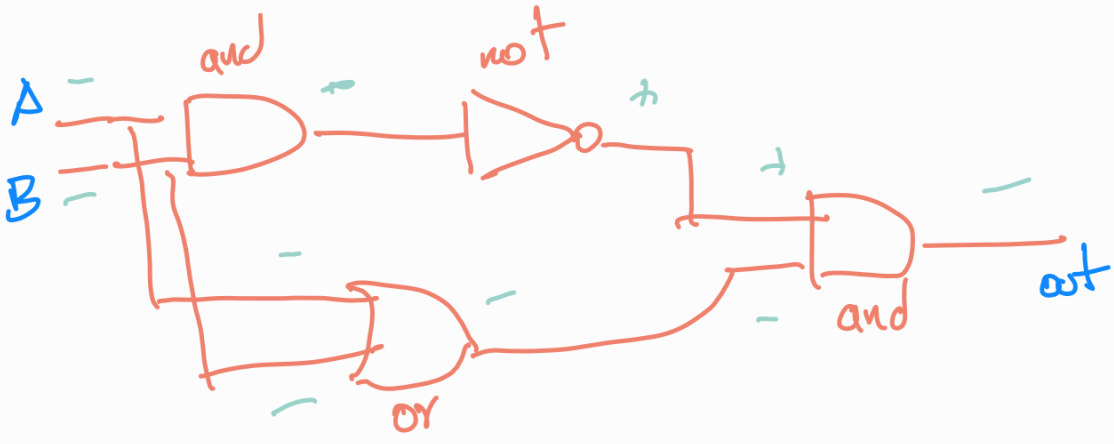
complemento de 2 (6bits)

001010 (+10) —comp 1→ 110101 + 1 = 110110 —+1→ 110110 (−10)

```
  1011 1
  001010
+ 110110
--------
 1000000
```

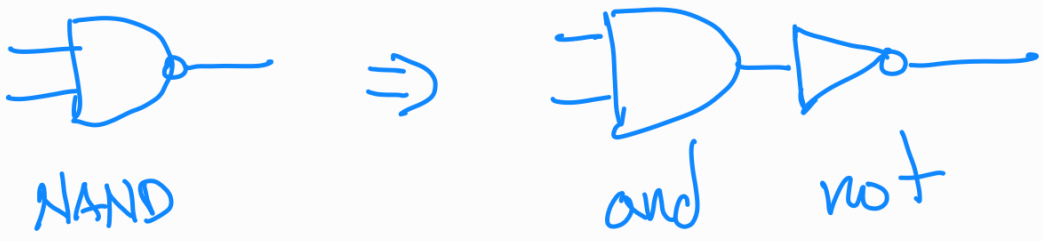
(6 bits)

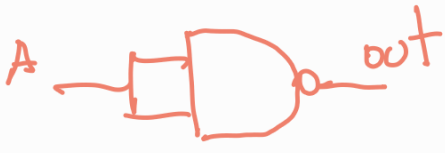




XOR.

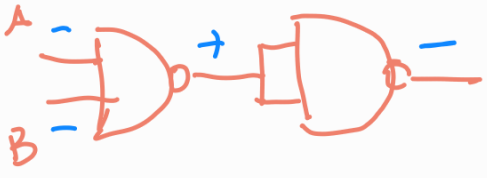
A	B	out
+	+	−
+	−	+
−	+	+
−	−	−





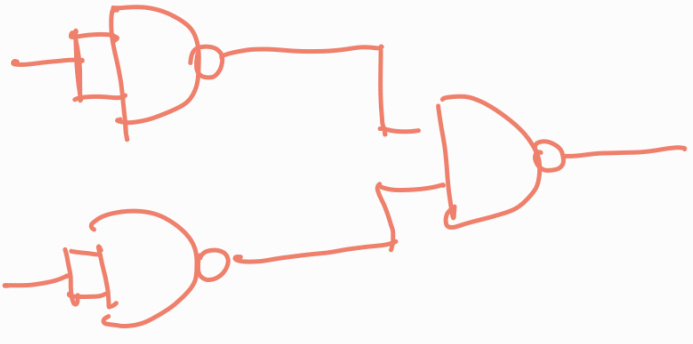
A	out
+	−
−	+

NOT



A	B	out
+	+	+
+	−	−
−	+	−
−	−	−

AND.



A	B	out
+	+	+
+	−	+
−	+	+
−	−	−

OR

NOR