

A

B

C

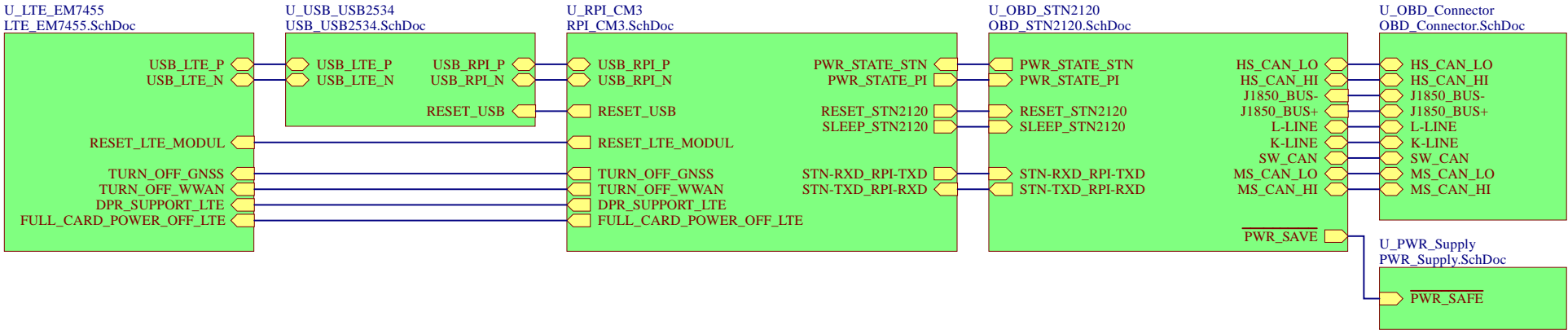
D

A

B

C

D



FD1
Fiducial Marker 1.0mm

FD2
Fiducial Marker 1.0mm

FD3
Fiducial Marker 1.0mm

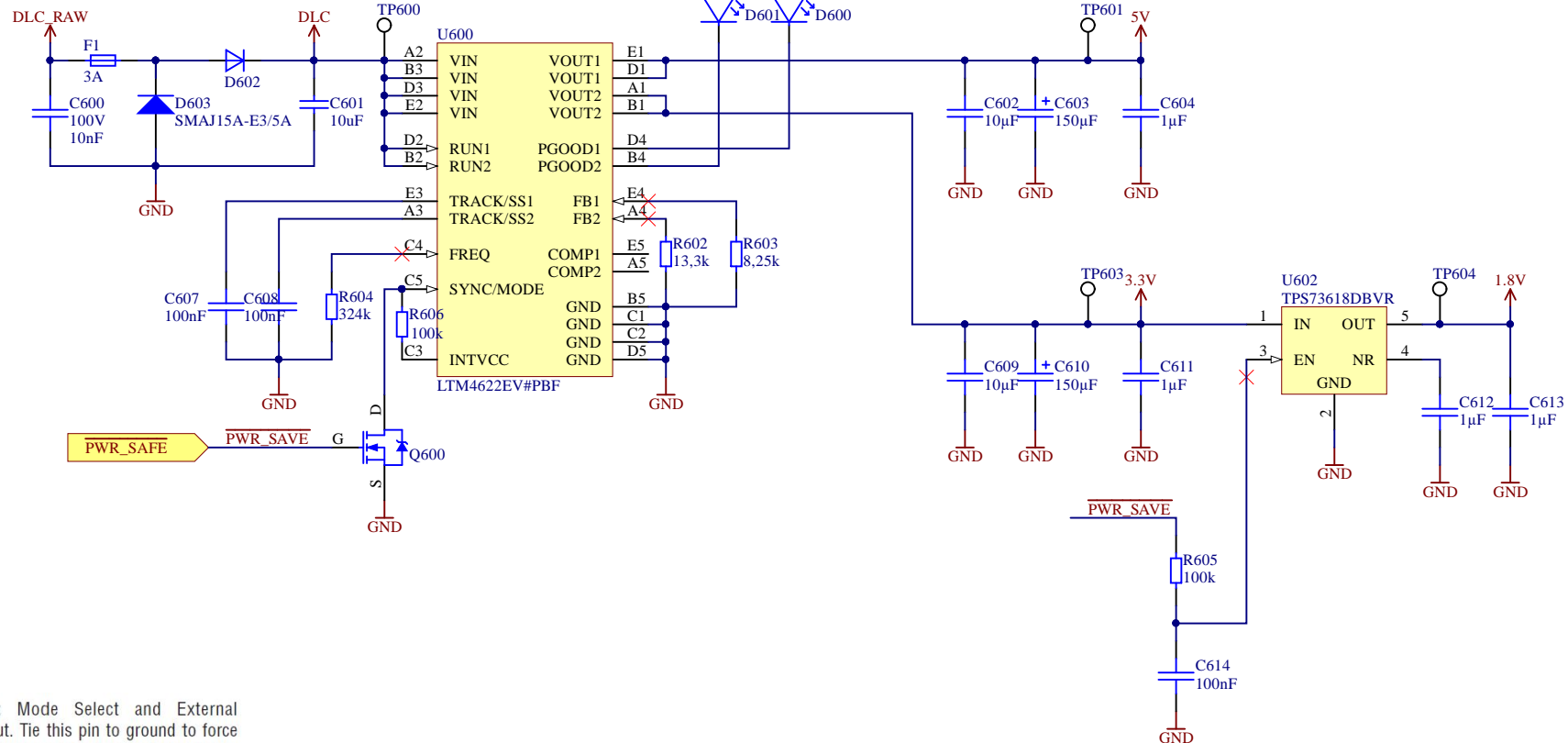
FD4
Fiducial Marker 1.0mm

FD5
Fiducial Marker 1.0mm


FD6
Fiducial Marker 1.0mm

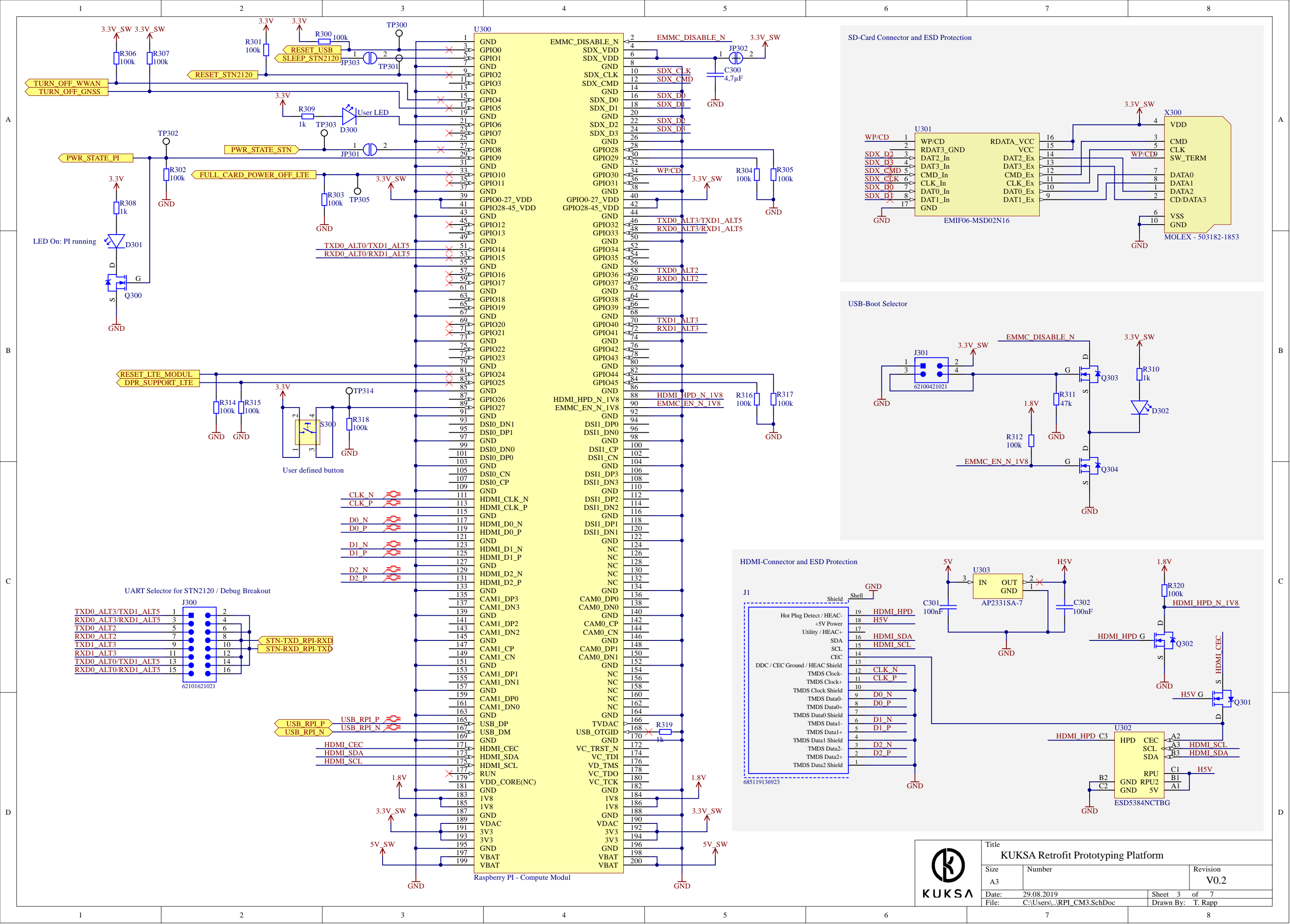


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SYNC/MODE (C5): Mode Select and External Synchronization Input. Tie this pin to ground to force continuous synchronous operation at all output loads. Floating this pin or tying it to INTV_{CC} enables high efficiency Burst Mode operation at light loads. Drive this pin with a clock to synchronize the LTM4622 switching frequency. An internal phase-locked loop will force the bottom power NMOS's turn on signal to be synchronized with the rising edge of the clock signal. When this pin is driven with a clock, forced continuous mode is automatically selected.

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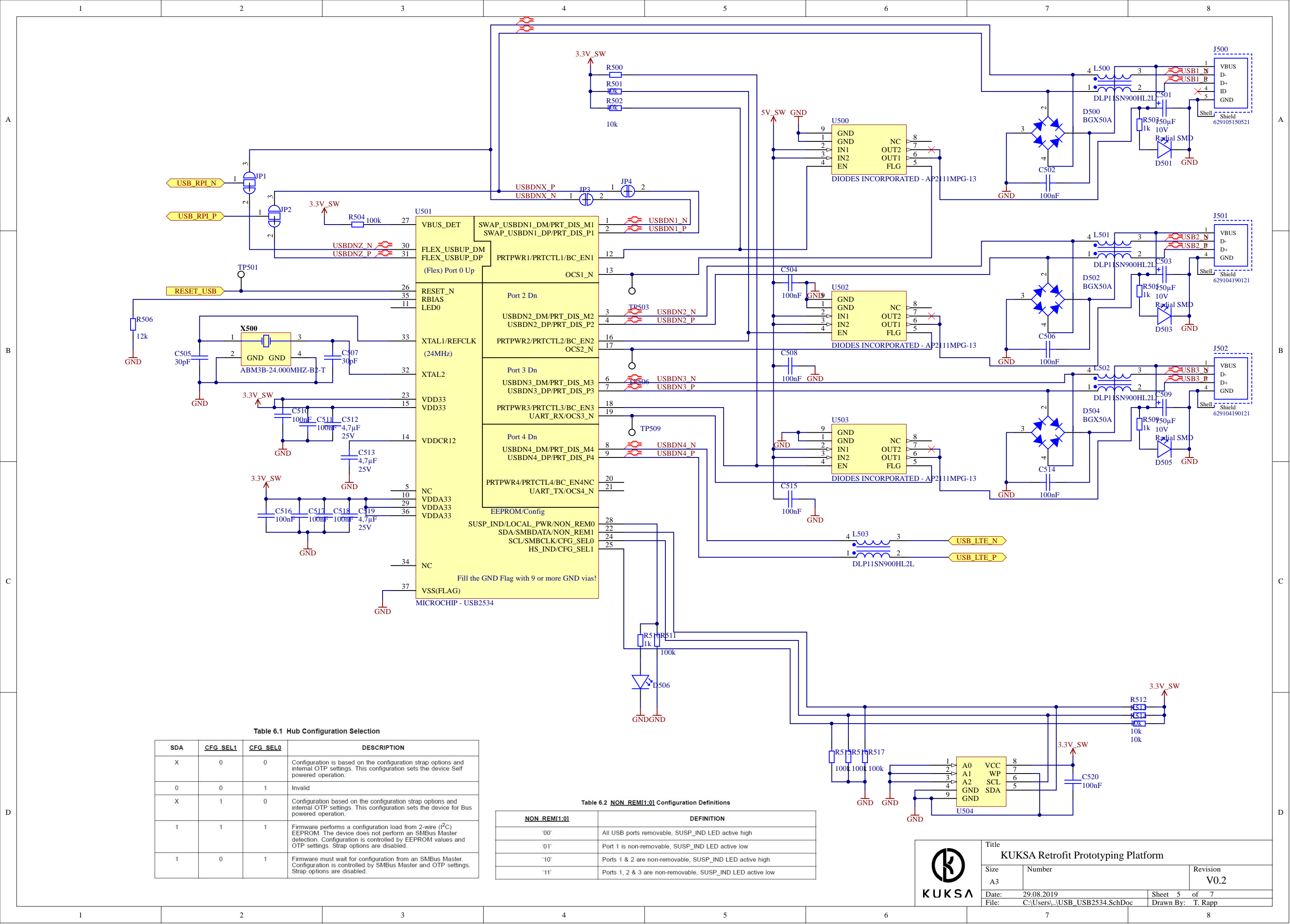


Table 6.1 Hub Configuration Selection

SDA	CFG_SEL1	CFG_SEL0	DESCRIPTION
X	0	0	Configuration is based on the configuration strap options and internal OTP settings. This configuration sets the device Self powered operation.
0	0	1	Invalid
X	1	0	Configuration based on the configuration strap options and internal OTP settings. This configuration sets the device for Bus powered operation.
1	1	1	Firmware performs a configuration load from 2-wire (I ² C) EEPROM. The device does not perform an SMBus Master detection. Configuration is controlled by EEPROM values and OTP settings. Strap options are disabled.
1	0	1	Firmware must wait for configuration from an SMBus Master. Configuration is controlled by SMBus Master and OTP settings. Strap options are disabled.

Table 6.2 NON_REM[1:0] Configuration Definitions

NON_REM[1:0]	DEFINITION
'00'	All USB ports removable, SUSP_IND LED active high
'01'	Port 1 is non-removable, SUSP_IND LED active low
'10'	Ports 1 & 2 are non-removable, SUSP_IND LED active high
'11'	Ports 1, 2 & 3 are non-removable, SUSP_IND LED active low



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