

Predicted: backdoor, Actual: backdoor

-10

-5

0

5

10



DTLBLoadMissWD

ICache_Misses

uop_p1

M_Ld_LLCH.XS_N

L1D_P_Miss_Oc

DTLBStoreMissW

M_Ld_Ret_L3Hit

DTLBStrMiss_WD

ITLBMissW

M_Ld_LLCH.XS_M

M_Ld_Ret_L1Hit

BrMispRetd_All

BrMispExec_Any

Ref_cyc

DTLBStrMiss_WC

L1D_Miss

Core_cyc

uop_p2

CPU_CLK_UNH_RF

IDQ_AllMite_UO

-10

-5

0

5

10

Model output value

