Short Quiz on 8086 Hardware Specifications

- Due Oct 15 at 9:30pm
- Points 22
- Questions 22
- Available Oct 15 at 9pm Oct 15 at 9:30pm 30 minutes
- Time Limit 30 Minutes

Instructions

Choose the correct answer.

Attempt History

	Attempt	Time	Score
LATEST	Attempt 1	19 minutes	22 out of 22

(!) Correct answers are hidden.

Score for this quiz: 22 out of 22 Submitted Oct 15 at 9:18pm This attempt took 19 minutes.

Question 1

1 / 1 pts

Which of the following devices do the DT/R and DEN control?

- Data Buffer
- Address Bus
- Data Bus
- Address Latch

Question 2

1 / 1 pts

Which operation mode of the 8086 uses the ALE pin to latch the address?

- Minimum Mode
- Maximum Mode

Question 3

1 / 1 pts

When to write data to the data bus during a 16-bit write operation?

address written at the address bus, BHE = 0 and WR = 0 address written at the address bus, BHE = 1 and WR = 1 address written at the address bus, BHE = 0 and WR = 1 address written at the address bus, BHE = 0 and WR = 1 Question 4 1 / 1 pts Which of the following represents the data bus of the 8086? \bigcirc AD₈ - AD₀ \bigcirc AD₁₅ - AD₀ \bigcirc AD₂₀ - AD₀ \bigcirc AD₁₉ - AD₀ Question 5 1 / 1 pts How many pins does the 8086 IC have? 0 18 24 **44** 9 40 Question 6 1 / 1 pts Which of the following pins of the 8086 are considered as control signals? M/IO **DEN** DT/R ALE Question 7 1 / 1 pts Which of the following pins of the 8086 indicates the availability of a valid address as well as enabling the address latch? HOLD READY

ALE

BHE

https://usc.instructure.com/courses/44172/quizzes/226096

M/IO RD

Question 18

1 / 1 pts

Which of the following pins of the 8086 indicates a data transfer via the high byte of the data bus D_{15} - D_8 ?



BLE

HLDA

ALE

Question 19

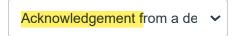
1 / 1 pts

Match the pins on the left to its description to the right.

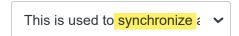
CLK



READY



TEST



RESET



Question 20

1 / 1 pts

Which of the following pins of the 8086 determines the direction of the data bus either input or output?

HOLD



O DEN

HOLDA

Question 21

1 / 1 pts

True or False: The clock frequency output of the 8284 clock generator is 1/3 of the oscillator frequency.

True

False

Question 22

1 / 1 pts

Why is there a need to latch the address bus?

None of the choices.

To retail the data when the CPU writes an address to the address bus.

To prevent collision between address and data.

To retain the address while performing a read/write operation.

Quiz Score: 22 out of 22