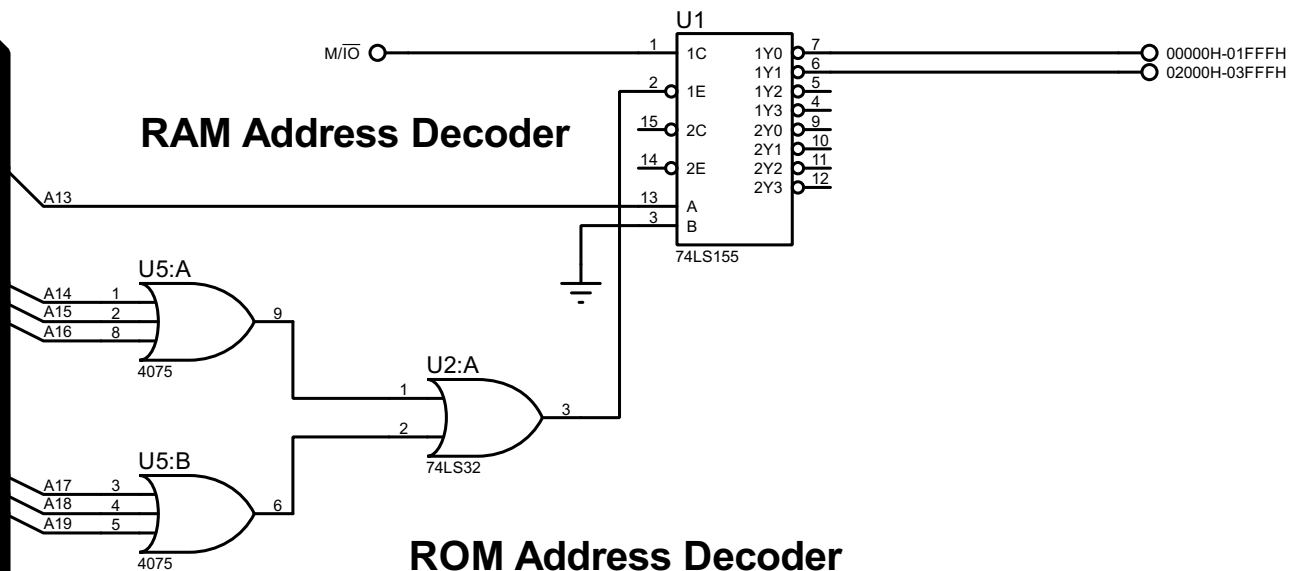
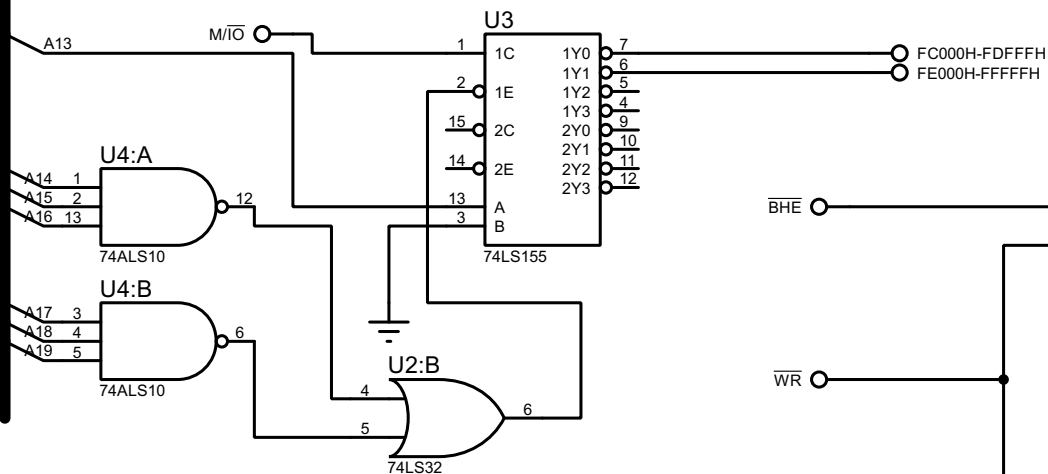


A[0-19]

RAM Address Decoder



ROM Address Decoder



Bank Select Circuit

