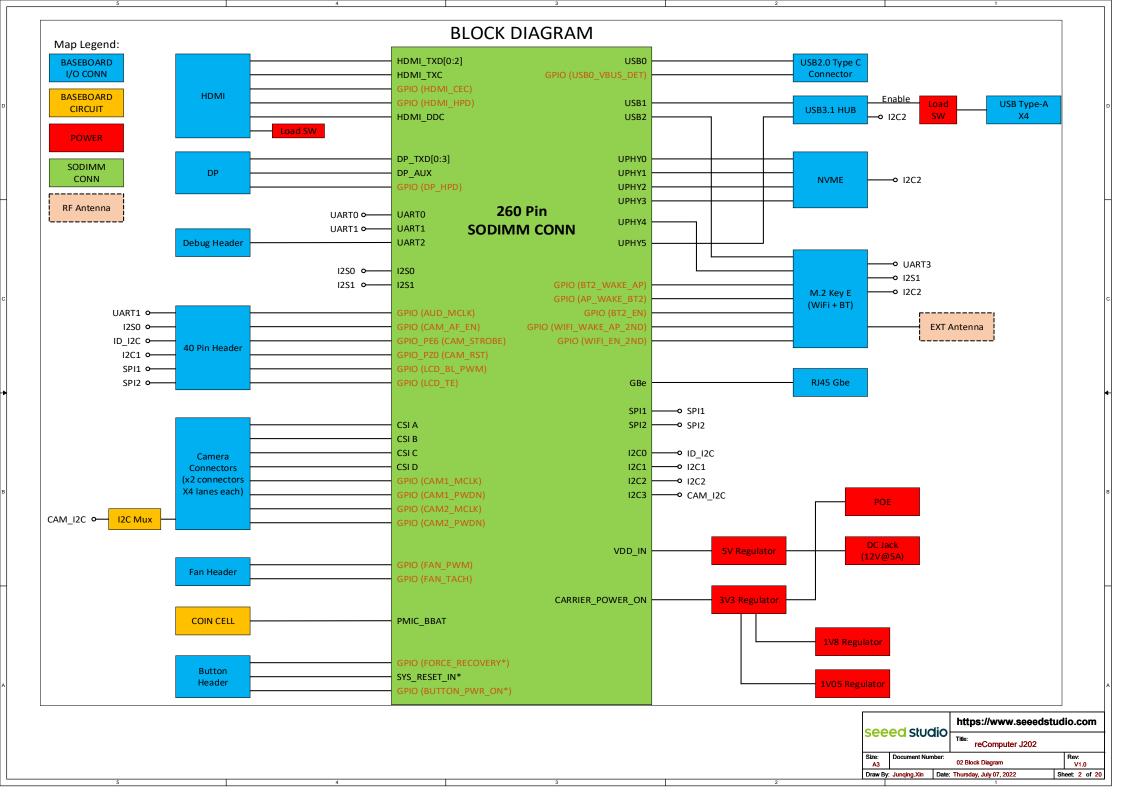
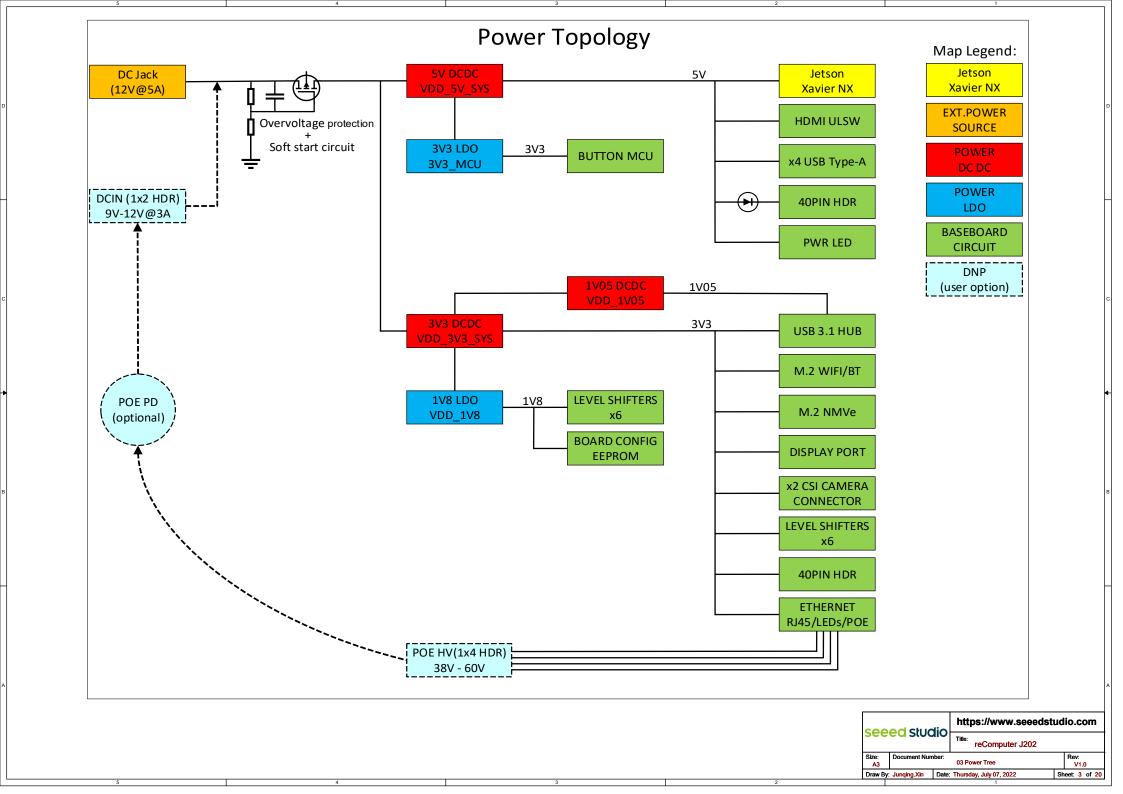
# Schematic: reComputer J202

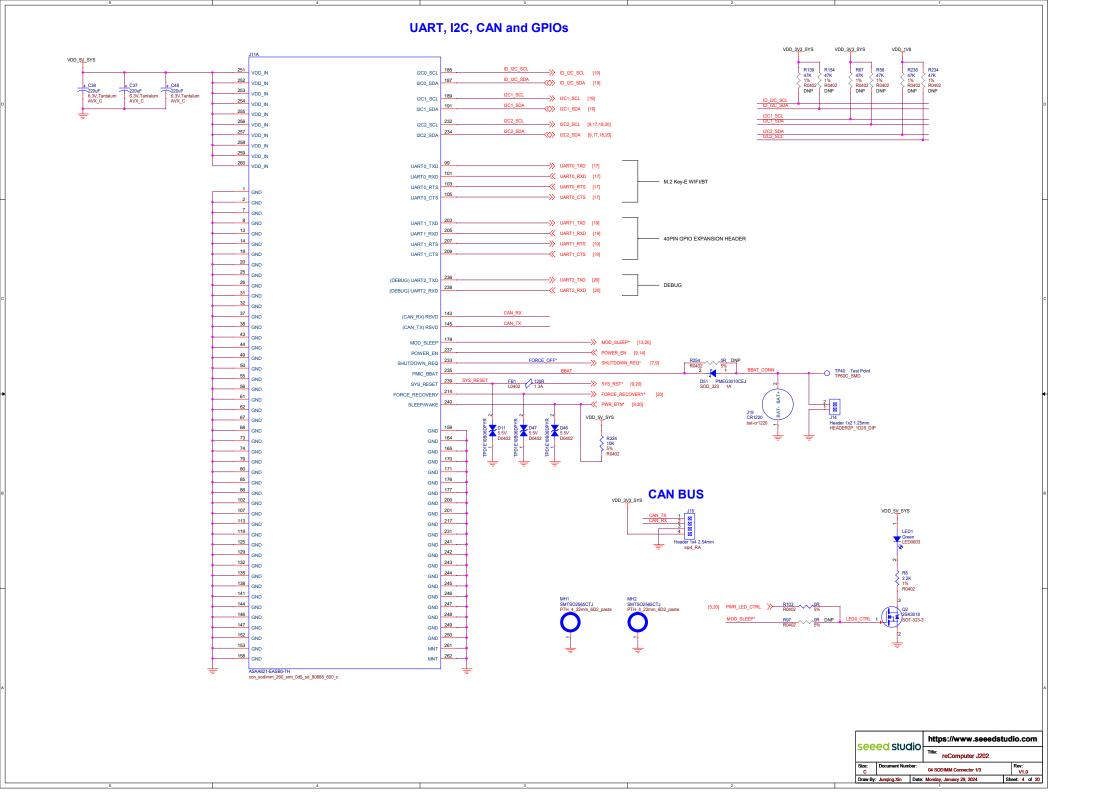
SHEET	SHEET NAME
01	Table of Contents
02	Block Diagram
03	Power Tree
04	SODIMM Connector 1/3
05	SODIMM Connector 2/3
06	SODIMM Connector 3/3
07	Power In
08	1V8 LDO & 3V3 AO Rails
09	Button MCU For Power Up
10	USB3.1 HUB
11	USB3.1 Type-A x2 (A)
12	USB3.1 Type-A x2 (B)
13	HDMI
14	Display Port
15	CAM Connector
16	Gigabit Ethernet, Type C
17	M.2 KEY-E (WIFI/BT)
18	M.2 KEY-M (NVME)
19	40Pin GPIO Expansion Header
20	Fan, Debug and EEPROM

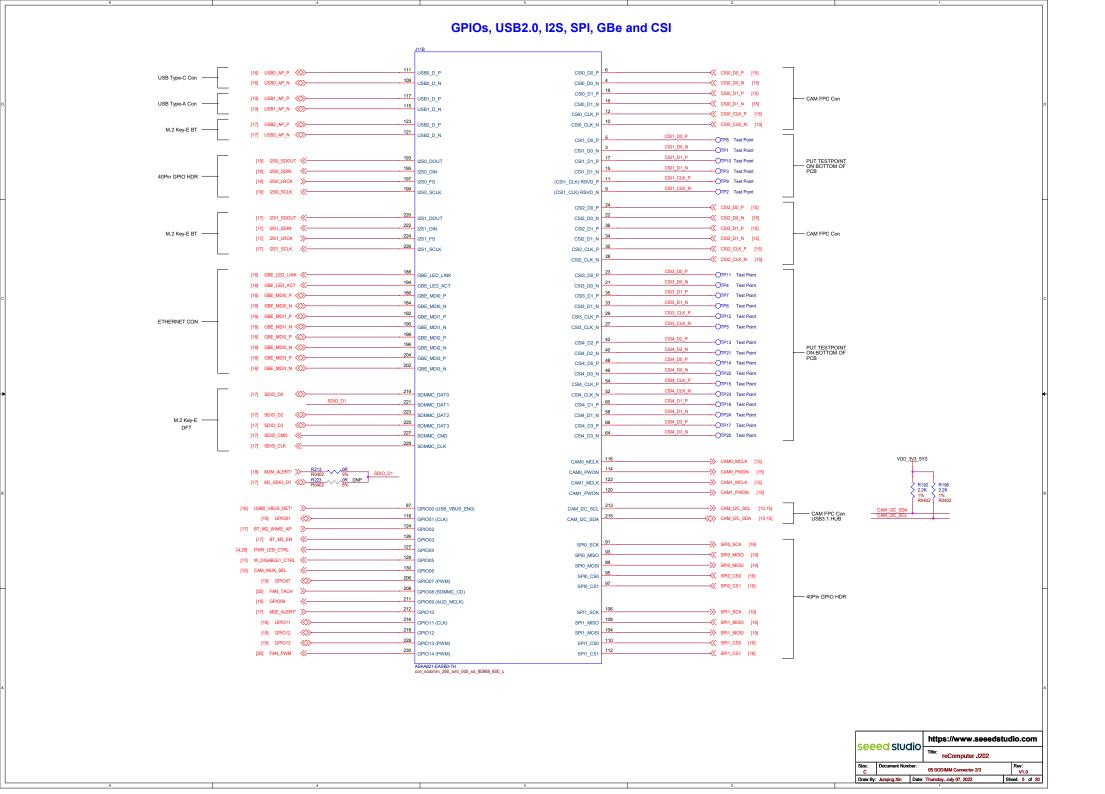
## Revision History

VER	DATE	REVISION	DESCRIPTION
V1.0	06/09/2022	reComputer J202_V1.0_SCH_220609	Initial Release.
V1.0	10/20/2023	reComputer J202_V1.0_SCH_231020	Page 09:Changed R185 from 499R to 0R; Changed X1 12MHz from CL=18pF to CL=10pF; Changed C147,C157 from 27pF to 12pF.
V1.0	01/29/2024	reComputer J202_V1.0_SCH_240129	Page 04:Changed C37,C38,C48 from 330uF/6.3V to 220uF/6.3V

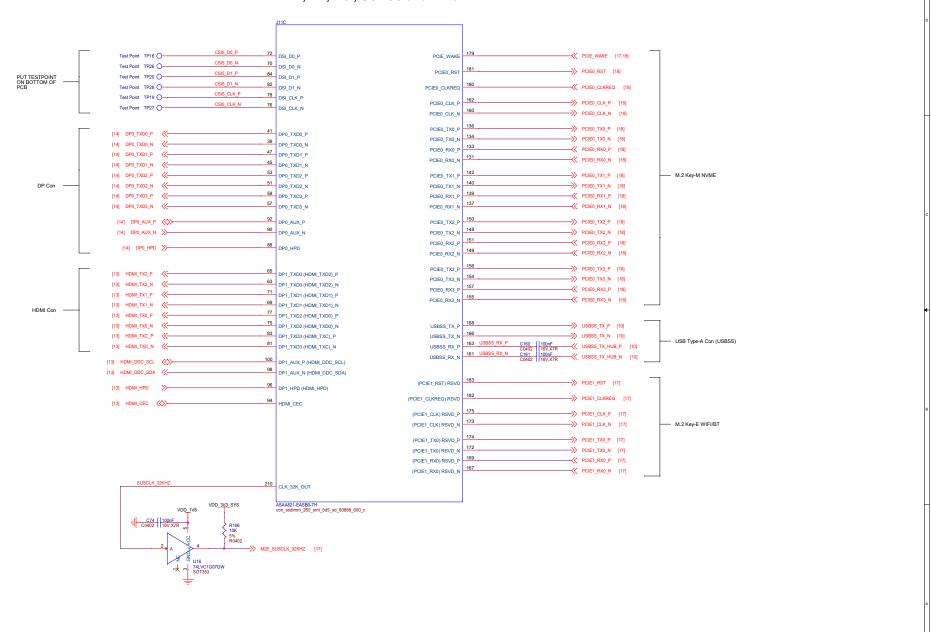








#### HDMI, DP, DSI, USBSS and PCIe



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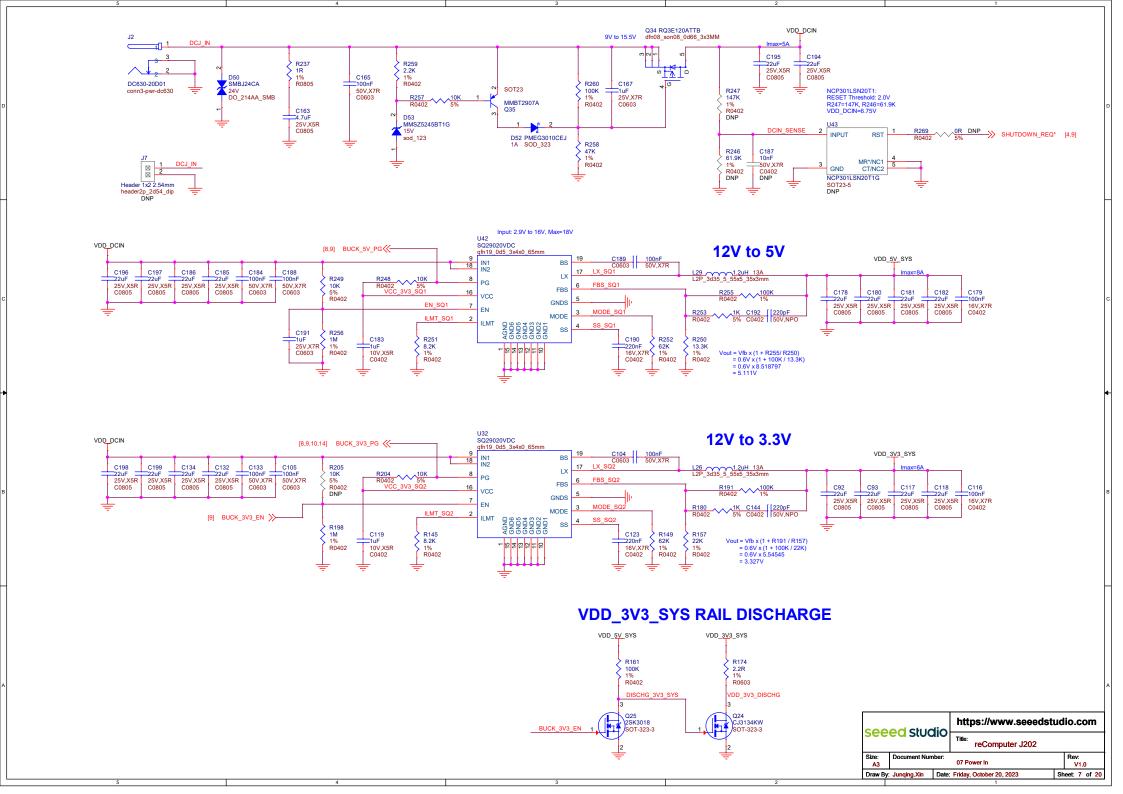
Sheet: 6 of 20

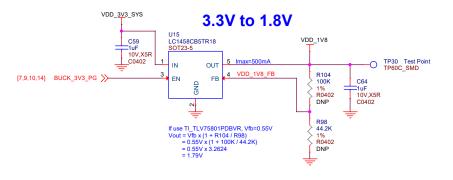
Title: reComputer J202

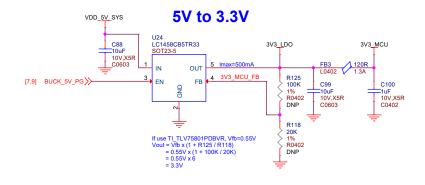
06 SODIMM Connector 3/3

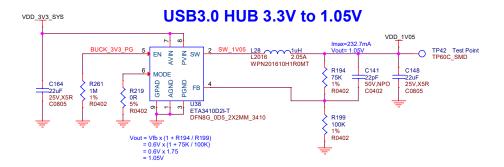
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Draw By: Junqing.Xin Date: Thursday, July 07, 2022

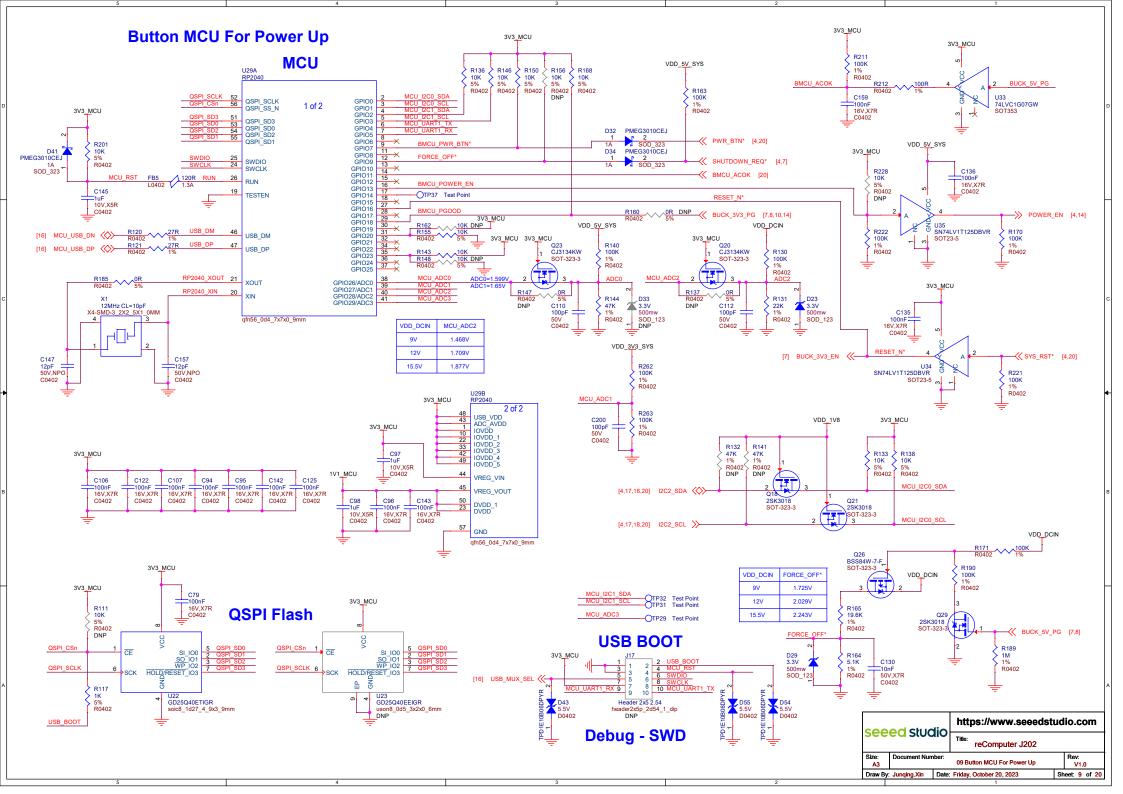


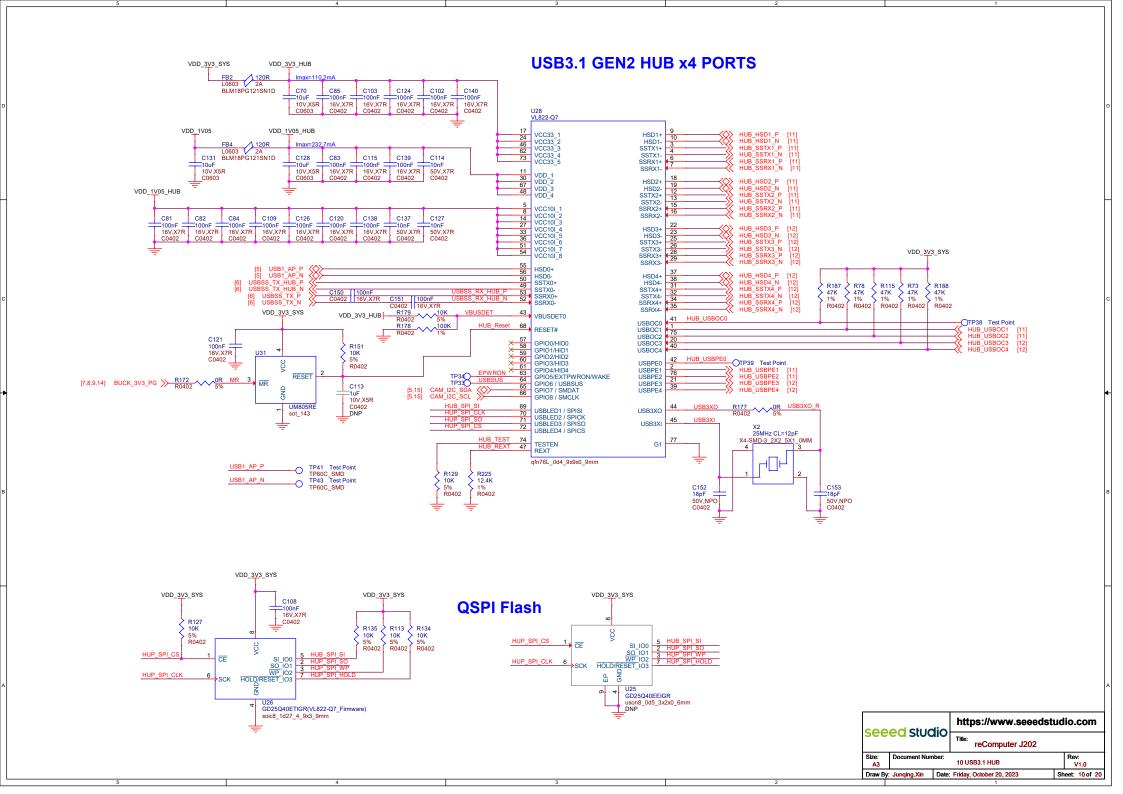


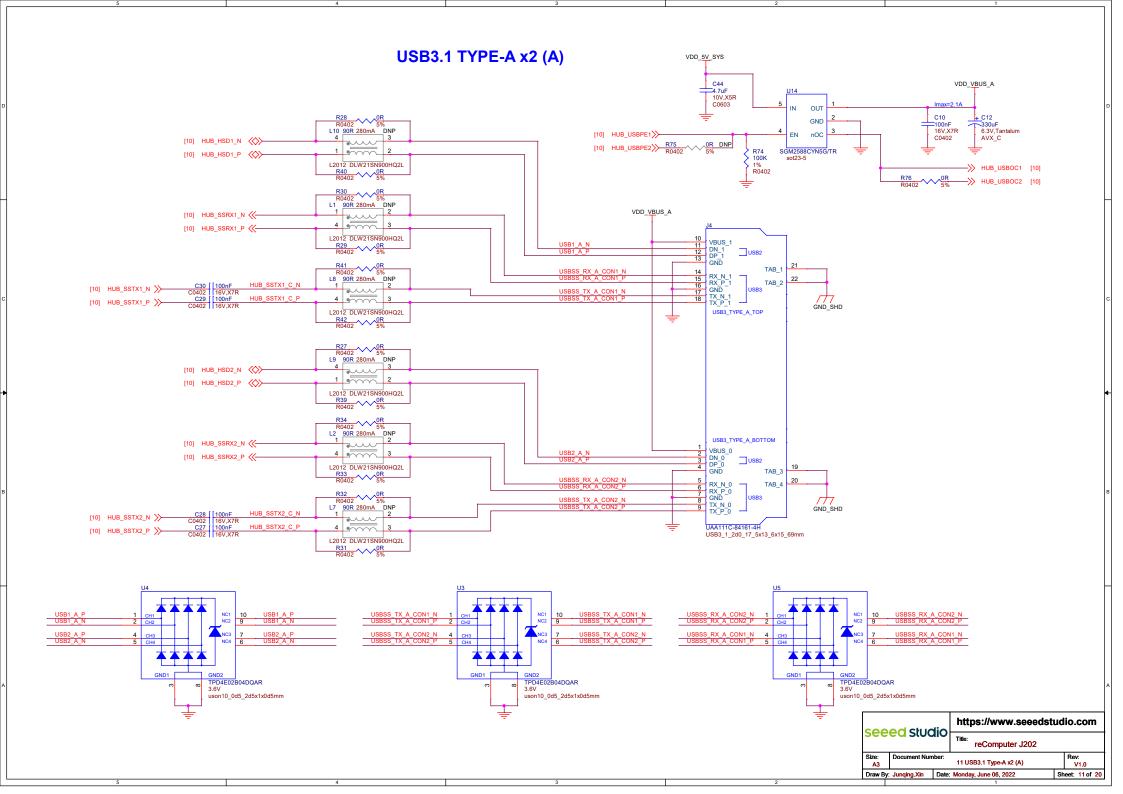


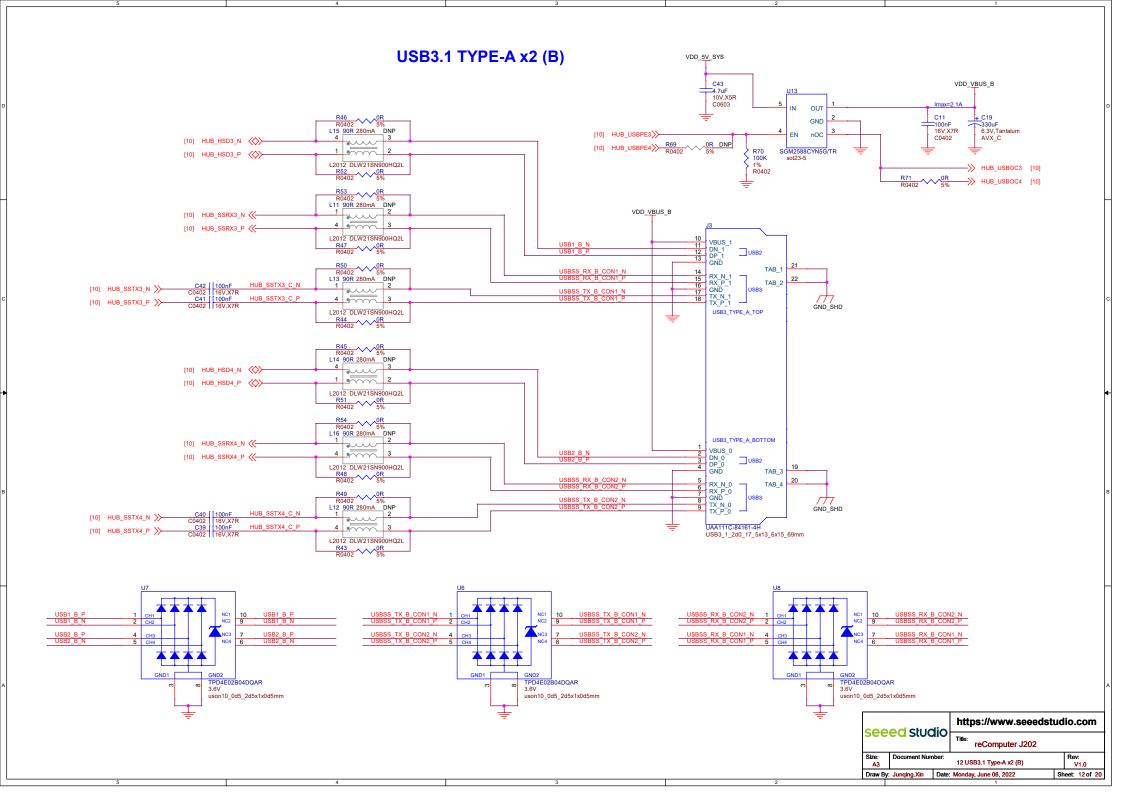


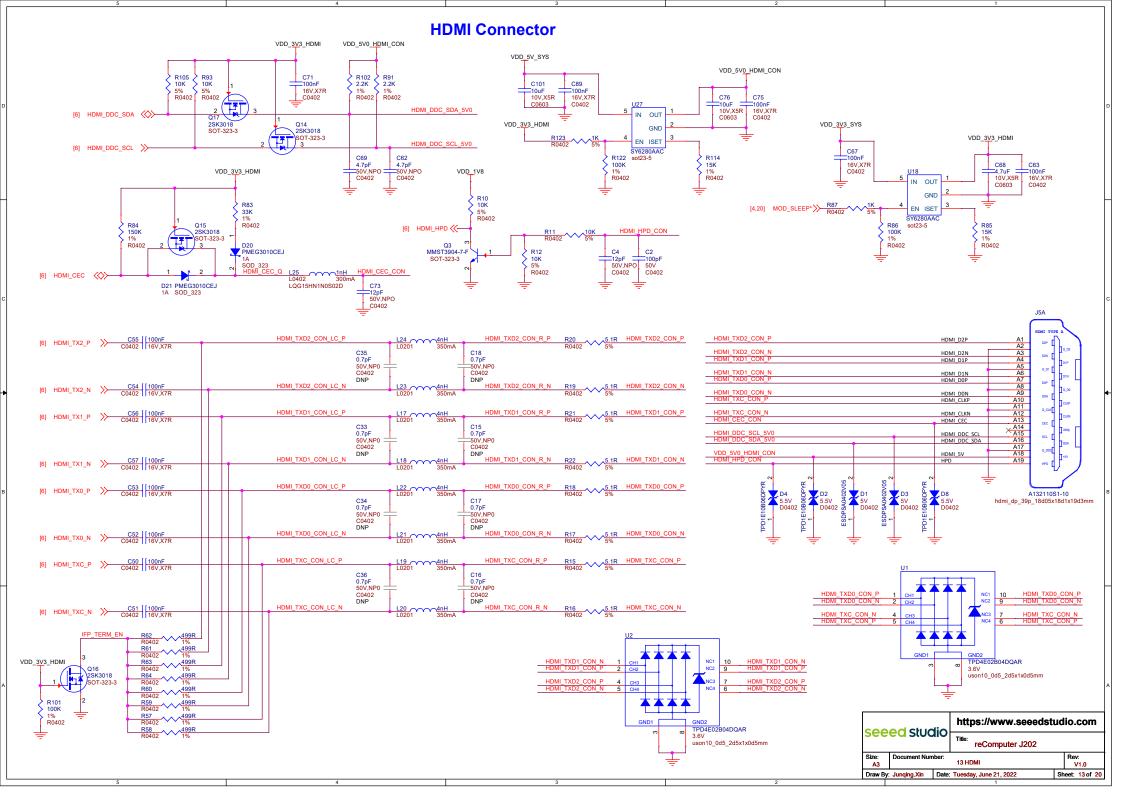
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				Title: reComputer J202		
	Size: A3	Document Number:		08 1V8 LDO & 3V3 AO Rails		Rev: V1.0
	Draw By: Junqing.Xin Date:		Date:	: Friday, October 20, 2023 S		eet: 8 of 20

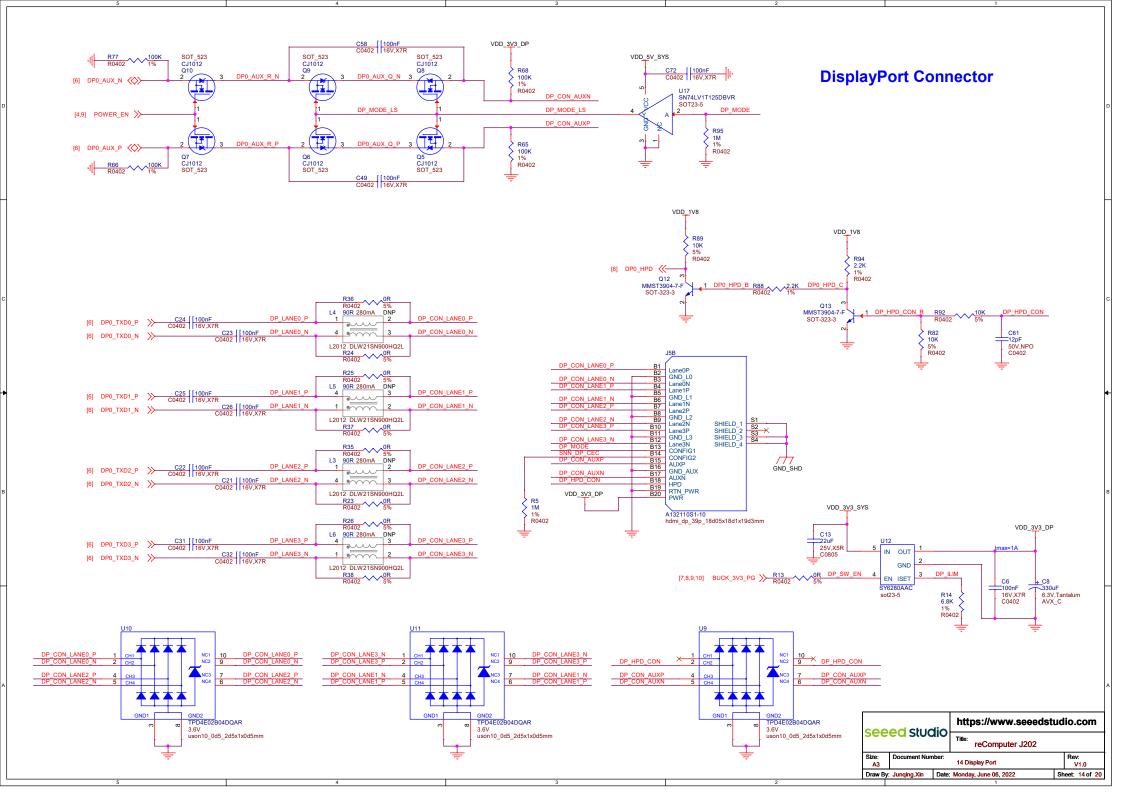


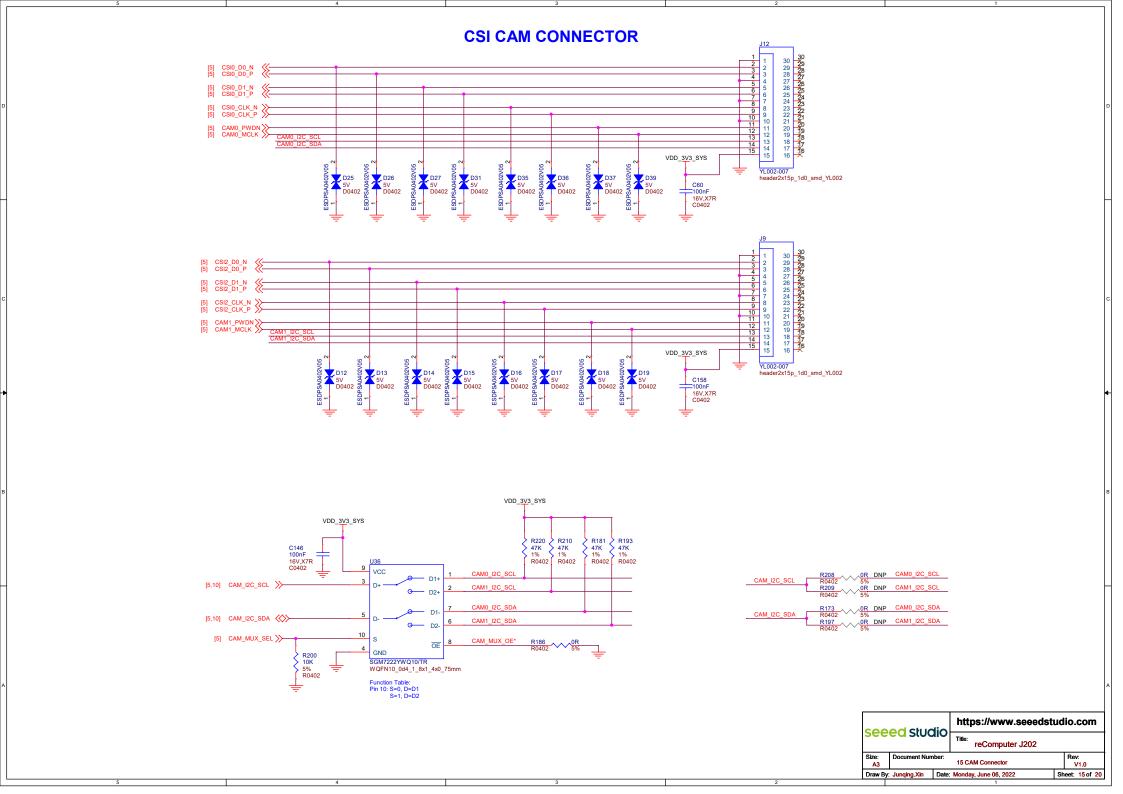


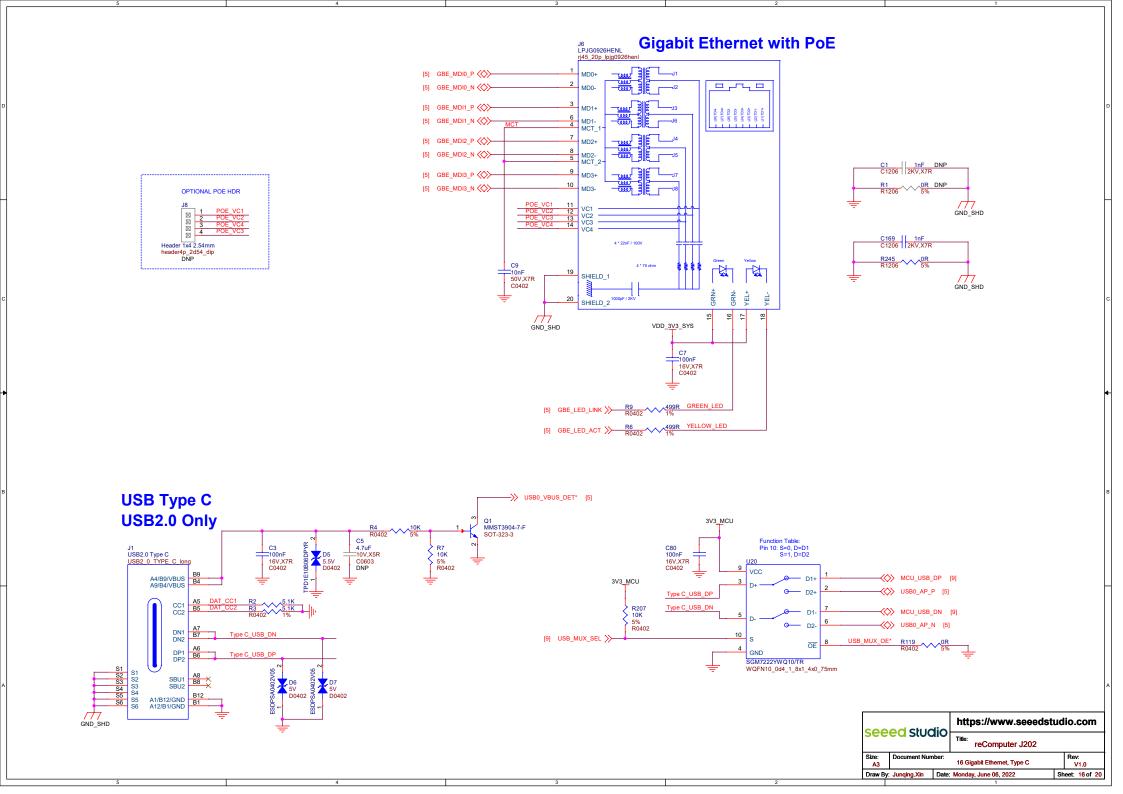




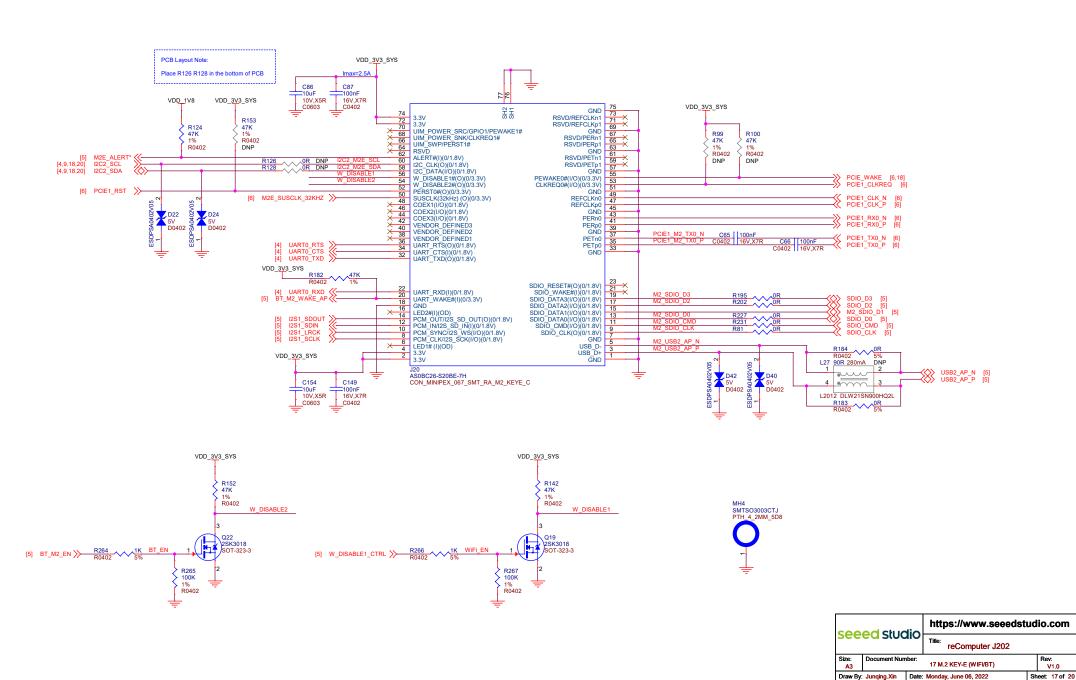




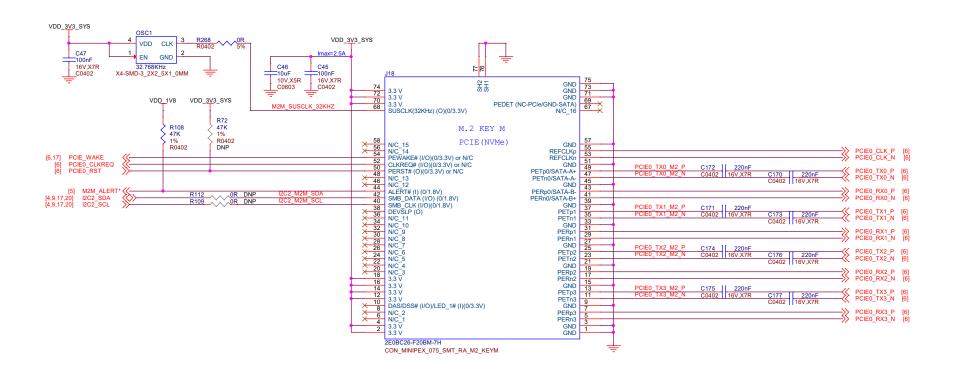




#### M.2 KEY-E (WIFI/BT)



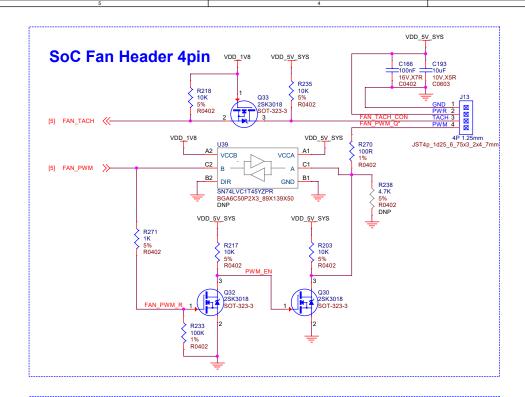
### M.2 KEY-M (NVME)

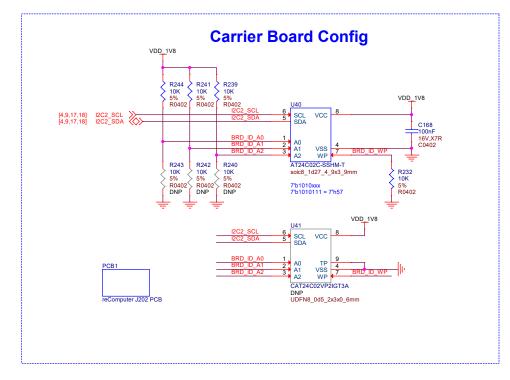


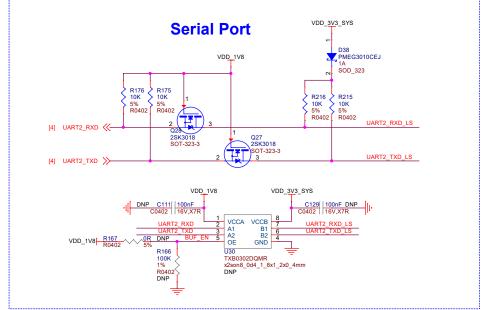


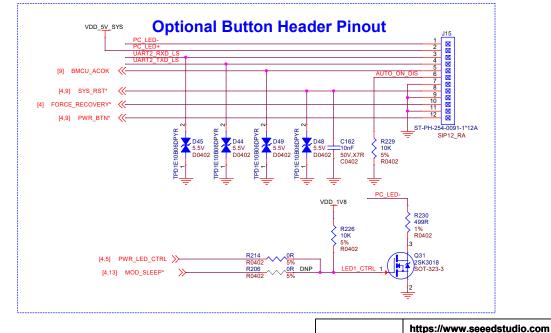
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Size: A3	Document Num	nber:	18 M.2 KEY-M (NVME)		Rev: V1.0
Draw By: Junqing.Xin Date:		Date:	Monday, June 06, 2022	Sh	eet: 18 of 20
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#### **40PIN GPIO EXPANSION HEADER & LEVEL SHIFTERS** VDD\_3V3\_SYS R116 100K R169 100K VDD 1V8 VDD\_3V3\_SYS VDD 1V8 VDD\_3V3\_SYS 1% R0402 1% R0402 U19 U21 C78 100nF C0402 16V,X7R DNP DNP VCCA VCCB VCCA VCCB SPIO\_SCK SPIO\_MISO SPIO\_MOSI SPIO\_CS0 SPIO\_CS1 GPIO07 GPIO12 SPI1\_SCK SPI1\_MISO SPI1\_MOSI SPI1\_CS0 SPI1\_CS1 B1 B2 B1 B2 B3 B4 B5 B6 B7 B8 A2 A3 A4 A5 A6 A7 A8 4 A3 5 A4 7 A5 8 A6 B3 B4 B5 B6 9 A7 A8 B7 B8 10 OE 10 OE VDD\_1V8 VDD\_1V8 11 11 GND GND R110 R0402 5% R107 1M R0402 1% TXB0108PWR TXB0108PWR tssop20-0\_65-6\_5x4\_4x1mm tssop20-0 65-6 5x4 4x1mm VDD\_3V3\_SYS VDD\_5V\_GPIO C14 C20 100nF 16V,X7R C0402 VDD\_1V8 VDD\_3V3\_SYS 100nF 16V X7R U37 VCCA VCCB 5V\_1 5V\_2 GND\_8 3V3 1 I2S0\_SCLK I2S0\_SDOUT I2S0\_SDIN I2S0\_LRCK GPI009 GPI013 GPI011 B1 B2 B3 B4 B5 GPIO2/SDA1 GPIO3/SCL1 UART1\_TXD\_LS UART1\_RXD\_LS I2S0\_SCLK\_LS A2 A3 A4 A5 A6 A7 GPIO4/GCKL TXD0/GPIO14 UART1 RTS LS GND\_1 GPIO17/GEN0 RXD0/GPIO15 GPIO18 B6 B7 GPIO27/GEN2 GPIO22/GEN3 GND\_7 GEN4/GPIO23 SPI1\_CS1\_LS 3V3\_2 GPIO10/MOSI GEN5/GPIO24 GND 6 19 21 23 25 27 29 31 33 35 37 GEN6/GPIO25 CE0/GPIO8 CE1/GPIO7 OE GPIO9/MISO GPIO11/SCLK VDD\_1V8 11 GPIOTI/ GND\_2 ID\_SD GPIO5 GPIO6 GPIO13 ID\_I2C\_SDA GPI001\_LS GPI011\_LS GPI013\_LS GND ID\_SC GND\_5 GPIO07 LS TXB0108PWR GPIO12 GND\_4 tssop20-0\_65-6\_5x4\_4x1mm UART1\_CTS\_LS I2S0\_SDIN\_LS I2S0\_SDOUT\_LS GPIO19 GPIO26 GPIO16 GPIO20 GND\_3 GPIO21 Header 2x20 2.54mm header2x20p\_2d54\_dip MAX: 1A VDD\_5V\_SYS VDD\_5V\_GPIO Q4 CJ2101 [4] ID\_I2C\_SDA ( ) [4] ID\_I2C\_SCL Q11B Q11A BC856S SOT363 BC856S SOT363 R80 100K 1% R0402 R79 100K 1% R0402 https://www.seeedstudio.com seeed studio reComputer J202 Rev: V1.0 19 40Pin GPIO Expansion Header Draw By: Junqing.Xin Date: Tuesday, June 07, 2022 Sheet: 19 of 20









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reComputer J202

20 Fan, Debug and EEPROM

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