

Schematic: reComputer J202

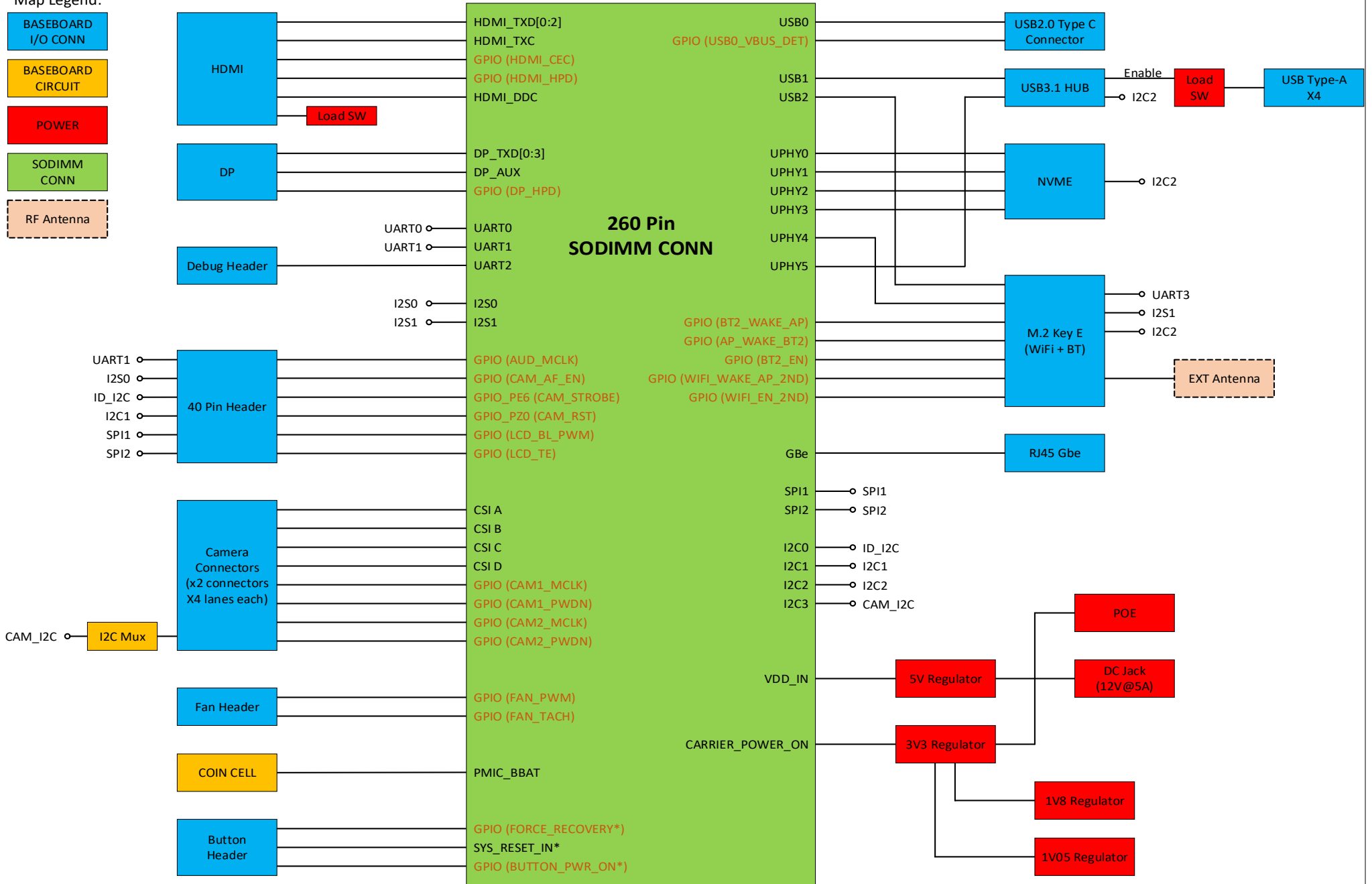
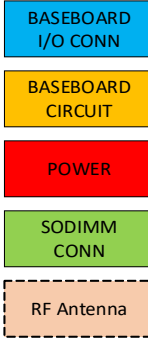
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Revision History

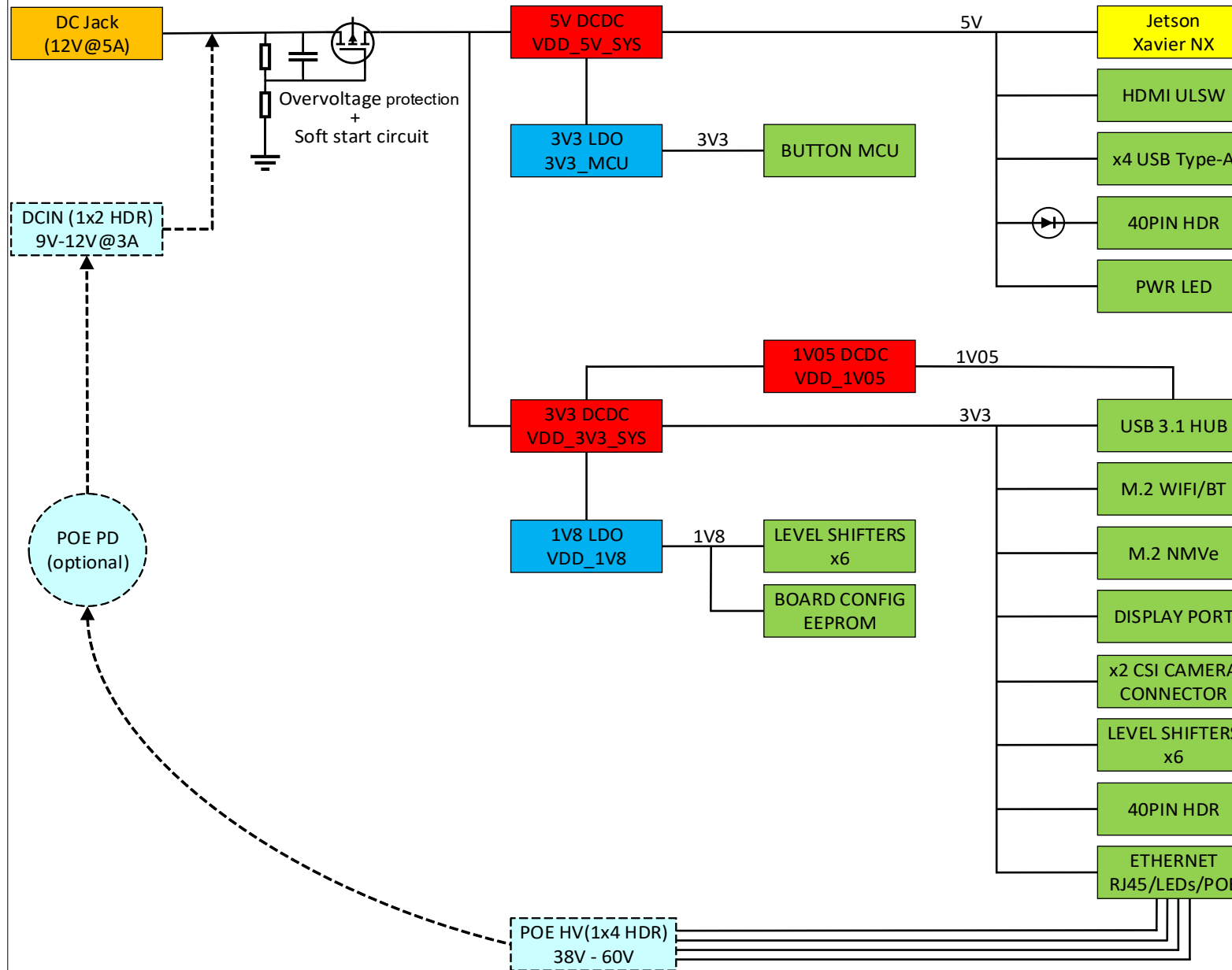
VER	DATE	REVISION	DESCRIPTION
V1.0	06/09/2022	reComputer J202_V1.0_SCH_220609	Initial Release.
V1.0	10/20/2023	reComputer J202_V1.0_SCH_231020	Page 09:Changed R185 from 499R to 0R; Changed X1 12MHz from CL=18pF to CL=10pF; Changed C147,C157 from 27pF to 12pF.
V1.0	01/29/2024	reComputer J202_V1.0_SCH_240129	Page 04:Changed C37,C38,C48 from 330uF/6.3V to 220uF/6.3V

# BLOCK DIAGRAM

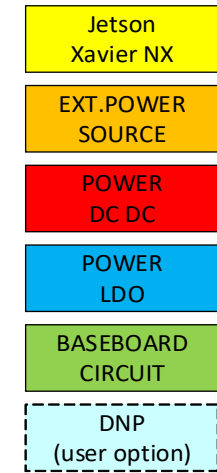
## Map Legend:



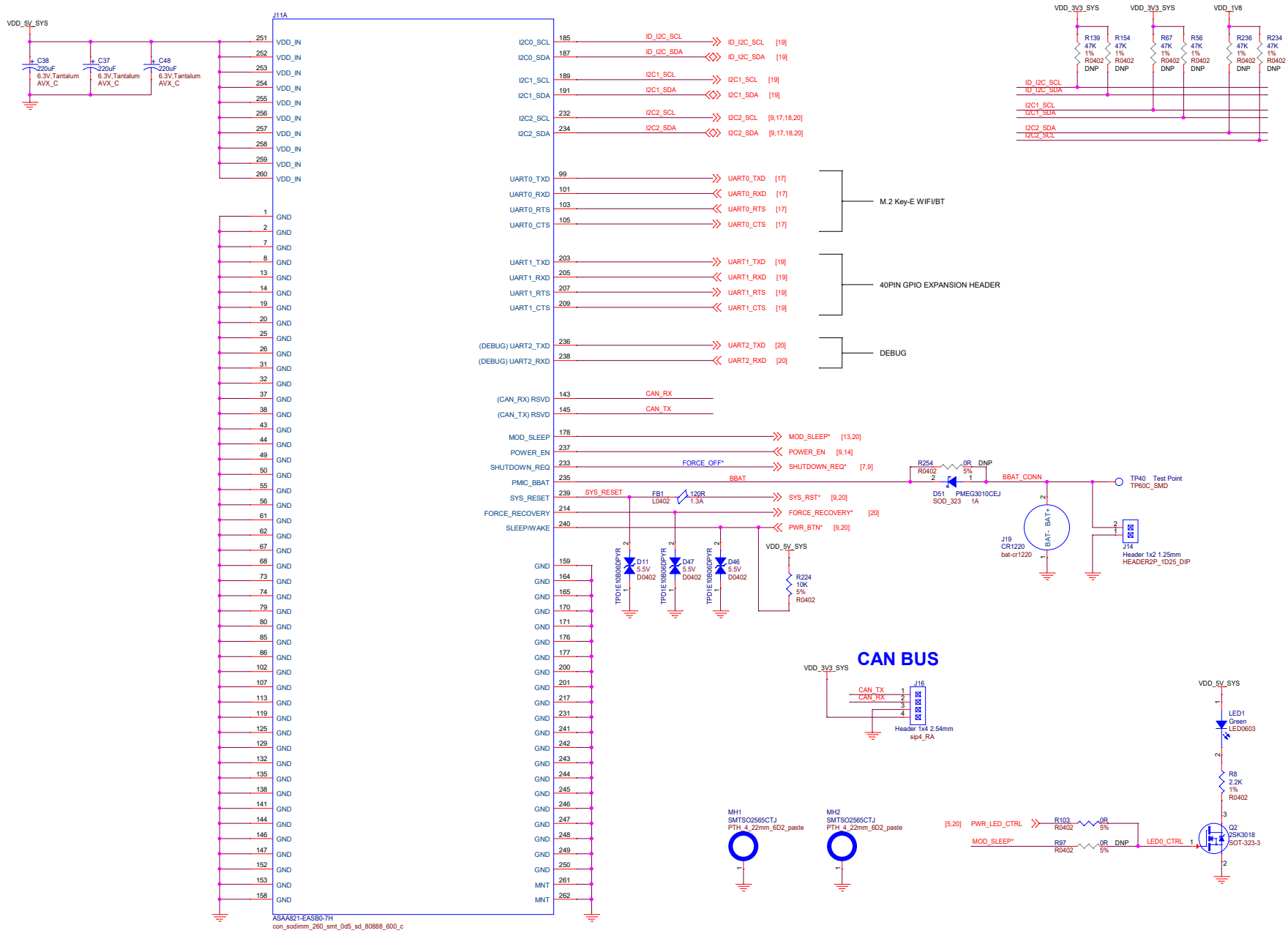
# Power Topology



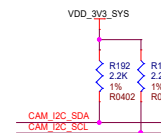
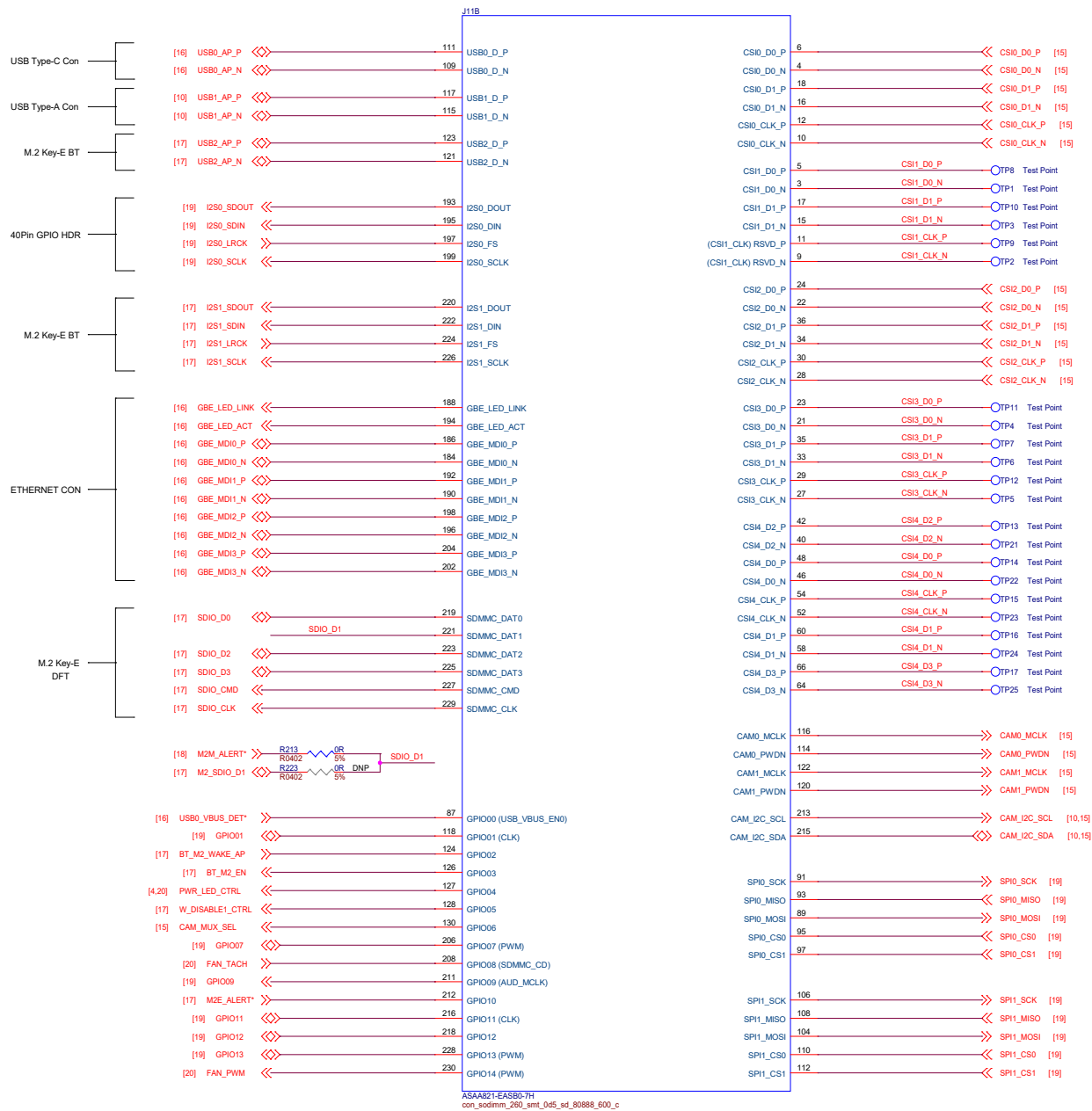
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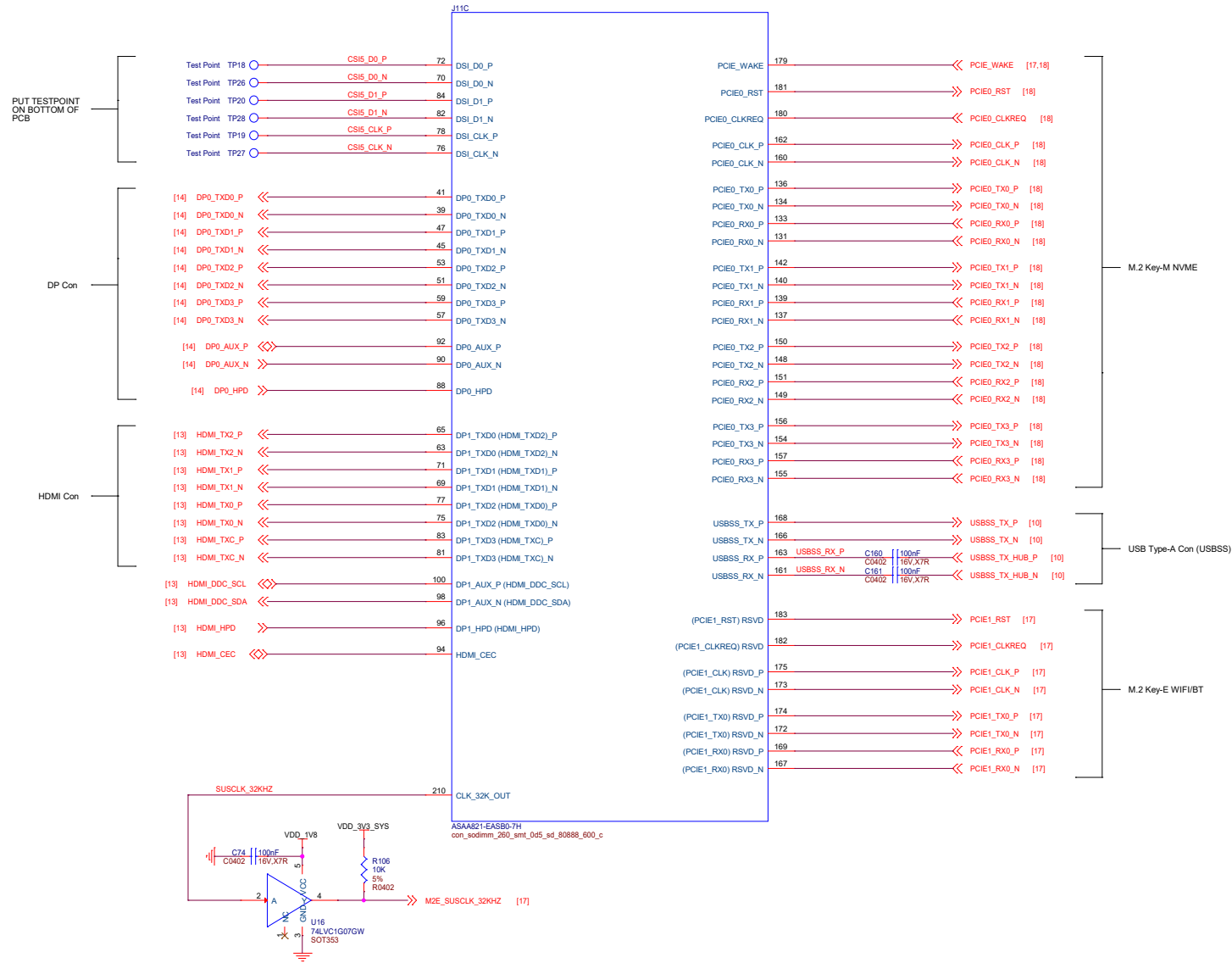
## UART, I2C, CAN and GPIOs

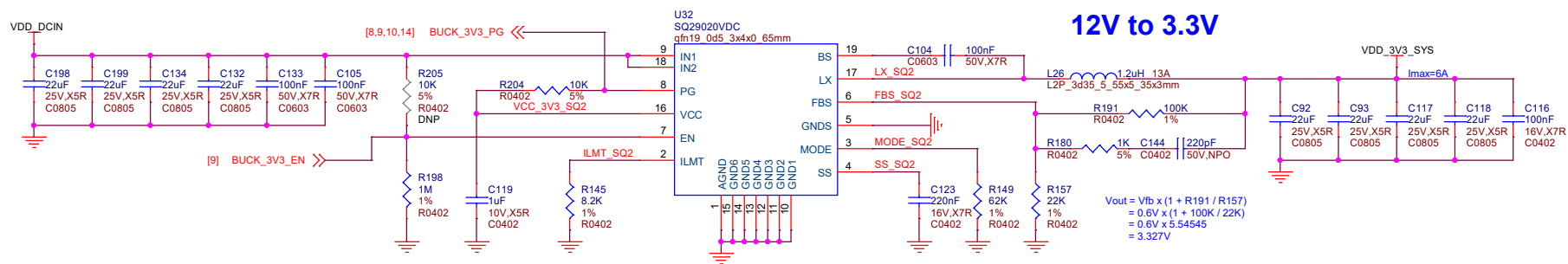
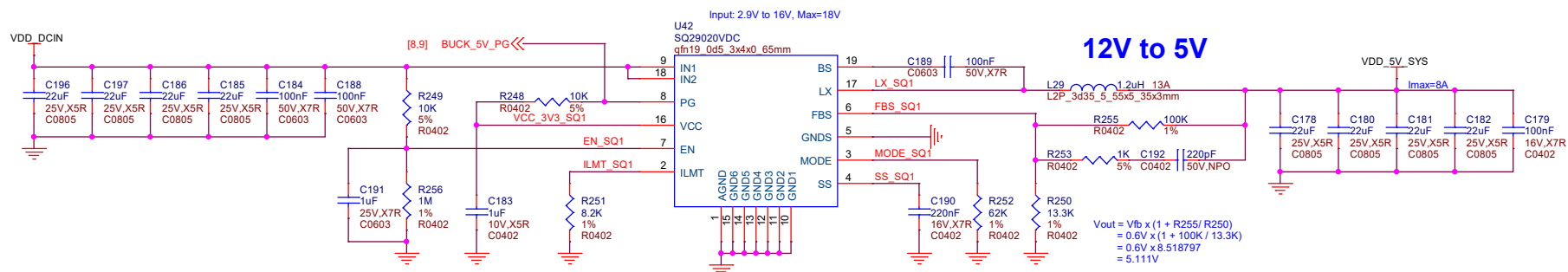
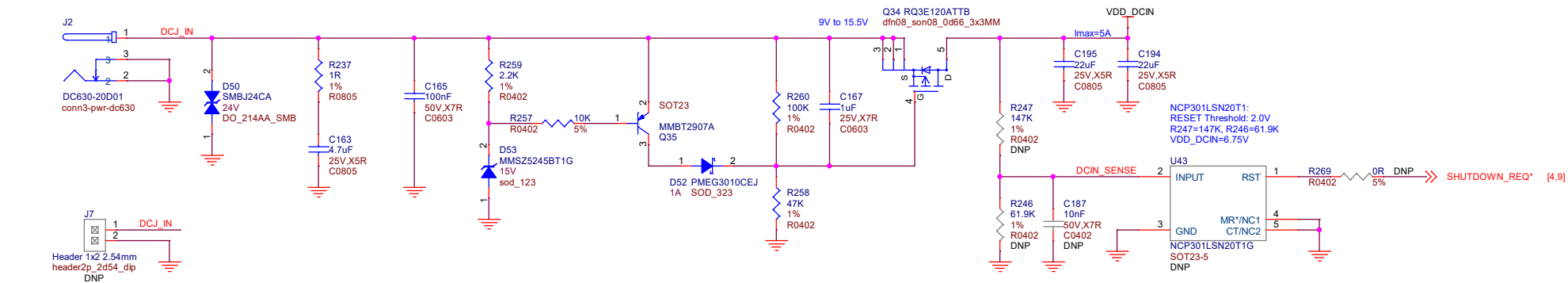


# GPIOs, USB2.0, I2S, SPI, GBE and CSI

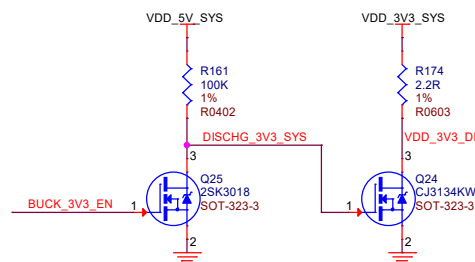


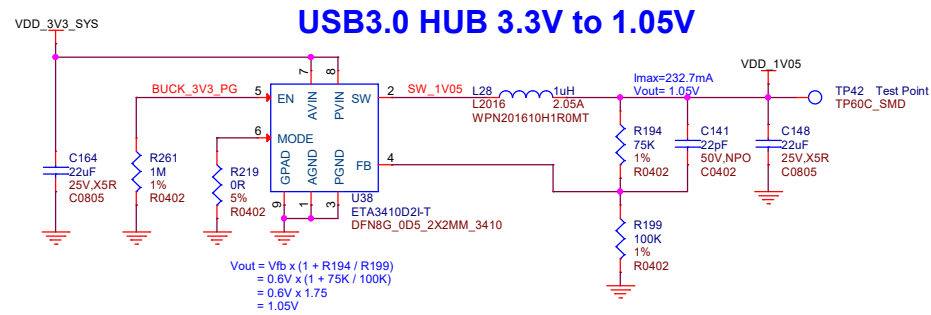
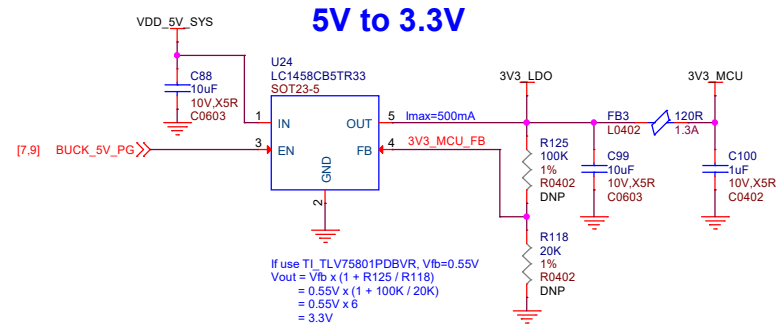
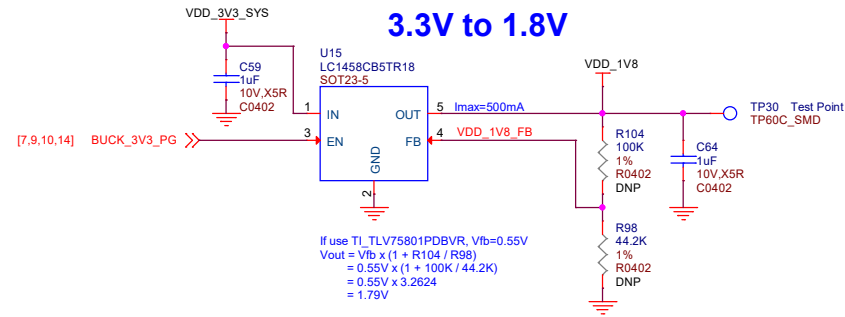
## HDMI, DP, DSI, USBSS and PCIe





## VDD\_3V3\_SYS RAIL DISCHARGE

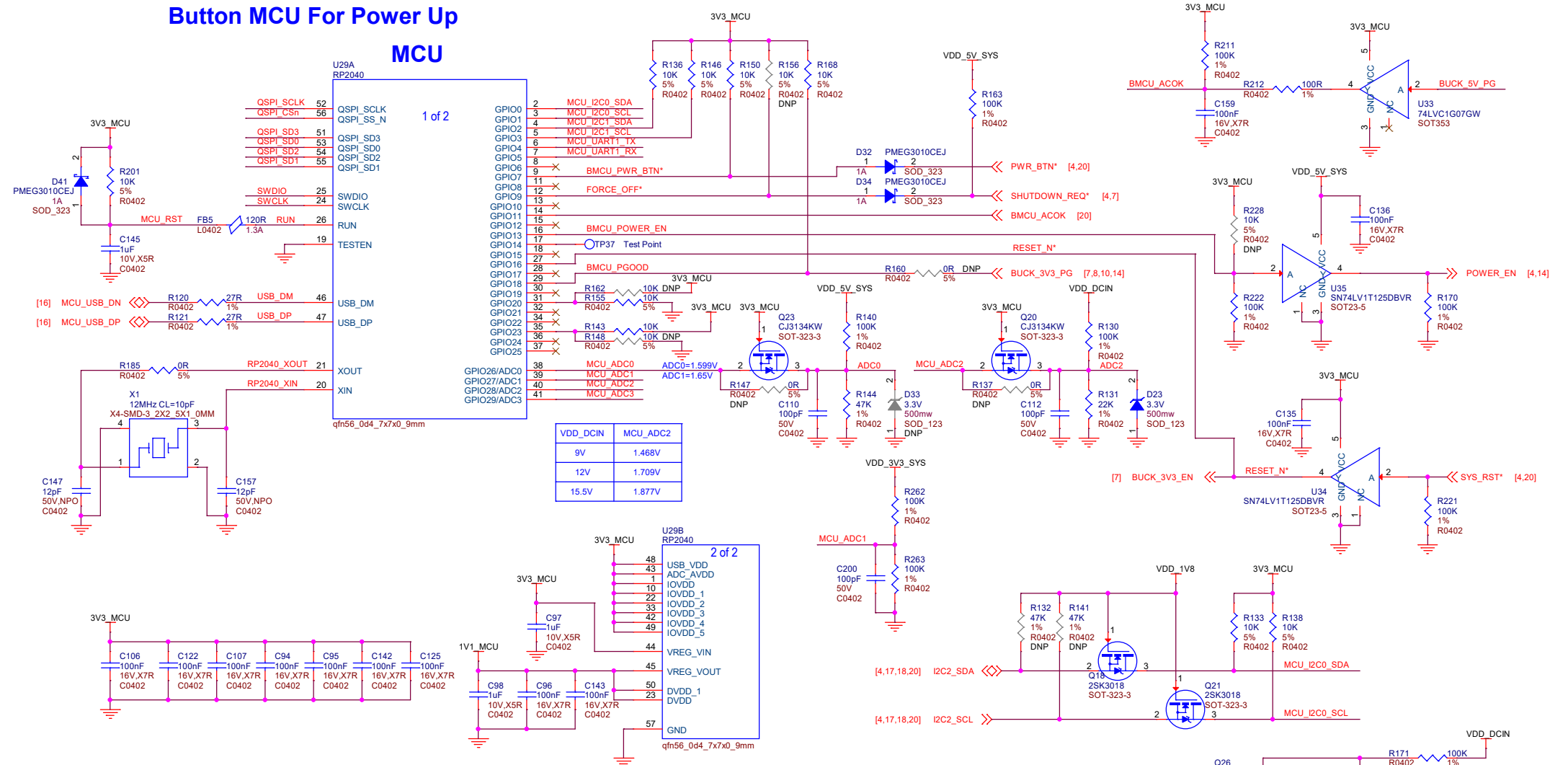




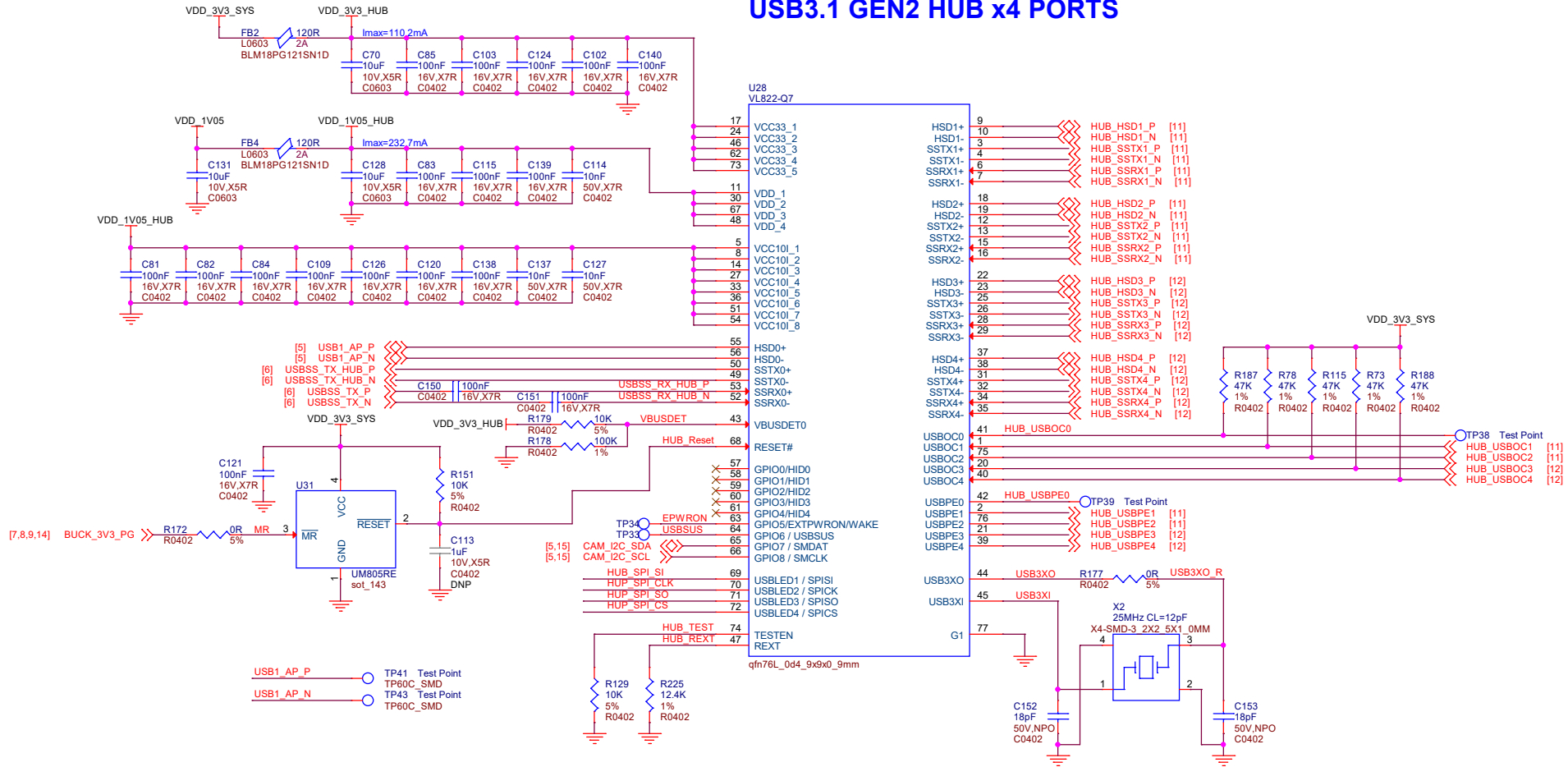


# Button MCU For Power Up

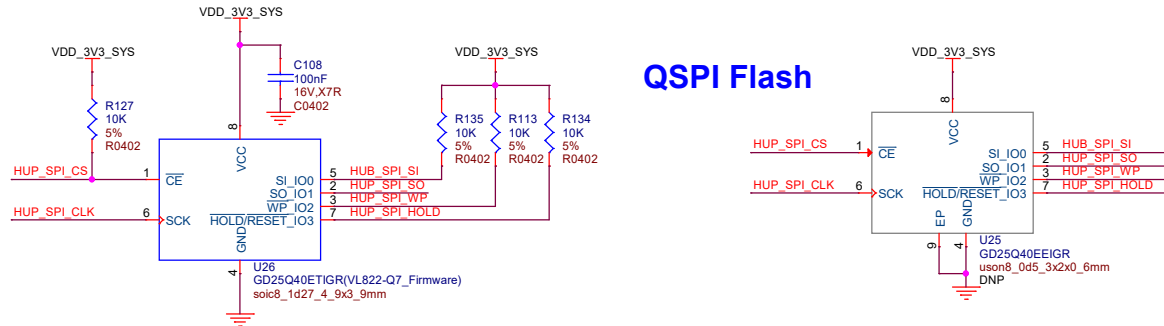
## MCU



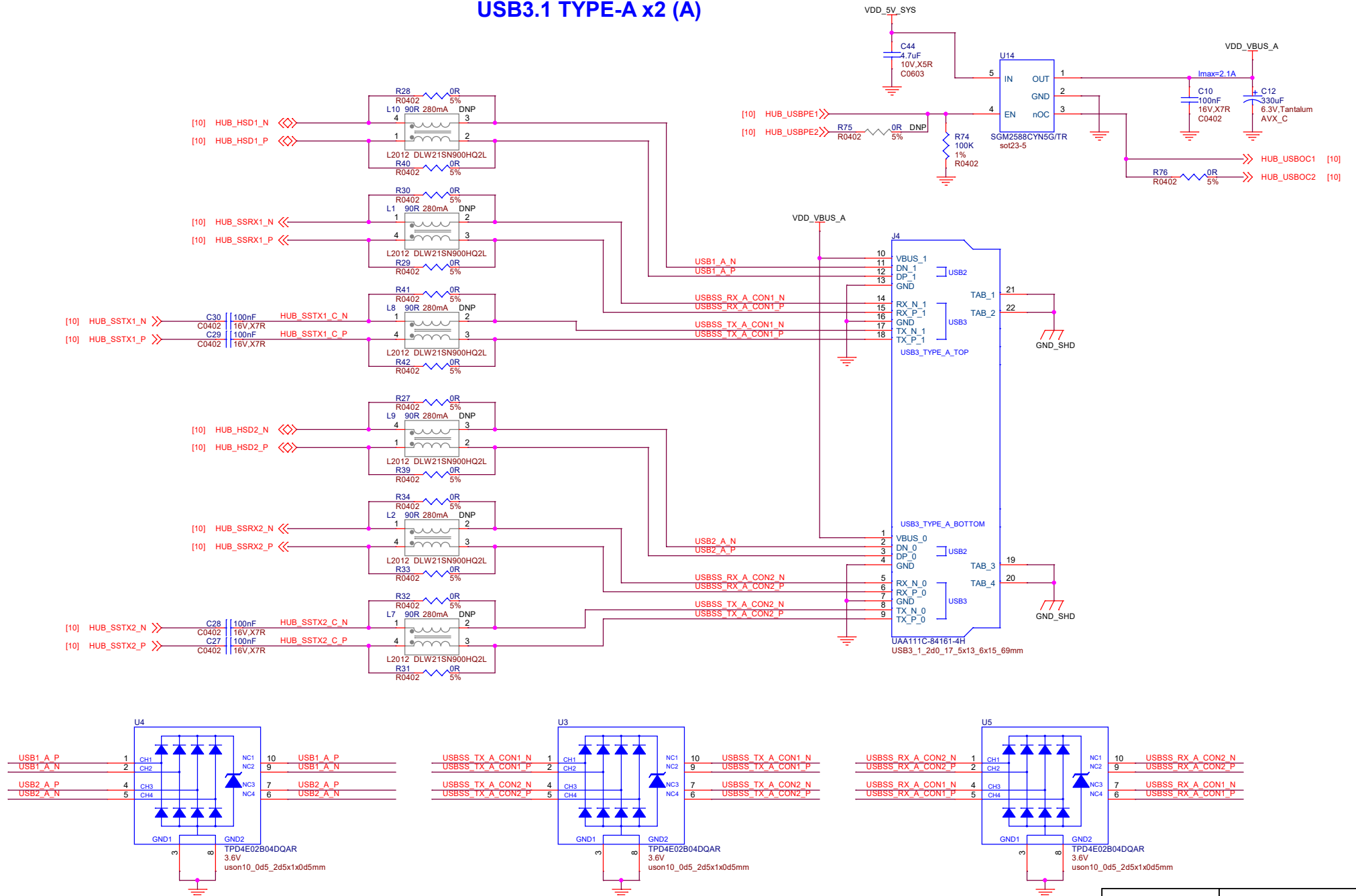
# USB3.1 GEN2 HUB x4 PORTS



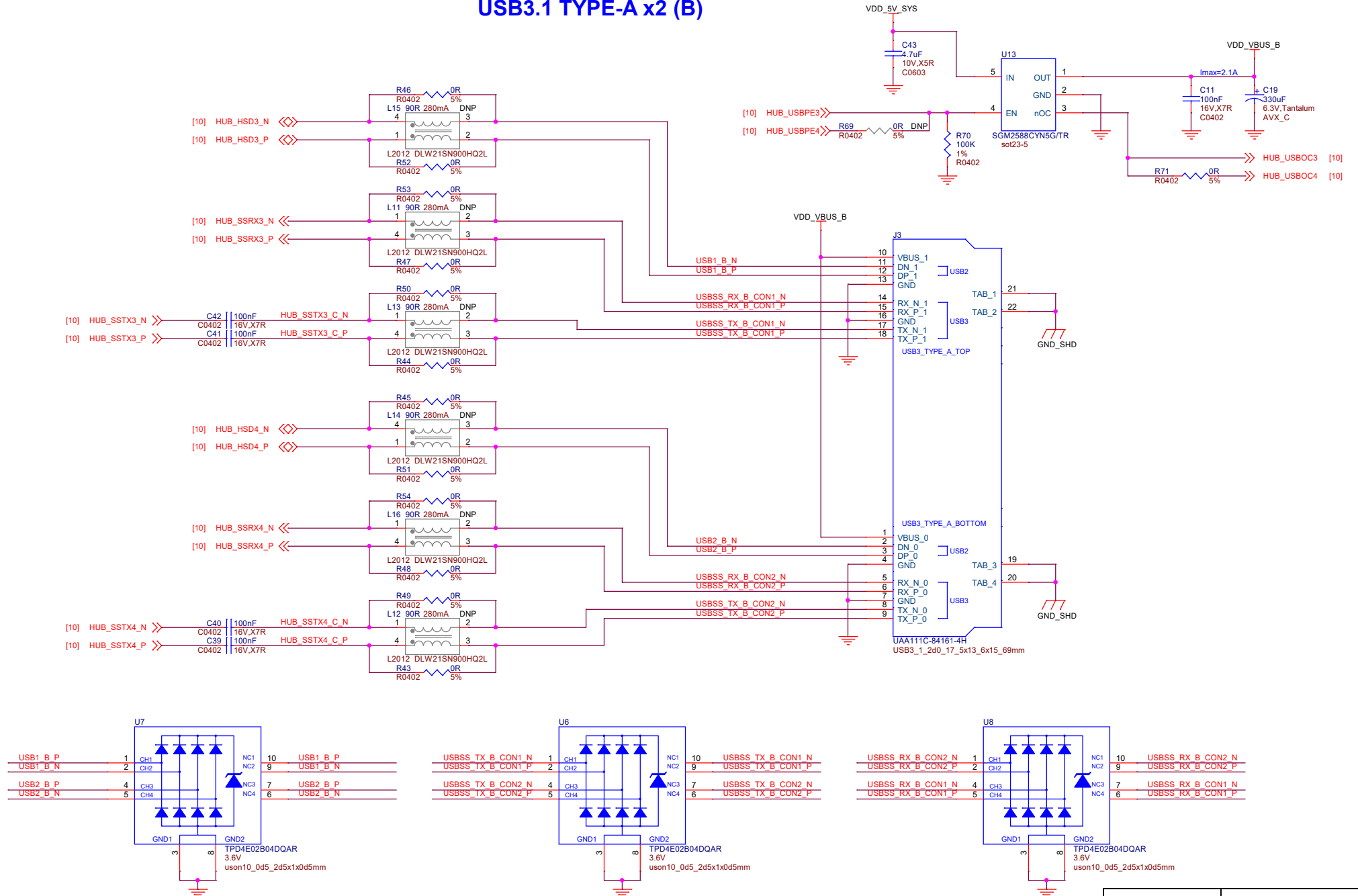
## QSPI Flash



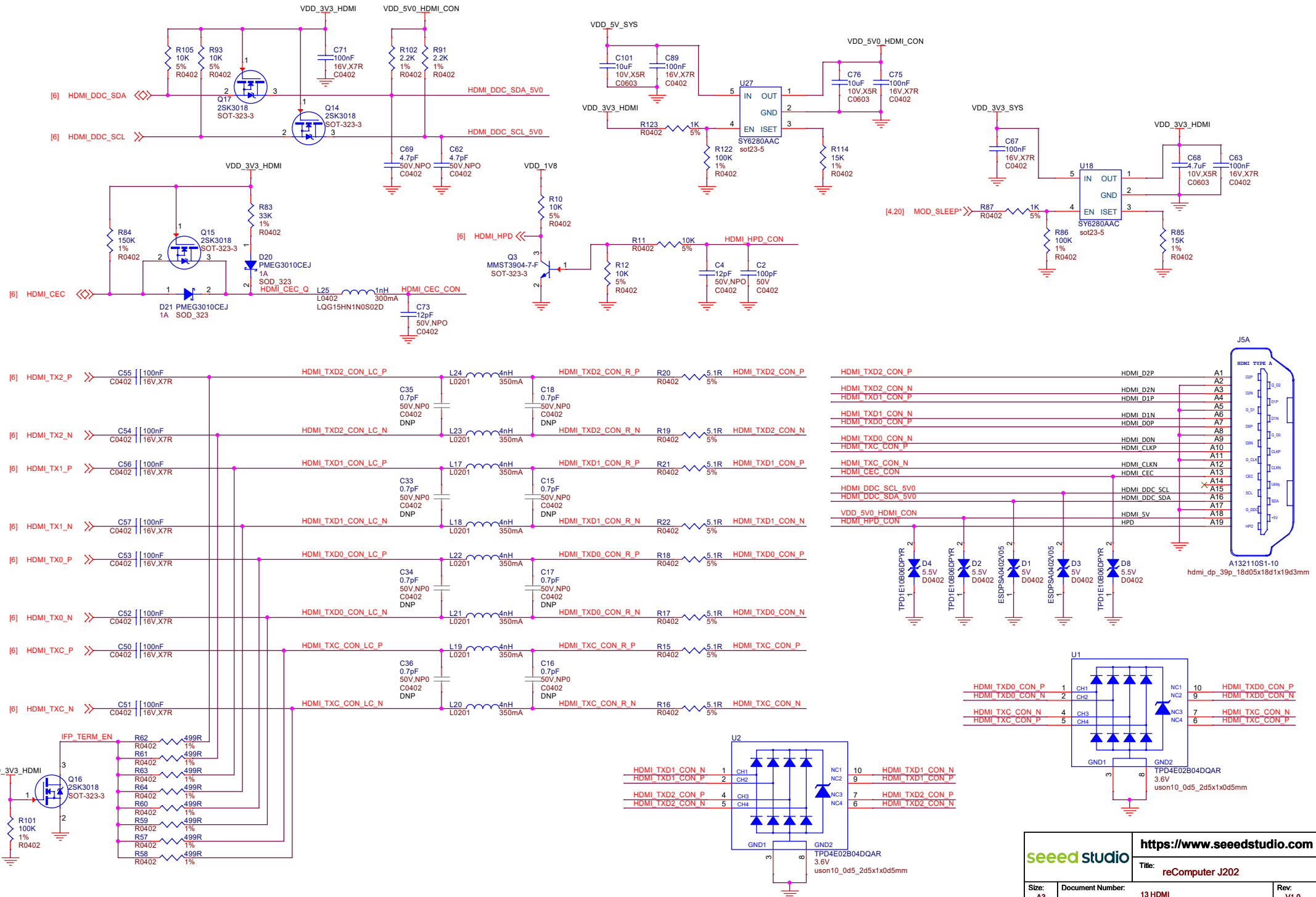
**USB3.1 TYPE-A x2 (A)**



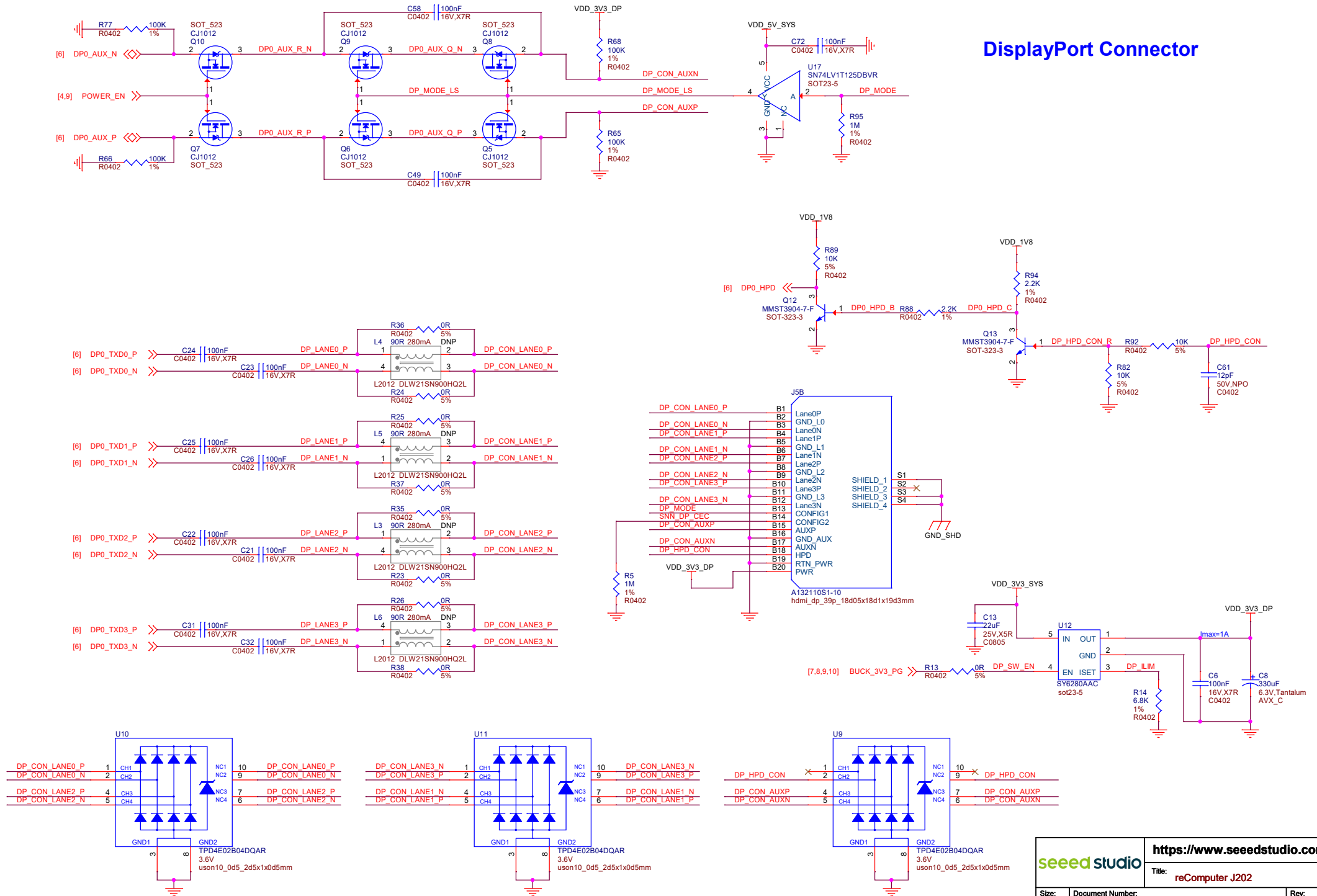
**USB3.1 TYPE-A x2 (B)**



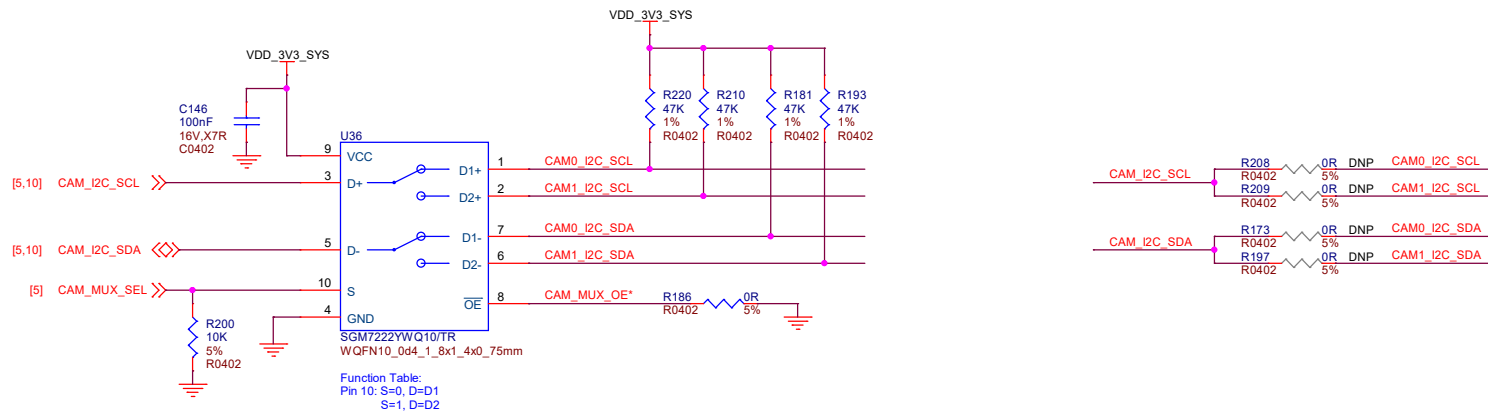
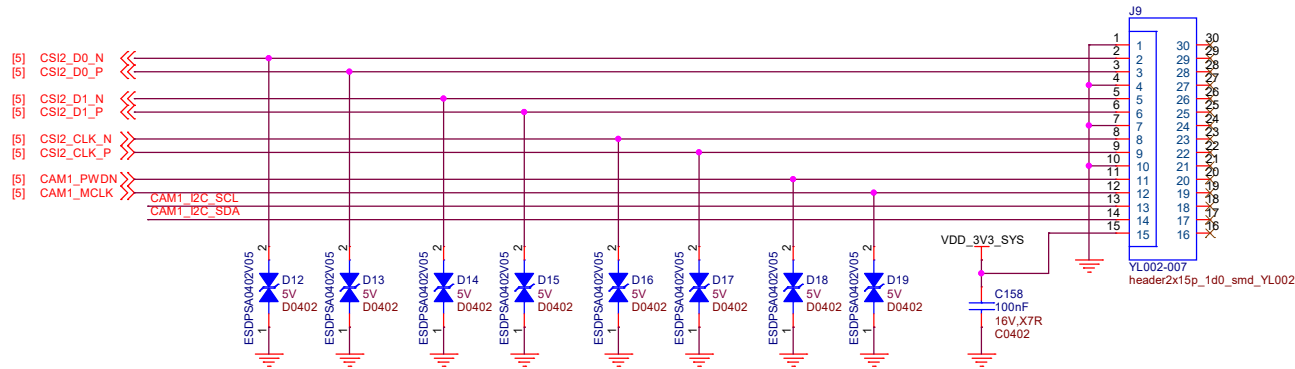
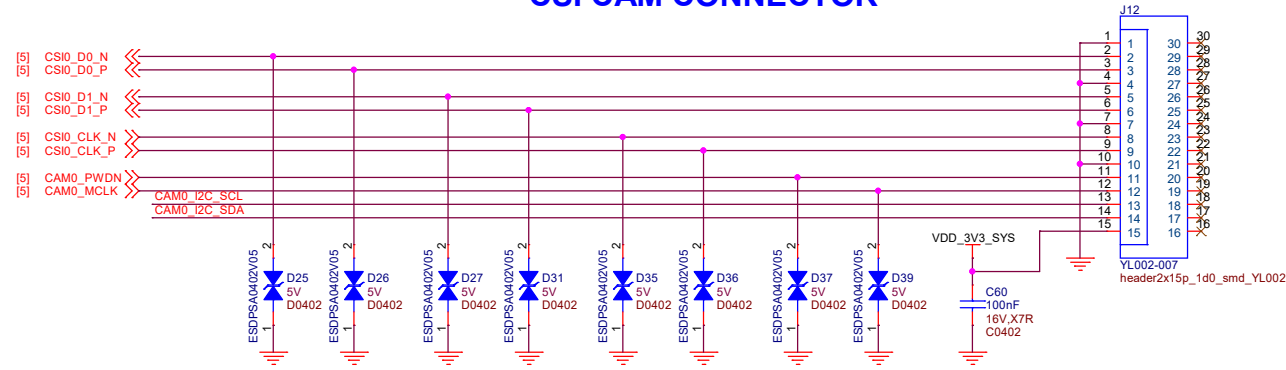
## HDMI Connector




# DisplayPort Connector



# CSI CAM CONNECTOR

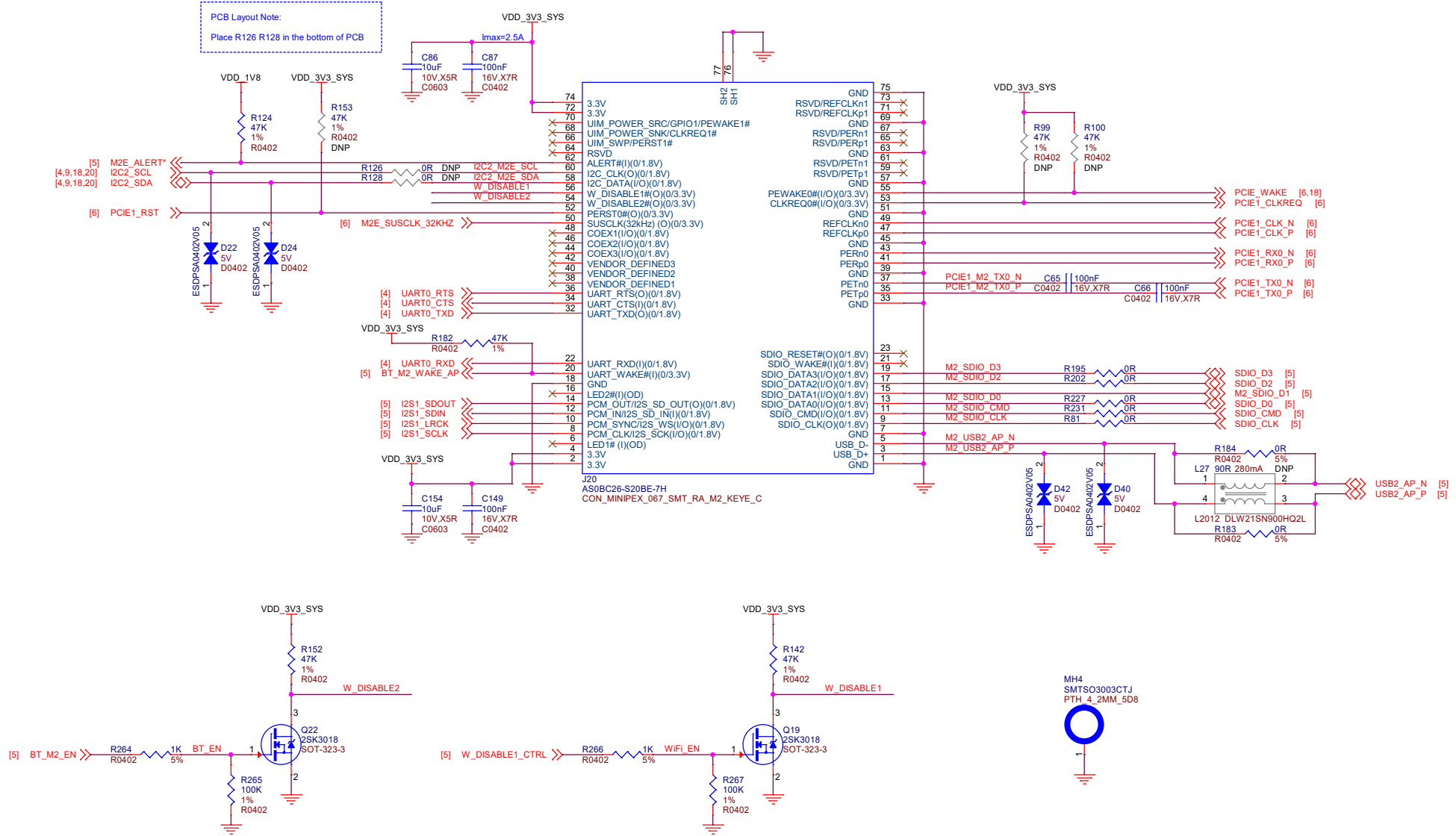


[illegible]

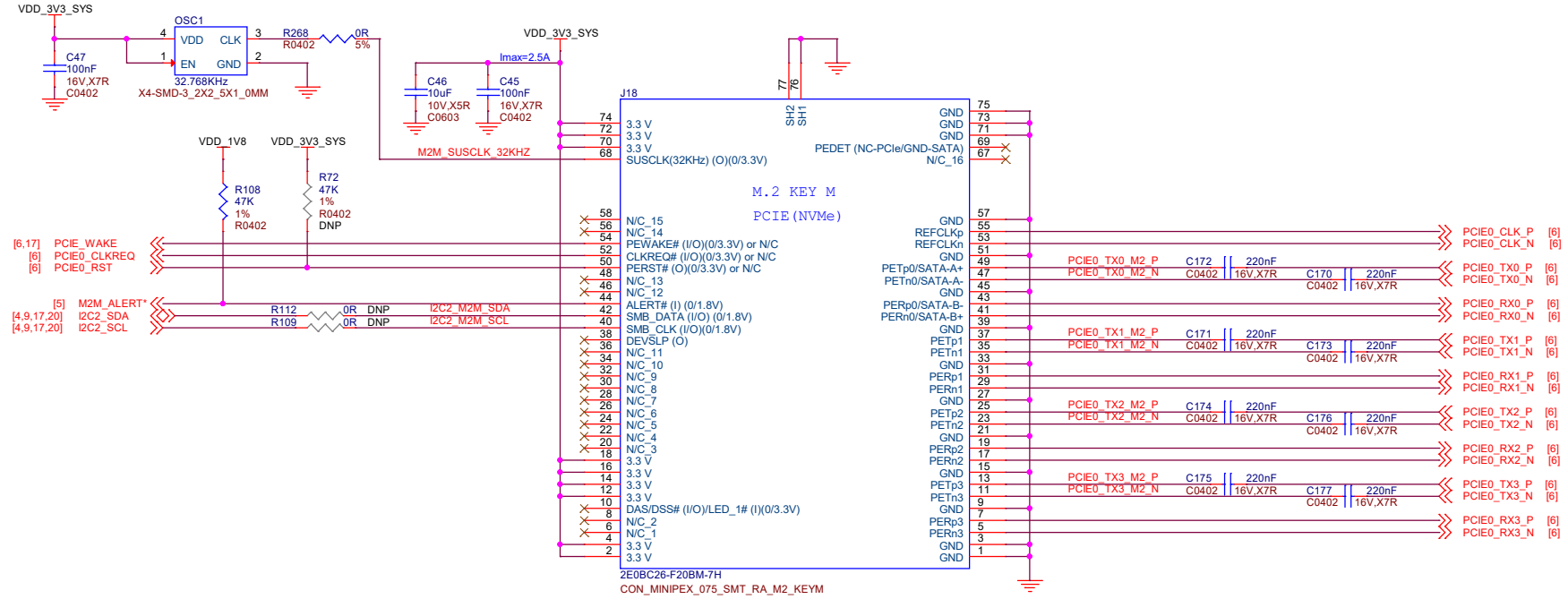
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		Title: <b>reComputer J202</b>	
Size: <b>A3</b>	Document Number:	<b>16 Gigabit Ethernet, Type C</b>	Rev: <b>V1.0</b>
Draw By: <b>Junqing_Xin</b>	Date: <b>Monday, June 06, 2022</b>	Sheet: <b>16 of 20</b>	



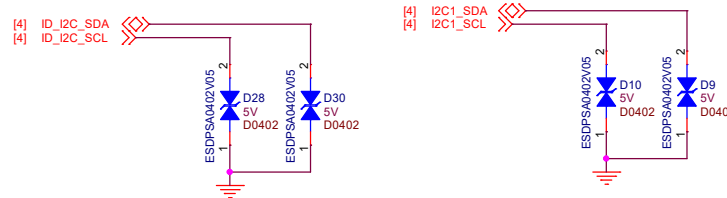
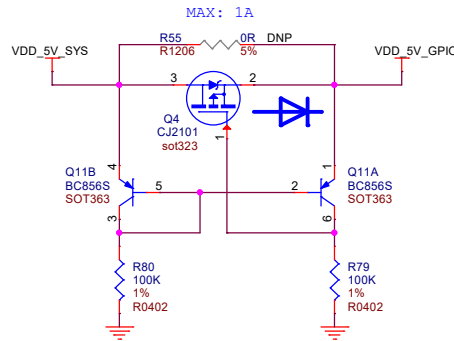
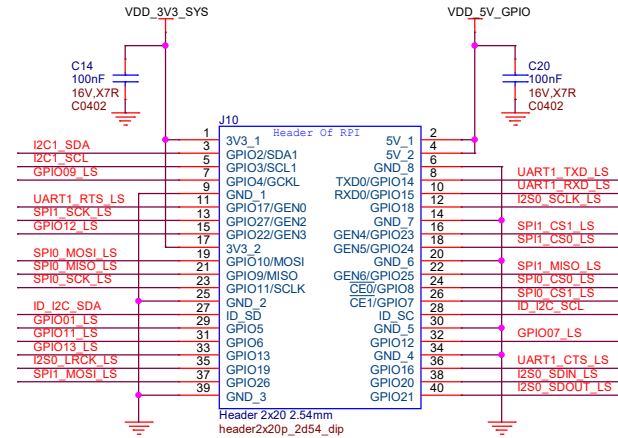
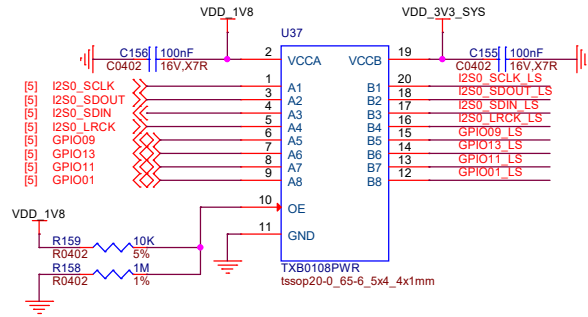
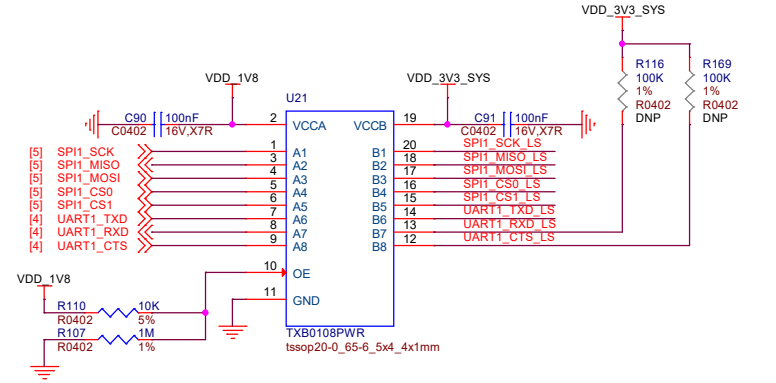
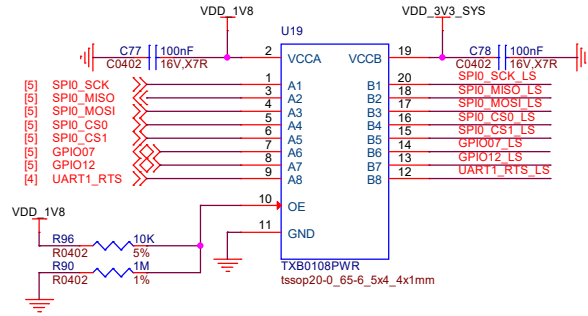
# M.2 KEY-E (WIFI/BT)



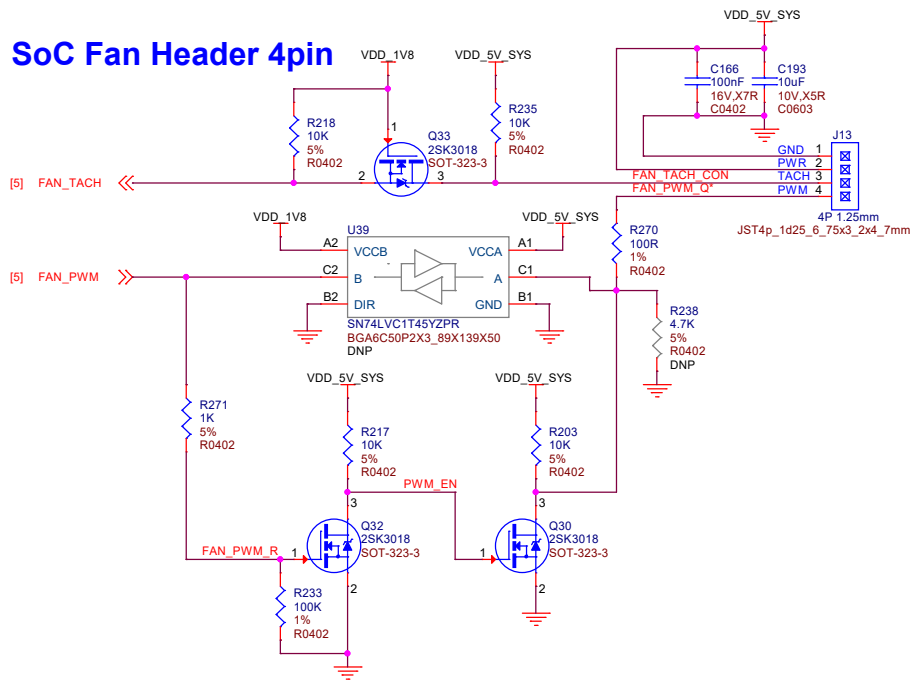
## M.2 KEY-M (NVME)



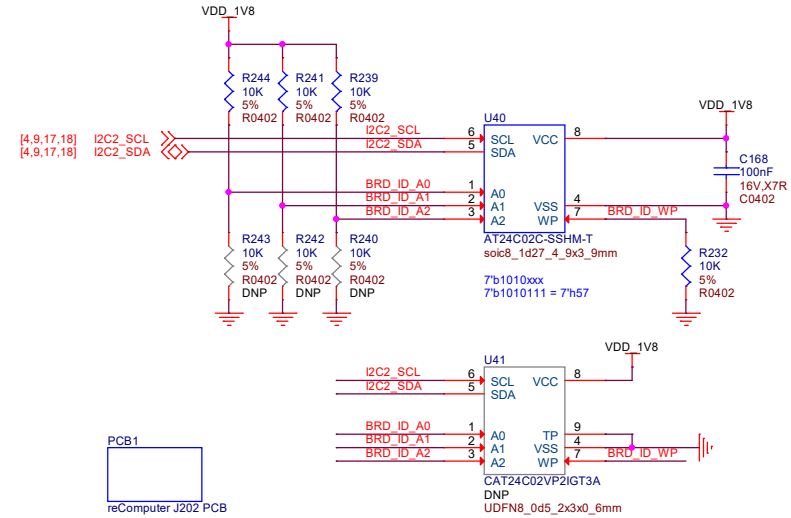
# 40PIN GPIO EXPANSION HEADER & LEVEL SHIFTERS



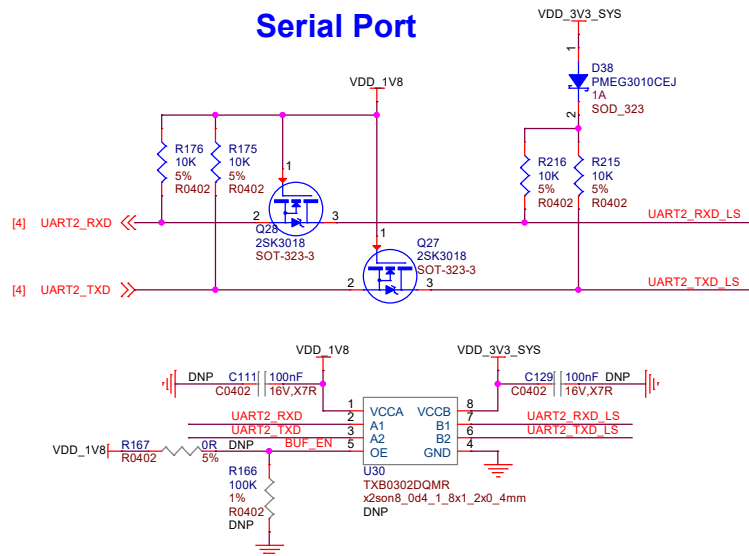
## SoC Fan Header 4pin



## Carrier Board Config



## Serial Port



## Optional Button Header Pinout

