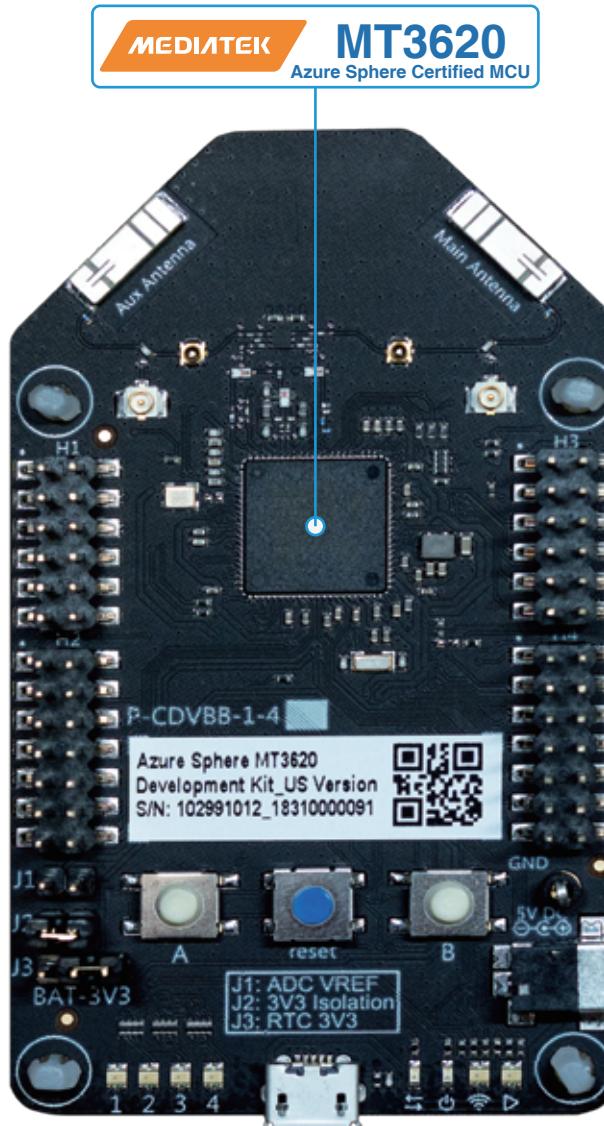


# MT3620 Development Board Pinmap



## Extention Header Pinmap

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**Extention Header Pinmap**

			H3					
CLK3 DATA3	TXD3	SCLK3	5V_OUT	1	GND			
	RTS3	MOSI3	3.3V	3	WAKEUP			
	RXD3	MISO3	GPIO 66	5	IO0_TXD			
	CTS3	CSA3	GPIO 67	7	IO1_TXD			
			GPIO 68	9	PMU_EN			
			GPIO 69	11	GPIO 70	CSB3		
DATA1	RXD1	MISO1	SWDIO	1	GND			
	TXD1	SCLK1	SWCLK	3	SWO			
	CTS1	CSA1	GPIO 33	5	GPIO 38	MISO2	RXD2	DATA2
	CLK1	RTS1	GPIO 31	7	GPIO 36	SCLK2	TXD2	
			GPIO 34	9	GPIO 39	CSA2	CTS2	
			GPIO 32	11	GPIO 37	MOSI2	RTS2	CLK2
		GPIO 35	13	GPIO 40	CSB2			

Note:  
[2018/09]

The current Azure Sphere software release does not support all features of the MT3620 hardware.

The following are not yet supported in software:

- 2 x ARM Cortex-M4 with FPU
  - ADC, I2C, I2S, PWM and SPI peripheral interfaces (GPIO and UART are supported)
  - Wi-Fi 802.11a (b/g/n are supported)
  - RTC with clock selection and battery backup