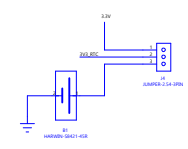
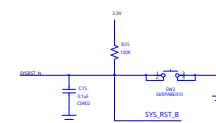
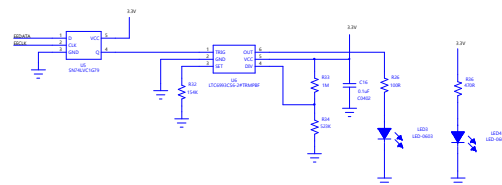
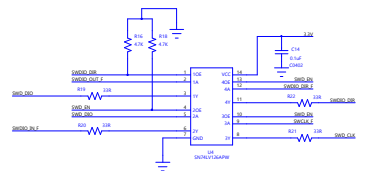
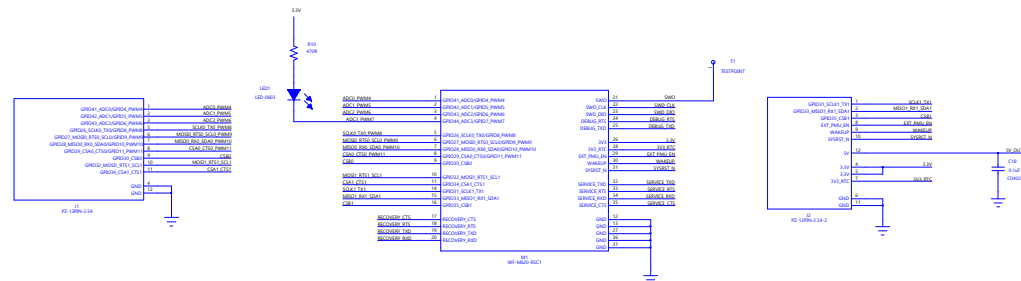
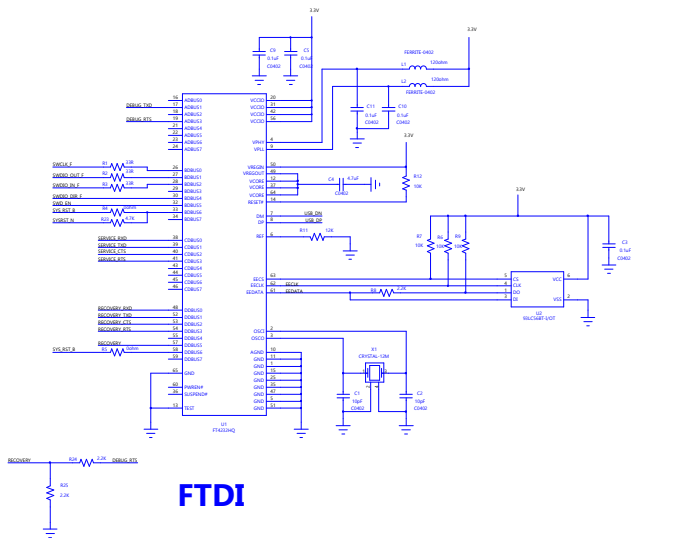
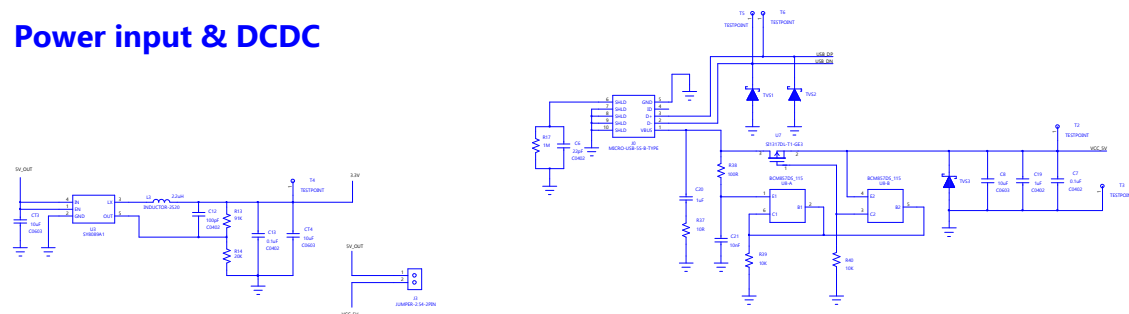


REVISION RECORD			
VER	REV NO	APPROVED	DATE



Power input & DCDC



COMPANY			
Seed			
TITLE			
MT3620 Mini Dev Board			
DESIGNER	Vincent yang	DATE	2019/5/6
CHECKED	Vincent Su	DATE	2019/5/6
QUALITY CONTROL		DATE	
REVIEWED		DATE	
SCALE	sheet 1 of 1		