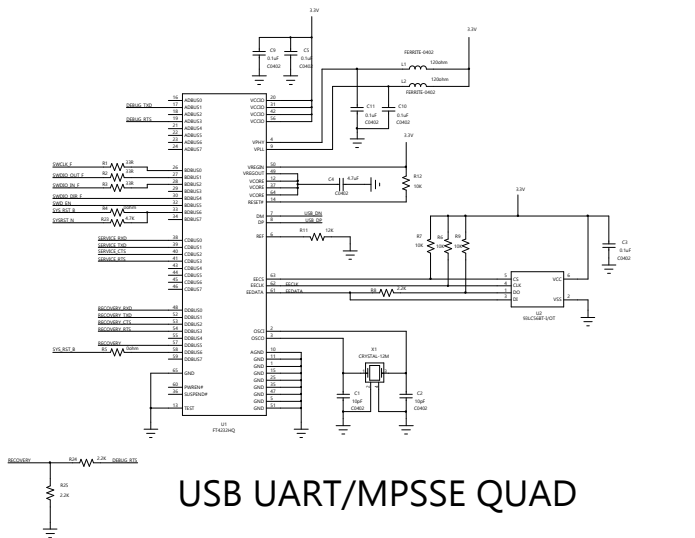
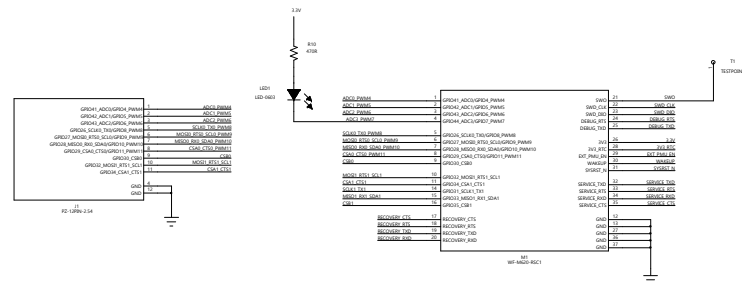


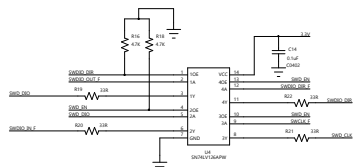
REVISION RECORD			
VER	REV NO	APPROVED	DATE



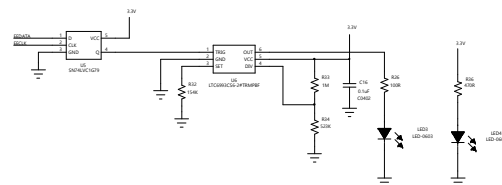
USB UART/MPSSSE QUAD



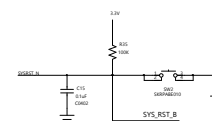
WIFI module



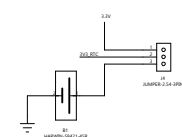
4-way bus buffer gate



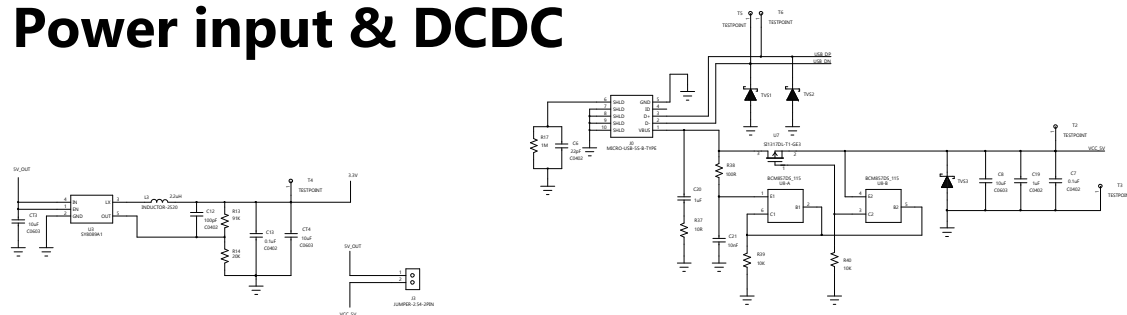
Monostable multivibrator&amp;LED



Reset &amp; RTC



## Power input & DCDC



COMPANY		Seed Studio			
TITLE		MT3620 DEVELOPMENT BOARD V2			
DESIGN	DATE	CODE	REV	DRAWING NO	REV
CHANGED	DATE				
QUALITY CONTROL	DATE				
RELEASED	DATE				
SCALE		A1		1	