

# 1. Description

## 1.1. Project

Project Name	digital_clock
Board Name	custom
Generated with:	STM32CubeMX 6.12.0
Date	09/20/2024

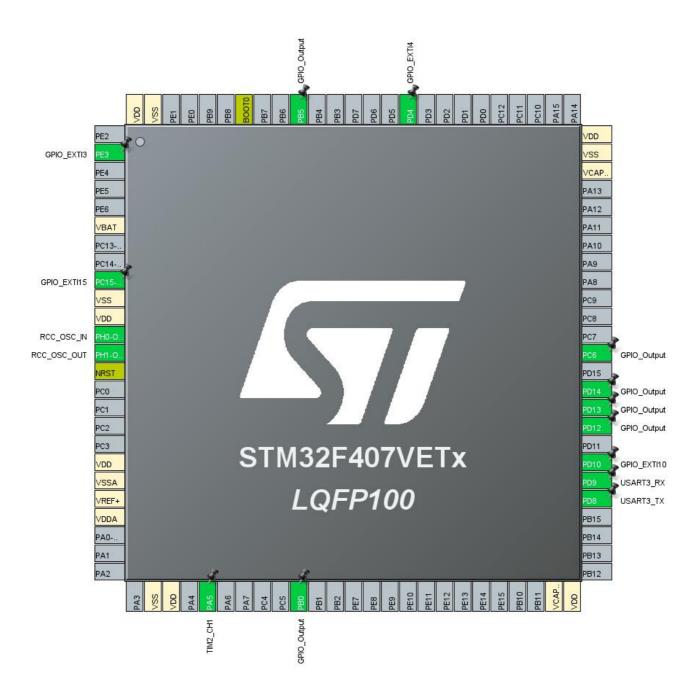
### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F407/417
MCU name	STM32F407VETx
MCU Package	LQFP100
MCU Pin number	100

## 1.3. Core(s) information

Core(s)	Arm Cortex-M4

# 2. Pinout Configuration

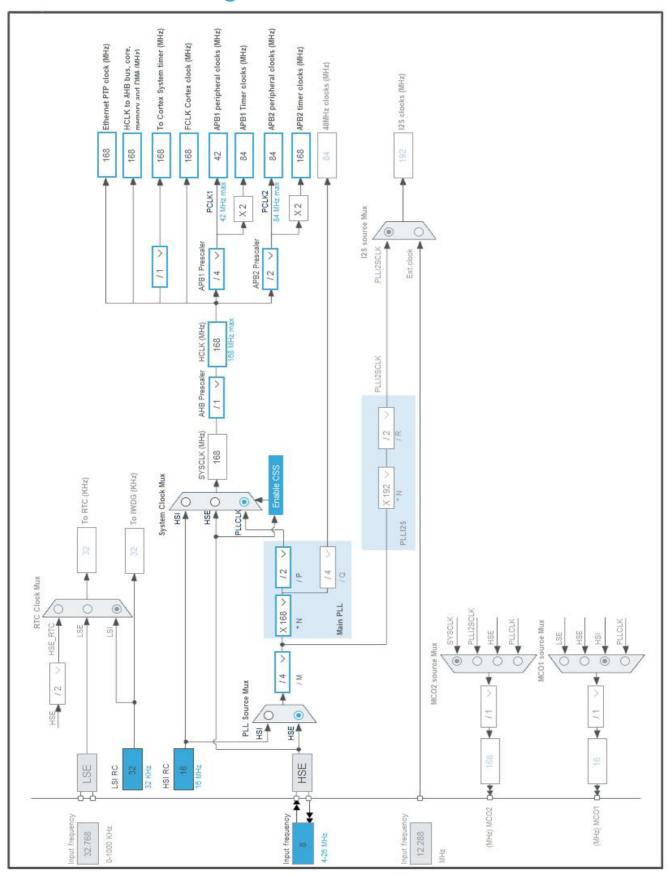


# 3. Pins Configuration

Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP100	(function after		Function(s)	
	reset)		,	
2	PE3	I/O	GPIO_EXTI3	
6	VBAT	Power		
9	PC15-OSC32_OUT	I/O	GPIO_EXTI15	
10	VSS	Power		
11	VDD	Power		
12	PH0-OSC_IN	I/O	RCC_OSC_IN	
13	PH1-OSC_OUT	I/O	RCC_OSC_OUT	
14	NRST	Reset		
19	VDD	Power		
20	VSSA	Power		
21	VREF+	Power		
22	VDDA	Power		
27	VSS	Power		
28	VDD	Power		
30	PA5	I/O	TIM2_CH1	
35	PB0 *	I/O	GPIO_Output	
49	VCAP_1	Power		
50	VDD	Power		
55	PD8	I/O	USART3_TX	
56	PD9	I/O	USART3_RX	
57	PD10	I/O	GPIO_EXTI10	
59	PD12 *	I/O	GPIO_Output	
60	PD13 *	I/O	GPIO_Output	
61	PD14 *	I/O	GPIO_Output	
63	PC6 *	I/O	GPIO_Output	
73	VCAP_2	Power		
74	VSS	Power		
75	VDD	Power		
85	PD4	I/O	GPIO_EXTI4	
91	PB5 *	I/O	GPIO_Output	
94	BOOT0	Boot		
99	VSS	Power		
100	VDD	Power		

<sup>\*</sup> The pin is affected with an I/O function

# 4. Clock Tree Configuration



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# 1. Power Consumption Calculator report

### 1.1. Microcontroller Selection

Series	STM32F4
Line	STM32F407/417
мси	STM32F407VETx
Datasheet	DS8626_Rev8

### 1.2. Parameter Selection

Temperature	25
Vdd	3.3

### 1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

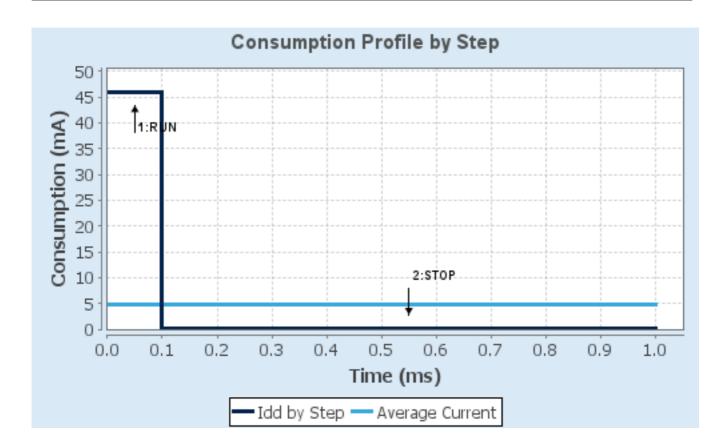
## 1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	Scale1-High	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	168 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator LP Flash-PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	46 mA	280 μA
Duration	0.1 ms	0.9 ms
DMIPS	210.0	0.0
Ta Max	98.47	104.96
Category	In DS Table	In DS Table

### 1.5. Results

Sequence Time	1 ms	Average Current	4.85 mA
Battery Life	29 days, 4 hours	Average DMIPS	210.0 DMIPS

### 1.6. Chart



# 2. Software Project

## 2.1. Project Settings

Name	Value
Project Name	digital_clock
Project Folder	C:\Project\digital_clock\digital_clock
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F4 V1.28.1
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

## 2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

### 2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_TIM2_Init	TIM2
4	MX_TIM6_Init	TIM6
5	MX_USART3_UART_Init	USART3

## 3. Peripherals and Middlewares Configuration

#### 3.1. RCC

#### High Speed Clock (HSE): Crystal/Ceramic Resonator

#### 3.1.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 5 WS (6 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

#### 3.2. SYS

Timebase Source: SysTick

3.3. TIM2

Clock Source: Internal Clock Channel1: PWM Generation CH1

#### 3.3.1. Parameter Settings:

#### **Counter Settings:**

Prescaler (PSC - 16 bits value) 41999 \*
Counter Mode Up

Counter Period (AutoReload Register - 32 bits value ) 9 \*

Internal Clock Division (CKD) No Division auto-reload preload Disable

**Trigger Output (TRGO) Parameters:** 

Master/Slave Mode (MSM bit)

Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx\_EGR)

**PWM Generation Channel 1:** 

Mode PWM mode 1

Pulse (32 bits value) 4 \*

Output compare preload Enable
Fast Mode Disable
CH Polarity High

#### 3.4. TIM6

mode: Activated

#### 3.4.1. Parameter Settings:

#### **Counter Settings:**

Prescaler (PSC - 16 bits value) 84-1 \*

Counter Mode Up

Counter Period (AutoReload Register - 16 bits value ) 1000-1 \*

auto-reload preload Disable

**Trigger Output (TRGO) Parameters:** 

Trigger Event Selection Reset (UG bit from TIMx\_EGR)

#### 3.5. **USART3**

#### **Mode: Asynchronous**

#### 3.5.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

#### **Advanced Parameters:**

Data Direction Receive and Transmit

Over Sampling 16 Samples

<sup>\*</sup> User modified value

# 4. System Configuration

## 4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label	
RCC	PH0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a		
	PH1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a		
TIM2	PA5	TIM2_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low		
USART3	PD8	USART3_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *		
	PD9	USART3_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High		
GPIO	PE3	GPIO_EXTI3	External Interrupt  Mode with  Rising/Falling edge	No pull-up and no pull-down	n/a		
	PC15- OSC32_OU T	GPIO_EXTI15	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a		
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low		
	PD10	GPIO_EXTI10	External Interrupt  Mode with  Rising/Falling edge	No pull-up and no pull-down	n/a		
	PD12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low		
	PD13	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low		
	PD14	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low		
	PC6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low		
	PD4	GPIO_EXTI4	External Interrupt  Mode with  Rising/Falling edge	No pull-up and no pull-down	n/a		
	PB5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low		

## 4.2. DMA configuration

nothing configured in DMA service

## 4.3. NVIC configuration

## 4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Pre-fetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	15	0	
EXTI line3 interrupt	true	0	0	
EXTI line4 interrupt	true	0	0	
USART3 global interrupt	true	0	0	
EXTI line[15:10] interrupts	true	0	0	
TIM6 global interrupt, DAC1 and DAC2 underrun error interrupts	true	0	0	
PVD interrupt through EXTI line 16	unused			
Flash global interrupt	unused			
RCC global interrupt	unused			
TIM2 global interrupt	unused			
FPU global interrupt	unused			

## 4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler	
Non maskable interrupt	false	true	false	
Hard fault interrupt	false	true	false	
Memory management fault	false	true	false	
Pre-fetch fault, memory access fault	false	true	false	
Undefined instruction or illegal state	false	true	false	
System service call via SWI instruction	false	true	false	
Debug monitor	false	true	false	
Pendable request for system service	false	true	false	
System tick timer	false	true	true	
EXTI line3 interrupt	true	true	true	
EXTI line4 interrupt	true	true	true	
USART3 global interrupt	true	true	true	
EXTI line[15:10] interrupts	true	true	true	

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
TIM6 global interrupt, DAC1 and DAC2 underrun error interrupts	true	true	true

<sup>\*</sup> User modified value

# 5. System Views

5.1. Category view

5.1.1. Current

			Middleware			
System Core	Analog	Timers	Connectivity	Multimedia	Security	Computing
DMA		TIM2 <b>⊘</b>	USART3 ♥			
GPIO ♥		TIM6 🤡				
NVIC <b>⊘</b>						
RCC ♥						
sys 🤡						

### 6. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32f405-415\_407-

417\_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis model/stm32f405-415 407-

417\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4-svd.zip

Description

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

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