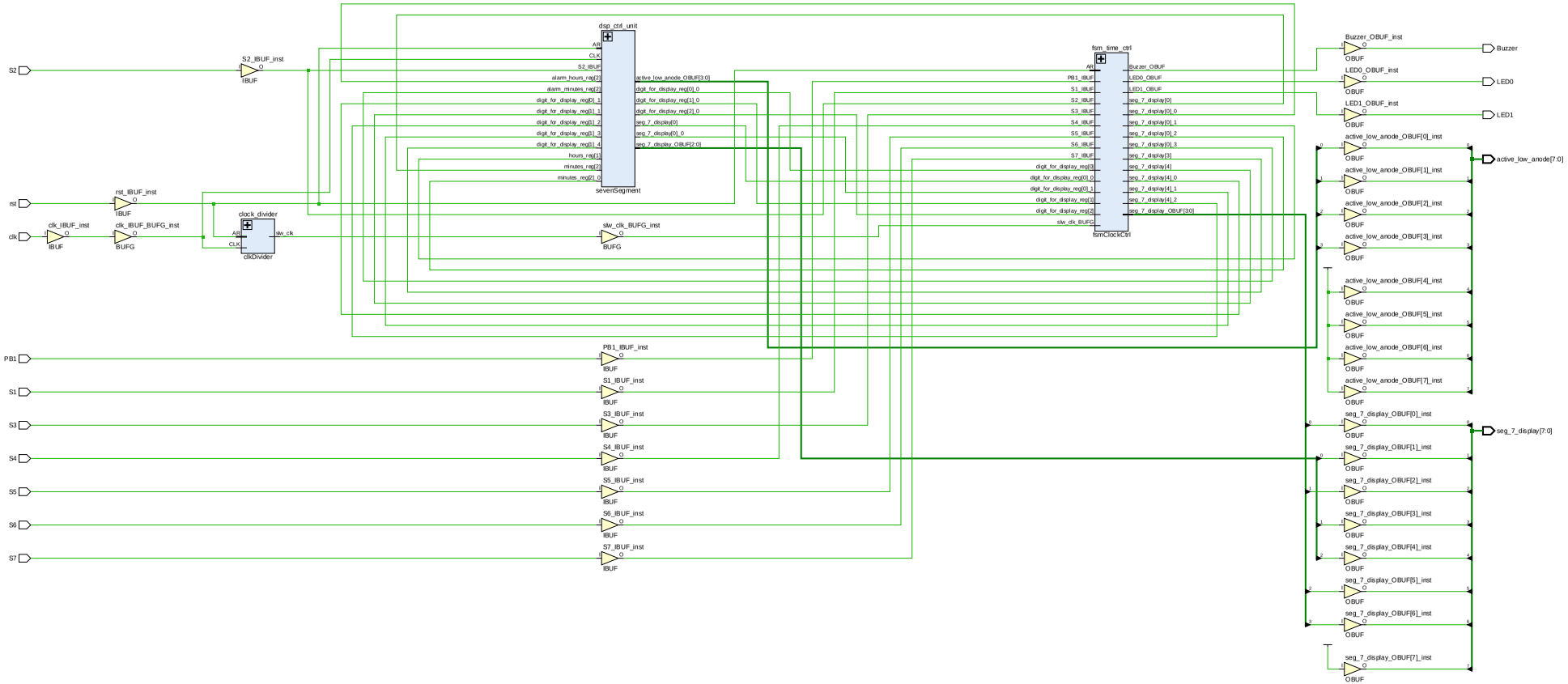
dsp_ctrl_unit
AB
CLK
S2_IBUF
alarm_hours_reg[2]
alarm_minutes_reg[2]
digit_for_display_reg[0]_1
digit_for_display_reg[1]_1
digit_for_display_reg[1]_2
digit_for_display_reg[1]_3
digit_for_display_reg[1]_4
hours_reg[1]
minutes_reg[2]
minutes_reg[2]_0
active_low_anode_OIBUF[3:0]
digit_for_display_reg[0]_0
digit_for_display_reg[1]_0
digit_for_display_reg[2]_0
seg_7_display[0]
seg_7_display[0]_0
seg_7_display_OBUF[2:0]
sevenSegment
fsm_time_ctrl
AB
PB1_IBUF
S1_IBUF
S2_IBUF
S3_IBUF
S4_IBUF
S5_IBUF
S6_IBUF
S7_IBUF
digit_for_display_reg[3]
digit_for_display_reg[0]_0
digit_for_display_reg[0]_1
digit_for_display_reg[3]
digit_for_display_reg[2]
slw_clk_BUFG
Buzzer_OBUF
LED0_OBUF
LED1_OBUF
seg_7_display[0]
seg_7_display[0]_0
seg_7_display[0]_1
seg_7_display[0]_2
seg_7_display[0]_3
seg_7_display[3]
seg_7_display[4]
seg_7_display[4]_0
seg_7_display[4]_1
seg_7_display[4]_2
seg_7_display_OBUF[3:0]
fsmClockCtrl
S2
S2_IBUF_inst
IBUF
rst
rst_IBUF_inst
IBUF
clk
clk_IBUF_inst
IBUF
clk_IBUF_BUFG_inst
BUFG
clock_divider
AB
CLK
slw_clk
clkDivider
slw_clk_BUFG_inst
BUFG
PB1
PB1_IBUF_inst
IBUF
S1
S1_IBUF_inst
IBUF
S3
S3_IBUF_inst
IBUF
S4
S4_IBUF_inst
IBUF
S5
S5_IBUF_inst
IBUF
S6
S6_IBUF_inst
IBUF
S7
S7_IBUF_inst
IBUF
Buzzer_OBUF_inst
OBUF
Buzzer
LED0_OBUF_inst
OBUF
LED0
LED1_OBUF_inst
OBUF
LED1
active_low_anode_OBUF[0]_inst
OBUF
active_low_anode_OBUF[1]_inst
OBUF
active_low_anode_OBUF[2]_inst
OBUF
active_low_anode_OBUF[3]_inst
OBUF
active_low_anode_OBUF[4]_inst
OBUF
active_low_anode_OBUF[5]_inst
OBUF
active_low_anode_OBUF[6]_inst
OBUF
active_low_anode_OBUF[7]_inst
OBUF
active_low_anode[7:0]
seg_7_display_OBUF[0]_inst
OBUF
seg_7_display_OBUF[1]_inst
OBUF
seg_7_display_OBUF[2]_inst
OBUF
seg_7_display_OBUF[3]_inst
OBUF
seg_7_display_OBUF[4]_inst
OBUF
seg_7_display_OBUF[5]_inst
OBUF
seg_7_display_OBUF[6]_inst
OBUF
seg_7_display_OBUF[7]_inst
OBUF
seg_7_display[7:0]