

Roaa

4/16/2023

- 8:20: modified the size of dataMem module to make each slot the size of a byte instead of a word
- 8:35: modified the control unit module adding a new control signal named read_Write
- 8:50: modified the imm gen to accommodate the lw instruction
- 9:07: modified the dataMem to accommodate all store instructions

4/19/2023

- Created test files for all instruction types and made fixed the final bugs

Seif

4/12/2023

- 3:00pm: imported the given alu and made the shifter
- 4:00pm: modified the control unit in order to accommodate the opcodes of all instructions

4/13/2023

- 2:00pm: Made the branching unit and modified ALU in order to support lui and auipc and ALU control and changed the pc mux and the write data mux to 4 by 2 muxes where they take 3 inputs
- 2:30pm: Modified ALU control