

CME 2203 Lab 6 Pre-lab

Due Date: 11 December 2019, 13:00

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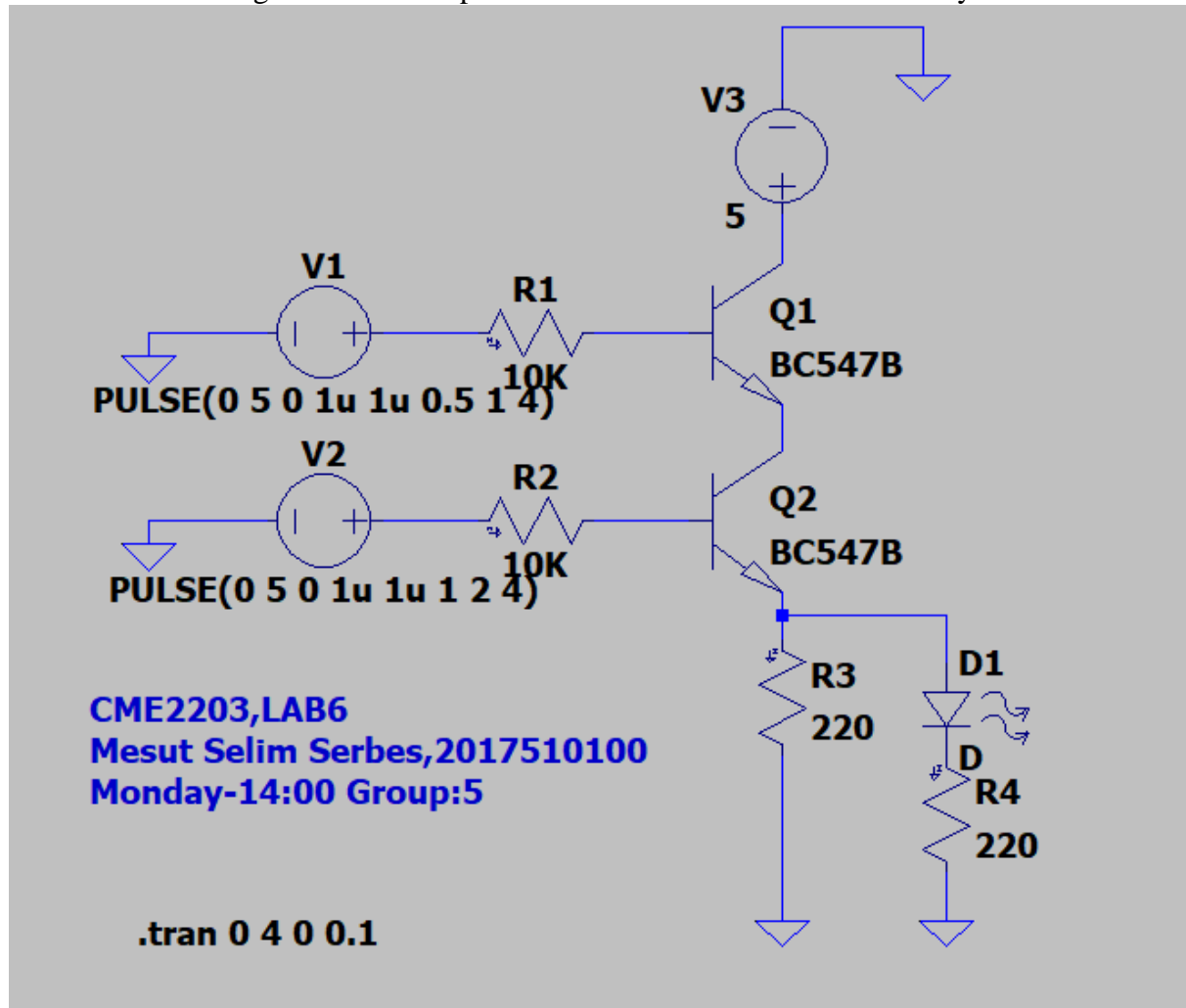
Student Number: 2017510100

Session- Group: (e.g. Monday 14:00, 5)

Subject: Transistors

Simulation 1


Draw the following circuit on LTSpice Schematics and save to a folder of your choice.




1. For each screenshot, top of the window should contain the graph plot panes, and the bottom of the window should contain:
 - a. The circuit diagram
 - b. The text label as in previous pre-labs
 - c. The Spice Error Log (see instructions below on showing the error log)
2. Insert the following components on your circuit:

- a. Three voltage sources V1, V2 and V3
 - b. Four resistors R1, R2, R3 and R4
 - c. Five GNDs
 - d. One LED
 - e. Two NPN transistors Q1 and Q2
3. Adjust the component values as follows:
- a. For V3 we can just assign a DC value of +5V.
 - b. We want V1 to act as a switch we open and close periodically as before, so we want it to produce a square wave voltage pattern. To achieve this, right click on V1 and click Advanced. Select PULSE. Enter the parameters as follows:

i. Vinitial(V)	:	0
ii. Von(V)	:	5
iii. Tdelay(s)	:	0
iv. Trise(s)	:	1u
v. Tfall(s)	:	1u
vi. Ton(s)	:	0.5
vii. Tperiod(s)	:	1
viii. Ncycles	:	4
 - c. Same thing for V2, but we want it to have a longer period. Right click on V1 and click Advanced. Select PULSE. Change only the given parameters as follows (others are the same as V1):

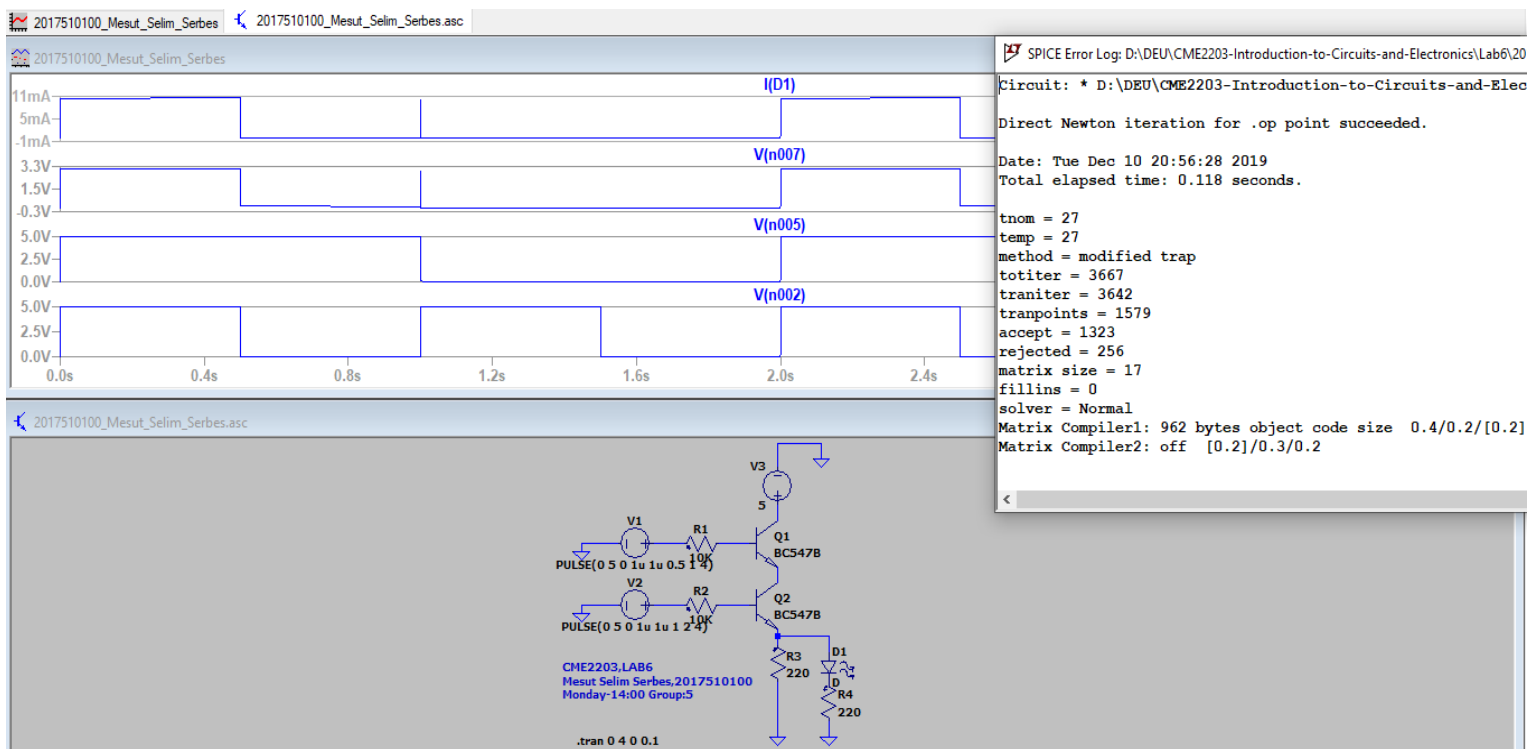
i. Ton(s)	:	1
ii. Tperiod(s)	:	2
 - d. Set the resistor values to $R1=R2=10K\Omega$, the resistor $R3=220\Omega$ and diode resistor $R4 = 220 \Omega$
 - e. For both Q1 and Q2, right click on the transistor. Click *Pick New Transistor*, and choose BC547B from the list. Click OK and close the dialog box (check you see BC547B on the circuit for both transistors).
 - f. Finally, go to Tools\Color Preferences. Select the Waveform tab, and click on the background of the graph, you should see “Click to edit Background color” and pull the Red, Green and Blue sliders to 255 to make it White.
4. Now, we are ready to run the simulation! Click on the running man  and edit the simulation command (Remember, you can also change this command later by going to Simulate\Edit Simulation Command):
- a. Under the Transient tab, select the following parameters:

i. Stop Time	:	4
ii. Time to Start Saving Data	:	0
iii. Maximum Timestep	:	0.1 and click OK.

Click the running man . You should see an empty graph on top of the window now. Let's fill it with graphs!

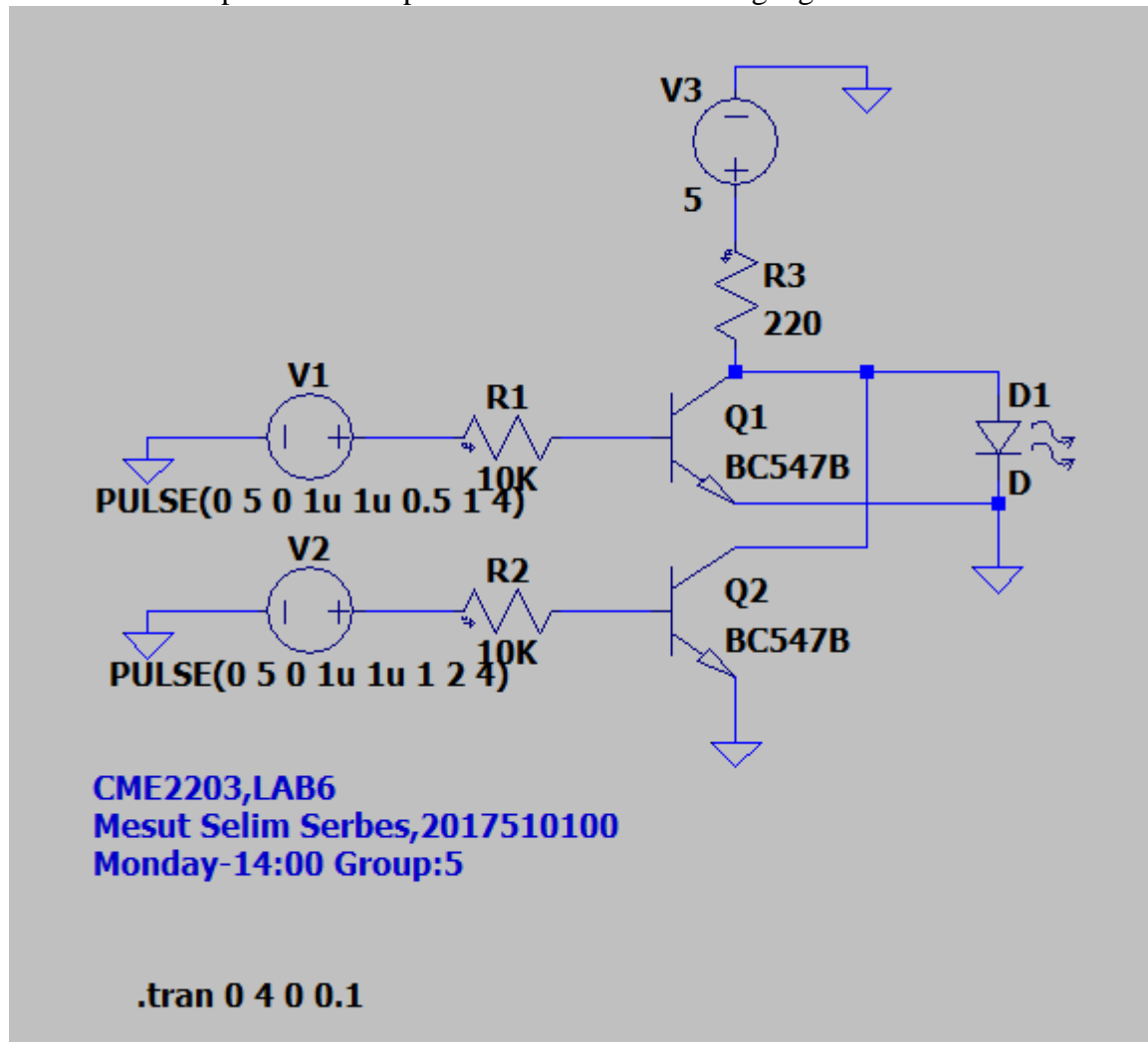
- b. Click the red probe appearing on the circuit to the wire connecting to positive side of V1 (node 2). Right click on V(n002) and change default color to blue to make it more visible against White background. This is logic input A.

- c. Now, move the cursor to the graph, right click and select Add Plot Pane. You should have a total of four plot panes. Click the red probe appearing on the circuit to the wire connecting to positive side of V2 (node 5). Right click on V2(n005) and change default color to blue to make it more visible against White background. This is logic input B.
- d. Now, move the cursor to the graph, right click and select Add Plot Pane. Click on the wire connecting to the positive side of the LED (node 4). Make its color blue as before. This is the logic output. Now you should have a total of three voltage plot panes.
- e. Now draw the current through the LED, so you should click exactly on the diode when you see the current meter symbol (a red arrow inside a black clamp). Again, make the trace color blue. So you should have a total of four panes on your graph.
- f. Finally, we want to view the Spice Error Log on the right side of the circuit. Go to View\SPICE Error Log and carry this window to the right side of the circuit. It should be visible on your screenshot. It should NOT block the graphs.



Simulation 2

Now we will repeat the same procedure for the second logic gate circuit:





1. Insert the following components on your circuit:
 - a. Three voltage sources V1, V2 and V3
 - b. Three resistors R1, R2 and R3
 - c. Five GNDs
 - d. One LED
 - e. Two NPN transistors Q1 and Q2
2. Adjust the component values as follows:
 - a. For V3 we can just assign a DC value of +5V.
 - b. We want V1 to act as a switch we open and close periodically as before, so we want it to produce a square wave voltage pattern. To achieve this, right click on V1 and click Advanced. Select PULSE. Enter the parameters as follows:

i. Vinitial(V)	:	0
ii. Von(V)	:	5
iii. Tdelay(s)	:	0
iv. Trise(s)	:	1u
v. Tfall(s)	:	1u
vi. Ton(s)	:	0.5
vii. Tperiod(s)	:	1
viii. Ncycles	:	4

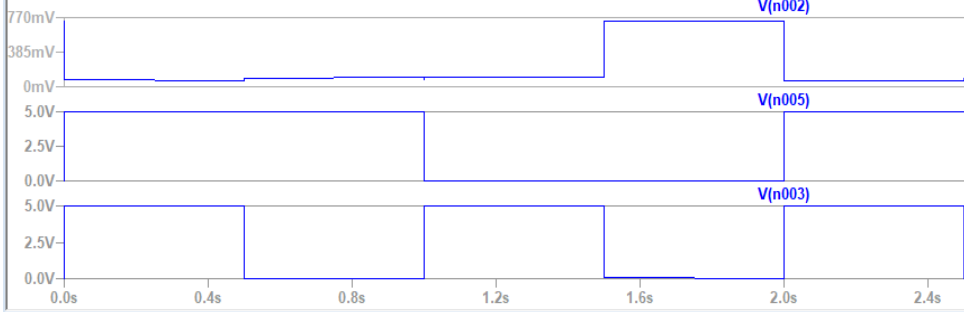
- c. Same thing for V2, but we want it to have a longer period. Right click on V1 and click Advanced. Select PULSE. Change only the given parameters as follows (others are the same as V1):

i. Ton(s)	:	1
ii. Tperiod(s)	:	2

- d. Set the resistor values to $R1=R2=10K\Omega$ and $R3=220\Omega$.
- e. For both Q1 and Q2, right click on the transistor. Click Pick New Transistor, and choose BC547B from the list. Click OK and close the dialog box (check you see BC547B on the circuit for both transistors).
3. Now, we are ready to run the simulation! Click on the running man  and edit the simulation command (Remember, you can also change this command later by going to Simulate\Edit Simulation Command):
- a. Under the Transient tab, select the following parameters:
- | | | |
|-------------------------------|---|-------------------|
| i. Stop Time | : | 4 |
| ii. Time to Start Saving Data | : | 0 |
| iii. Maximum Timestep | : | 0.1 and click OK. |

Click the running man . You should see an empty graph on top of the window now. Let's fill it with graphs!

- b. Click the red probe appearing on the circuit to the wire connecting to positive side of V1 (node 3). Change the trace color to blue as before. Now, move the cursor to the graph, right click and select Add Plot Pane.
- c. Click on the wire connecting to the positive side of V2 (node 5). Make its trace color blue. Add another plot pane as described above.
- d. Click on the wire connecting to the positive side of the LED. Make its trace color blue. So you should have a total of three voltage plot panes on your graph.
- e. Finally, we want to view the Spice Error Log on the right side of the circuit. Go to View\SPICE Error Log and carry this window to the right side of the circuit. It should be visible on your screenshot and added to your report.



SPICE Error Log: D:\DEU\CME2203-Introduction-to-Circuits-and-Electronics\Lab6\2017510100_Mesut_Selim_Serbes.asc

Circuit: * D:\DEU\CME2203-Introduction-to-Circuits-and-Electronics\Lab6\2017510100_Mesut_Selim_Serbes.asc

Direct Newton iteration for .op point succeeded.

Date: Tue Dec 10 21:18:24 2019

Total elapsed time: 0.098 seconds.

tnom = 27

temp = 27

method = modified trap

totiter = 3540

traniter = 3533

tranpoints = 1581

accept = 1325

rejected = 256

matrix size = 15

fillins = 0

solver = Normal

Matrix Compiler1: 862 bytes object code size 0.4/0.2/[0.1]

Matrix Compiler2: off [0.1]/0.2/0.2

