

**BIRLA INSTITUTE OF TECHNOLOGY & SCIENCE, PILANI
WORK INTEGRATED LEARNING PROGRAMMES (WILP) DIVISION**

Final Evaluation Sheet

BITS ID No. : 2022HT80170

NAME OF THE STUDENT : SELVA KUMAR S

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NAME OF THE SUPERVISOR : SIVA SELVAMANI

PROJECT TITLE : Creating and deploying an open standard SPI
on an FPGA, involving C based test case
development for emulation


Final Evaluation Please put a tick (✓) mark in the appropriate box)

S. No.	Evaluation Component	Excellent	Good	Fair	Poor
1.	Final Project Report	✓			
2.	Final Seminar and Viva-Voce	✓			

S. No.	Evaluation Criteria	Excellent	Good	Fair	Poor
1	Technical/Professional Competence	✓			
2	Work Progress and Achievements	✓			
3	Documentation and expression		✓		
4	Initiative and Originality	✓			
5	Research & Innovation	✓			
6	Relevance to the work environment		✓		

Please ENCIRCLE the Recommended Final Grade: Excellent

Remarks of the Supervisor: This project is an innovative initiative within the organization. The outcome covered by this project seems to be interesting so that we are commissioning for our future projects in the organization. This idea is reducing the efforts spent and efficiently deliver the Organization's deliverables.

	Supervisor	Additional Examiner
Name	Siva Selvamani	Siva Selvamani
Qualification	M.Tech	M.Tech
Designation	Staff Engineer	Staff Engineer
Employing Organization & Location	Qualcomm India Pvt Ltd Bengaluru	Qualcomm India Pvt Ltd Bengaluru
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Place & Date	30 - 04 - 2024	30 - 04 - 2024