

# Neural Machine Translation

Seminar Report-NLP

Arul, Ehsan

Rheinische Friedrich-Wilhelms-Universität Bonn

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**Abstract—Abstract**

## I. INTRODUCTION

## II. DEEP LEARNING - AN INTRODUCTION

In the recent years, Deep learning has been making a big impact in various fields of computer science. This impact is profound in the perceptual learning task in the fields like computer vision, natural language processing, etc., [10]. Though the idea of training a multi layered neural network to perform function approximation is known to the research community for at least a couple of decades[11], due to the nature of training problem requiring large computational resources, the multi layered neural network remained less practical. With the advent of general purpose graphical processing units(GPGPUs) and the availability of training data, the neural networks are now a practical solution for many perceptual tasks. One of the early success of deep learning was in the field of image classification. In the annual ImageNet classification challenge [3], AlexNet [9] showed a remarkable improvement in the state-of-the-art accuracy. Within a few years, more sophisticated architectures like Google Inception Network [12], Deep Residual Network [4], etc., has improved the accuracy to be comparable with a human in that task. The generality of the neural network made it easily possible to be used for a wide variety of tasks. With more people working on deep learning and the ideas arising in solving on the problems being easily transferable, the deep learning is the state-of-the-art for many learning tasks. In the following section, we will briefly summarize the basics of deep learning.

The basic building block of a Multilayer network, or in more technical term Multilayer perceptron(MLP) is a perceptron. The perceptron 3 closely resembles a neuron in a human brain 1. A perceptron takes a set of input values, computes a weighted sum of the inputs, and outputs a scalar value of a after applying an activation function (mostly non-linear). The weights for computing the weighted sum and the threshold in the activation function are initially set to random values and are learned from the training data. Mathematically, a perceptron with the weight matrix  $A$  and the threshold  $T$  for an step activation function, for the input vector  $x$ , outputs the following,

$$f(x) = \begin{cases} 1, & \text{if } Ax > c \\ 0, & \text{otherwise} \end{cases}$$

A layer has many number of neurons and many layers are stacked one on top of the other to form a MLP. An MLP with very many layers is called as Deep Neural Network(DNN). In

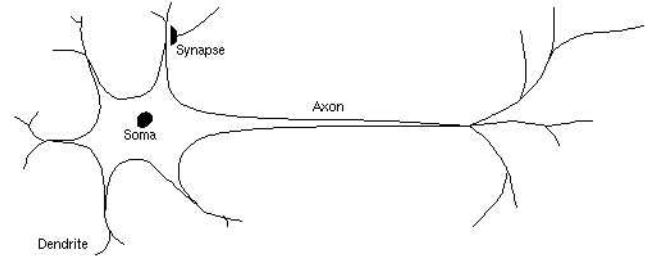


Fig. 1: A biological neuron.

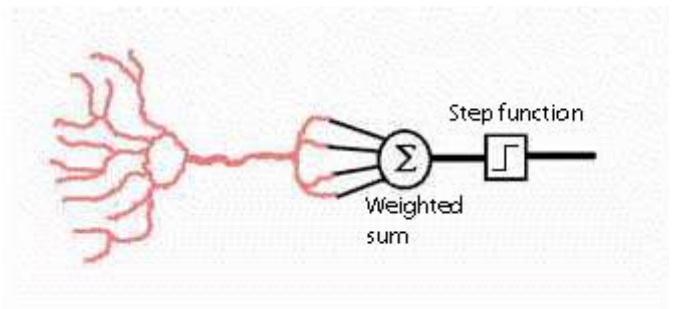


Fig. 2: An artificial neuron (perceptron)

the following section we will discuss the properties of DNNs and how they are useful in the context of supervised machine learning.

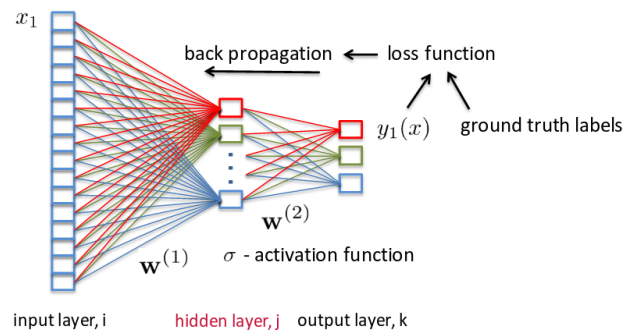


Fig. 3: Layered representation in an MLP

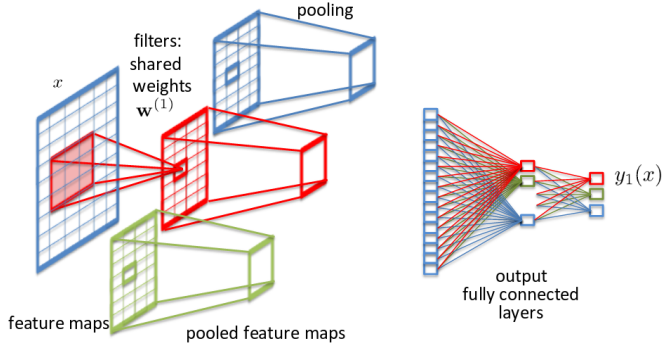


Fig. 4: CNN

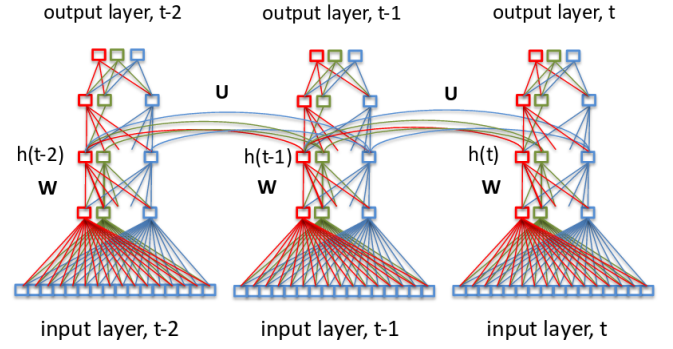


Fig. 5: Recurrent connections in a RNN

### A. Supervised learning

Supervised learning is the problem of predicting a new output  $y'$  for a new input  $x'$ , and set of training data that provides a set of input and output  $\{x \mapsto y\}$ . Most of the supervised learning problems can be formulated as either a classification or regression problem. Classification is the problem of predicting the label for a given  $x'$  among a set of given labels  $\{Y\}$  while regression of predicting a continuous valued output for a given input. The usual way of doing supervised learning is to extract features from the given data and train a model to perform classification or regression.

$$x \xrightarrow[\text{Feature extraction}]{\mathbb{D}(x)} x'_{\text{intermediate}} \xrightarrow[\text{classifier/regressor}]{\mathbb{F}(x'|\theta)} y$$

The power of the DNNs lie in the fact that the models learn the features directly from the given training data and avoids hand engineered features.

$$x \xrightarrow[\text{hierachical}]{\mathbb{M}(x|\Theta)} y$$

The DNNs with neurons in one layers being connected to all the neurons in the next layer are called Feed Forward Networks. The feed forward networks are harder to train since they have lots of parameters. To make a the DNNs learn useful representation for performing supervised learning, we need special types of connections between the neurons. Two of the most commonly used DNN architectures are Convolutional Neural Networks(CNN) and Recurrent Neural Networks(RNN). In the following section we will discuss these architectures in detail.

#### 1) CNN: todo

The Convolutional NN are shown in 4

2) RNN: The neurons in the RNNs have recurrent self connections. i.e.The output of the neurons are connected back to the inputs. Thus the output at time step  $t_1$  is computed with taking output at time step  $t_0$  as input. The recurrent connections are shown Fig.5 This enables the RNNs to have an internal memory. RNNs are trained with the a modified version of back propagation called **Back Propagation Through Time(BPTT)** [13].

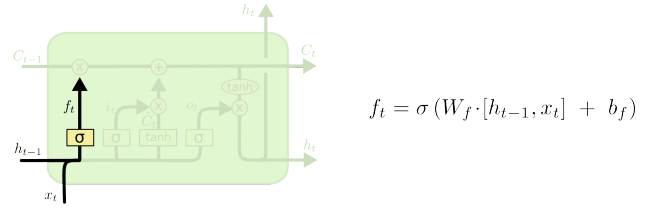


Fig. 6: Forget gate in LSTM

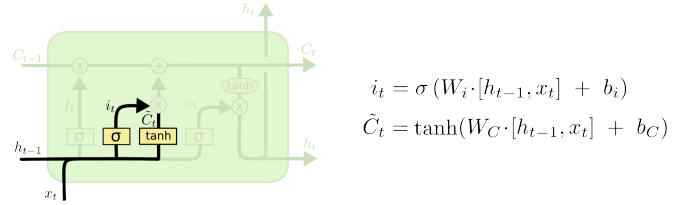


Fig. 7: Add gate computing parts to be modified in LSTM

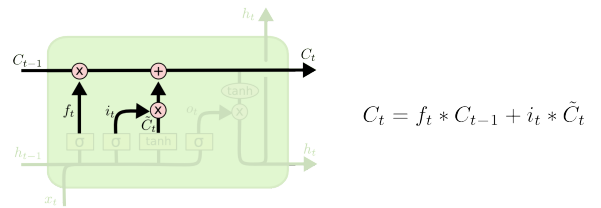


Fig. 8: Add gate computing data to be added to cell state in LSTM

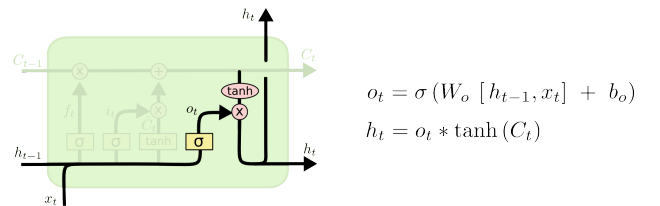


Fig. 9: Add gate computing output in LSTM

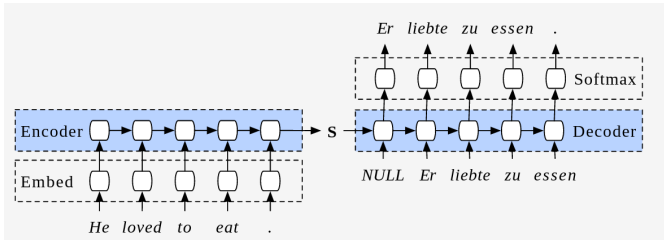


Fig. 10: Simple Encoder-Decoder architecture for machine translation

RNNs in general are harder to train and this is particularly evident when the BPTT is done over a larger number of time steps. This is because the gradients simply converge to zero after a few time steps. This problem known as **vanishing gradients problem** is a well studied phenomenon [2].

To alleviate the vanishing gradients problem, a special architecture called **Long Short Term Memory (LSTM)** [6] is used. An LSTM cell is shown in .

LSTM cells have separate internal memory vector and has three gates—forget gate, add gate, and output gate. To understand the Mathematical intuition behind these gates, let us consider a LSTM cell in a hidden state  $h$  and internal cell memory vector  $c_{t-1}$ . At time  $t$ , the cell takes input  $x_t$ , previous output  $h_{t-1}$  and update the cell memory to  $c_t$ , produces the output  $h_t$ . The forget gate computes a vector size  $c$  between 0 and 1. This vector determines which information should be preserved and what should be forgotten based on the current input  $x_t$  as shown in 6. This vector is later multiplied with the cell memory. If the vector is all zeros, multiplying makes all cell state to forget everything while all ones preserve everything. Then the add gate computes which parts in the cell states to be updated as shown in 7 and the new data to be updated as shown in 8. Finally the output gate generates the output  $h_t$  and does not modify the cell state as shown in 9.

### B. Related work

PBMT explanation. [8]

### C. Neural Machine Translation

Though usage of neural networks did not yield promising results in the early years, the recurrent neural networks started to achieve performance comparable to PBMT as in [7] and [5]. Most of the early architectures were simple encoder-decoder architectures. An simple working of encoder-decoder architecture for machine translation is shown in Fig.10. The architecture has an encoder that takes a sentence in source language and encodes it into a vector  $S$  of fixed length. The decoder takes the embedding  $S$  as input and generates the sentence in the target language. Some of the major drawbacks with the simple architecture is that

- 1) The encoder has to encode all the information in the source sentence into fixed size embedding  $S$ .
- 2) The decoder never sees the actual input sentence and has to rely completely on  $S$  for generating target sentence.

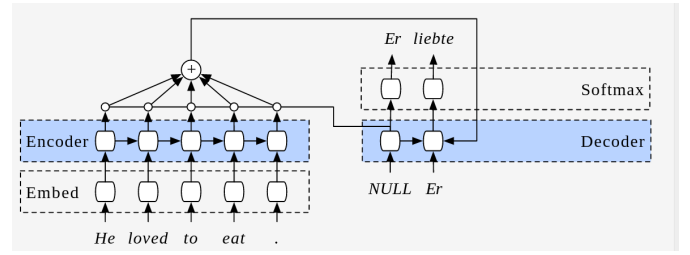


Fig. 11: Context for machine translation

- 3) Having fixed size embedding vector makes the architecture less flexible. Smaller size means less information where as using larger vector means for smaller sentences we need zero padding and more computation time.

The paper [1] address this issue of having a fixed size embedding by introducing the idea of context 11. The main proposal was to use

- 1) Encoder output for each word in the source sentence  $F_s$ .
- 2) One context vector  $C$  in the size of the input sentence that has values between 0 and 1.
- 3) Each element of the embedding  $F_s$  is multiplied with one element in  $C$ .

The whole embedding is made available to the decoder and  $C$  describes which part of the embedding should be focused to generate the current word based on the previous word.

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