

${\sf PMEM\text{-}Volatile} + {\sf DRAM\text{-}pthread}$	XXX	${\sf PMEM\text{-}Persist} + {\sf DRAM\text{-}pthread}$
${\sf PMEM\text{-}Volatile} + {\sf PMEM\text{-}pthread}$		${\sf PMEM\text{-}Persist} + {\sf PMEM\text{-}pmemobjlock}$



