

Smart Eraser

Project Charter

Heather Libeck - Chris Quesada - Juan Colin

Technical Advisor: Dr. Hovannes Kulhandjian

Friday, December 7, 2018

ECE 186A - Senior Design I

Fall 2018 - Dr. Aaron Stillmaker

California State University, Fresno

Electrical and Computer Engineering Department

CONTENTS

I	Introduction	1
II	Project Objectives and Success Criteria	2
III	High-Level Requirements	3
IV	Assumptions, Constraints, and Standards	4
IV-A	Camera Ethernet Connectivity and Data Transfer	5
IV-B	Data Storage	5
IV-C	Image Processing Program Design	5
IV-D	Translate Markings to Coordinate System (stepper motor rotations)	5
IV-E	Stepper Motor System Design	5
IV-F	Microcontroller Involvement	5
IV-G	Shortest Path Algorithm	6
IV-H	Motion-Detection Program	6
V	Project Description and Boundaries	6
VI	High-Level Risks	11
VII	Milestone Schedule	12
VIII	Gantt Charts	13
IX	Equipment and Budget	15
X	Roles of Team Members	15
XI	Stakeholder List	16
XII	Project Approval Requirements	16
XIII	Approvals	17
	References	17

LIST OF FIGURES

1	A rough layout of the final product	1
2	Block diagram of main system components	7
3	Flow Chart of the Smart Eraser System	8
4	GPIO Header pin layout	9
5	Overview of Interface for Ethernet Communications	9
6	Data sheet for the 23HS22-2804S stepper motor	10
7	Microstep resolution guide for the DM542 stepper motor driver	11
8	Dynamic current settings guide for the DM542 stepper motor driver	11
9	GANTT chart for Senior Design Semester 1 - Research Phase	13
10	GANTT chart for Senior Design Semester 2 - Implementation Phase	14

LIST OF TABLES

I	Senior Design Semester 1 - Research Phase.	12
II	Senior Design Semester 2 - Implementation Phase.	13
III	Estimated costs of components for project	15

REVISION HISTORY

Version 1 - finalized October 25, 2018. Changes made by: Heather Libeck, Chris Quesada, Juan Colin. Changes made: the initial rough draft of the Project Charter was created, including the proper sections and figures specified by Dr. Stillmaker.

ABSTRACT

The Smart Eraser is a tool created with the express purpose of making the lives of teachers easier, by allowing them to utilize valuable class time in a productive way. It aims to assist teachers who write lengthy, involved examples on a whiteboard while lecturing on the material they want their students to understand. The Smart Eraser will erase the information written on the whiteboard at the teacher's will, allowing them to continue lecturing without wasting precious class time on erasing the board between examples. This will allow students to learn in an environment with less interruptions and distractions, resulting in improved overall focus and retention.

The project itself, in the simplest terms, is an automatic whiteboard eraser with smart capabilities. The eraser will be on a tracking system that will allow it to cover the x-y plane of the whiteboard's surface, and it will move based on the instructions given to it by the microcontroller it is connected to. This microcontroller will have four push-buttons that will be programmed to do one of four activities: erase the board using smart processing, erase the entire board from top to bottom without smart processing, stop the eraser's movement in case of emergency, and reset the eraser's current process in order to send it back to its stand-by position. A camera will be at a fixed location facing the board to allow the entire whiteboard's image to be recorded. The image recorded will go through an image-processing algorithm, which will accomplish one of two things: it will detect the presence of a person in front of the whiteboard, causing the eraser to stop in its tracks, or it will detect the markings on the board and translate their locations into coordinates. These coordinates will then be processed in an algorithm which will determine the shortest path that needs to be taken in order to erase all the markings. Finally, after all procedures are done, the eraser will return to its stand-by position.

I. INTRODUCTION

The purpose of the Smart Eraser is to allow class time to be utilized for learning. It will assist teachers who write lengthy, involved examples on a whiteboard while lecturing, by erasing the board in between examples while the teacher continues to lecture. This will allow students to learn in an environment with less interruptions and distractions, which will result in improved overall focus and retention of the information being taught.

The Smart Eraser, in the simplest terms, is an automatic whiteboard eraser. The main deliverable of this project will be an eraser which can move left-to-right on a track, and up-and-down on a linear motion system attached to the track. This system will be able to detect where markings are on a whiteboard through the use of a camera and an image-processing program. The camera will send the image of the whiteboard to a microcontroller which will process the image, detect where the markings are, and convert their locations to a coordinate system that the mechanical aspects of the eraser will be able to read. This coordinate system will essentially convert the locations of the markings into rotations of the stepper motors attached to the tracks, and this will allow the eraser to move. The eraser will then find the quickest route to erase all of the markings, using a sorting algorithm, before returning to its stand-by position. Finally, the eraser will be able to detect an obstruction in front of the whiteboard, specifically the presence of a person, through the use of the camera and motion-detection technology. This will check if there is a person moving in front of the whiteboard, and if there is, the result would be an immediate termination of the process the Smart Eraser was carrying out; this is to ensure the safety of those around the Smart Eraser while it is operating. The figure below shows the rough layout of the Smart Eraser, and the numbered arrows are explained below it.

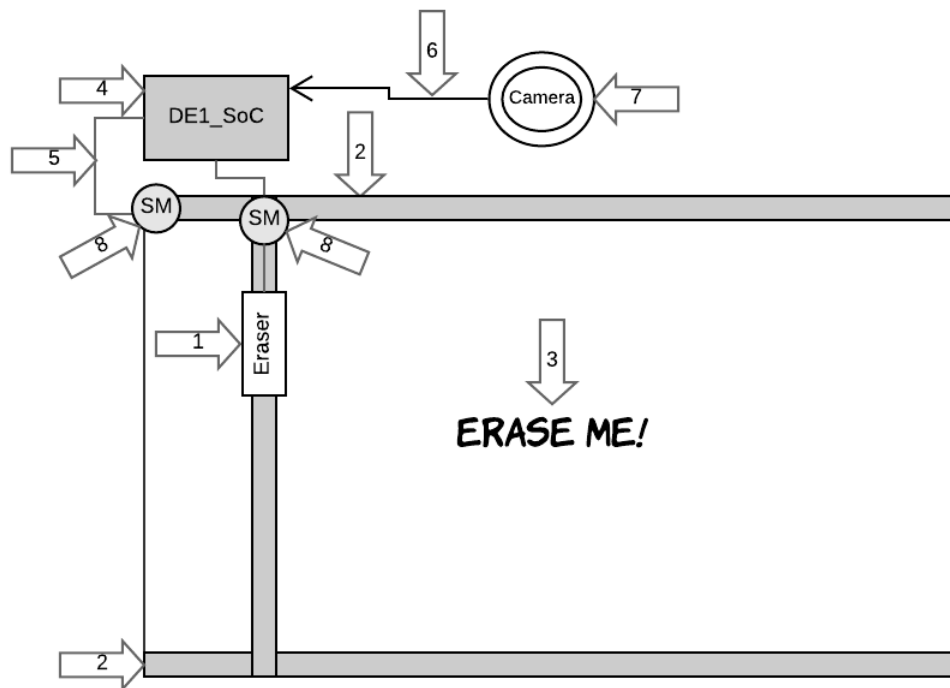


Fig. 1: A rough layout of the final product

- 1) The linear motion system with the attached eraser. This will allow the eraser to move in the y-axis direction.
- 2) The tracking system attached above and below the whiteboard. This will allow the eraser to move in the x-axis direction.

- 3) The text that will be erased. The image of this text will be seen through the camera, and the text's location will be determined through the use of image-processing software within the DE1_SoC microcontroller.
- 4) The DE1_SoC microcontroller, which will contain the image-processing program, the information on how to move the stepper motors, and the motion-detection program.
- 5) The wires connecting the DE1_SoC board to the stepper motors.
- 6) This arrow represents the flow of data from the camera that will be mounted across from the board, to the microcontroller for processing.
- 7) The camera that will be connected to the DE1_SoC board through an Ethernet cable.
- 8) The stepper motors (as well as the PCB that will be made for their H-Bridge connections).

The microcontroller that will be controlling the entire system has four push-buttons which will be utilized in the following way: push-button 0 will erase the board using the smart processing method, push-button 1 will erase the entire board without using the smart processing method, push-button 2 will stop the eraser in the middle of whatever process it was performing until it is pressed again, allowing it to resume the process it was carrying out, or until push-button 3 is pressed, which will reset the system and send the eraser back to its stand-by position.

Heather Libeck is responsible for being the project manager of this project. She will be researching the DE1_SoC microcontroller, its relevant components, the ARM assembly language it uses, and the GPIO pin connections it is capable of. She will also be researching the stepper motors, the drivers needed to operate them, and she will be contributing to the creation of the PCB for the H-bridges. Therefore, she will be in charge of all connections to the DE1_SoC, as well as the connections between the stepper motors, their H-bridge PCBs and drivers, and the additional power needed to make them work properly.

Chris Quesada will be researching the ethernet communication between the DE1_SoC and the camera, as well as the datasheet for the camera itself. He will also be researching the image processing program, how to implement it using the C programming language, and how this program will be able to run on the DE1_SoC. He will then research the motion-detection program, as well as the algorithm for finding the quickest route to the markings on the board, with assistance from Heather. Therefore, he will be in charge of all interactions with the camera and its smart processing capabilities.

Juan Colin will be researching the SDRAM interface standards in order to save images from the camera to the DE1_SoC. He will also be researching the physical mechanical system, its required connections, and the power system that will need to be created in order to allow it to run properly. He will then need to configure the power system so the mobile parts of the project can move the way they are intended to, and he will also be assisting in the creation of the PCBs for the H-bridges that will be connected to the stepper motors. Therefore, he will be in charge of the entire physical mechanical system of the eraser.

During the project life cycle, there will also be multiple deliverables that will need to be completed and turned in to Dr. Stillmaker. These deliverables will be worked on by all three members of the team to ensure a consistent flow of information throughout all written documentation.

II. PROJECT OBJECTIVES AND SUCCESS CRITERIA

The description of the Smart Eraser in the Project Description section of this document specifies what the ideal final product will be, so within this section, the actual objectives of the Smart Eraser will be listed.

Main Objectives

- Create a functioning mechanical system that allows the eraser to erase the entire whiteboard.
- Create a coordinate system based on processed image to move eraser to specific markings.
- Create an algorithm to sort the order in which the markings should erase to ensure the shortest path is taken.
- Create a functioning Smart Eraser that erases detected markings in a timely manner.

- Create an image processing program to detect said markings on the whiteboard.
- Create a motion-detection program to check for people obstructing the whiteboard.

In this section, the specific criteria that need to be met in order to consider this project a success will be listed. These criteria will help those contributing to the completion of this project to be able to measure the actual success of the final product. There will be simple success criteria listed, as well as more ambitious success criteria that will describe an ideal version of this project, including additional features that could be added if there is a significant amount of extra time after accomplishing the simple success criteria.

Simple success criteria

- The tracking system moves the eraser to all parts of the board Eraser erases the entire whiteboard with no smart processing (covers entire board)
- Image processing program converts abnormal color changes to array
 - Array converts to location coordinates of the color changes
- Coordinate system, in the form of stepper motor rotations, moves the eraser to a specific part of the board
- Camera connects to DE1_SoC board, image from camera saves to microcontroller SDRAM memory
- Image processing program on DE1_SoC works with camera to process images in real-time
- Location of markings found with camera via image processing convert to necessary coordinates for mechanical system to read
- Motion-detection program creates TRUE signal to be sent to microcontroller if large obstruction (a person) is in front of whiteboard, returns to FALSE when obstruction gone
- TRUE signal from motion-detection program halts the movement of the eraser, resumes the eraser's process when FALSE signal received
- Push-buttons on DE1_SoC perform specifications, stated in the Introduction section of this project charter, using interrupts

Ambitious success criteria

- Visual feedback on DE1_SoC board's LEDs, HEX display, or LCD screen on current process being performed by eraser
- Phone or tablet application shows a live feed of the whiteboard from the camera
 - Application can send specific coordinates to the whiteboard in order to “pick and choose” what section of the board to erase
- Attachable spray system applies whiteboard liquid cleaning solution to perform “full clean” of whiteboard
 - Timer on DE1_SoC tells eraser to perform a “full clean” during the night, when no one is using the classroom
- Eraser can be raised off of the whiteboard surface and subsequently re-pressed on to the board as needed
- Smart Eraser patent

III. HIGH-LEVEL REQUIREMENTS

The high-level requirements associated with the completion of this project are outlined in the following list.

The project should:

- Be completed with the outlined budget
- Be implementable within 2 semesters

- Be complex enough to warrant the title “senior design project”
- Produce a complete Project Charter outlining the various project information, figures, and tables
- Have significant, roughly equal portions of the project be completed by each team member
- Utilize material learned in core and technical elective classes throughout our college careers
- Produce a deliverable that can be presented in the Senior Project Presentation Day event usually held in the Satellite Student Union building on campus
- Be able to be scaled to a bigger surface than the prototype

IV. ASSUMPTIONS, CONSTRAINTS, AND STANDARDS

Based on the preliminary research conducted on what information, components, and protocols will be useful to know for the completion of this project, the team members wrote a list of their strengths and weaknesses to show what they can each do, and what needs more work. The specific courses with the relevant information needed for this project are also listed.

Heather Libeck

- Strengths: programming (assembly, some verilog), DE1_SoC programming, mathematics, debugging, circuit implementation, problem solving, technical writing, public speaking
- Weaknesses: circuitry design, coding algorithms, power systems, PCB design

Chris Quesada

- Strengths: programming (assembly, verilog), embedded systems, algorithms, brainstorming
- Weaknesses: circuitry design, mathematics, public speaking, ethernet capabilities

Juan Colin

- Strengths: electrical systems, circuitry design, problem solving, power systems, public speaking/relations
- Weaknesses: programming (assembly, verilog), technical writing and spelling

Based on this list, the background information that will need to be further researched is: image processing programs, connecting physical devices for the track of the eraser, camera and DE1_SoC connectivity, PCB design for the stepper motor H-bridges, and other research as necessary. A few possible constraints on the project are the budget funding, the additional power supply needed to make the more powerful stepper motors operate, and the power supply to the camera.

The following list outlines the relevant courses and the material that will be used from them.

- ECE 178 - Embedded Systems
 - Development of algorithms, setting up and using interrupts with the microcontroller, interfacing with peripherals on the microcontroller
- ECE 146 - Computer Networks
 - Ethernet connection between systems and how data is transferred over link
- ECE 90 - Principles of Electrical Circuits
 - Developing the power scheme and parameters for the stepper motors and track system.
- ECE 118 - Microprocessor Architecture and Programming
 - Recursion programming and algorithms developed for shortest-tree path, determining how to store memory in DE1_SoC, working with ARM processor, GPIO connections to DE1_SoC, using stepper motors, H-bridge, and power supply to operate stepper motors with DE1_SoC, programming push-buttons on DE1_SoC
- ECE 85 - Digital Logic Design

- Developing PCB for H-bridge connections, any state diagrams needed for logic between processes
- ECE 106 - Switching Theory and Logical Design
 - Developing flow charts and block diagrams of the overall system

More extensive research has also been done on specific parts of the project needed to allow the project to work as intended, and they are listed below.

A. Camera Ethernet Connectivity and Data Transfer

After further research, the original idea of connecting and powering the camera over an Ethernet connection needed to be modified a bit. In order for the camera to be powered through Ethernet, the connection needed to provide 48V to the camera. Since the DE1_SoC can only supply a maximum of 5V to any external device, the camera will now be powered through the included 12V 1A power adapter.

B. Data Storage

The images taken from the camera will need to be saved to the DE1_SoC board in order to allow the image processing program to work on them. The SDRAM on the DE1_SoC board will be utilized, using the load and store commands in the ARM programming language. The coordinate system that will need to be created from the marking locations will also be saved to the memory on the DE1_SoC, as well as the program that connects the stepper motors to the system as well.

C. Image Processing Program Design

After receiving feedback, it was decided that it would be an easier route to abandon trying to interface with the DE1_SoC through MATLAB. This was the initial plan due to MATLAB's extensive image processing libraries. However, it has been decided that C will be more beneficial when using the Altera Monitor program. Further research will be towards learning how to do image processing specifically in C and how detect marks on the whiteboard.

D. Translate Markings to Coordinate System (stepper motor rotations)

Through team collaboration, it was decided that each stepper motor rotation would represent one pixel length. This would be how a coordinate system can be developed. For example, if the image processing algorithm picks up at mark that is 56 pixels from the left of the image, and 178 pixels from the top of the image, this would result in 56 rotations to the right on the x-axis motor and 178 rotations down on the y-axis motor. Further research and configuration would need to be done towards how many degrees of rotation of the stepper motor would represent 1 pixel length.

E. Stepper Motor System Design

The stepper motors themselves will have attachable components to a pulley system, which will move the components on the board. In order to move the stepper motors, specific drivers will need to be attached to them through an H-bridge. This H-bridge will be designed on a PCB in order to minimize odd connections and spaghetti wires in the final product.

F. Microcontroller Involvement

The push-buttons will be used to enable different functions of the system and use the HEX 7-seg displays as visual feedback for which actions is be carried out. The microcontroller will be the central hub where all components of the system interact.

G. Shortest Path Algorithm

In order to find the shortest path between all marks detected by the image processing program, Dijkstra's algorithm will be applied to the array containing the translated image. This translated image will be contained in an array that represents the detected marks from the board. The algorithm will then start at the top left most mark and traverse the array to find the shortest path between all marks in the array.

H. Motion-Detection Program

Not much research has been looked into for this aspect of the project as it is one of the last items on the GANTT chart. It is assumed that some sort of image processing algorithm will be needed to detect rapid changes in pixels. More research will need to be done.

Standards

The standards listed below will be followed while completing this project.

- IEEE 802.3-2018 - IEEE Standard for Ethernet
 - Applies to the Ethernet connection we will be using
- IEEE 1233-1996/Cor a-1998 - IEEE Guide for Developing System Requirements Specifications
 - Applies to the development of our project
- ARM IHI 0042F Procedure Call Standard for the ARM Architecture
 - Applies to the assembly language we will be using to code our project
- IEEE Editorial Style Manual
 - Standards to follow when writing this Project Charter and any other reports that will be written for this project
- NEMA Standards Publication ICS 16 Motion/Position Control Motors, Controls, and Feedback Devices
 - Applies to the stepper motors we will be using and their operation

V. PROJECT DESCRIPTION AND BOUNDARIES

Detailed project description here

The following lists contain the major and minor components to be used in this project.

Major Components

- DE1_SoC Development Board
- Camera
- Stepper Motors
- Stepper Motor Drivers
- Linear Motion System (x & y direction)
- Eraser
- Whiteboard
- PCB's

Minor Components

- Various Wire Connections
- CAT-6 Aable
- Timing Belt
- Timing Belt Pulley Flange
- Stepper Motor Mounting Brackets
- Screws

- Screw Drivers
- Drill

There are a few different major difficult areas that will take up a majority of the development phase of the project. The first difficulty to overcome is establishing a working connection between the camera and the DE1_SoC board. This needs to be done through the ethernet communication protocol standard IEEE 802.3-2018 for Ethernet. Having had no experience in this yet there will be quite the learning curve in getting it to work properly.

Once an Ethernet connection is established, the next major difficulty will be to perform image processing on the images being sent from the camera. This processing will be accomplished by using the ARM IHI 0042F Procedure Call standard for the ARM architecture in the altera monitor program using the C language. The program needs to analyze the data in order to detect markings within the image and store the pixel locations of these markings in an array to be processed by the Dijkstra algorithm to generate a shortest path between all markings that were detected. This path will be the path that the eraser will take when erasing all the markings.

The last major obstacle will be to translate the derived shortest path from the Dijkstra algorithm to stepper motor rotations. Along with this, the degree of rotation of the stepper motor will need to be developed so that it is equivalent to the length of one pixel on the image grid.

Technical Advisor Background

The technical advisor for this project is Dr. Hovannes Kulhandjian, who specializes in digital signal processing, and wireless communications and networking. He will contribute advice and information pertaining to the ethernet connectivity of the camera to the image processing microcontroller. Dr. Kulhandjian was the original mind behind the idea of this project as well. Because of this, he will also be contributing more specifications and features to the project as its completion progresses.

The following figures will be used to implement and complete the Smart Eraser.

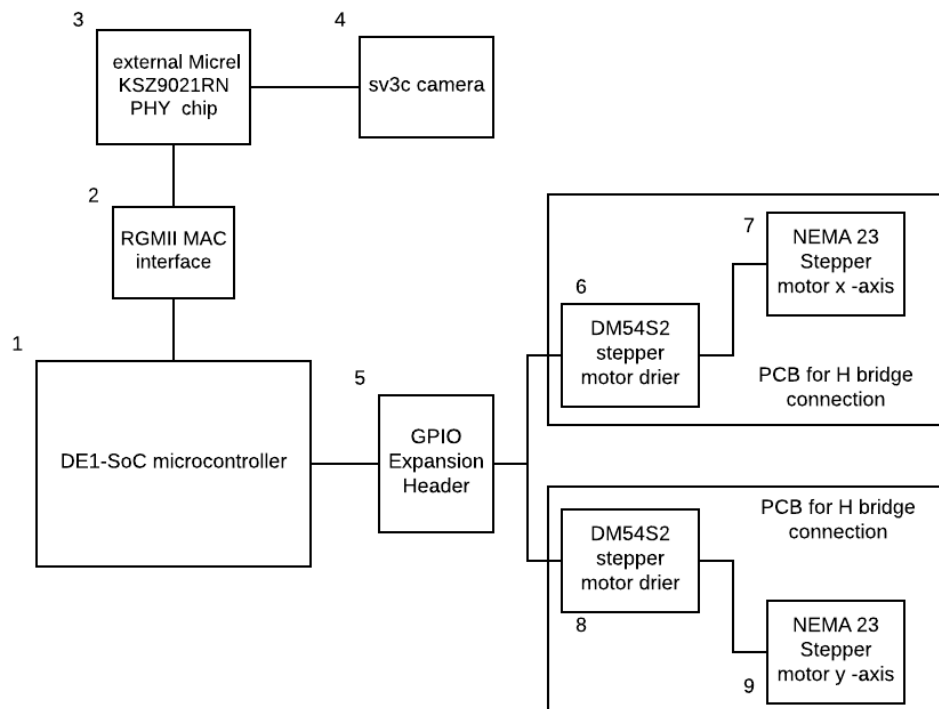


Fig. 2: Block diagram of main system components

The description of Fig. 2 and the labeling of each block goes here

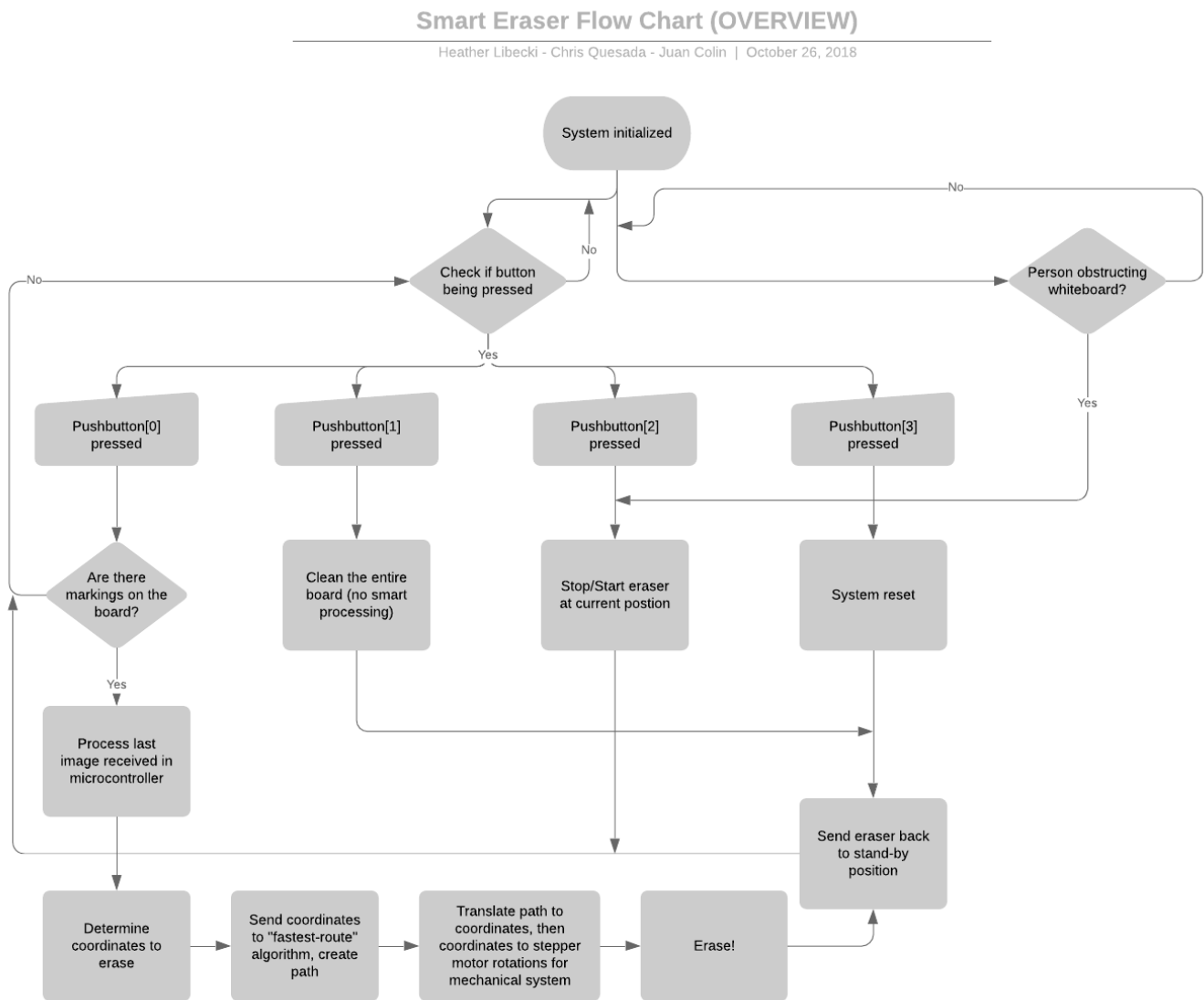


Fig. 3: Flow Chart of the Smart Eraser System

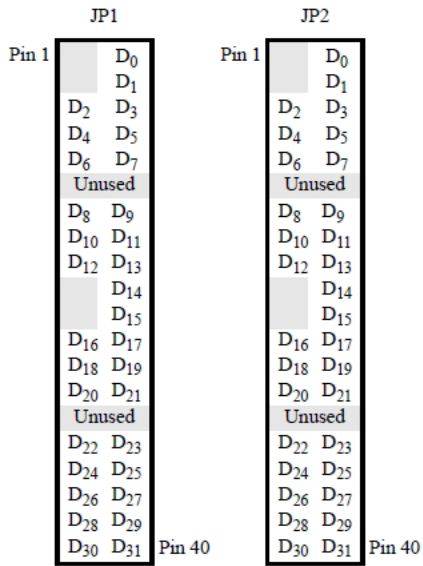


Fig. 4: GPIO Header pin layout

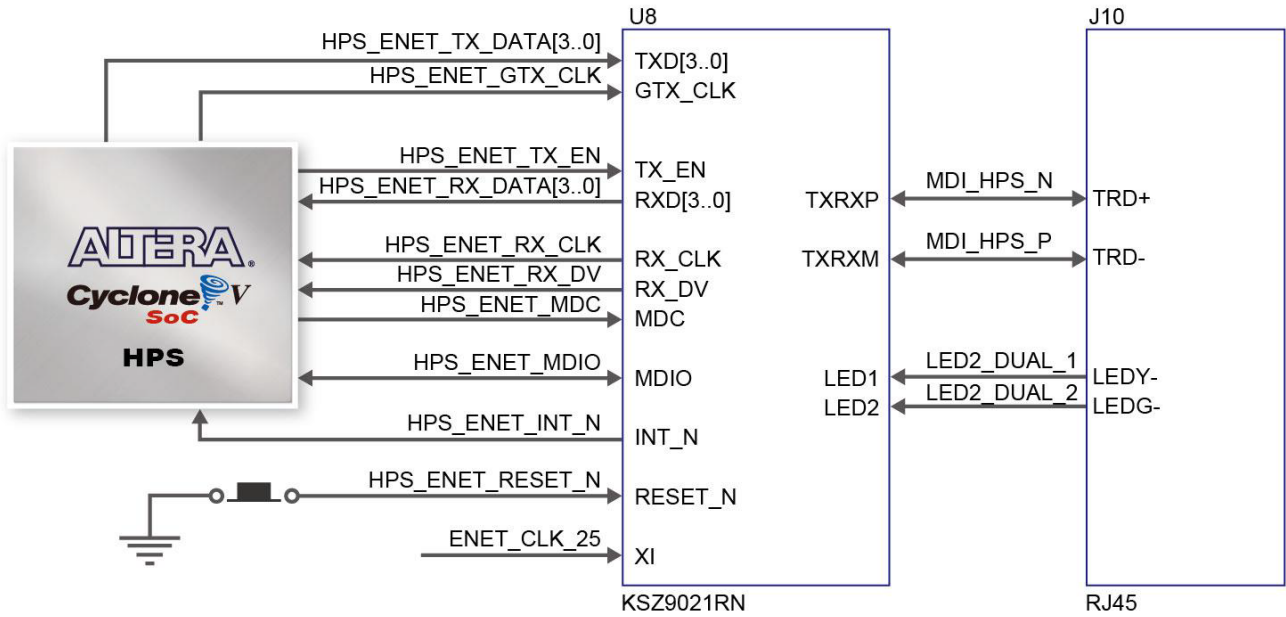


Fig. 5: Overview of Interface for Ethernet Communications

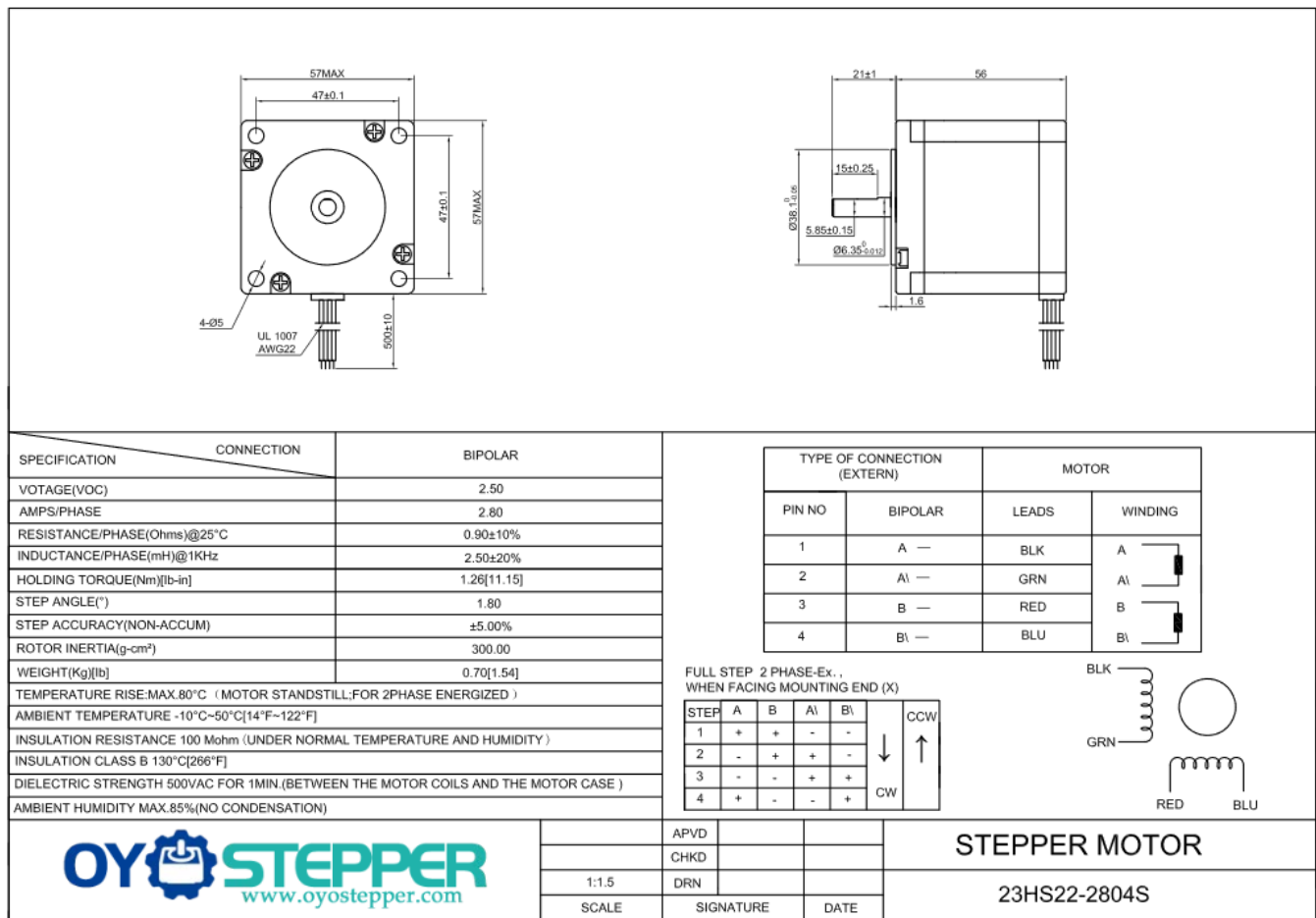
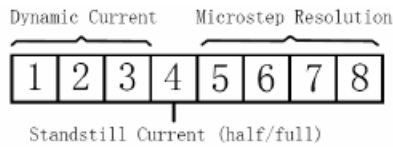


Fig. 6: Data sheet for the 23HS22-2804S stepper motor



Microstep Resolution Selection

Microstep resolution is set by SW5, 6, 7, 8 of the DIP switch as shown in the following table:

Microstep	Steps/rev.(for 1.8°motor)	SW5	SW6	SW7	SW8
2	400	OFF	ON	ON	ON
4	800	ON	OFF	ON	ON
8	1600	OFF	OFF	ON	ON
16	3200	ON	ON	OFF	ON
32	6400	OFF	ON	OFF	ON
64	12800	ON	OFF	OFF	ON
128	25600	OFF	OFF	OFF	ON
5	1000	ON	ON	ON	OFF
10	2000	OFF	ON	ON	OFF
20	4000	ON	OFF	ON	OFF
25	5000	OFF	OFF	ON	OFF
40	8000	ON	ON	OFF	OFF
50	10000	OFF	ON	OFF	OFF
100	20000	ON	OFF	OFF	OFF
125	25000	OFF	OFF	OFF	OFF

Fig. 7: Microstep resolution guide for the DM542 stepper motor driver

Dynamic Current Setting

Peak Current	RMS Current	SW1	SW2	SW3
1.00A	0.71A	ON	ON	ON
1.46A	1.04A	OFF	ON	ON
1.91A	1.36A	ON	OFF	ON
2.37A	1.69A	OFF	OFF	ON
2.84A	2.03A	ON	ON	OFF
3.31A	2.36A	OFF	ON	OFF
3.76A	2.69A	ON	OFF	OFF
4.20A	3.00A	OFF	OFF	OFF

Fig. 8: Dynamic current settings guide for the DM542 stepper motor driver

VI. HIGH-LEVEL RISKS

The following list details the high-level risks of this project, and what will be done as a precaution against these risks in order to keep the development of the Smart Eraser safe.

- Power systems malfunctioning
 - Ensure a secure connection of the power system to the wall outlet and the board and components themselves

- Moving parts attached to the tracks on the whiteboard
 - Always standing away from the board to ensure no parts will injure the members of the team
 - Introducing the failsafe program that will stop the movement of the components at the push of a button, in case there is a malfunction in the stepper motor movement code
- Shorts between components causing burnout of parts
 - Testing will be done with lower voltages than the final product to ensure that the components do not burn out

VII. MILESTONE SCHEDULE

The following tables show the milestone schedule for the Smart Eraser project over the next two semesters.

Member Assign.	Start-End Date	Description
All	10/12-10/19/18	Complete Smart Eraser Project Proposal to be submitted to DPS Telecom for review.
All	10/12-10/19/18	Finalize the specifics of the budget.
All	10/15-10/19/18	Create the Project Charter rough draft to be turned in.
All	10/16-10/26/18	Draft a more detailed blueprint of the physical Smart Eraser deliverable.
All	10/16-10/26/18	Revise the Project Description; complete for future reference.
All	10/16-10/26/18	Draft the flowchart to show the logical relationships between all connected devices within the project.
All	10/18-10/19/18	Complete bi-monthly update presentation for Senior Design class.
Juan C.	10/26-11/10/18	Complete a block diagram detailing the specific connections between the devices within the project.
Chris Q.	10/26-11/15/18	Research communication and protocols to be used.
Heather L.	10/26-11/15/18	Research the camera and how it will send data through the ethernet cord.
All	11/1-11/2/18	Complete bi-monthly update presentation for Senior Design class.
Heather L.	11/1-11/21/18	Research the microcontroller to be used (DE1_SoC).
Chris Q.	11/1-11/21/18	Research the image processing program and what programming language to use.
Juan C.	11/1-12/1/18	Research the mechanical system and the power connection it requires.
All	11/15-11/16/18	Complete bi-monthly update presentation for Senior Design class.
Chris Q.	11/15-11/25/18	Test initial information found on image processing program.
Heather L.	11/15-11/25/18	Test the microcontroller after researching the ports needed for the project.
Heather L.	11/20-12/1/18	Research the coordinate system; converts pixels to stepper motor rotations in the mechanical system.
All	11/29-11/30/18	Complete bi-monthly update presentation for Senior Design class.
All	12/1-12/17/18	Complete the final draft of the Project Charter.
All	12/13-12/14/18	Present Project Charter to Senior Design class, professor, and academic advisor.

TABLE I: Senior Design Semester 1 - Research Phase.

Member Assign.	Start-End Date	Description
Chris Q.	1/1-1/20/19	Develop the code for the image processing program.
Juan C.	1/1-1/15/19	Configure the power system for the mechanical parts of the Smart Eraser.
Juan C.	1/1-1/15/19	Build the mechanical system the eraser will be attached to.
Heather L.	1/15-2/15/19	Develop the coordinate system.
Heather & Chris	1/15-1/30/19	Develop the algorithm to determine the quickest path to erase markings on the board.
All	1/15-1/30/19	Integrate the microcontroller with the mechanical system.
All	1/20-1/30/19	Test the newly formed microcontroller-mechanical system.
Heather L.	2/1-2/10/19	Connect the camera to the microcontroller ports.
Chris Q.	2/1-2/25/19	Test the image processing program with the camera.
Chris Q.	2/25-3/25/19	Create the motion-detecting program.
All	3/26-4/15/19	Integrate the motion-detecting program with the camera and microcontroller-mechanical system.
All	4/1/-4/20/19	Test the motion-detecting program.
All	4/20-5/13/19	Add potential additional features to be decided upon at a later time (if ahead of schedule).
All	4/20-5/13/19	Final Project Presentation.

TABLE II: Senior Design Semester 2 - Implementation Phase.

VIII. GANTT CHARTS

The following figures show the GANTT chart schedules over the next two semesters. These list the tasks to be completed, who is in charge of what task, and the time duration the task is expected to take.

[Smart Eraser] Project Schedule Sem 1

[Lyles College of Engineering]

Gantt Chart Template © 2006-2018 by Vertex42.com

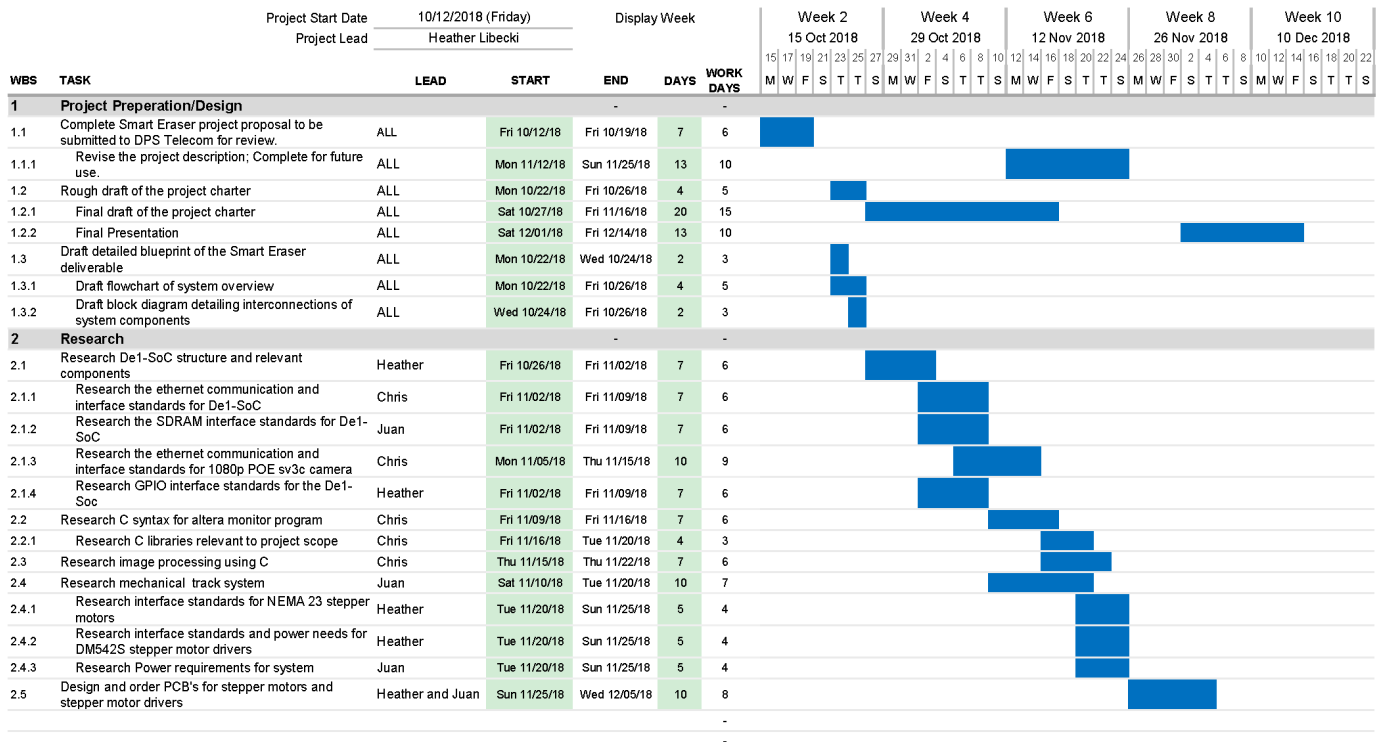


Fig. 9: GANTT chart for Senior Design Semester 1 - Research Phase

[Smart Eraser] Project Schedule Sem 2
[Lyles College of Engineering]

Smart Chart Template © 2006-2018 by Vitehd2.com

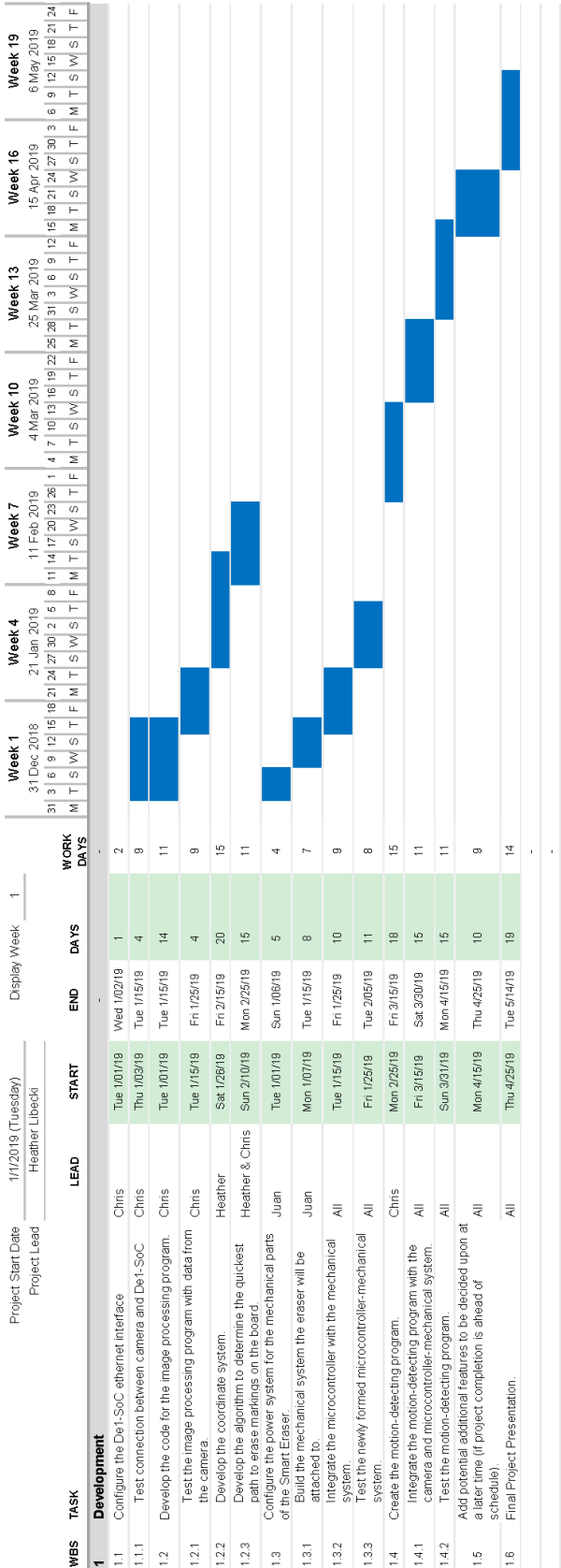


Fig. 10: GANTT chart for Senior Design Semester 2 - Implementation Phase

IX. EQUIPMENT AND BUDGET

The following table lists the components that are expected for the completion of this project, as well as the Production Company responsible for the creation of the product listed, and the prices of each component before tax and shipping costs. The “unknown” listed prices will be known with more design implementation of the project.

Component - Production Company	Est. Price
DE1_Soc FPGA Development Board - Terasic	\$175
CNC stepper motor driver - STEPPERONLINE	\$67.90 (\$33.95)
Carriage with Stainless Steel Balls; for use with 115RC Linear Tack - Accuride	\$67.16 (\$33.58 x2)
115RC 47in Linear Motion Aluminum Track Systems -Accuride	\$62.68 (\$31.34 x2)
Nema 23 CNC 2.8A Stepper Motor - STEPPERONLINE	\$52.00 (\$26.00)
1080p POE Camera - sv3c	\$38.99
Dry Erase Board (prototype) 36" x 24" - VIZ-PRO	\$31.90
Stepper Motor Encasing - D.Y Engineering	\$25.98 (\$12.99 X2)
5 Meter GT2 timing Belt (6mm width) - Mercury	\$17.98 (\$8.99 X2)
6.35mm GT2 40 Teeth Pulley Flange - uxcell	\$14.38 (\$7.19 X2)
Nema 23 Stepper Motor Steel Mount Bracket w/ Screws - HobbyUnlimited	\$10.99
Dry Erase Whiteboard Block Eraser - Expo	\$8.90
PCB for H Bridge (for Stepper Driver) and Stepper Motors, possible LCD screen	Unknown
Various Wires and Connection Cables	Unknown
Total Rough Budget	\$573.86

TABLE III: Estimated costs of components for project

Along with the components listed in the budget, the following resources will also be needed to ensure the completion of this project.

- A DC power source to provide the appropriate voltage to the stepper motors
- A power outlet for the DE1_SoC board
- Various connections and jumper cables between the power system and the Smart Eraser
- A place to store the components we will be using when they are not being utilized, to ensure their safety and reliability
- Testing items:
 - DC power source
 - Digital multimeter
 - Solderless breadboard
 - A monitor to display the camera feed
 - A computer with the Altera Monitor Program installed in order to write, run, and debug the ARM assembly language on the DE1_SoC

X. ROLES OF TEAM MEMBERS

Heather Libecky will be project manager of the Smart Eraser project. She has experience with programming the specific board (the DE1_SoC) that will be used in this project, as well as connecting physical devices to it and controlling them via the GPIO ports. This project will be dependent on translating data from the camera into information that the microcontroller will be able to process, and her previous experience with the board plus her ability to learn and adapt quickly will help in that implementation. She is adept at solving problems, debugging and error detection, and technical writing, which will be an integral part in the completion of this project. A solid understanding of the microcontroller full capabilities

will need to be further ascertained, as well as how the coordinate system will work in conjunction with the stepper motors, which will be connected to the DE1_SoC.

Chris Quesada has experience in working with embedded systems and developing code for different applications. This project will be heavy on the software side, involving either C or assembly language to take in data, analyze it, and then send an output. So, someone versed in both programming and interfacing with microcontrollers will be needed. A good understanding of networking will also be needed, because an ethernet connection will be used as the medium between the camera and the microcontroller. He is developing a solid knowledge based on how to interact with networks which can be applied to configuring an interface to communicate with and read data from the camera, which will be used in the creation of the image processing program, as well as the motion detection program.

Juan Colin has experience in working with electrical systems, and physical circuit design. He is proficient in the use of problem solving techniques to create a functioning system with given design specifications. His part of the project will be dependent on learning the physical mechanical aspects of the design, and how the connected parts will be powered. Therefore, he will need to further his understanding of how the parts should be connected in order to ensure the most spatial efficiency, as well as the connection of the power system that will allow all parts of the system to work properly and move the way they need to.

XI. STAKEHOLDER LIST

- 1) Dr. Aaron Stillmaker, Ph.D.: Is the professor of the Senior Design course, therefore wants to guide the students working on the Smart Eraser toward success through the completion of this project. Is also the manager of the project deliverable due dates.
- 2) Dr. Hovannes Kulhandjian, Ph.D.: Is the technical advisor of this project, and he held the original concept of this project, therefore he wants to help give technical advice to the students working on the Smart Eraser, as well as see the final product of their efforts.
- 3) DPS Telecom: Potential sponsor, therefore would like to see the final product they invested in monetarily.
- 4) Heather Libeck: Project Manager, Team Member
- 5) Chris Quesada: Team Member
- 6) Juan Colin: Team Member

XII. PROJECT APPROVAL REQUIREMENTS

The following list details the requirements that must be met in order for the Smart Eraser to be approved by the various stakeholders in the project.

- All requirements listed in the High-level requirements section of this project charter, which state that the project must:
 - Be completed with the outlined budget
 - Be implementable within 2 semesters
 - Be complex enough to warrant the title “senior design project”
 - Produce a complete Project Charter outlining the various project information, figures, and tables
 - Have significant, roughly equal portions of the project be completed by each team member
 - Utilize material learned in core and technical elective classes throughout our college careers

- Produce a deliverable that can be presented in the Senior Project Presentation Day event usually held in the Satellite Student Union building on campus
- Be able to be scaled to a bigger surface than the prototype
- Any additional requirements given by DPS Telecom if they become a sponsor of the project

XIII. APPROVALS

Signature: _____
Heather Libecky - Project manager

Signature: _____
Chris Quesada - Team member

Signature: _____
Juan Colin - Team member

Signature: _____
Hovannes Kulhandjian, Ph.D. - Technical advisor

Signature: _____
Aaron Stillmaker, Ph.D. - Course Instructor

Potentially DPS Telecom

[1] [2] [3]

REFERENCES

- [1] Altera, "DE1_SoC Reference Manual," <http://www.ee.ic.ac.uk/pcheung/teaching/ee2digital/DE1SoCUsermanual.pdf>,
 [2] Oyostepper, "23HS22-2804S data sheet," <https://www.oyostepper.com/images/upload/File/23HS22-2804S.pdf>,
 [3] L. Leadshine Technology CO, "DM542 User Manual," <http://robokits.download/datasheets/Leadshine>