# The Smart Eraser

ECE 186A - Senior Design I Fall 2018 - Dr. Stillmaker

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#### I. Introduction

Our project, the Smart Eraser, is an automatic whiteboard eraser. The main deliverable of this project will be an eraser which can move left-to-right on a track, and up-and-down on a bar attached to the track. This eraser will be able to automatically detect on a whiteboard where there are markings through the use of a camera and image-detection programming. The camera will send the coordinates of the markings to the eraser, which will then find the quickest route to erase the markings, before returning to its stand-by position. The eraser will also be able to detect the presence of a person, resulting in the immediate termination of whatever process it was carrying out.

Heather Libecki is responsible for being the project manager, the wireless communication between the camera and the imbedded system, and she will also be contributing to the completion of the image-processing program which will detect where the markings on the board are. Chris Quesada is responsible for developing the microcontroller code, particularly the physical movement protocols of the system, using the coordinates specified by the camera, and he will also be contributing to the completion of the image-processing program which will detect if a person is in front of the board. Juan Colin is responsible for the creation and execution of the physical, mechanical system that the eraser will be attached to, including the wiring of the system itself, and he will also be contributing to the research and implementation of the power system. All three of the team members will assist each other with the initial research, then break off into their own specialties as the project completion progresses.

# II. BACKGROUND

The main goal of this project is to create a fully-functional automatic eraser that detects where it needs to erase as well as if there is a person in front of the board. The following objectives will need to be accomplished in order to obtain this goal: design a track system for the eraser, design a power system for the track, design a control panel/interface, create a compact unit that can easily be moved on the track, create an image-processing program in Matlab, create the path/coordinate system for the eraser to follow, communicate the created path to the Raspberry Pi, and detect if a person is standing in front of the board. The following list contains the strengths and weaknesses that were found for each member of the group.

### Heather Libecki

- Strengths: programming (assembly, Matlab), mathematics, debugging, circuit implementation, problem solving, technical writing, public speaking
- Weaknesses: programming (verilog, Python), circuitry design, coding algorithms, power systems, Github Chris Quesada
- Strengths: programming (assembly, verilog, Matlab) embedded systems, algorithms, brainstorming
- Weaknesses: programming (Python), circuitry design, mathematics, Github, public speaking

Juan Colin

- Strengths: electrical systems, circuitry design, problem solving, power systems, problem solving, public speaking/relations
- Weaknesses: programming (assembly, verilog, Matlab, Python), Github, technical writing and spelling

The background information that will need to be researched in order to implement this project is: image-processing programs, translating data from image-processing into coordinates, wireless communication between separate devices, Simulink in Matlab, Github repositories, Python usage, connecting physical devices for the track of the eraser, language and usage of Raspberry Pi, and other research as necessary. The specific courses that this group has taken that will be used in the implementation of this project are: Embedded Systems (ECE 178), Introduction to Electrical and Computer Engineering Tools (ECE 72), Computer Networks (ECE 146), Principles of Electrical Circuits (ECE 90), Microprocessor Architecture and Programming (ECE 118), Digital Logic Design (ECE 85), and Switching Theory and Logical Design (ECE 106).

#### III. PROJECT DESCRIPTION

# A. Design track system

### B. Configure MATLAB to Communicate with FPGA

Using Mathworks information and references, an FPGA device will be configured so it will work with Matlab code. The specific FPGA board that will be used is the DE2-115 development board. First, we would set up the synthesis tool path within Matlab, which is also known as the HDL coder. Upon completion, this will allow a connection to be established between the computer and the development board. After creating a new project, which involves a function and testbench file, the HDL workflow advisor will then be used to correctly communicate with the board and run Matlab code on it as well. Integration of the board with Matlab will be advantageous because of its image processing toolbox.

#### C. Configure bluetooth connection via MATLAB

In order to receive images from the camera for processing, a bluetooth connection will need to be established so the information can be transmitted wirelessly. This connection will be established via a USB bluetooth dongle which will be plugged into the FPGA, and Mathworks provides additional information on how to establish this connection, which is another reason why Matlab will need to be integrated with the FPGA.

# D. Process an image in Matlab

The primary reason for using Matlab in this project is to take advantage of the image processing capabilities it has to offer through its image processing toolbox. There will be an external camera pointing at the board which will capture an image, transmit that imageâĂŹs data via a wireless bluetooth connection to the DE2-115 microcontroller, and there the image will be processed in a Matlab code. This will allow

the eraser to move to where the markings are on the board in a âĂIJsmartâĂİ way. The most convenient mode of image processing for the purposes of this project is to convert the image to a grayscale version of itself, which will allow for an easy detection of where any outlying markings are located. Their location will be converted into coordinates for the eraser to follow. The main obstacle of this objective is creating the algorithm which takes the markingâĂŹs locations and converts them to a coordinate system that the eraser will be able to utilize. Another obstacle is figuring out a way to make sure the eraser takes the shortest path possible when erasing the marks, in order to keep the system efficient.

# E. Create a path to follow based on the image processing done in Matlab

In the previous objective, it was stated that the eraser should follow the shortest path possible when erasing the markings, which means it will be taking a âĂIJsmart-pathâĂİ. This means that the program will need to find the shortest, quickest path possible while reaching all the coordinates that need to be erased. This can be accomplished by finding the shortest path between a source node (or coordinate) to all other nodes in the graph; this is also known as the shortestpath tree. There are many algorithms that can accomplish this, and information from the ECE catalogâĂŹs microprocessor course will also aid in the completion of this objective. It was decided that this âĂIJsmart-pathâĂİ should be created because it provides more of a challenge to the project. Otherwise, it would be relatively simple to create a random path for the eraser to follow, or even just programming it to move back and forth to erase the entire board every time it is activated.

# F. Communicate path via FPGA to track system

There will be two stepper motors that work in conjunction to drive the track system, and they need to be able to communicate with the FPGA. This will be done via the GPIO pins attached to the board, which are also known as the expansion header. Using the Reference Manual provided from Altera, this system component will be configured to send the smart-path data to the stepper motors via an H-Bridge. The H-bridge is necessary to drive the motors forwards or backwards. This is done by turning a specific combination of the 4 switches on or off, which will allow the polarity of the voltage being applied to the load to change.

## G. Drive stepper motors to move track system along path

Another one of the larger obstacles that needs to be overcome is translating the smart-path to a usable coordinate system which will allow the stepper motors to move the eraser to the correct position on the board. When completed, it should move the way a vinyl plotter does, which works by having a track that can move left and right, and a roller that can move it up and down, to cover all directions of a 2D grid. The two stepper motors will need to be able to accomplish this for the Smart Eraser. The algorithm that converts the processed image into coordinates will need to be expressed in terms of rotation patterns in order to move the eraser correctly to the desired location.

#### H. Implement movement detection

A potential safety issue concerns the movement of the eraser while someone is by, or using, the whiteboard. In order to prevent any potential bodily collisions with the device while it is in motion, there will need to be a movement detection system put into place. This detection system will make the eraser stop moving wherever it is whenever a person is standing in front of the whiteboard. The best way to accomplish this is to set up an ISR (interrupt service routine) which will be communicated with the camera. In addition, the camera will need to take an image and send it to the FPGA every second or so to detect a change in the image, so a subroutine comprised of this logic will also need to be created. If movement is detected, an interrupt will be issued in order to halt the eraserâĂŹs motion. The eraserâĂŹs original task will resume when there is no more movement being detected, or when there is a physical override detected from a programmed pushbutton.

#### I. Estimated Costs

Component	Est. Price
Standard Eraser	mula
Standard Whiteboard	mula
Stepper Motors	mula
H-bridge	mula
HD camera	mula
Metal Tracks	mula
Pulleys	mula
various wires and connections	mula

TABLE I: Estimated costs of components for project

REFERENCES